Section	Name of document	Page	Document number	Version
2	Revisions	2–3	1MRK 580 241-XEN	Version 2.0-00
	Service notes	2–5	1MRK 580 419-XEN	Version 2.0-00
3	Product introduction	3–5	1MRK 580 276-XEN	Version 2.0-00
	Functions	3–7	1MRK 580 277-XEN	Version 2.0-00
	Requirements	3–25	1MRK 580 278-XEN	Version 2.0-00
	Technical data	3–29	1MRK 580 250-XEN	Version 2.0-00
	Ordering data sheet	3–47	1MRK 580 279-XEN	Version 2.0-00
4	Installation and commissioning	4–5	1MRK 580 289-XEN	Version 2.0-ab
	Local human-machine interface	4–25	1MRK 580 290-XEN	Version 2.0-00
	Menu tree	4–41	1MRK 580 291-XEN	Version 2.0-00
	Appendix – Menu tree structure for REx 5xx terminals	4–51	1MRK 580 292-XEN	Version 2.0-00
5	Terminal identification	5–3	1MRK 580 294 -XEN	Version 2.0-00
	Activation of setting group	5–7	1MRK 580 295-XEN	Version 2.0-00
	Restricted settings via human-machine interface	5–11	1MRK 580 296-XEN	Version 2.0-00
	I/O system configuration	5–15	1MRK 580 297-XEN	Version 2.0-00
	Configurable logic	5–25	1MRK 580 298-XEN	Version 2.0-00
	Self-supervision	5–45	1MRK 580 299-XEN	Version 2.0-00
	Blocking of functions during test	5–49	1MRK 580 300-XEN	Version 2.0-00
	Time synchronisation	5–51	1MRK 580 302-XEN	Version 2.0-00
6	Introduction to functions	6–23	1MRK 580 313-XEN	Version 2.0-00
	Instanteaneous phase overcurrent protection	6–27	1MRK 580 335-XEN	Version 2.0-00
	Time delayed phase overcurrent protection	6–37	1MRK 580 336-XEN	Version 2.0-00
	Pole discordance protection	6–45	1MRK 580 338-XEN	Version 2.0-00
	Breaker-failure protection	6–53	1MRK 580 339-XEN	Version 2.0-00
	Instantaneous residual overcurrent protection (non-dir)	6–65	1MRK 580 340-XEN	Version 2.0-00
	Time delayed residual overcurrent protection (non-dir)	6–75	1MRK 580 318-XEN	Version 2.0-00
	Residual overcurrent protection (dir and nondir)	6–83	1MRK 580 345-XEN	Version 2.0-00
	Communication logic for residual overcurrent protection	6–97	1MRK 580 346-XEN	Version 2.0-00
	Time delayed undervoltage protection	6–103	1MRK 580 351-XEN	Version 2.0-00
	Time delayed overvoltage and residual overvoltage protection	6–109	1MRK 580 352-XEN	Version 2.0-00
	Loss of voltage check	6–117	1MRK 580 355-XEN	Version 2.0-00
	Fuse failure supervision (negative sequence)	6–125	1MRK 580 358-XEN	Version 2.0-00
	Fuse failure supervision (zero sequence)	6–135	1MRK 580 359-XEN	Version 2.0-00
	Apparatus control for single and multiple bays	6–145	1MRK 580 371-XEN	Version 2.0-00
	Interlocking	6–229	1MRK 580 372-XEN	Version 2.0-00
	Command control	6–335	1MRK 580 373-XEN	Version 2.0-00

Section	Name of document	Page	Document number	Version
	Synchro- and energising check for single circuit breaker	6–341	1MRK 580 363-XEN	Version 2.0-00
	Synchro- and energising check, double circuit breakers	6–367	1MRK 580 362-XEN	Version 2.0-00
	Synchro- and energising check 1 1/2 CB arrangement	6–385	1MRK 580 364-XEN	Version 2.0-00
	Phasing, synchro- and energising check, single CB	6–407	1MRK 580 365-XEN	Version 2.0-00
	Phasing, synchro- and energising check, double CBs	6–437	1MRK 580 366-XEN	Version 2.0-00
	Autorecloser, single, two and/or three phase	6–463	1MRK 580 367-XEN	Version 2.0-00
	Autorecloser, three phase	6–493	1MRK 580 368-XEN	Version 2.0-00
	Single or two pole trip logic	6–517	1MRK 580 379-XEN	Version 2.0-00
	Serial communication	6–527	1MRK 580 301-XEN	Version 2.0-00
	Command function	6–541	1MRK 580 392-XEN	Version 2.0-00
	Event function	6–545	1MRK 580 370-XEN	Version 2.0-00
	Command function	6–533	1MRK 580 310-XEN	Version 2.0-00
	Disturbance report - Introduction	6–557	1MRK 580 383-XEN	Version 2.0-00
	Disturbance report - Settings	6–563	1MRK 580 384-XEN	Version 2.0-00
	Disturbance report - Indications	6–573	1MRK 580 386-XEN	Version 2.0-00
	Disturbance report - Disturbance recorder	6–575	1MRK 580 387-XEN	Version 2.0-00
	Disturbance report - Event recorder	6–583	1MRK 580 385-XEN	Version 2.0-00
	Disturbance report - Trip value recorder	6–585	1MRK 580 389-XEN	Version 2.0-00
	Monitoring of AC analogue measurements	6–589	1MRK 580 390-XEN	Version 2.0-00
	Monitoring of DC analogue measurements	6–607	1MRK 580 391-XEN	Version 2.0-00
	Pulse counter	6–627	1MRK 580 394-XEN	Version 2.0-00
7	Hardware design	7–3	1MRK 580 395-XEN	Version 2.0-00
	Construction and hardware characteristics	7–9	1MRK 580 396-XEN	Version 2.0-00
	Serial communication module	7–21	1MRK 580 398-XEN	Version 2.0-00
8	Terminal diagrams and default configurations	8–1	1MRK 580 400-XEN	Version 2.0-00
	How to use the default configuration	8–2	1MRK 580 377-XEN	Version 2.0-00
	Terminal diagram	N/A	1MRK 001 452-AA	Rev ind 2
	Default configuration	N/A	1MRK 001 697-20	Rev ind 01
9	Index	9–1	1MRK 580 305-XEN	Version 2.0-00