Rack-based I/O

I/O System

The rack-based I/O system consists of a central rack and one or two 19 inch expansion racks intended for double size Euro boards. Each rack is equipped with a power supply unit PSF.

For a SattCon 35 system the control unit CU45 is connected to the central rack.

For a Series 200 system connected to a central rack-based I/O, a rack adapter board 200-RANN is mounted in the central rack. This board communicates with the Series 200 CPU via the system bus.

For a Series 200 system connected to a remote rack-based I/O, a rack adapter board 200-RACN is mounted in the central rack. This board communicates with the Series 200 via the ControlNetTM network.

Two expansion racks can be connected to the central rack in a central I/O system and one in a remote I/O system. Each expansion rack is equipped with a bus decoder board PBAD, which communicates with CU45, 200-RANN or 200-RACN in the central rack through a parallel bus cable.

The rack-based system is intended for industrial use and meets the EMC directive 89/336/EEC and the Low Voltage Directive, LVD, 73/23/EEG.

The rack-based I/O has the following main features:

- High density I/O system
- Replacement with power on
- Large number of I/O boards and accessories
- Central or Remote I/O
- Compatible with both SattCon 35 and Series 200
- Digital and analogue I/O boards are galvanically separated
- Easy connection of shielded cables
- Simple upgrading of older I/O systems





Bus decoders PBAD

Bus decoder board including bus interconnection socket XPIOS. Handles analogue and digital communication between the central rack and the expansion rack. Also supervises the expansion rack's internal bus.

The board is powered by +12 V DC from the power supply unit PSF.

Two LEDs on the front indicate power and error.

Two switches on the front are intended for fault diagnosis in the system. The Run/Reset switch resets all the outputs in the expansion rack. The Enable/Disable switch disconnects a faulty rack for diagnostics without shutting down the rest of the system.

A DIP switch on the side of the board determines the identity of connected expansion racks.

200-RANN

Rack adapter board for Series 200 system. Handles the communication between system Series 200 and central rack-based I/O system, and controls the scanning of digital and analogue I/O signals on the local I/O bus.

Each 200-RANN can handle three racks – the central rack where it is installed and two expansion racks.

The board is powered from two sources. Series 200 powers the onboard CPU, memory and related logic whereas the PSF powers the interface between the CPU part of the board and the local I/O bus.

Three LEDs on the front indicate fail, power and disabled I/O copy (stop).

200-RANN/A

Rack adapter board 200-RANN with analogue to digital converter, ADSF, included.



POWE

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RUN

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RESET

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200-RACN

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MODE

€ X10

⊕ X1

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F1 220, 240 VAC 0.8 AT

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Rack adapter board for Series 200 system. Handles the communication between system Series 200 and remote rack-based I/O system, and controls the scanning of digital and analogue I/O signals on the local I/O bus.

Each 200-RACN can handle two racks – the central rack where it is installed and one expansion rack.

The board is powered from the PSF power supply.

Five LEDs on the front indicate fail, power, disabled I/O copy (stop) and communication status.

Two connectors and the "MODE" rotary switch on the front are used for test and service purposes only.

The other two rotary switches on the front are used to set the network node address.

200-RACN/A

Rack adapter board 200-RACN with analogue to digital converter, ADSF, included.

Power supply PSF

Power supply unit for the PIOS35 rack. One is required in each rack.

The board is powered either from 230 VAC mains or from a separate 24 V DC power unit and produces 5 VDC and 12 V DC, together with a power failure signal.

Maximum load is 50 W when supplied with 230 V AC and 70 W at 24 V DC.

Four LEDs on the front panel indicate supply for +5 V, +5 VL, +12 V and power failure.

Four sockets for test purposes are located next to the LEDs.

Mains supply selection 110, 220/230 or 240 V is made with a switch on the side of the board.

The mains supply is protected by a fuse fitted in the front for easy access.

The secondary side of the mains transformer and the 24 V DC source are also protected by fuses, located on the side of the board.

I/O boards General information

Boards can be replaced during operation. The switch on the front panel should be set in the "0" position, where the input status is frozen in the central unit and the outputs are set to zero. During normal operation, the switch should be in the "1" position.

LEDs on the front of the board indicate input/output status. The octal address of the input/output is marked beside each LED.

IDPG24 / IDPG48

Digital input board with 32 direct current inputs using positive logic. The inputs are galvanically isolated with optocouplers. The board is available in two versions for signal levels of 24 and 48 V.

IDPG may be used in applications where the plant 0 V is not connected to the system 0 V, or where there is a special requirement for galvanic signal isolation. IDPG is used to interface signals from devices such as limit switches, photocells etc.



IAPG 230

Digital input board with 16 AC inputs (230 V) using positive logic. The inputs are galvanically isolated with optocouplers.

IAPG 230 is used to interface signals from devices such as power switches, relay contacts etc.



ODPG.8

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Digital output board for DC voltage with 32 optocoupled outputs, divided into four separate groups of eight. The maximum output capacity is 0.8 A per output, subject to maximum 3.2 A per group and 8 A for the entire board.

ODPG.8 is designed for supply voltages between 10–60 V. Since the four groups are completely separated from each other, it is possible to use four different supply voltages for the same board.

Several outputs can be connected to the same load if they are fed from the same voltage source. If not, they need to have a protective diode in series with the output.

ORG24

Digital output board with 16 relay outputs. The relays have single-pole change-over contacts and are provided with varistor protection.

Maximum relay contact current is 2 A and maximum relay contact voltage is 250 V AC alternatively 120 V DC.

The supply voltage to the relay coils (24 V DC) is connected to a terminal located on the rear side of the rack.

ODSG

Digital short-circuit proof output board with two groups of 16 optocoupled outputs powered by an external supply (+19–30 V DC). Each group is monitored by an overload/short circuit detection bistable. The maximum load is 0.8 A for each output and 16 A for the complete board.

ODSG is intended for loads such as lamps, relays, solenoid valves etc.

When an overload or short circuit condition occurs, the output is switched off and the LED for the corresponding channel dims. If the Man/Auto input is active, a yellow LED for that group lights.

IPA4

IPA4 is a digital input board designed to count pulses. The maximum pulse frequency is 10 kHz.

IPA4 has four inputs, each with an 8-bit counter, capable of counting up to 255 pulses. The inputs can be combined in pairs to make two 16-bit counters with a capacity of up to 65535 pulses.

To suppress electrical interference, transmitters with complementary signals should be used.

Phase displaced pulse trains can be used for up/down-counting. The counters can be started, stopped and reset individually.

The common signal reference voltage level can be adjusted and detection of positive and/or negative-going edges can be selected individually for each input.

ÎPA4 can be used in applications for pulse counting, quantity counting, positioning and speed calculation.

16 LEDs on the front panel show the four counter channels, each with two inputs A and B. The inputs A and B can be combined in different ways, to measure motion in two directions, for instance.

Three sockets for test purposes are located on the front.

The counter channels also have gate inputs to start and stop the counting, and clear inputs to reset the respective counter.

All the input circuits are provided with comparators for transmitters with complementary signals, a means to effectively suppress electrical noise.

IBA

Analogue input board with eight inputs. IBA can either be used on its own or with associated modules. The plug-in modules are for galvanic isolation (MCVG), differential input (MCV200), temperature measurement (MP, MN) or resistance measurement (MR).

When IBA is used without associated modules, the following inputs can be connected: 0–20 mA, 4–20 mA, 0–10 V, 0–5 V and 1–5 V. Each channel is individually selectable by straps, located on the board.

IBA has high flexibility, very high repeatability and high temperature stability.

If the LED input (current) is used, the intensity of the LED varies with the value of the input signal.



OA
 OB
 IA
 IB
 2A
 2B
 3A
 3B
 OG
 IG
 2G
 3G
 OC
 IC
 2C
 3C

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8+ ov

IPA4



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OCVA

Analogue output board with four channels for voltage signals 0–10 V and current signals 0–20 or 4–20 mA, selectable with straps on the board. The resolution is 8 bits.

The analogue circuits are electrically isolated from the digital control logic and the control system. The channels are not galvanically separated from each other.

OCAHG generates analogue signals to control devices, instruments, etc. By means of the signals DDC and MAN, control can either be automatic (from the control system) or manual via the INCREASE/DECREASE inputs.

Manual control can be executed from a hand module, push-buttons or a digital output board.

OCAHG retains its output values in the event of a CPU failure.

OCVA

Analogue output board with two channels for current signals 0-20 mA or 4-20 mA and voltage signals ± 10 V or 0-10 V. The various ranges are selected using connections in the process connector cable.

Each channel can supply both current and voltage output signals at the same time. When using a current output the corresponding voltage range must be set to 0-10 V.

The analogue circuits are electrically isolated from the digital control logic and the control system. The channels are not galvanically separated from each other.

OCVA can be set up so that the outputs either go to zero or remain at the selected safety state values during CPU shut down.

The board has 12-bit resolution for the ± 10 V ranges and 11-bit resolution for the other current and voltage ranges.

Accessories

Order codes	Function	Order codes	Function
PIOS/R	Basic rack PIOS35 with address panel, power supply PSF and 200-RANN.	Modules for IBA	Five accessory modules for analogue input board IBA:
PIOS/RA Basic rack PIOS35 with address pane power supply PSF and 200-RANN/A.	Basic rack PIOS35 with address panel, power supply PSF and	MCV200	Analogue module which permits up to 200 VDC common-mode voltage for each channel, individually.
	200-RANN/A.	MCVG	Analogue module which provides indi- vidual galvanic isolation of up to 750 VDC per channel.
PIUS/RC	power supply PSF and 200-RACN.	MP	Measures temperature with a Pt100 sensor. MP is linearized. Six ranges.
PIOS/RCA	Basic rack PIOS35 with address panel,	MN	Measures temperature with a Ni1000 sensor. MN is not linearized.
	200-RACN/A.	MR	Measures resistance and is used for position sensing, pressure measure- ment etc. Three ranges.
PIOS/P	Basic rack PIOS35 with address panel, power supply PSF, bus interconnec- tion socket XPIOS and bus decoder board, PBAD (also includes screws and mounting devices).	PTU2	Two ready wired I/O connectors with cables and screw terminal blocks mounted on an aluminium profile.
PIOS35	Empty basic rack (for use as expan- sion or central rack).	200-MOUNTKIT	Mounting kit for mounting Series 200 central system in a 19" rack.
XPIOS	Bus interconnection socket. To be mounted on the rear part of the rack to enable connection of two bus cables to PBAD. Always included with PBAD.	PTC	Digital simulation board with a panel which has 32 switches for simulation of digital signals.
ADSF	Central A/D converter. To be mounted on rack adapter board 200-RANN.	ΙVΑΡΟΤ	Analogue simulation board with eight potentiometers for simulation of ana-logue signals.
IOC351.0M	Assembly kit with the following parts: an expansion cable (1 metre) connect- ing the central rack to the first expan- sion rack, a bus extension unit XPBAD, a connector and a cover.		
IOC352.5M	D:o 2.5 metre.		
CPIOS1.0M	Cable (1 metre) for parallel bus con- nection between two expansion racks.		
CPIOS2.5M	D:o 2.5 metre.		
200-CBR/R600	Cable (0.6 metre) for connecting the Series 200 system to the rack adapter board 200-RANN located in a central rack.		
200-CBR/R1000	D:o 1 metre.		



Configuration

The central and the expansion racks are of a common type called PIOS35. The rack has space for 16 I/O boards and all input/output boards fit in any slot intended for I/O boards.

The I/O boards are connected to the rack via two different connectors. The lower connector (X2) is used for connecting the rack's internal bus and power supply. The upper connector (X1) is used for connection of input/ output signals for the process using crimp sleeves.

For more detailed information, see the manual "Rack-based I/O, Installation and Maintenance".

Bus system

The signals between the racks are distributed via parallel bus cables. Two expansion racks can be connected to the parallel bus. If RACN or RACN/A is used, only one expansion rack may be connected.

There are two lengths of the cables to be used between the central rack and the expansion rack, or between the expansion racks. Their lengths are 1 and 2.5 metres, although only one 2.5 metres cable may be connected in a system.

Mains supply

Each rack contains a power supply PSF, located at the right hand side of the rack. It can be supplied via the mains or 24 V DC. Filters and mains connections are fitted at rear of the rack.

Technical data

General		200-RACN	
Temperature Operating	+5°C to +55°C (max. mean tem-	Number of channels Communication	1
Non-operating	perature over 24 hours is 50 C) $-25^{\circ}C$ to $+70^{\circ}C$	protocol Serial interface	ControlNet Separated via a signal trans-
Humidity Protection class	Max. 90%, non-condensing IP20	Speed of transfer	former 5 Mbit/sec.
Design standard	Fulfils the EMC directive 89/336/ EEC and the Low Voltage Direc- tive, LVD, 73/23/EEG for indus- trial environments	Power supply Current consumption 200-RACN	From Power supply unit PSF 400 mA/+5V; max.100 mA/+12V
Rated current for crimp sleeves	5 A	ControlNet connector	BNC 75 Ω
Board type Order code Manual "Rack-based I/O Installation and	Double size Euro board	Dimensions (incl. panel) Order codes	H 262 x W 60 x D 190 mm 200-RACN 200-RACN/A
Maintenance"	RACKIO-IME		

PBAD

The inaccuracy of bus decoder board PBAD is insignificantly low compared with that of the analogue boards and A/D converters, i.e. the board has no influence on the overall technical specification

Current consumption from PSF Dimensions (incl. panel) H 262 x W 20 x D 190 mm Order code

Max. 50 mA PBAD

200-RANN

from PSF

Order codes

Current consumption from NNbus

200-RANN 200-RANN/A 400 mA 600 mA

Current consumption Max. 100 mA **Dimensions (incl. panel)**

H 262 x W 60 x D 190 mm 200-RANN 200-RANN/A

PSF

Supply voltage

Mains supply Frequency 24 V DC Ripple on 24 V DC

Current consumption (nominal input voltage) 230 V AC mains 24 V DC supply

Output voltages^a

+5 V DC, +5 VL

Max. current output

Min. current output +12 V DC

Output power

Mains supply 24 V DC Continuous working voltage Isolation test voltage

Front panel temperature **Dimensions (incl. panel)** Order code

110/220/240 V AC +15/–10% selectable, 220 V AC accepts also 230 V AC +10/–15% 50 Hz ±5% or 60 Hz ±5% 24 V DC +20/-15% excl. ripple Peak 5% of nominal voltage

Typ. 0.5 A for max. load 50 W Typ. 3.5 A for max. load 70 W

5.1 V DC +0.1/-0.3 V DC during normal operation 4 A (+5 V DC and +5 VL combined) 0.2 A 12.5 V DC ±5 V DC max. 4 A, min. 0.2 A

Max. 50 W Max. 70 W

240 V AC 1776 V AC for one second, between mains input and protective ground and DC side. < 40°C above ambient H 262 x W 40 x D 190 mm PSF

a. (the total output power can limit the current)

IDPG24 / IDPG48

Number of inputs Input voltages IDPG24 IDPG48 Overvoltage rating Input current "1" level Filter time constant Logical levels IDPG24 IDPG48 Continuous working voltage Isolation test voltage Current consumption System power

Miscellaneous

Dimensions (incl. panel) Order codes

32 24 V DC +20/-25% 48 V DC +20/-25% 220 V AC max. 10 s. Typ. 10 mA (+20/-25%) Typ. 10 ms 0 < 5 V DC, 1 > 16 V DC 0 < 10 V DC, 1 > 32 V DC < 75 V DC relative to system logic 500 V DC (one minute) 40 mA (internal +12 V DC) 4 mA/active input +5 VL (= typ. 130 mA, max. 190 mA +5 VL) Galvanic isolation in pairs with one common connection H 262 x W 20 x D 190 mm IDPG24

IDPG48

Load current

Surge current Leakage current Recommended external fuses Voltage drop, output Activation time Deactivation time Continuous working voltage Isolation test voltage

Dimensions (incl. panel) Order code

Max. 0.8 A per output Max. 3.2 A per group Max. 8 A per board Max. 2 A for 50 ms Max. 2 mA typ. < 0.5 mA

3.2 A fast-blow per group Max. 2.5 V DC typ. < 1 V DC Max. 10 μs typ. 5 μs Max. 400 μs typ. 200 μs < 75 V DC relative to system ground 500 V DC for one minute, betweeen each group and system logic 30 mA (internal +12 V DC) 2–7 mA/active output (+5 VL). Totally 60–230 mA, typ. 140 mA H 262 x W 20 x D 190 mm ODPG.8

ORG24

		Number of outputs	16 24 V DC
IAPG230		Current drawn from relay supply (VR) per	17–25 mA at 24 V DC
Number of inputs Input voltage Overvoltage rating Input current for activated input Frequency range Filter time constant Logical levels	16 optocoupled 230 V AC +10/-15% 500 V AC max. 10 s. 6.5-14 mA, typ. 10 mA (230 V AC +10/-15%, 47-63 Hz) 47-63 Hz Typ. 20 ms Typ. 0 < 70 V DC, 1 > 130 V DC	energized relay coil Contact voltage ratings AC DC Contact current ratings (r AC DC DC DC	(VR = 24 V DC, +20/–15%) 250 V max. 120 V max. resistive load) 2 A max. 2 A max. (24 V DC +20%) 0.6 A max. (48 V DC +20%) Internal varistor for surge sup-
Continuous working voltage Isolation test voltage	230 V AC 1752 V AC for one second, between inputs and between any system logic and front panel.	Min. contact current	pression. External overload pro- tection required for all loads and external surge suppression for inductive loads 100 mA at 12 V DC
Current consumption System power	20 mA (internal +12 V DC) 5–15 mA/active input +5 VL (typ. 160 mA, tot. max. 80–240 mA)	Relay operating time Relay release time Contact bounce period	15 ms max., typ. 8 ms 10 ms, typ. 4 ms 2 5 ms max
Dimensions (incl. panel) Order code	H 262 x W 20 x D 190 mm IAPG230	Relay working life (resisti DC AC Continuous working voltage Isolation test voltage	 2 A: 150000 operations 2 A: 80000 operations 250 V AC max. 1800 V AC for one second, between each individual relay contact and between any relay contact and system logic or front panel.
ODPG.8	22 optocoupled	Current consumption	20 mA (+12 V DC). 5–15 mA/ active output, typ. 10 mA (+5 VL). Totally 80–240 mA, typ.
Number of separate groups Supply voltage Peak voltage	4 10–60 V DC. The groups can have common or separate sup- plies 75 V DC (mean value), max. 60 V DC as above	Dimensions (incl. panel) Order code	160 mA H 262 x W 20 x D 190 mm ORG24

ODSG

Number of outputs	32 optocoupled and 2 optocoupled error bistables	Number of inputs	8
Number of inputs	2 optocoupled 24 V DC. 10 mA reset signals	Current	250 Ω without LED 300 kΩ
External supply voltage Voltage limit	19–30 V DC Max. 50 V DC for 1 min. (25 [°] C)	Voltage drop	Max. 5 V DC without LED Max. 8 V with LED
Load current	Max. 0.8 A per output Max 16 A per board	Accuracy	±0.2% FS at 25°C
Short circuit current	Typ. 4 A, max. 10 A, fuse trip	Max, permanent permissi	±0.002% FS per C
Leakage current	delay \leq 20 ms (25 C) Max. 100 μ A, typ. 10 μ A. Typical value for a short circuited output	Current Voltage	30 mA 30 V DC
	is 10 mA	Input type	Single-ended common type
Recommended external fuses	16 A (slow) per board	Input filter (time to 63% of FS)	70 ms \pm 10% filter of first order. Breaking point at \approx 2.3 Hz
Resistance	Typ. 0.4 Ω, max. 0.6 Ω at 0.5 A. Typ. 0.6 Ω at 0.8 A (25 °C)	Input connector Crosstalk between	Harting, crimp-pin
Activation time	Тур. 10 µs	channels	79 dB attenuation
Deactivation time	Тур. 150 µs	Non-linearity	0.02% FS
Current consumption		Repeating accuracy	0.001% FS
System (12 V DC) LED (5 VL)	40 mA 7 mA/active output, typ. 140 mA	RFI immunity	Error of less than 0.8% of FS at 10 V/m, 27–1000 MHz
Continuous working voltage	< 75 V DC relative to system ground	Current consumption	Typ. 90 mA (+12 V DC) for the board. When modules are used,
Isolation test voltage	500 V DC for one minute, between I/O and system logic		the current consumption increas- es
Dimensions (incl. panel)	H 262 x W 20 x D 190 mm	Dimensions (incl. panel)	H 262 x W 20 x D 190 mm
Order code	ODSG		IDA

IBA

IPA4

Number of inputs	4 (with 8-bit counter) 2 (with 16-bit counter)
Galvanic isolation	No
Input impedance	1 k Ω or \approx 90 Ω
Power dissipation	Max. 0.25 W across connected terminating resistor
Max. pulse amplitude, complementary inputs	33 V DC (1 k Ω input impedance) or 10 V DC (90 Ω impedance)
Max. pulse amplitude, non-complementary inputs	24 V DC
Input voltage difference	Min. 1 V DC rel. inverted input or rel. transition level
Max pulse frequency	10 kHz
Process cabling	Twisted-pair cable with individual shielding for each pair of wires
Current consumption	150 mA (+12 V DC, 1.8 W)
Reference voltage level	0–10.5 V DC, adjustable
Dimensions (incl. panel)	H 262 x W 20 x D 190 mm
Order code	IPA4

IBA-module MCV200

Common mode suppression Continuous working voltage		
Common mode voltage relative to ground		
Impedance		
Common mode Differential mode		
Temperature stability		
Current consumption		
Miscellaneous		
Dimensions		

Order code

72 dB attenuation with 0–10 V \pm 5 V DC, otherwise 66 dB < 75 V DC relative to system ground

Max. 200 V DC (by design)

 $\begin{array}{l} 400 \ k\Omega \\ 800 \ k\Omega \\ Max. \pm 0.003\% \ FS \ per \ ^\circ C \\ 4 \ mA \ (+12 \ V \ DC \) \ per \ module \\ All \ other \ technical \ data \ are \ the \\ same \ as \ for \ the \ IBA \\ H \ 107 \ x \ W \ 18 \ x \ D \ 13 \ mm \\ MCV200 \end{array}$

IBA-module MCVG

Continuous working voltage Common mode voltage relative to ground

Common mode suppression Leakage current Accuracy Temperature stability Non-linearity **Current consumption Miscellaneous**

Dimensions Order code

< 75 V DC relative to system around 750 V DC without LEDs (by design 250 V DC with LEDs (by design) 94 dB attenuation 220 V, 50 Hz, 2 µA typ. ±0.3% FS at 25 °C Max. ±0.002% FS per °C Max. 0.1% FS 18 mA (+12 V DC) per module All other technical data are the same as for the IBA H 107 x W 18 x D 13 mm MCVG

Input filter (time to 63% of FS) MP100 **MP130** MP160 MP200 MP400 MP600 **MN80 MR100 MR140** MR1000 All Cabling Non-linearity MP MN MR **Current consumption** MP160 with transmitter MP160 without transmitter Other with transmitter Other without transmit-

ter

Miscellaneous

Dimensions Order codes 200 ms 200 ms 200 ms 150 ms Second breakpoint at 3.4 Hz Shielded, no joints. Max. conductor resistance 50 Ω . Uniform length Max. 0.3% FS See curve above Max. 0.3% FS +12 V DC. 24 mA 34 mA. Note that with MP160 it is not possible to have more than

Second order filter ± 20%,

first breakpoint

500 ms

300 ms

300 ms

300 ms

200 ms

180 ms

14 boards in a rack, each with 8 MP160 modules. 20 mA

30 mA All other technical data are the same as for the IBA

H 107 x W 18 x D 13 mm

MP100 MP200 MP400 MP600 MP130 MP160 MN80 **MR100 MR140** MR1000

OCAHG

Number of outputs 4 Supply voltage 20-30 V DC Supply current Max. 200 mA at 24 V DC (except MAN/DDC ind.) Analogue outputs (time to 63% of FS in DDC mode) ≈ 150 ms Load current for voltage outputs Max. 7 mA per output Load resistance for Max. 750 Ω current outputs **MAN/DDC** indication Max. 50 mA per output Accuracy ±0.5% of FS within the temperature range +5 to +55 C Resolution 8 bits **Current consumption** 1 mA (+12 V DC) **Continuous working** < 75 V DC relative to system voltage ground Common mode voltage relative to ground 500 V DC **Dimensions (incl. panel)** H 262 x W 20 x D 190 mm Order code OCAHG

IBA-modules MP, MN and MR

Input impedance Accuracy at 25°C MP100-MP400

MP600

MN80

MR100-MR1000 **Temperature stability** Influence of conductor resistance Sensor current (typical) MP160 **MN80 MR1000** Other Measuring ranges MP100 MP130 MP160 MP200 MP400 MP600 **MN80 MR100 MR140** MR1000

0 -19 -8 3 14 25 36 47 58 69 80 $1000 \ \Omega = 0 \ C$ $871.7 \Omega = -30$ °C $1390.1 \Omega = 80^{\circ}C$ ±0.5% FS Max. ±0.01% FS per °C Max. $\pm 0.1\%$ FS per 10 Ω

Min. 10 $M\Omega$

 $\alpha = 0.00385$

α=0.00385

Indicated error

4 mA

0.5 mA

0.5 mA

±0 to +100°C

-30 to +130°C

±0 to +160°C

±0 to +200°C

±0 to +400°C

±0 to +600°C

-30 to +80°C

0 to 100 Ω

0 to 140 Ω

0 to 1000 Ω

3 mA

±0.4% FS, DIN 43760,

±0.6% FS, DIN 43760,

Follows the curve, ±0.4% FS

Temp

10

OCVA

Number of outputs	2
Supply voltage	24 V DC +20 to -10%
Max. ripple	5% of nominal voltage
Supply current	Max. 140 mA at 24 V DC
Response time to 63% of FS of output	1.5 ms
Load current for voltage outputs	Max. 5 mA per output
Load resistance for current outputs	Max. 750Ω
Max. recommended cable length	500 m
Accuracy	Max. ±0.3%, typ. ±0.15% of FS at 25 °C
Temperature stability	±0.002% of FS per [°] C
Linearity error	Max. 0.05%, typ. 0.02% of FS within the temperature range +5 to +55°C
Offset	Max. 19.5 mV, 19.5 µA (±4 LSB)
Resolution	
–10 V – +10 V DC	12 bits
0–10 V DC	11 bits
4–20 mA	11 bits
0–20 mA	11 bits
Current consumption	45 mA (+12 V DC) 70 mA (+5 VL)
Continuous working voltage	< 75 V DC relative to system ground
Common mode voltage relative to ground Dimensions (incl. panel) Order code	500 V DC H 262 x W 20 x D 190 mm OCVA



ABB regional center Europe and Africa Västerås, Sweden Phone:+46 (0) 21 34 20 00 Fax:+46 (0) 21 13 78 45

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ABB regional center Americas Rochester, USA Phone:+1 716 292 6131 Fax:+1 716 273 7014 **ABB regional center Germany Munich, Germany** Phone:+49 (0) 89 84000-144 Fax:+49 (0) 89 84000-100 ABB regional center Asia Pacific Kuala Lumpur, Malaysia Phone:+60 (0) 3 973 2685 Fax:+60 (0) 3 973 9685