

ISPSD 2020

HITACHI ABB Power Grids Papers

Date / Time (CET)	Stream	Paper	Author/s
Tuesday, 15/09/2020 17:10-17:35	Virtual Festsaal B2L-A High Voltage Silicon Devices 1	Bidirectional Phase Control Thyristor (BiPCT): A New Anti-parallel Thyristor Concept	Jan Vobecky, Umamaheswara Reddy Vemulapati, Renata Bessa-Duarte Hitachi ABB Power Grids, CH
Wednesday, 16/09/2020 16:45-17:10	Virtual Zeremoniensaal C4L-B High Voltage Silicon Devices 2	The p-Ring Trench Schottky IGBT: A Solution Towards Latch-Up Immunity and an Enhanced Safe-Operating Area	Marina Antoniou ⁴ , Florin Udrea ² , Neophytos Lophitis ³ , Chiara Corvasce ¹ , Luca De-Michiellis ¹ Hitachi ABB Power Grids ¹ , CH; University of Cambridge ² , UK; University of Nottingham ³ , UK; University of Warwick ⁴ , UK
Thursday, 17/09/2020 14:00-15:00	Virtual Festsaal D1P-C SiC and other Materials (Poster Session)	Single and Repetitive Surge Current Events of 3.3 kV-20 a 4H-SiC JBS Rectifiers: The Impact of the Anode Layout	Nazareno Donato ² , Florin Udrea ² , Andrei Mihaila ¹ , Lars Knoll ¹ , Gianpaolo Romano ¹ , Lukas Kranz ¹ , Marina Antoniou ³ Hitachi ABB Power Grids ¹ , CH; University of Cambridge ² , UK; University of Warwick ³ , UK
Thursday, 17/09/2020 14:00-15:00	Virtual Festsaal D1P-C SiC and other Materials (Poster Session)	Surge Current Capability of 6.5kV-Rated SiC MOSFETs	Andrei Mihaila ¹ , Chunlei Liu ¹ , Gianpaolo Romano ¹ , Enea Bianda ² , Stephan Wirths ¹ , Yulieth Arango ¹ , Lars Knoll ¹ , Andreas Baschnagel ¹ , Boni Boksteen ¹ Hitachi ABB Power Grids ¹ , CH; ABB Switzerland Ltd. ² , CH
Thursday, 17/09/2020 14:00-15:00	Virtual Festsaal D1P-C SiC and other Materials (Poster Session)	Vertical Power SiC MOSFETs with High-K Gate Dielectrics and Superior Threshold Voltage Stability	Stephan Wirths ¹ , Yulieth Arango ¹ , Andrei Mihaila ¹ , Marco Bellini ¹ , Gianpaolo Romano ¹ , Giovanni Alfieri ¹ , Manuel Belanche ¹ , Lars Knoll ¹ , Enea Bianda ² , Elena Mengotti ² Hitachi ABB Power Grids ¹ , CH; ABB Switzerland Ltd. ² , CH

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Thursday, 17/09/2020 14:00-15:00	Virtual Festsaal D1P-C SiC and other Materials (Poster Session)	6.5kV Silicon Carbide MOSFETs with 5kV RB Safe Operating Area and MOS-Channel Surge Capability	Yulieth Arango ¹ , Gianpaolo Romano ¹ , Andrei Mihaila ¹ , Marco Bellini ¹ , Chunlei Liu ¹ , Peter Steimer ¹ , Stephan Wirths ¹ , Lars Knoll ¹ , Enea Bianda ² Hitachi ABB Power Grids ¹ , CH; ABB Switzerland Ltd. ² , CH
Thursday, 17/09/2020 17:00-18:00	Virtual Zeremoniensaal D3P-D High Voltage Si Devices (Poster Session)	Integrated Gate Commutated Thyristor: from Trench to Planar	Umamaheswara Reddy Vemulapati ¹ , Thomas Stiasny ¹ , Tobias Wikström ¹ , Neophytos Lophitis ³ , Florin Udrea ² Hitachi ABB Power Grids ¹ , CH; University of Cambridge ² , UK; University of Nottingham ³ , UK
Thursday, 17/09/2020 17:00-18:00	Virtual Zeremoniensaal D3P-D High Voltage Si Devices (Poster Session)	Pushing the Limits of the Maximum Punch-Through Design with an Advanced Buffer for Thin Wafer IGBTs	Elizabeth Buitrago ¹ , Nick Schneider ¹ , Chiara Corvasce ¹ , Jan Vobecky ¹ , Luca De-Michiellis ¹ , Munaf Rahimo ² Hitachi ABB Power Grids ¹ , CH; MTAL GmbH ² , CH