Relion® 650 series

Line distance protection REL650 Commissioning Manual

Power and productivity
for a better world™

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Warranty

Please inquire about the terms of warranty from your nearest ABB representative.

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Conformity

This product complies with the directive of the Council of the European Communities on the approximation of the laws of the Member States relating to electromagnetic compatibility (EMC Directive 2004/108/EC) and concerning electrical equipment for use within specified voltage limits (Low-voltage directive 2006/95/EC).

This conformity is proved by tests conducted by ABB AB in accordance with the generic standard EN 50263 for the EMC directive, and with the standards EN 60255-5 and/or EN 50178 for the low voltage directive.

This product is designed and produced for industrial use.

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The data, examples and diagrams in this manual are included solely for the concept or product description and are not to be deemed as a statement of guaranteed properties. All persons responsible for applying the equipment addressed in this manual must satisfy themselves that each intended application is suitable and acceptable, including that any applicable safety or other operational requirements are complied with. In particular, any risks in applications where a system failure and/or product failure would create a risk for harm to property or persons (including but not limited to personal injuries or death) shall be the sole responsibility of the person or entity applying the equipment, and those so responsible are hereby requested to ensure that all measures are taken to exclude or mitigate such risks.

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Safety information

Dangerous voltages can occur on the connectors, even though the auxiliary voltage has been disconnected.

Non-observance can result in death, personal injury or substantial property damage.

Only a competent electrician is allowed to carry out the electrical installation.

National and local electrical safety regulations must always be followed.

The frame of the IED has to be carefully earthed.

The IED contains components which are sensitive to electrostatic discharge. Unnecessary touching of electronic components must therefore be avoided.

Whenever changes are made in the IED, measures should be taken to avoid inadvertent tripping.

Table of contents

Section 1 Introduction

1.1 This manual

The commissioning manual contains instructions on how to commission the IED. The manual can also be used by system engineers and maintenance personnel for assistance during the testing phase. The manual provides procedures for checking of external circuitry and energizing the IED, parameter setting and configuration as well as verifying settings by secondary injection. The manual describes the process of testing an IED in a substation which is not in service. The chapters are organized in chronological order in which the IED should be commissioned.

1.2 Intended audience

This manual addresses the personnel responsible for commissioning, maintenance and taking the IED in and out of normal service.

The commissioning personnel must have a basic knowledge of handling electronic equipment. The commissioning and maintenance personnel must be well experienced in using protection equipment, test equipment, protection functions and the configured functional logics in the IED.

1.3 Product documentation

1.3.1 Product documentation set

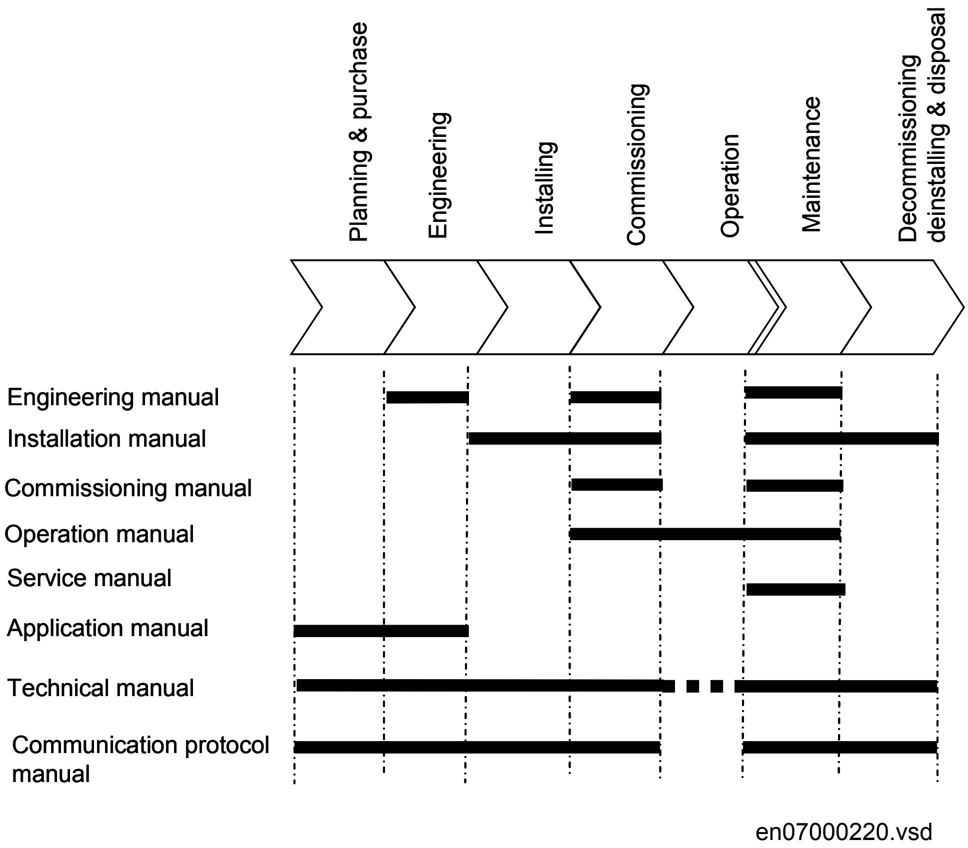

Figure 1: The intended use of manuals in different lifecycles

The engineering manual contains instructions on how to engineer the IEDs using the different tools in PCM600. The manual provides instructions on how to set up a PCM600 project and insert IEDs to the project structure. The manual also recommends a sequence for engineering of protection and control functions, LHMI functions as well as communication engineering for IEC 61850 and DNP3.

The installation manual contains instructions on how to install the IED. The manual provides procedures for mechanical and electrical installation. The chapters are organized in chronological order in which the IED should be installed.

The commissioning manual contains instructions on how to commission the IED. The manual can also be used by system engineers and maintenance personnel for assistance during the testing phase. The manual provides procedures for checking of external circuitry and energizing the IED, parameter setting and configuration as well as verifying settings by secondary injection. The manual describes the process of testing an IED in a substation which is not in service. The chapters are organized in chronological order in which the IED should be commissioned.

The operation manual contains instructions on how to operate the IED once it has been commissioned. The manual provides instructions for monitoring, controlling and setting the IED. The manual also describes how to identify disturbances and how to view calculated and measured power grid data to determine the cause of a fault.

The service manual contains instructions on how to service and maintain the IED. The manual also provides procedures for de-energizing, de-commissioning and disposal of the IED.

The application manual contains application descriptions and setting guidelines sorted per function. The manual can be used to find out when and for what purpose a typical protection function can be used. The manual can also be used when calculating settings.

The technical manual contains application and functionality descriptions and lists function blocks, logic diagrams, input and output signals, setting parameters and technical data sorted per function. The manual can be used as a technical reference during the engineering phase, installation and commissioning phase, and during normal service.

The communication protocol manual describes a communication protocol supported by the IED. The manual concentrates on vendor-specific implementations.

The point list manual describes the outlook and properties of the data points specific to the IED. The manual should be used in conjunction with the corresponding communication protocol manual.

The service manual is not available yet.

1.3.2 Document revision history

Document revision/date	Product series version	History
-/September 2009	1.0	First release

1.3.3 Related documents

Documents related to REL650	Identity number
Commissioning manual	1MRK 506 307-UEN
Technical manual	1MRK 506 304-UEN
Application manual	1MRK 506 305-UEN

Table continues on next page

Documents related to REL650	Identity number
Product Guide, configured	1MRK 506 308-BEN
Type test certificate	1MRK 506 308-TEN

650 series manuals	Identity number
Operation manual	1MRK 500 088-UEN
Communication protocol manual, DNP3	1MRK 511 224-UEN
Communication protocol manual, IEC 61850	1MRK 511 205-UEN
Engineering manual	1MRK 511 206-UEN
Installation manual	1MRK 514 013-UEN
Point list manual, DNP3	1MRK 511 225-UEN

1.4 Symbols and conventions

1.4.1 Safety indication symbols

The electrical warning icon indicates the presence of a hazard which could result in electrical shock.

The warning icon indicates the presence of a hazard which could result in personal injury.

The caution icon indicates important information or warning related to the concept discussed in the text. It might indicate the presence of a hazard which could result in corruption of software or damage to equipment or property.

The information icon alerts the reader to important facts and conditions.

The tip icon indicates advice on, for example, how to design your project or how to use a certain function.

Although warning hazards are related to personal injury, it should be understood that operation of damaged equipment could, under certain operational conditions, result in degraded process performance leading to personal injury or death. Therefore, comply fully with all warning and caution notices.

1.4.2 Manual conventions

Conventions used in IED manuals. A particular convention may not be used in this manual.

- Abbreviations and acronyms in this manual are spelled out in Glossary. Glossary also contains definitions of important terms.
- Push button navigation in the LHMI menu structure is presented by using the push button icons, for example:
  To navigate between the options, use ↑ and ↓.
- HMI menu paths are presented in bold, for example:
  Select **Main menu/Settings**.
- LHMI messages are shown in Courier font, for example:
  To save the changes in non-volatile memory, select `Yes` and press ↵.
- Parameter names are shown in italics, for example:
  The function can be enabled and disabled with the *Operation* setting.
- The ^ character in front of an input or output signal name in the function block symbol given for a function, indicates that the user can set an own signal name in PCM600.
- The * character after an input or output signal name in the function block symbol given for a function, indicates that the signal must be connected to another function block in the application configuration to achieve a valid application configuration.

1.4.3 Functions included in 650 series IEDs

Table 1: Main protection functions

IEC 61850	ANSI	Function description
Impedance protection		
ZQDPDIS	21	Five zone distance protection, quadrilateral characteristic
FDPSPDIS	21	Phase selection with load enchroachment, quadrilateral characteristic
ZMOPDIS	21	Five zone distance protection, mho characteristic
FMPSPDIS	21	Faulty phase identification with load enchroachment for mho
ZDNRDIR	21	Directional impedance quadrilateral and mho
PPLPHIZ		Phase preference logic
ZMRPSB	68	Power swing detection
ZCVPSOF		Automatic switch onto fault logic, voltage and current based

Table 2: Back-up protection functions

IEC 61850	ANSI	Function description
Current protection		
PHPIOC	50	Instantaneous phase overcurrent protection
OC4PTOC	51/67	Four step directional phase overcurrent protection
EFPIOC	50N	Instantaneous residual overcurrent protection
EF4PTOC	51N/67N	Four step directional residual overcurrent protection
SDEPSDE	67N	Sensitive directional residual overcurrent and power protection
UC2PTUC	37	Time delayed 2-step undercurrent protection
LPTTR	26	Thermal overload protection, one time constant
CCRBRF	50BF	Breaker failure protection
STBPTOC	50STB	Stub protection
CCRPLD	52PD	Pole discordance protection
BRCPTOC	46	Broken conductor check
GUPPDUP	37	Directional underpower protection
GOPPDOP	32	Directional overpower protection
DNSPTOC	46	Negative sequence based overcurrent function
Voltage protection		
UV2PTUV	27	Two step undervoltage protection
OV2PTOV	59	Two step overvoltage protection
ROV2PTOV	59N	Two step residual overvoltage protection
LOVPTUV	27	Loss of voltage check
Frequency protection		
SAPTUF	81	Underfrequency function
SAPTOF	81	Overfrequency function
SAPFRC	81	Rate-of-change frequency protection

Table 3: Control and monitoring functions

IEC 61850	ANSI	Function description
Control		
SESRSYN	25	Synchrocheck, energizing check, and synchronizing
SMBRREC	79	Autorecloser
SCILO	3	Logical node for interlocking
BB_ES	3	Interlocking for busbar earthing switch
A1A2_BS	3	Interlocking for bus-section breaker
A1A2_DC	3	Interlocking for bus-section disconnector
ABC_BC	3	Interlocking for bus-coupler bay
BH_CONN	3	Interlocking for 1 1/2 breaker diameter
Table continues on next page		

IEC 61850	ANSI	Function description
BH_LINE_A	3	Interlocking for 1 1/2 breaker diameter
BH_LINE_B	3	Interlocking for 1 1/2 breaker diameter
DB_BUS_A	3	Interlocking for double CB bay
DB_BUS_B	3	Interlocking for double CB bay
DB_LINE	3	Interlocking for double CB bay
ABC_LINE	3	Interlocking for line bay
AB_TRAFO	3	Interlocking for transformer bay
SCSWI		Switch controller
SXCBR		Circuit breaker
SXSWI		Circuit switch
POS_EVAL		Evaluation of position indication
SELGGIO		Select release
QCBAY		Bay control
LOCREM		Handling of LR-switch positions
LOCREMCTRL		LHMI control of PSTO
SLGGIO		Logic Rotating Switch for function selection and LHMI presentation
VSGGIO		Selector mini switch extension
DPGGIO		IEC61850 generic communication I/O functions double point
SPC8GGIO		Single point generic control 8 signals
AUTOBITS		AutomationBits, command function for DNP3.0
Secondary system supervision		
CCSRDIF	87	Current circuit supervision
SDDRFUF		Fuse failure supervision
TCSSCBR		Breaker close/trip circuit monitoring
Logic		
SMPPTRC	94	Tripping logic
TMAGGIO		Trip matrix logic
OR		Configurable logic blocks, OR
INVERTER		Configurable logic blocks, Inverter
PULSETIMER		Configurable logic blocks, PULSETIMER
GATE		Configurable logic blocks, Controllable gate
XOR		Configurable logic blocks, exclusive OR
LOOPDELAY		Configurable logic blocks, loop delay
TimeSet		Configurable logic blocks, timer
AND		Configurable logic blocks, AND
SRMEMORY		Configurable logic blocks, set-reset memory
RSMEMORY		Configurable logic blocks, reset-set memory
FSDSIGN		Fixed signal function block
B16I		Boolean 16 to Integer conversion
Table continues on next page		

IEC 61850	ANSI	Function description
B16IFCVI		Boolean 16 to integer conversion with logic node representation
IB16A		Integer to Boolean 16 conversion
IB16FCVB		Integer to boolean 16 conversion with logic node representation
Monitoring		
CVMMXN		Measurements
CMMXU		Phase current measurement
VMMXU		Phase-phase voltage measurement
CMSQI		Current sequence component measurement
VMSQI		Voltage sequence measurement
VNMMXU		Phase-neutral voltage measurement
CNTGGIO		Event counter
DRPRDRE		Disturbance report
AxRADR		Analog input signals
BxRBDR		Binary input signals
SPGGIO		IEC61850 generic communication I/O functions
SP16GGIO		IEC61850 generic communication I/O functions 16 inputs
MVGGIO		IEC61850 generic communication I/O functions
MVEXP		Measured value expander block
LMBRFLO		Fault locator
SPVNZBAT		Station battery supervision
SSIMG	63	Insulation gas monitoring function
SSIML	71	Insulation liquid monitoring function
SSCBR		Circuit breaker condition monitoring
Metering		
PCGGIO		Pulse counter logic
ETPMMTR		Function for energy calculation and demand handling

Table 4: Designed to communicate

IEC 61850	ANSI	Function description
Station communication		
		IEC61850 communication protocol
		DNP3.0 for TCP/IP communication protocol
GOOSEINTLK RCV		Horizontal communication via GOOSE for interlocking
GOOSEBINR CV		GOOSE binary receive
Scheme communication		
ZCPSCH	85	Scheme communication logic for distance or overcurrent protection
Table continues on next page		

IEC 61850	ANSI	Function description
ZCRWPSCH	85	Current reversal and weak-end infeed logic for distance protection
ZCLCPLAL		Local acceleration logic
ECPSCH	85	Scheme communication logic for residual overcurrent protection
ECRWPSCH	85	Current reversal and weak-end infeed logic for residual overcurrent protection

Table 5: Basic IED functions

IEC 61850	Function description
Basic functions included in all products	
INTERRSIG	Self supervision with internal event list
	Time synchronization
SETGRP	Setting group handling
ACTVGRP	Parameter setting groups
TESTMODE	Test mode functionality
CHNGLCK	Change lock function
ATHSTAT	Authority status
ATHCHCK	Authority check

Section 2 Starting up

2.1 Factory and site acceptance testing

Testing the proper IED operation is carried out at different occasions, for example:

- Acceptance testing
- Commissioning testing
- Maintenance testing

This manual describes the workflow and the steps to carry out the commissioning testing.

Factory acceptance testing (FAT) is typically done to verify that the IED and configuration meets the requirements by the utility or industry. This test is the most complex and in depth, as it is done to familiarize the user to a new protection or to verify a new configuration. The complexity of this testing depends on several factors.

- New IED type
- New configuration
- Pre-configured
- Modified existing configuration

Site acceptance testing (SAT or commissioning testing) is typically done to verify that the new installed IED is correctly set and connected to the power system. SAT requires that the acceptance testing has been performed and that the application configuration is verified.

Maintenance testing is a periodical verification that the IED is healthy and has correct settings depending on changes in the power system. There are also other types of maintenance testing.

2.2 Commissioning checklist

Before starting up commissioning at site, check that the following items are available.

- Single line diagram
- Protection block diagram
- Circuit diagram
- Setting list and configuration
- Crossed Ethernet cable (CAT 5)
- Three-phase test equipment

- PC with PCM600 installed along with the connectivity packages corresponding to the IED used
- Administration rights on the PC to set up IP addresses
- Product documentation (engineering manual, installation manual, commissioning manual, operation manual, technical manual and communication protocol manual)

2.3 Checking the power supply

Check that the auxiliary supply voltage remains within the permissible input voltage range under all operating conditions. Check that the polarity is correct.

2.4 Energizing the IED

2.4.1 Check the IED operation

Check all connections to external circuitry to ensure that the installation was made correctly, before energizing the IED and carrying out the commissioning procedures.

Energize the power supply of the IED to start it up. This could be done in number of ways, from energizing a whole cubicle to energizing a single IED. The IED time must be set. The self-supervision function in the **Main menu/Diagnostics/Internal events**or**Main menu/Diagnostics/IED status/General** menu in LHMI should also be checked to verify that the IED operates properly.

2.4.2 IED start-up sequence

The following sequence is expected when the IED is energized.

- The green Ready LED starts instantly flashing and the ABB logo is shown on the LCD.
- After approximately 30 seconds "Starting" is shown on the LCD.
- Within 90 seconds the main menu is shown on the LCD and the green Ready LED shows a steady light, which indicates a successful start-up.

The start-up times depend on the size of the application configuration. Application configuration with less functionality means shorter start-up times.

If the green Ready LED continues to flash after start-up, the IED has detected an internal error. Navigate via **Main menu/Diagnostics/IED status/General** to investigate the fault.

2.5 Setting up communication between PCM600 and the IED

The communication between the IED and PCM600 is independent of the used communication protocol within the substation or to the NCC.

The communication media is always Ethernet and the used protocol is TCP/IP.

Each IED has an Ethernet interface connector on the front and on the rear side. The Ethernet connector can be used for communication with PCM600.

When an Ethernet based station protocol is used, PCM600 communication can use the same Ethernet port and IP address.

For the connection of PCM600 to the IED two basic variants have to be considered.

- Direct point to point link between PCM600 and the IED front port.
- Indirect link via a station LAN or from remote via a network.

The physical connection and the IP address must be configured in both cases to enable communication.

The communication procedures are in both cases the same.

1. If needed, set the IP address for the IEDs.
2. Set up the PC or workstation for a direct link (point to point), or
3. Connect the PC or workstation to the LAN/WAN network.
4. Configure the IED IP addresses in the PCM600 project for each IED to match the IP addresses of the physical IEDs.

Setting up IP addresses

The IP address and the corresponding mask can be set via the LHMI for each available Ethernet interface in the IED. Each Ethernet interface has a default factory IP address when the complete IED is delivered. This is not given when an additional Ethernet interface is installed or an interface is replaced.

- The default IP address for the IED front port is 10.1.150.3 and the corresponding subnetwork mask is 255.255.255.0, which can be set via the LHMI path **Main menu/Configuration/Communication/TCP-IP configuration/1:ETHFRNT**
- The default IP address for the IED rear port is 192.168.1.10 and the corresponding subnetwork mask is 255.255.0.0, which can be set via the LHMI path **Main menu/Configuration/Communication/TCP-IP configuration/1:ETHLAN1**

The front and rear port IP addresses cannot belong to the same subnet or communication will fail. It is recommended to change the

IP address of the front port, if the front and rear port are set to the same subnet.

Setting up the PC or workstation for point to point access to IEDs front port

A special cable is requested to connect two physical Ethernet interfaces together without a hub, router, bridge or switch in between. The Tx and Rx signal wires must be crossed in the cable to connect Tx with Rx on the other side and vice versa. These cables are known as null-modem cable or cross-wired cable. The minimum length should be about 2 m. The connector type is RJ-45, see Figure 2.

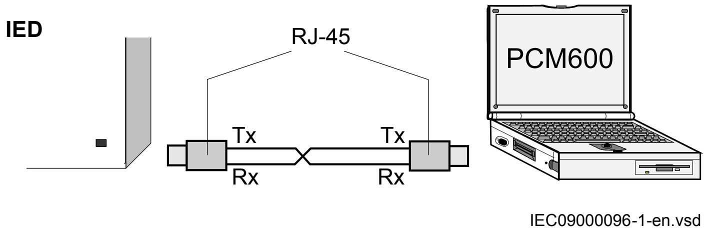

Figure 2: Point to point link between IED and PCM600 using a null-modem cable

The following description is an example valid for standard PCs using Microsoft Windows operating system. The example is taken from a Laptop with one Ethernet interface.

Administrator rights are requested to change the PC communication setup. Some PCs have the feature to automatically detect that Tx signals from the IED are received on the Tx pin on the PC. Thus straight (standard) Ethernet cable can be used.

When a computer is connected to the IED and the setting *DHCPServer* is set to *On* via the LHMI path **Main menu/Configuration/Communication/TCP-IP configuration/1:ETHFRNT/DHCPServer**, the IEDs DHCP server for the front port assigns an IP address for the computer. The computer must be configured to obtain its IP address automatically as described in the following procedure.

1. Select *Network Connections* in the PC, see Figure 3.

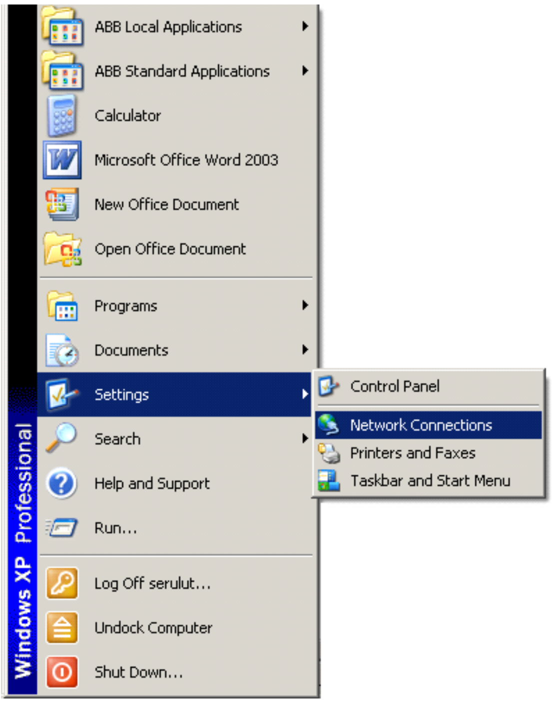

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Figure 3: Select: Network connections

2. Select *Properties* in the status window, see [Figure 4](#).

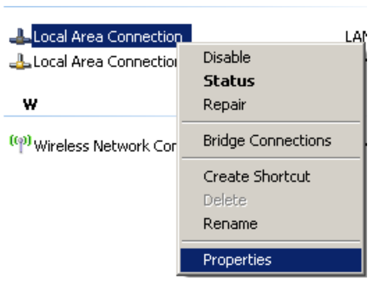

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Figure 4: Right-click Local Area Connection and select Properties

3. Select the TCP/IP protocol from the list of configured components using this connection and click *Properties*, see [Figure 5](#).

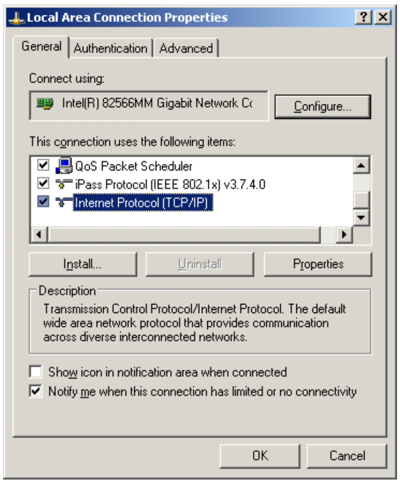

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Figure 5: Select the TCP/IP protocol and open Properties

4. Select *Obtain an IP address automatically*if the parameter *DHCPServer* is set to *On*, see Figure 6.

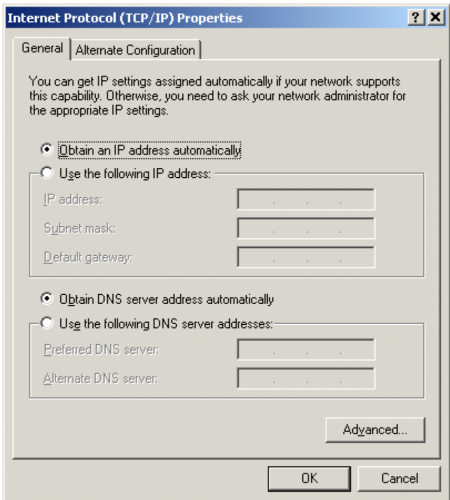

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Figure 6: Select: Obtain an IP address automatically

5. Select *Use the following IP address* and define *IP address* and *Subnet mask* if the parameter *DHCPServer* is set to *Off*, see Figure 7. The IP address must be different from the IP address chosen for the IED.

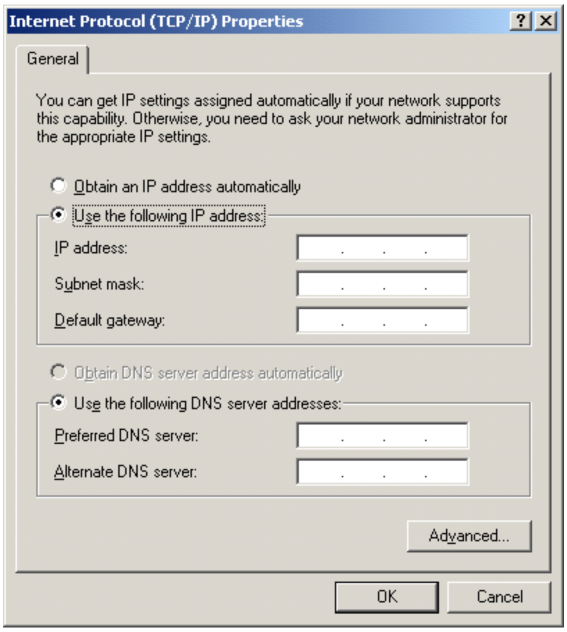

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Figure 7: Select: Use the following IP address

6. Close all open windows and start PCM600.

Setting up the PC to access the IED via a network

This task depends on the used LAN/WAN network. PC and IED must belong to the same subnetwork.

2.6 Writing an application configuration to the IED

Ensure that the IED includes the correct application configuration according to project specifications.

The application configuration is created using PCM600 and then written to the IED. Establish a connection between PCM600 and the IED when an application configuration must be written to IED.

When writing an application configuration to the IED, the IED is automatically set in configuration mode. When the IED is set in configuration mode, all functions are blocked. The red Trip LED on the IED flashes, and the green Ready LED is lit while the IED is in the configuration mode. When the writing procedure is completed, the IED is automatically set into normal mode.

Be sure to set the correct technical key in IED and PCM600 to prevent writing an application configuration to the wrong IED.

See the engineering manual for information on how to create or modify an application configuration and how to write to IED.

Set setting parameters through the LHMI. See the operation manual for information on how to change a setting from the LHMI.

2.7 Checking CT circuits

- Primary injection test to verify the current ratio of the CT, the correct wiring up to the protection IED and correct phase sequence connection (that is L1, L2, L3.)
- CT secondary loop resistance measurement to confirm that the current transformer secondary loop dc resistance is within specification and that there are no high resistance joints in the CT winding or wiring.
- Earthing check of the individual CT secondary circuits to verify that each three-phase set of main CTs is properly connected to the station earth and only at one electrical point.
- Insulation resistance check.

Both primary and secondary sides must be disconnected from the line and IED when plotting the excitation characteristics.

2.8 Checking VT circuits

Check that the wiring is in strict accordance with the supplied connection diagram.

Do not continue before any errors are corrected.

Test the circuitry.

- Polarity check
- VT circuit voltage measurement (primary injection test)
- Earthing check
- Phase relationship
- Insulation resistance check

The polarity check verifies the integrity of circuits and the phase relationships. The check should be performed as close to the IED as possible.

The primary injection test verifies the VT ratio and the wiring all the way through from the primary system to the IED. Injection must be performed for each phase-to-neutral circuit and each phase-to-phase pair. In each case voltages in all phases and neutral are measured.

2.9 Checking the RTXP test switch

The RTXP test switch is designed to provide the means of safe testing of the IED. This is achieved by the electromechanical design of the test switch and test plug handle, when the test plug handle is inserted it first blocks the trip and alarm circuits then it short circuit CT secondary circuit and opens VT secondary circuits making the IED available for secondary injection.

When pulled out the test handle is mechanically stopped in half withdrawn position, in this position the current and voltage enter the protection, but the alarm and trip circuits are still isolated.

Not until the test handle is completely removed the trip and alarm circuits are restored for operation.

Verify by pulling in all cables that the contact sockets have been crimped correctly and that they are fully inserted. This should never be done with current circuits in service.

Current circuit

1. Verify that the contact are of current circuit type.
2. Verify that the short circuit jumpers are located in the correct slots.

Voltage circuit

1. Verify that the contact are of voltage circuit type.
2. Check that no short circuit jumpers are located in the slots dedicated for voltage.

Trip and alarm circuits

1. Check that the correct type of contacts are used.

2.10 Checking transducer circuits

Verify from the manufacturer that the total circuit resistance is under specified values from the instrument (transducer) manufacturer.

2.11 Checking binary input and output circuits

2.11.1 Binary input circuits

Preferably, disconnect the binary input connector from the binary input cards. Check all connected signals so that both input level and polarity are in accordance with the IEDs specifications.

2.11.2 Binary output circuits

Preferably, disconnect the binary output connector from the binary output cards. Check all connected signals so that both load and polarity are in accordance with IED specifications.

2.12 Checking optical connections

Check that the Tx and Rx optical connections are correct.

An IED equipped with optical connections requires a minimum depth of 180 mm for plastic fiber cables and 275 mm for glass fiber cables. The allowed minimum bending radius has to be checked from the optical cable manufacturer.

Section 3 Establishing connection and verifying the IEC 61850 station communication

3.1 Setting the station communication

To enable IEC 61850 station communication:

- The IEC 61850-8-1 station communication functionality must be on in the LHMI. Navigate to **Main menu/Configuration/Communication/Station communication/1:IEC61850-8-1** and set parameter *Operation* to *On*.
- To enable GOOSE communication the *Operation* parameter for the corresponding GOOSE function blocks (GOOSEBINRCV and GOOSEINTLKRCV) must be set to *On* in the application configuration.
- To enable GOOSE communication via the front port the parameter *GOOSE* in **Main menu/Configuration/Communication/Station communication/IEC61850-8-1** must be set to *Front*. To enable GOOSE communication via rear port the parameter *GOOSE* must be set to *LAN1*.

3.2 Verifying the communication

Connect your PC to the nearby switch and ping the connected IED and the Substation Master PC to verify that the communication is working (up to the transport layer). If it is possible to see all of them, then they can see each other.

The best way to verify the communication up to the application layer is to

- use a protocol analyzer, for example an Ethereal that is connected to the substation bus, and
- monitor the communication

Section 4 Testing IED operation

4.1 Preparing the IED to verify settings

If a test switch is included, start preparation by making the necessary connections to the test switch. This means connecting the test equipment according to a specific and designated IED terminal diagram.

Put the IED into test mode to facilitate the test of individual functions and prevent unwanted operation caused by other functions. The test switch should then be connected to the IED.

Verify that analog input signals from the analog input module are measured and recorded correctly by injecting currents and voltages required by the specific IED.

To make testing even more effective PCM600 should be used. PCM600 includes the Signal monitoring tool, which is useful to read of the individual currents and voltages, their amplitudes and phase angles. In addition PCM600 contains the Disturbance handling tool. The content of reports generated by the Disturbance handling tool can be configured which makes the work more efficient. For example, the tool may be configured to only show time tagged events and to exclude analog information and so on.

The disturbance report settings must be checked to ensure that indications are correct.

Functions to test, signal and parameter names can be found in the technical manual. The correct initiation of the disturbance recorder is made on start and/or release or trip from a function. Also check that the wanted recordings of analogue (real and calculated) and binary signals are achieved.

The 650 series products can have between 1 to 4 individual parameter setting groups prepared with full sets of different parameters for all functions. The purpose of these groups is to be able to handle different power system load conditions to optimize the parameters settings of the different functions for these different power systems conditions (for example summer/winter and day/night).

Parameters can be entered into different setting groups. Make sure to test functions for the same parameter setting group. If needed the tests must be repeated for all different setting groups used. The difference between testing the first parameter setting group and the remaining is that there is no need for testing the connections.

Observe during testing that the right method for testing must be used that corresponds to the actual parameters set in the activated parameter setting group.

In LHMI the sensitive directional earth fault protection SDEPSDE parameter group 4 is active indicated by the * next to #4 and the test of the SDEPSDE must be performed according to the instructions given for the setting *OpMode* and setting value *3I03U0cosfi*.

Set and configure the function(s) before the testing can start. Most functions are highly flexible in that it permits a choice of functional and tripping modes. The various modes are checked at the factory as part of the design verification. In certain cases only modes with a high probability of coming into operation need to be checked when commissioned to verify the configuration and settings.

Requirements for testing the function.

- Calculated settings
- Valid configuration diagram for the IED
- Valid terminal diagram for the IED
- Technical manual
- Three-phase test equipment

Content of the technical manual.

- Application and functionality summaries
- Function blocks
- Logic diagrams
- Input and output signals
- A list of setting parameters
- Technical data for the function

The test equipment should be able to provide a three-phase supply of currents (and for some START functions also voltage). The magnitude and angle of currents (and voltages) should be possible to vary. Check that the IED is prepared for test before starting the test session. Consider the logic diagram of the function when performing the test.

The response from a test can be viewed in different ways.

- Binary output signals
- Service values in LHMI (logical signal or phasors)
- A PC with PCM600 (configuration software) in debug mode

4.2 Activating test mode

The IED shall be put into test mode before testing. Test mode blocks all functions in the IED and the individual functions to be tested can be unblocked to prevent unwanted operation caused by other functions. In this way, it is possible to test slower back-up measuring functions without the interference from faster measuring functions. Test mode is indicated when the yellow Start LED flashes.

Procedure

1. Browse to **Main menu/Tests/IED test mode/1:TESTMODE**
2. Set parameter *TestMode* to *On* and save the changes.
   As a consequence the yellow Start LED will start flashing as a reminder and remains flashing until the test mode is switched off.

4.3 Preparing the connection to the test equipment

The IED can be equipped with a test switch of type RTXP8, RTXP18 or RTXP24. The test switch and its associated test plug handle (RTXH8, RTXH18 or RTXH24) are a part of the COMBITEST system, which provide secure and convenient testing of the IED.

When using the COMBITEST, preparations for testing are automatically carried out in the proper sequence, that is for example, blocking of tripping circuits, short circuiting of CT’s, opening of voltage circuits, making IED terminals available for secondary injection). Terminals 1 and 8, 1 and 18 as well as 1 and 12 of the test switches RTXP8, RTXP18 and RTXP24 respectively are not disconnected as they supply DC power to the protection IED.

The RTXH test-plug handle leads may be connected to any type of test equipment or instrument. When a number of protection IEDs of the same type are tested, the test-plug handle only needs to be moved from the test switch of one protection IED to the test switch of the other, without altering previous connections.

Use COMBITEST test system to prevent unwanted tripping when the handle is withdrawn, since latches on the handle secure it in the half withdrawn position. In this position, all voltages and currents are restored and any reenergizing transients are given a chance to decay before the trip circuits are restored. When the latches are released, the handle can be completely withdrawn from the test switch, restoring the trip circuits to the protection IED.

If a test switch is not used, take measures according to provided circuit diagrams.

Never disconnect the secondary connection of a current transformer circuit without short-circuiting the transformer's secondary winding. Operating a current transformer with the secondary winding open will cause a massive potential build up that may damage the transformer and injure humans.

4.4 Connecting test equipment to the IED

Connect the test equipment according to the IED specific connection diagram.

Pay attention to the current polarity. Make sure that the connection of input and output current terminals and the connection of the residual current conductor is correct. Check that the input and output logical signals in the logic diagram for the function under test are connected to the corresponding binary inputs and outputs of the IED under test.

4.5 Releasing the function to be tested

Release or unblock the function to be tested. This is done to ensure that only the function or the chain of functions to be tested are in operation and that other functions are prevented from operating. The user can release the tested function(s) by setting the corresponding *Blocked* parameter under Function test modes to *No* in LHMI.

When testing a function in this blocking feature, remember that not only the actual function must be activated, but the whole sequence of interconnected functions (from measuring inputs to binary output contacts), including logic and so on. Before starting a new test mode session the user should scroll through every function to ensure that only the function to be tested (and the interconnected ones) have the parameters *Blocked* and eventually *EvDisable* are set to *No* and *Yes* respectively. Remember that a function is also blocked if the BLOCK input signal on the corresponding function block is active, which depends on the configuration. Ensure that the logical status of the BLOCK input signal is equal to 0 for the function to be tested. Event function blocks can also individually be blocked to ensure that no events are reported to a remote station during the test. This is done by setting the parameter *EvDisable* to *Yes*.

Any function is blocked if the corresponding setting in LHMI under **Main menu/Tests/Function test modes** menu remains *On*, that is the parameter *Blocked* is set to *Yes* and the parameter *TestMode* under the **Main menu/Tests/IED test mode** remains active. All functions that were blocked or released from a previous test mode session, that is the parameter *Test mode* is set to *On*, are reset when a new test mode session is started.

Procedure

1. Browse to the **Function test modes** menu.

The Function test modes menu is located in LHMI under **Main menu/Tests/ Function test modes**.

2. Browse to the function instance that should be released.
3. Set parameter *Blocked* for the selected function to *Yes*.

4.6 Verifying analog primary and secondary measurement

Verify that the connections are correct and that measuring and scaling is correctly. This is done by injecting current and voltage to the IED.

Apply input signals as needed according to the actual hardware and the application configuration made in PCM600.

Procedure

1. Inject a symmetrical three-phase voltage and current at rated value.
2. Compare the injected value with the measured values.
   The voltage and current phasor menu in LHMI is located under **Main menu/ Measurements/Analog primary values**and**Main menu/Measurements/ Analog secondary values**.
3. Compare the frequency reading with the set frequency and the direction of the power.
   The frequency and active power are located under **Main menu/Tests/ Function status/Monitoring/CVMMXN/1:CVMMXN/Outputs**. Then navigate to the bottom of the list to find the frequency.

   Check both analog primary and secondary values, because then the CT and VT ratios entered into the IED are also checked.

   These checks shall be repeated for Analog primary values.
4. Inject an unsymmetrical three phase voltage and current, to verify that phases are correctly connected.

If some setting deviates check the analog input settings under

Main menu/Configuration/Analog modules

Measured values such as current and voltages as well as active, reactive and apparent power, power factor phase angles as well as positive and negative and zero sequence currents and voltages are available in LHMI under **Main menu/ Tests/Function status/Monitoring**.

Navigate to the measurement function that contains the quantity to be checked.

Table 6: Measurement functions

Function	Quantity	Description
CMMXU	IL1 to IL3	amplitude, range and angle
CMSQI	$3I_0$; I1 and I2	amplitude, range and angle
CVMMXN	S; P; Q; PF; Ilag; Ilead; U; I and f	amplitude, range and angle
VMMXU	UL12 to UL31 i.e. phase-to-phase	amplitude, range and angle
VMSQI	$3U_0$; U1 and U2	amplitude, range and angle
VNMMXU	UL1 to UL3 i.e. phase-to-neutral	amplitude, range and angle

Also the Signal monitoring tool in PCM600 can be used to read measured values. In many cases it is more convenient to use PCM600 since, among many things, reports on measured values can be exported from the Signal monitoring tool to other tools (for example MS Excel) further analysis.

4.7 Testing protection functionality

Each protection function must be tested individually by secondary injection.

- Verify operating levels (trip) and timers.
- Verify alarm and blocking signals.
- Use the disturbance handling tool in PCM600 to evaluate that the protection function has received the correct data and responded correctly (signaling and timing).
- Use the event viewer tool in PCM600 to check that only expected events have occurred.

Section 5 Testing functionality

5.1 Testing disturbance report

5.1.1 Introduction

The following sub-functions are included in the disturbance report function:

- Disturbance recorder
- Event list
- Event recorder
- Fault locator
- Trip value recorder
- Indications

If the disturbance report is set on, then its sub-functions are also set up and so it is not possible to only switch these sub-functions off. The disturbance report function is switched off (parameter *Operation* = *Off*) in PCM600 or the local HMI under **Main menu/Settings/IED Settings/Monitoring/Disturbance Report/ 1:DRPRDRE**.

5.1.2 Disturbance report settings

When the IED is in test mode, the disturbance report can be made active or inactive. If the disturbance recorder is turned on during test mode, recordings will be made. When test mode is switched off all recordings made during the test session are cleared.

Setting *OpModeTest* for the control of the disturbance recorder during test mode are located in the local HMI under:

Main menu/Settings/IED Settings/Monitoring/Disturbance Report/ 1:DRPRDRE

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5.1.3 Disturbance recorder (DR)

A *Manual Trig* can be started at any time. This results in a recording of the actual values from all recorded channels.

The *Manual Trig* can be initiated in two ways:

1. From the local HMI under: **Main menu/Disturbance records**.

1.1. Enter on the row at the bottom of the HMI called **Manual Trig**.
The newly performed manual trig will result in a new row.
1.2. Navigate to **General information** or to **Trip values** to obtain more detailed information.

2. Open the Disturbance handling tool for the IED in the plant structure in PCM600.
   2.1. Right-click and select *Execute manual Trig* in the window *Available recordings in IED*.
   2.2. Read the required recordings from the IED.
   2.3. Refresh the window *Recordings* and select a recording.
   2.4. Right-click and select *Create Report* or *Open With* to export the recordings to any disturbance analyzing tool that can handle Comtrade formatted files.

Evaluation of the results from the disturbance recording function requires access to a PC either permanently connected to the IED or temporarily connected to the Ethernet port (RJ-45) on the front. The PCM600 software package must be installed in the PC.

Disturbance upload can be performed by the use of PCM600 or by any third party tool with IEC 61850 protocol. Reports can automatically be generated from PCM600. Disturbance files can be analyzed by any tool reading Comtrade formatted disturbance files.

It could be useful to have a printer for hard copies. The correct start criteria and behavior of the disturbance recording function can be checked when IED protective functions are tested.

When the IED is brought into normal service it is recommended to delete all recordings, made during commissioning to avoid confusion in future fault analysis.

All recordings in the IED can be deleted in two ways:

1. in local HMI under **Main menu/Clear/Clear disturbances**, or
2. in the Disturbance handling tool in PCM600 by selecting *Delete all recordings in the IED...* in the window *Available Recordings in IED*.

5.1.4 Event recorder (ER) and Event list (EL)

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

The result from the event recorder and event list can be viewed on the local HMI or, after upload, in PCM600 as follows:

1. on the local HMI under **Main menu/Events**, or in more details via
2. the *Event Viewer* in PCM600.

The internal FIFO register of all events will appear when the event viewer is launched.

When the IED is brought into normal service it is recommended to delete all events resulting from commissioning tests to avoid confusion in future fault analysis. All event in the IED can be cleared in the local HMI under **Main Menu/Clear/Clear internal event list** or **Main menu/Clear/Clear process event list**. It is not possible to clear the event lists from PCM600.

When testing binary inputs, the event list (EL) might be used instead. No uploading or analyzing of registrations is then needed since the event list keeps running, independent of start of disturbance registration.

5.2 Identifying the function to test in the technical reference manual

Use the Technical Manual (TM) to identify function blocks, logic diagrams, input and output signals, setting parameters and technical data.

5.3 Testing impedance protection functions

5.3.1 Five zone distance protection, quadrilateral characteristic ZQDPDIS

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

Consider releasing Zone 1, the Phase selection with load encroachment, quadrilateral characteristic FDPSDPIS and the tripping logic SMPPTRC.

Measure operating characteristics during constant current conditions. Keep the measured current as close as possible to its rated value or lower. But make sure it is higher than 30% of the rated current.

Ensure that the maximum continuous current in an IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

The test procedure has to take into consideration that the shaped load encroachment characteristic is active. It is therefore necessary to check the setting. To verify the settings with the shaped load encroachment characteristic the test should be carried out according to figures 8 and 9 and tables 7 and 8.

To verify the settings the following fault types should be tested:

- One phase-to-phase fault
- One phase-to-earth fault

The shape of the operating characteristic depends on the values of the setting parameters.

The angles a (angle on blinder in second quadrant for forward direction), b (load angle determining the load impedance area), c (angle to blinder in fourth quadrant for forward direction), d (line angle) and e (angle for earth compensation factor KN) in the figures below are adjusted with the parameters *ArgNegRes, ArgLd*, *ArgDir*, *LineAng* and *KNAng* respectively.

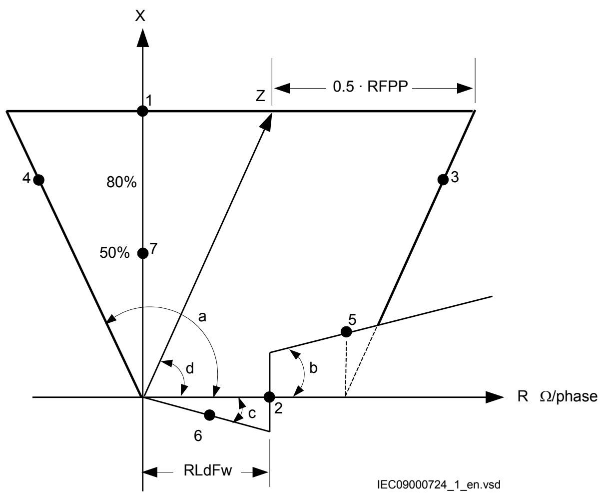

Figure 8: Distance protection characteristic with test points for phase-to-phase measurements

Table 7 is used in conjunction with figure 8.

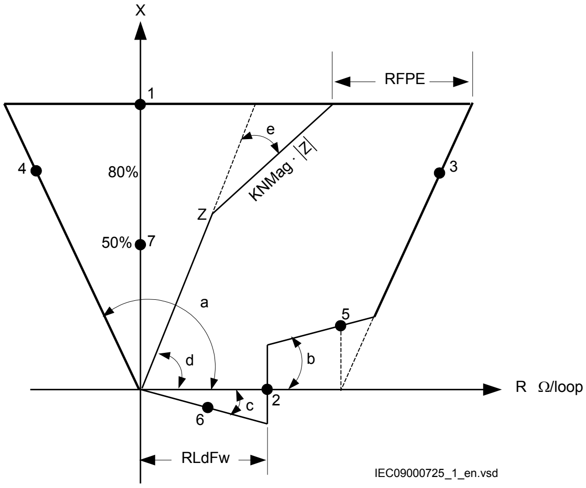

Figure 9: Distance protection characteristic with test points for phase-to-earth measurements

Table 8 is used in conjunction with figure 9.

Table 7: Test points for phase-to-phase loops L1-L2 (Ohm/phase)

Test point	Reach	Set value	Comments
1	X	Z · Sin (LineAng)	
	R	0	
2	X	0	
	R	RLdFw	
3	X	0.8 · Z · Sin (LineAng)	
	R	0.8 · Z · Cos (LineAng) + RFPP/2	
4	X	0.8 · Z · Sin (LineAng)	
	R	-0.8 · Z · Sin (LineAng) · tan (ArgNegRes-90)	
5	X	0.5 · RFPP · tan (ArgLd)	
	R	0.5 · RFPP	
6	X	-0.5 RLdFw · tan (ArgDir)	
	R	0.5 · RLdFw	
7	X	0.5 · Z · Sin (LineAng)	
	R	0	

Table 8: Test points for phase-to-earth L3-E (Ohm/Loop)

Test point	Reach	Value	Comments
1	X	Z · Sin (LineAng) + KNMag · Z · Sin (LineAng + KNAng)	
	R	0	
2	X	0	
	R	RLdFw	
3	X	0.8 (Z · Sin (LineAng) + KNMag · Z · Sin (LineAng + KNAng))	
	R	0.8(Z · Cos (LineAng) + KNMag · Z · Cos (LineAng + KNAng)) + RFPE	
4	X	0.8 (Z · Sin (LineAng) + KNMag · Z · Sin (LineAng + KNAng))	
	R	-(0.8 (Z · Sin (LineAng) + KNMag · Z · Sin (LineAng + KNAng)) · tan (ArgNegRes-90)	
5	X	RFPE · tan (ArgLd)	
	R	RFPE	
6	X	-0.5 RLdFw · tan (ArgDir)	
	R	0.5 RLdFw	
7	X	0.5 (Z · Sin (LineAng) + KNMag · Z · Sin (LineAng + KNAng))	
	R	0	

5.3.1.1 Measuring the operating limit of set values with shaped load encroachment characteristics

Procedure.

1. Subject the IED to healthy normal load conditions for at least two seconds.
2. Apply the fault condition and slowly decrease the measured impedance to find the operating value of the phase-to-phase fault L1-L2 for zone 1 according to test point 1 in figure 8 and table 7. Compare the result of the measurement with the set value.
3. Repeat steps 1 to 2 to find the operating value for the remaining test points. Observe that the zones that are not tested have to be blocked and the zone that is tested has to be released.
4. Repeat steps 1 to 3 above to find the operating value for the phase-to-earth fault L3-E according to figure 9 and table 8

Test point 6 is intended to test the directional line of impedance protection. Since directionality is a common function for all 5 measuring zones, it is only necessary to test point 6 once, in the forward direction. Directional functionality testing (trip inside, no-trip outside) should always be carried for all impedance zones set with directionality (forward or reverse).

5.3.1.2 Measuring the operate time of distance protection zones

Procedure

1. Subject the IED to healthy normal load conditions for at least two seconds.
2. Apply the fault condition to find the operating time for the phase-to-phase fault according to test point 7 in figure 8 and table 7 for zone 1. Compare the result of the measurement with the setting *t1PP*.
3. Repeat steps 1 to 2 to find the operating time for the phase-to-earth fault according to test point 7 in figure 9 and table 8. Compare the result of the measurement with the setting *t1PE*.
4. Repeat steps 1 to 2 to find the operating time for all other used measuring zones. Observe that the zones that are not tested have to be blocked and the zone that is tested has to be released.

5.3.1.3 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.3.2 Phase selection with load encroachment, quadrilateral characteristic FDPSPDIS

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

The phase selectors operate on the same measuring principles as the impedance measuring zones. So it is necessary to follow the same principles as for distance protection, when performing the secondary injection tests.

Measure operating characteristics during constant current conditions. Keep the measured current as close as possible to the rated value of its associated input transformer, or lower. But ensure that it is higher than 30% of the rated current.

Ensure that the maximum continuous current of an IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

To verify the settings the operating points according to figures 10 and 11 should be tested. See also tables 9 and 10 for information.

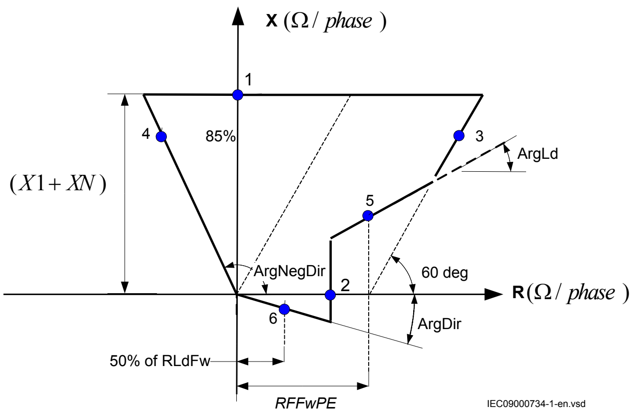

Figure 10: Operating characteristic for phase selection function, forward direction single-phase faults.

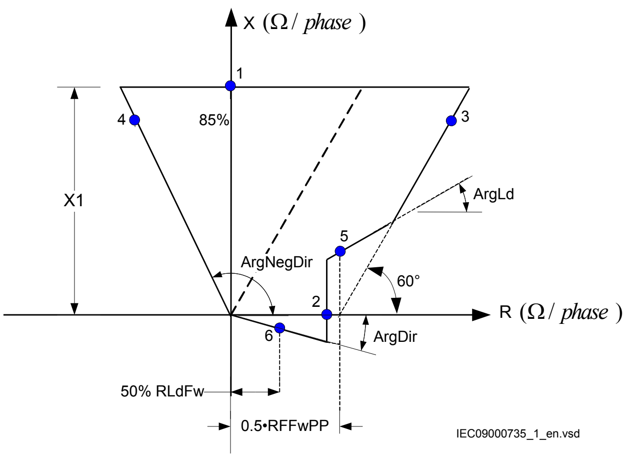

Figure 11: Operating characteristic for phase selection characteristic for phase-to-phase faults.

Table 9: Test points for phase-earth loop L3-E (Ohm/loop)

Test point		Value	Comments
1	X	[X1+XN]	XN=(X_0-X_1)/3
	R	0	
2	X	0	
	R	RLDFw	
Table continues on next page			

Test point		Value	Comments
3	X	0.85·[X1+XN]	R≈0.491·(X1+XN)+RFFwPE
	R	0.85·[X1+XN]·1/tan(60°)+RFFwPE	
4	X	0.85·[X1+XN]	
	R	-0.85·[X1+XN]· tan (AngNegRes-90°)	
5	X	RFFwPE·tan (ArgLd)	
	R	RFFwPE	
6	X	-0.5·RLdFw·tan (ArgDir)	
	R	0.5·RLdFw	

The table showing test points for phase-to- earth loops is used together with figure 10.

Table 10: Test points for phase-to-phase loops L1–L2

Test point		Value	Comments
1	X	X1	
	R	0	
2	X	0	
	R	RLDFw	
3	X	0.85·X1	R=0.491·X1+0.5 RFFwPP
	R	0.85·X1·1/tan(60°)+0.5 RFFwPP	
4	X	0.85·X1	
	R	-0.85·X1·tan (AngNegRes-90°)	
5	X	0.5·RFFwPP·tan (ArgLd)	
	R	0.5·RFFwPP	
6	X	-0.5·RLdFw·tan (ArgDir)	
	R	0.5·RLdFw	

The table showing test points for phase-to-phase loops is used together with figure 11.

5.3.2.1 Measuring the operate limit of set values

Procedure

1. Supply the IED with healthy conditions for at least two seconds.
2. Apply the fault condition and slowly decrease the measured impedance to find the operate value for of the phase-to-earth loop L3, test point 1, according to figure 10. Compare the result of the measurement with the expected value according to table 9.

The corresponding binary signals that inform about the operation of the phase selection measuring elements are available in the local HMI under the menu: **Main menu/Tests/Function status/Impedance/FDPSPDIS**.

3. Repeat steps 1 to 2 to find the operate values for the remaining test points according to figures 10 and table 9.
4. Repeat steps 1 to 3 to find the operate value for the phase-to-phase fault in L1 — L2 according to figure 11 and table 10.

5.3.2.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.3.3 Five zone distance protection, mho characteristic function ZMOPDIS

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

Keep the current constant when measuring operating characteristics. Keep the current as close as possible to its rated value or lower. But make sure it is higher than 30% of the rated current.

Ensure that the maximum continuous current in an IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

To verify the mho characteristic at least two points should be tested.

5.3.3.1 Phase-to-phase faults

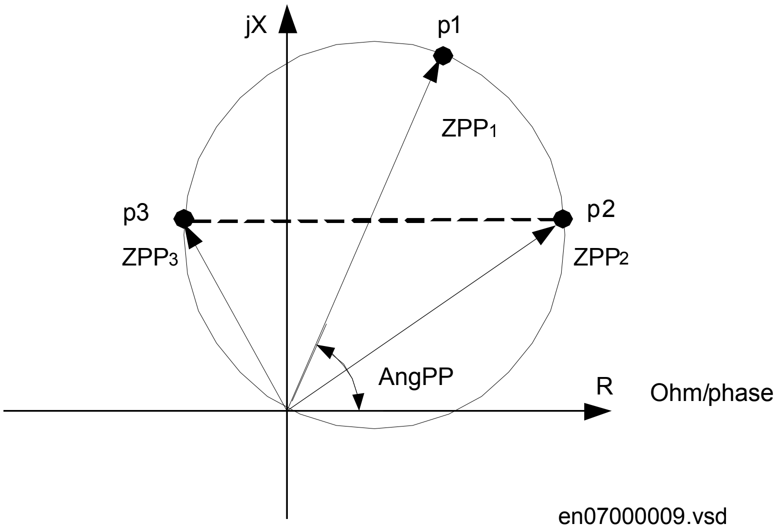

Figure 12: Proposed test points for phase-to-phase fault.

Label	Description
ZPP1	The measured impedance for phase-to-phase fault at point 1 (zone reach ZPP) ohm/phase.
ZAngPP	The characteristic angel for phase-to-phase fault in degrees.
ZPP2 and ZPP3	The fault impedance for phase-to-phase fault at the boundary for the zone reach at point 2 and 3.

Table 11: Test points for phase-to-phase

Test points	R	X
1	ZPP*cos(ZAngPP)	ZPP*sin(ZAngPP)
2	ZPP/2+ΔR=ZPP/2*(1+cos(ZAngPP))	ZPP/2*sin(ZAngPP)
3	ZPP/2-ΔR=ZPP/2*(1-cos(ZAngPP))	ZPP/2*sin(ZAngPP)

Change the magnitude and angle of phase-to-phase voltage to achieve impedances at test points p1, p2 and p3. For each test point, observe that the output signals, START and STLx are activated where x refers to the actual phase to be tested . After the timer *tPP* for the actual zone has elapsed, also the signals TRIP and TRx shall be activated.

5.3.3.2 Phase-to-earth faults

For simplicity, we propose the same test points as for phase-to-phase faults but considering new impedance values.

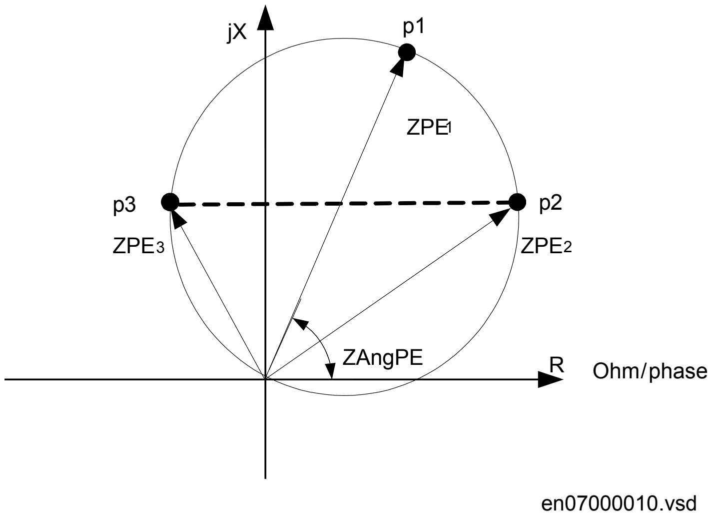

Figure 13: Proposed test points for phase-to-earth faults.

Label	Description
ZPE1	The measured impedance for phase-to-earth fault at point 1 (zone reach ZPE) ohm/ phase.
ZAngPE	The characteristic angel for phase-to-earth fault in degrees.
ZPE2 and ZPE3	The fault impedance for phase-to-earth fault at the boundary for the zone reach at point 2 and 3.

Table 12: Test points for phase-to-phase loops L1-L2 (Ohm/Loop)

Test points	Set	Comments
1	ZPE*cos(ZAngPE)	ZPE*sin(ZAngPE)
2	ZPE/2+ΔR=(ZPE/2)*(1-cos(ZAngPE))	ZPE/2*sin(ZAngPE)
3	ZPE/2-ΔR=ZPE/2*(1-cos(ZAngPE))	ZPE/2*sin(ZAngPE)

Check also in the same way as for phase-to-earth fault for each test point that the output signals START and STLx and are activated where x refer to the actual phase to be tested . After the timer *tPE* for the zone has elapsed, also the signals TRIP and TRx shall be activated.

5.3.4 Phase preference logic PPLPHIZ

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

The Phase preference logic function (PPLPHIZ) is tested with a three-phase testing equipment for distance protections. Phase preference logic is tested in co-operation with the Five zone distance protection, quadrilateral characteristic (ZQDPDIS). The distance protection and the phase preference logic shall be set to values according to the real set values to be used. The test is made by means of injection of voltage and current where the amplitude of both current and voltage and the phase angle between the voltage and current can be controlled.

During the test the following binary signals (outputs) shall be monitored:

- Trip signal from distance protection
- Start signal from Phase preference logic

1. Connect the test set for injection of voltage and current.
2. Inject voltages and currents corresponding to a phase-to-phase to earth-fault within zone 1 of the distance protection function. In the test one of the current inputs (one of the faulted phases) is disconnected. The remaining current is the fault current out on the protected line. All combinations of two phase-to-earth faults with one phase current are tested. The result shall be according to table 13. It should be checked that the fault will give phase-to-phase voltage, phase-to-earth voltage, zero sequence voltage and phase current so that the conditions set for the logic are fulfilled.
3. The same test is done for a phase-to-phase fault in zone 2.

Table 13: Operation at different combinations of faults and operation mode

OperMode	Fault type/Faulted phase current to the IED					
	L1L2N/IL1	L1L2N/IL2	L2L3N/IL2	L2L3N/IL3	L3L3N/IL1	L3L1N/IL3
No Filter	Trip	Trip	Trip	Trip	Trip	Trip
No Pref	Trip	Trip	Trip	Trip	Trip	Trip
1231c	Trip	No Trip	Trip	No Trip	No Trip	Trip
1321c	No Trip	Trip	No Trip	Trip	Trip	No Trip
123a	Trip	No Trip	Trip	No Trip	Trip	No Trip
132a	Trip	No Trip	No Trip	Trip	Trip	No Trip
213a	No Trip	Trip	Trip	No Trip	Trip	No Trip
231a	No Trip	Trip	Trip	No Trip	No Trip	Trip
312a	Trip	No Trip	No Trip	Trip	No Trip	Trip
321a	No Trip	Trip	No Trip	Trip	No Trip	Trip

5.3.4.1 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.3.5 Power swing detection ZMRPSB

The aim is to verify that the settings of Power swing detection (ZMRPSB) function is according to the setting table and to verify that ZMRPSB function operates as expected.

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

Before starting this process, all impedance measuring zones shall be set and in operation. Test the outer resistive boarder in forward and reverse direction, *RLdOutFw* and *RLdOutRv* and the inner reactive boarder in forward and reverse direction *X1InFw* and *X1InRv*. See figure 14.

The corresponding resistive boarder for the inner resistive boundary and outer reactive boundary is calculated automatically from the setting of *kLdRFw* and *kLdRRv*.

The inner zone of ZMRPSB must cover all zones to be blocked by ZMRPSB by at least 10% margin.

The test is mainly divided into two parts, one which aim is to verify that the settings are in accordance to the selective plane and a second part to verify the operation of Power swing detection function. The proposed test points for validation of the settings are numbered according to figure 14

Test of the interactions or combinations that are not configured are not considered in this instruction.

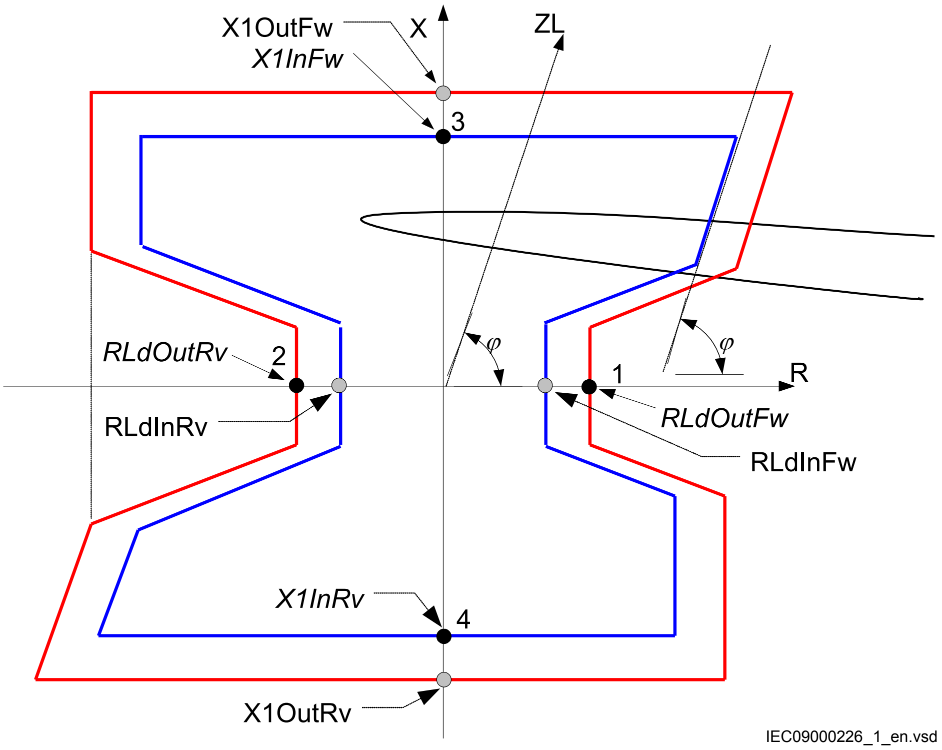

Figure 14: Operating principle and characteristic of the Power swing detection function (settings parameters in italic)

Where:

RLdInFw=*RLdOutFw ·kLdRFw*

RLdInRv=*RLdOutRv ·kLdRRv*

X1OutFw=*X1InFw+(RLdOutFw*-RLdInFw)

X1OutRv=*X1InRv+(RLdOutFw*-RLdInFw)

5.3.5.1 Verifying the settings

Preconditions

The following output signal shall be configured to binary output available: ZOUT, measured impedance within outer impedance boundary.

Procedure

1. Keep the measured current as close as possible to its rated value or lower. Keep it constant during the test, but ensure that it is higher than 30% of the rated current.
2. Ensure that the maximum continuous current of the IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.
3. Make the necessary connections and settings of the test equipment for test of point 1 according to figure 14.
4. Decrease the measured impedance slowly and observe the operation value for the signal ZOUT.
5. Check the operation value with the setting table.
6. Do the necessary change of the setting of the test equipment and repeat paragraph 4 and 5 for point 2, 3 and 4 according to figure 14.

5.3.5.2 Testing the Power swing detection function

Preconditions

The following output signal shall be configured to a binary output: ZOUT, measured impedance within outer impedance boundary, ZIN, measured impedance within inner impedance boundary and START, power swing detection.

Procedure

1. Slowly decrease the measured impedance in all three phases until the START signal gets activated.
2. Increase the measured voltages to their rated values.
3. Decrease instantaneously voltages in all three phases to the values, which are approximately 20% lower than the voltage that gives the set value *R1LIn* at the pre-defined test current.
4. The START signal must not appear.
5. Increase the measured voltages to their rated values.

5.3.5.3 Testing the tR1 timer

Preconditions

- The input I0CHECK, residual current (3IO) detection used to inhibit start output is configured to the output signal STPE on Phase selection with load encroachment, quadrilateral characteristic function (FDPSPDIS).
- The input BLKI02, block inhibit of start output for subsequent residual current detection is connected to FALSE.

Procedure

1. Program the test equipment for a single phase-to-earth fault and energize FDPSPDIS function and check that the input BLOCK on Power swing detection function (ZMRPSB) will be activated.
2. Make a test sequence so that a single phase-to-earth fault occurs after that the trajectory of the impedance has passed the outer and inner boundary of ZMRPSB function during power swing. Use the result from test of ZMRPSB function above to determine when the fault shall be applied. The earth-fault must be activated before *tR1* has elapsed.
3. Start the sequence and observe that the START signal will not be activated.

5.3.5.4 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.3.6 Automatic switch onto fault logic, voltage and current based ZCVPSOF

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

Automatic switch onto fault logic, voltage and current based function (ZCVPSOF) is checked using secondary injection tests together with the Scheme communication logic for distance or Overcurrent protection function (ZCPSCH) and with the Dead Line Detection (DLD) function which is embedded in ZCVPSOF function. ZCVPSOF function is activated either by the external input BC, or by the internal DLD function which is integrated in Fuse failure supervision (SDDRFUF). The latter is done with a prefault condition where the phase voltages and currents are at zero. A reverse three-phase fault with zero impedance and a three-phase fault with an impedance corresponding to the whole line is applied. This fault shall cause an instantaneous trip and result in a TRIP indication.

5.3.6.1 External activation of ZCVPSOF

Procedure

1. Activate the switch onto fault BC input.

During normal operating conditions, the BC input is de-energized.
2. Apply a three-phase fault condition corresponding to a fault at approximately 45% of the line or with an impedance at 50% of used zone setting and current greater than 30% of I_r.
3. Check that the correct trip outputs, external signals and indication are obtained.

5.3.6.2 Automatic initiation of ZCVPSOF

Procedure

1. Deactivate the switch onto fault BC input.
2. Set current and voltage inputs to 0 for at least 1 second.
3. Apply a three-phase fault condition corresponding to a fault at approximately 45% of the line or with an impedance at 50% of used zone setting and current greater than 30% of I_r.
4. Check that the correct trip outputs, external signals and indication are obtained.

5.3.6.3 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4 Testing current protection functions

5.4.1 Instantaneous phase overcurrent protection PHPIOC

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

To verify the settings the following fault type should be tested:

- Phase-to-earth fault

Ensure that the maximum continuous current, supplied from the current source used for the test of the IED, does not exceed four times the rated current value of the IED.

5.4.1.1 Measuring the operate limit of set values

Procedure:

1. Inject a phase current into the IED with an initial value below that of the setting.
2. Increase the injected current until the TRIP signal appears.
3. Switch the fault current off.
   Observe to not exceed the maximum permitted overloading of the current circuits in the IED
4. Compare the measured operating current with the set value.

5.4.1.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.2 Four step phase overcurrent protection OC4PTOC

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.4.2.1 Verifying the settings

Procedure:

1. Connect the test set for appropriate current injection to the appropriate IED phases.
   If there is any configuration logic that is used to enable/block any of the four available overcurrent steps, make sure that the step under test is enabled, for example end fault protection.
   Connect the symmetrical three-phase injection current into phases L1, L2 and L3.
2. Connect the test set for the appropriate three-phase voltage injection to the IED phases L1, L2 and L3. The protection shall be fed with a symmetrical three-phase voltage.
3. Set the injected polarizing voltage slightly larger than the set minimum polarizing voltage (default is 5% of *UBase*) and set the injection current to lag the appropriate voltage by an angle of about 80° if forward directional function is selected.

If 1 of 3 currents for operation is chosen: The voltage angle of phase L1 is the reference.
The voltage angle of phase L1 is the reference.
If reverse directional function is selected, set the injection current to lag the polarizing voltage by an angle equal to 260° (equal to 80° + 180°).

4. Increase the injected current and note the operated value of the tested step of the function.
5. Decrease the current slowly and note the reset value.
6. If the test has been performed by injection of current in phase L1, repeat the test when injecting current into phases L2 and L3 with polarizing voltage connected to phases L2 respectively L3 (1 of 3 currents for operation).
7. If the test has been performed by injection of current in phases L1 – L2, repeat the test when injecting current into phases L2 – L3 and L3 – L1 with appropriate phase angle of injected currents.
8. Block higher set stages when testing lower set stages according to below.
9. Connect a trip output contact to a timer.
10. Set the injected current to 200% of the operate level of the tested stage, switch on the current and check the time delay.
    For inverse time curves, check the operate time at a current equal to 110% of the operate current for *txMin*.
11. Check that all trip and start contacts operate according to the configuration (signal matrixes)
12. Reverse the direction of the injected current and check that the protection does not operate.
13. Repeat the above-described tests for the higher set stages.
14. Finally check that start and trip information is stored in the event menu.

Check of the non-directional phase over-current function. This is done in principle as instructed above, without applying any polarizing voltage.

5.4.2.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.3 Instantaneous residual overcurrent protection EFPIOC

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

To verify the settings the following fault type should be tested:

- Phase-to-earth fault

Ensure that the maximum continuous current, supplied from the current source used for the test of the IED, does not exceed four times the rated current value of the IED.

5.4.3.1 Measuring the operate limit of set values

Procedure

1. Inject a phase current into the IED with an initial value below that of the setting.
2. Increase the injected current in the Ln or in the neutral (summated current input) phase until the TRIP signal appears.
3. Switch the fault current off.
   Observe to not exceed the maximum permitted overloading of the current circuits in the IED
4. Compare the measured operating current with the set value.

5.4.3.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.4 Four step residual overcurrent protection EF4PTOC

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.4.4.1 Four step directional residual overcurrent protection

Procedure:

1. Connect the test set for single current injection to the appropriate IED terminals.

Connect the injection current to terminals L1 and neutral, or to terminals N and neutral.

2. Set the injected polarizing voltage slightly larger than the set minimum polarizing voltage (default 1 % of Ur) and set the injection current to lag the voltage by an angle equal to the set reference characteristic angle (*AngleRCA*) if the forward directional function is selected.
   If reverse directional function is selected, set the injection current to lag the polarizing voltage by an angle equal to RCA+ 180°.
3. Increase the injected current and note the value at which the studied step of the function operates.
4. Decrease the current slowly and note the reset value.
5. If the test has been performed by injection of current in phase L1, repeat the test when injecting current into terminals L2 and L3 with a polarizing voltage connected to terminals L2 respectively L3.
6. Block lower set steps when testing higher set steps according to the instructions that follow.
7. Connect a trip output contact to a timer.
8. Set the injected current to 200 % of the operate level of the tested step, switch on the current and check the time delay.
   For inverse time curves, check the operate time at a current equal to 110 % of the operate current for *tnmin*.
9. Check that all trip and start contacts operate according to the configuration (signal matrixes)
10. Reverse the direction of the injected current and check that the step does not operate.
11. Check that the protection does not operate when the polarizing voltage is zero.
12. Repeat the above-described tests for the higher set steps.
13. Finally, check that start and trip information is stored in the event menu.

5.4.4.2 Four step non-directional residual overcurrent protection

Procedure:

1. Do as described in "Four step directional residual overcurrent protection", but without applying any polarizing voltage.

5.4.4.3 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.5 Sensitive directional residual overcurrent and power protection SDEPSDE

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

Figure 15 shows the principal connection of the test-set during the test of the sensitive directional residual overcurrent protection. Observe that the polarizing voltage is equal to -3U_0.

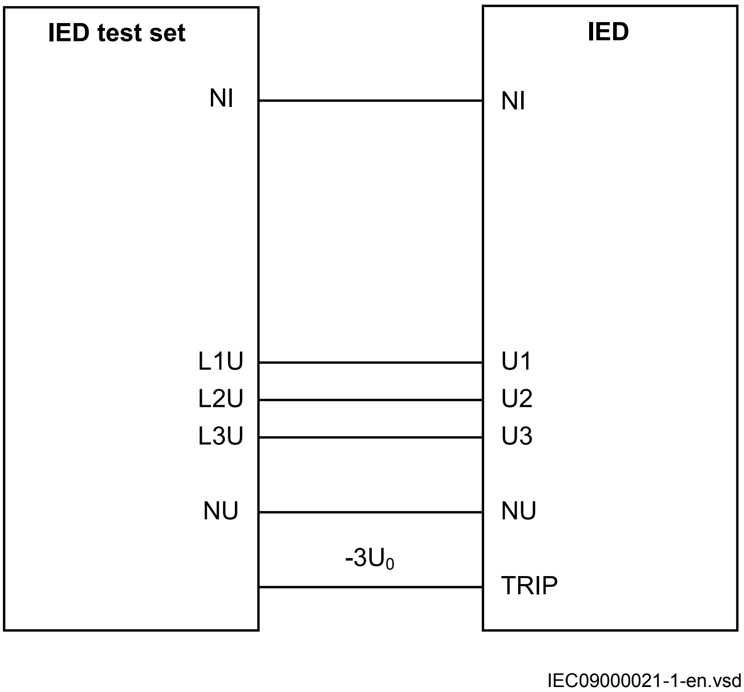

Figure 15: Principle connection of the test-set

5.4.5.1 Measuring the operate and time limit for set values

Operation mode $3I_0 \cdot \cos\varphi$

Procedure:

1. Set the polarizing voltage to 1.2 · *UNRel>* and the phase angle between voltage and current to the set characteristic angle (*RCADir*), the current lagging the voltage.

Take setting *RCAComp* into consideration if not equal to 0.

2. Measure that the operate current of the set directional element is equal to the *INcosPhi>* setting.
   The I Dir (I_0 cos(Angle)) function activates the START and STDIRIN output.
3. Measure with angles φ = RCADir +/- 45° that the measuring element operates when I_0 cos (RCADir - φ) = I_0cos(+/-45) =*INcosPhi>*.
4. Compare the result with the set value.
   Take the set characteristic into consideration, see figure 16 and figure 17.
5. Measure the operate time of the timer by injecting a current two times the set *INcosPhi>* value and the polarizing voltage 1.2 · *UNRel>*.

$$Tinv = kSN \cdot Sref / 3I_{0test} \cdot \cos(\varphi)$$

(Equation 1)

6. Compare the result with the expected value.
   The expected value depends on whether definite or inverse time was selected.
7. Set the polarizing voltage to zero and increase until the boolean output signal UNREL is activated, which is visible in the Application configuration tool in PCM600 when the IED is in online mode. Compare the voltage with the set value *UNRel>*.
8. Continue to test another function or complete the test by setting the test mode to off.

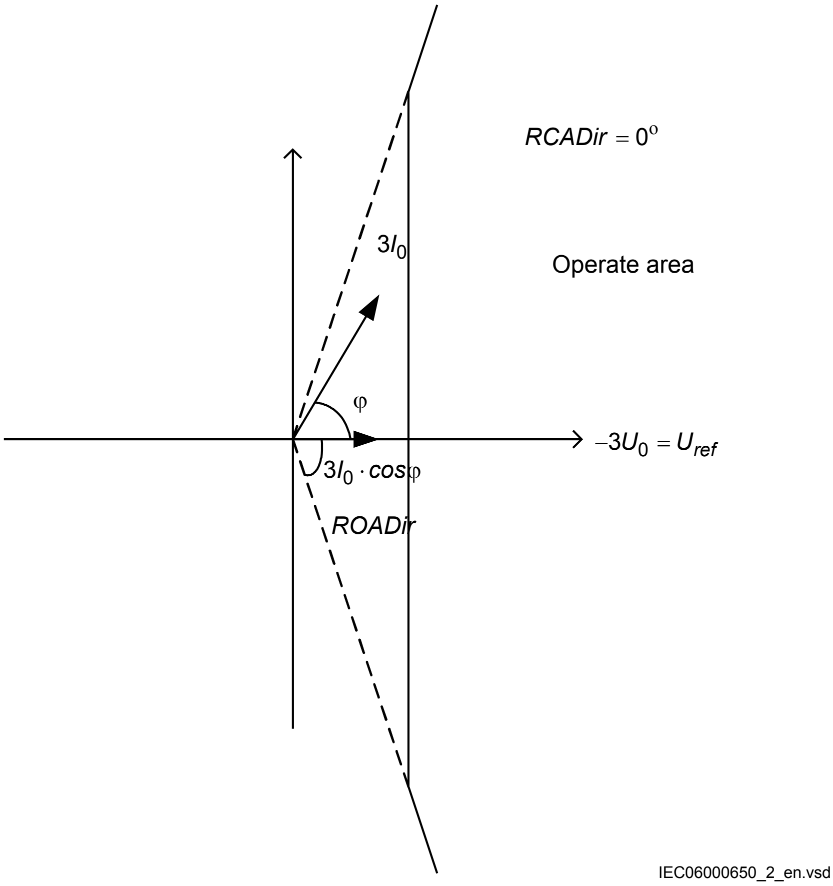

Figure 16: Characteristic with ROADir restriction

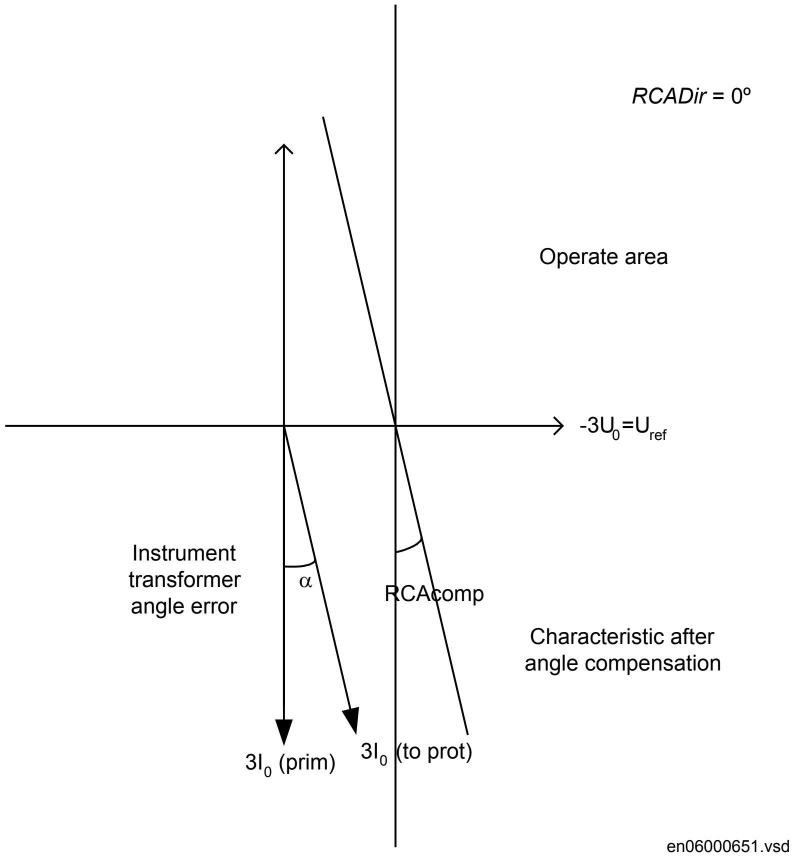

Figure 17: Explanation of RCAcomp.

Operation mode $3I_0 \cdot 3U_0 \cdot \cos\varphi$

Procedure:

1. Set the polarizing voltage to 1.2 · *UNRel>* and the phase angle between voltage and current to the set characteristic angle (*RCADir*), the current lagging the voltage.
2. Measure that the operate power is equal to the *SN>* setting for the set directional element.
   Note that for operation, both the injected current and voltage must be greater than the set values *INRel>* and *UNRel>* respectively.
   The function activates the START and STDIRIN outputs.
3. Measure with angles φ = *RCADir* +/- 45° that the measuring element operates when $3I_0 \cdot 3U_0 \cdot \cos$ (*RCADir* - φ) = 3I0 · 3U0 · cos(+/-45) =*SN>*.
4. Compare the result with the set value. Take the set characteristic into consideration, see figure 16 and figure 17.
5. Measure the operate time of the timer by injecting 1.2 · *UNRel>* and a current to get two times the set *SN>* operate value.

$$Tinv = kSN \cdot Sref\ / 3I_{0test} \cdot 3U_{0test} \cdot \cos(\varphi)$$

(Equation 2)

6. Compare the result with the expected value.
   The expected value depends on whether definite or inverse time was selected.
7. Continue to test another function or complete the test by setting the test mode to off.

Operation mode $3I_0$ and fi

Procedure:

1. Set the polarizing voltage to 1.2 · *UNRel>* and the phase angle between voltage and current to the set characteristic angle (*RCADir*), the current lagging the voltage.
2. Measure that the operate power is equal to the *INDir>* setting for the set directional element.
   Note that for operation, both the injected current and voltage must be greater than the set values *INRel>* and *UNRel>* respectively.
   The function activates the START and STDIRIN output.
3. Measure with angles φ around *RCADir +/- ROADir*.
4. Compare the result with the set values, refer to figure 18 for example characteristic.
5. Measure the operate time of the timer by injecting a current to get two times the set *SN>* operate value.

   $$Tinv = kSN\ Sref / 3I_{0test} \cdot 3U_{0test} \cos(\varphi)$$

(Equation 3)

6. Compare the result with the expected value.
   The expected value depends on whether definite or inverse time was selected.
7. Continue to test another function or complete the test by setting the test mode to off.

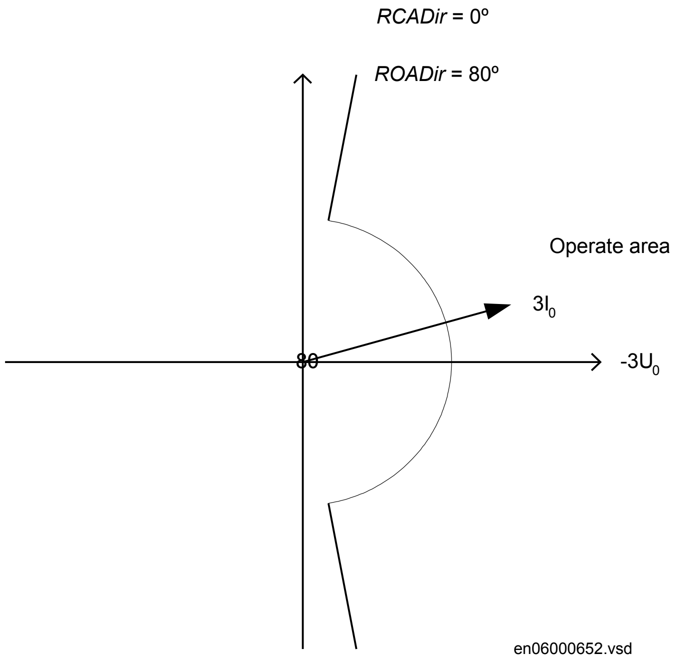

Figure 18: Example characteristic

Non-directional earth fault current protection

Procedure

1. Measure that the operate current is equal to the *INNonDir>* setting.
   The function activates the START and STDIRIN output.
2. Measure the operate time of the timer by injecting a current to get two times the set *INNonDir>* operate value.
3. Compare the result with the expected value.
   The expected value depends on whether definite time *tINNonDir* or inverse time was selected.
4. Continue to test another function or complete the test by setting the test mode to off.

Residual overvoltage release and protection

Procedure:

1. Measure that the operate voltage is equal to the *UN>* setting.
   The function activates the START and STUN signals.
2. Measure the operate time by injecting a voltage 1.2 timers set *UN>* operate value.
3. Compare the result with the set *tUN* operate value.
4. Inject a voltage 0.8 · *UNRel>*and a current high enough to operate the directional function at the chosen angle.
5. Increase the voltage until the directional function is released.
6. Compare the measured value with the set *UNRel>* operate value.

5.4.5.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.6 Thermal overload protection, one time constant LPTTR

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

Check that the input logical signal BLOCK is logical zero and that on the local HMI, the logical signal TRIP, START and ALARM are equal to logical zero.

5.4.6.1 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.7 Breaker failure protection CCRBRF

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

Breaker failure protection (CCRBRF) should normally be tested in conjunction with some other function that provides a start signal. An external START signal can also be used.

To verify the settings in the most common back-up trip mode *1 out of 3* it is sufficient to test phase-to-earth -faults.

At mode *2 out of 4* the Phase current setting, *IP>* can be checked by single phase injection where the return current is connected to the summated current input. The value of residual (EF) current “IN” set lower than *IP>* is easiest checked in back-up trip mode *1 out of 4*.

5.4.7.1 Checking the phase current operate value, *IP>*

Check the current level *IP>* where setting *FunctionMode*=*Current* and setting *BuTripMode*=*1 out of 3 or 2 out of 4* as set under **Main menu/Settings/IED Settings/CCRBRF(50BF)/1:CCRBRF**.

Procedure:

1. Apply the fault condition, including START of CCRBRF, with a current below set *IP>*.
2. Repeat the fault condition and increase the current in steps until a trip occurs.
3. Compare the result with the set *IP>*.
4. Disconnect AC and START input signals.
   Note: If “No I> check” or “*Retrip off*” is set, only back-up trip can be used to check set *IP>*.

5.4.7.2 Checking the residual (EF) current operate value *IN>* set below *IP>*

Check the low set *IN>* current where setting *FunctionMode*=*Current* and setting *BuTripMode*=*1 out of 4* as set under **Main menu/Settings/IED Settings/CCRBRF(50BF)/1:CCRBRF**.

Procedure

1. Apply the fault condition, including *start* of CCRBRP, with a current just below set *IN>*.
2. Repeat the fault condition and increase the current in steps until trip appears.
3. Compare the result with the set *IN>*.
4. Disconnect AC and start input signals.

5.4.7.3 Checking the re-trip and back-up times

The check of the set times can be made in connection with the check of operate values above.

Choose the applicable function and trip mode, such as *FunctionMode*=*Current* and setting *RetripMode*=*No* CBPos. Check as set under **Main menu/Settings/IED Settings/CCRBRF(50BF)/1:CCRBRF**.

Procedure

1. Apply the fault condition, including start of CCRBRF, well above the set current value. Measure time from “Start" of CCRBRF.
2. Check the re-trip *t1* and back-up trip times *t2*

3. Disconnect AC and start input signals.

5.4.7.4 Verifying the re-trip mode

Choose the mode below, which corresponds to the actual case.

In the cases below it is assumed that *FunctionMode*=*Current* as set under **Main menu/Settings/IED Settings/CCRBRF(50BF)/1:CCRBRF**.

Checking the case without re-trip, *RetripMode* = *Retrip Off*

Procedure:

1. Set *RetripMode = Retrip Off*.
2. Apply the fault condition, including start of CCRBRF, well above the set current value.
3. Verify that no re-trip, but back-up trip is achieved after set time.
4. Disconnect AC and start input signals.

Checking the re-trip with current check, *RetripMode* = *CB Pos Check*

Procedure

1. Set *RetripMode = CB Pos Check* check.
2. Apply the fault condition, including start CCRBRF, well above the set current value.
3. Verify that retrip is achieved after set time *t1* and back-up trip after time *t2*
4. Apply the fault condition, including start of CCRBRF, with current below set current value.
5. Verify that no re-trip, and no back-up trip is obtained.
6. Disconnect AC and start input signals.

Checking re-trip without current check, *RetripMode* = *No CBPos Check*

Procedure:

1. Set *RetripMode = No CBPos Check*.
2. Apply the fault condition, including start of CCRBRF, well above the set current value.
3. Verify that re-trip is achieved after set time *t1*, and back-up trip after time *t2*.
4. Apply the fault condition, including start of CCRBRF, with current below set current value.
5. Verify that re-trip is achieved after set time *t1*, but no back-up trip is obtained.
6. Disconnect AC and start input signals.

5.4.7.5 Verifying the back-up trip mode

In the cases below it is assumed that *FunctionMode = Current* is selected.

Checking that back-up tripping is not achieved at normal CB tripping

Use the actual tripping modes. The case below applies to re-trip with current check.

Procedure:

1. Apply the fault condition, including start of CCRBRF, with phase current well above set value “IP”
2. Arrange switching the current off, with a margin before back-up trip time, *t2*. It may be made at issue of re-trip command.
3. Check that re-trip is achieved, if selected, but no back-up trip.
4. Disconnect AC and start input signals.

The normal mode *BuTripMode = “1 out of 3*” should have been verified in the tests above. In applicable cases the modes “*1 out of 4*” and “*2 out of 4*” can be checked. Choose the mode below, which corresponds to the actual case.

Checking the case *BuTripMode = 1 out of 4*

It is assumed that EF current setting *IN*> is below phase current setting *IP*>.

Procedure:

1. Set *BuTripMode = 1 out of 4.*
2. Apply the fault condition, including start of CCRBRF, with one phase current below set *IP*> but above *IN*>. The residual (EF) should then be above set IN>.
3. Verify that back-up trip is achieved after set time. If selected, re-trip should also appear.
4. Disconnect AC and start input signals.

Checking the case *BuTripMode = 2 out of 4*

The EF current setting *IN*> may be equal to or below phase current setting *IP*>.

Procedure:

1. Set *BuTripMode = 2 out of 4*
2. Apply the fault condition, including start of CCRBRF, with one phase current above set *IP*> and residual (EF) above set *IN* It can be obtained by applying a single phase current.
3. Verify that back-up trip is achieved after set time. If selected, re-trip should also appear.
4. Apply the fault condition, including start of CCRBRF, with at least one phase current below set *IP*> and residual (EF) above set *IN*>. The current may be arranged by feeding three (or two) phase currents with equal phase angle (I0-component) below *IP*>, but of such value that the residual (EF) current ($3I_0$) will be above set value *IN*>.
5. Verify that back-up trip is not achieved.
6. Disconnect AC and start input signals.

5.4.7.6 Verifying the case *RetripMode* = *No CB Pos Check*

It is assumed that re-trip without current check is selected, *RetripMode = No CB Pos Check* check.

Procedure:

1. Set *FunctionMode = Contact*
2. Apply input signal for CB closed to input CBCLD
3. Apply input signal, for start of CCRBRF. The value of current could be low.
4. Verify that re-trip and back-up trip are achieved after set times.
5. Disconnect the start signal. Keep the CB closed signal.
6. Apply input signal, for start of CCRBRF. The value of current could be low.
7. Arrange disconnection of CB closed signal well before set back-up trip time *t2*.
8. Verify that back-up trip is not achieved.
9. Disconnect injected AC and start input signals.

5.4.7.7 Verifying the function mode *Current&Contact*

To be made only when *FunctionMode = Current&Contact* is selected.

Checking the case with fault current above set value *IP>*

The operation shall be as in *FunctionMode = Current.*

Procedure:

1. Set *FunctionMode = Current&Contact.*
2. Leave the inputs for CB close inactivated. These signals should not influence.
3. Apply the fault condition, including start of CCRBRF, with current above the set *IP>* value.
4. Check that the re-trip, if selected, and back-up trip commands are achieved.
5. Disconnect injected AC and start input signals.

Checking the case with fault current below set value *I>BlkCont*

The case shall simulate a case where the fault current is very low and operation will depend on CB position signal from CB auxiliary contact. It is suggested that re-trip without current check is used; setting *RetripMode = No CBPos Check*.

Procedure:

1. Set *FunctionMode = Current&Contact* check.
2. Apply input signal for CB closed to relevant input or inputs CBCLDL1 (2 or 3)
3. Apply the fault condition with input signal(s) for start of CCRBRF. The value of current should be below the set value *I>BlkCont*
4. Verify that re-trip (if selected) and back-up trip are achieved after set times. Failure to trip is simulated by keeping the signal(s) CB closed activated.
5. Disconnect the AC and the start signal(s). Keep the CB closed signal(s).

6. Apply the fault and the start again. The value of current should be below the set value *I>BlkCont.*
7. Arrange disconnection of BC closed signal(s) well before set back-up trip time *t2*. It simulates a correct CB tripping.
8. Verify that back-up trip is not achieved. Re-trip can appear for example, due to selection “Re-trip without current check”.
9. Disconnect injected AC and start input signals.

5.4.7.8 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.8 Stub protection STBPTOC

Logical signals for STBPTOC protection are available under menu tree:**Main menu/Tests/Function status/Current/STBPTOC(50STB)/1:STBPTOC/Inputs or Outputs**

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.4.8.1 Measuring the operate limit of set values

Procedure

1. Check that the input logical signals BLOCK and RELEASE and the output logical signal TRIP are all logical zero.
2. Activate the input RELEASE on STBPTOC function block
3. For a short while inject a current (fault current) in one phase to about 110% of the set operating current, and switch the current off.
   Observe to not exceed the maximum permitted overloading of the current circuits in the IED.
4. Switch the fault current on and measure the operating time of the STBPTOC protection.
   Use the TRIP signal from the configured binary output to stop the timer. The operation should be instantaneously.
5. Activate the input BLOCK on STBPTOC function block.
6. Switch on the fault current (110% of the setting).
   No TRIP signal should appear.

7. Switch off the fault current.
8. For a short while inject a current (fault current) in same phase to about 90% of the set operating current, and switch the current off with the switch.
9. Switch the fault current on.
   No TRIP signal should appear.
10. Switch the fault current off.
11. Reset the RELEASE binary input.
12. Switch the fault current on.
    No TRIP signal should appear.
13. Switch the fault current off.

5.4.8.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.9 Pole discordance protection CCRPLD

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.4.9.1 Verifying the settings

Procedure:

1. When the CCRPLD function is set for external, set setting *ContSel* to *PD signal from CB* under: **Main menu/Settings/IED Settings/Current/CCRPLD/1:CCRPLD** to activate the logic that detects pole discordance when external pole discordance signalling is used (input EXTPDIND) in the application configuration.
2. Activate the input EXTPDIND on the CCRPLD function block, and measure the operating time of the CCRPLD protection.
3. Compare the measured time with the set value *tTrip*.
4. Reset the EXTPDIND input.
5. When CCRPLD function is set for unsymmetry current detection with CB monitoring, set setting *CurrSel* under **Main menu/Settings/IED Settings/Current/CCRPLD/1:CCRPLD** to *CB oper monitor*.
   Use the TRIP signal from the configured binary output to stop the timer.

6. Activate input CLOSECMD on CCRPLD function block and measure the operating time of the CCRPLD protection.
7. Set measured current in one phase to 110% of *CurrRelLevel*
8. Deactivate input CLOSECMD. Set measured current in one phase to 90% of *CurrRelLevel*. Activate CLOSECMD.
   NO TRIP signal should appear.
9. Repeat point 4 and 5 using OPENCMD instead of CLOSECMD. Set all three currents to 110% of *CurrRelLevel*. Activate CLOSECMD.
   NO TRIP signal should appear due to symmetrical condition.
10. Deactivate the CLOSECMD. Set measured current in one phase to 120% of the *CurrUnsymmLevel* compared to the other two phases.
11. Set the two other phase currents to 0.
12. Activate CLOSECMD and measure the operating time of the CCRPLD protection.
    Use the TRIP signal from the configured binary out put stop the timer.
13. Deactivate the CLOSECMD. Set measured current in one phase to 80% of the *CurrUnsymmLevel*.
14. Set the two other phase currents to 0.
15. Activate CLOSECMD.
    NO TRIP signal should appear.

5.4.9.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.10 Broken conductor check BRCPTOC

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.4.10.1 Measuring the operate and time limit of set values

Procedure

1. Check that the input logical signal BLOCK to the function block BRCPTOC is logical zero and note on the local HMI that the output signal TRIP from the function block BRCPTOC is equal to the logical 0.
2. Set the measured current (fault current) in one phase to about 110% of the set operating current *IP>*.
   Observe to not exceed the maximum permitted overloading of the current circuits in the terminal.
3. Switch on the fault current and measure the operating time of the BRCPTOC protection.
   Use the TRIP signal from the configured binary output to stop the timer.
4. Compare the measured time with the set value *tOper*.
5. Activate the BLOCK binary input.
6. Switch on the fault current (110% of the setting) and wait longer than the set value *tOper*.
   No TRIP signal should appear.
7. Switch off the fault current.
8. Set the measured current (fault current) in same phase to about 90% of the set operating current *IP>*. Switch off the current.
9. Switch on the fault current and wait longer than the set value *tOper*.
   No TRIP signal should appear.
10. Switch off the fault current.

5.4.10.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.11 Directional under-power protection GUPPDUP

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.4.11.1 Verifying the settings

The under-power protection shall be set to values according to the real set values to be used.

The test is made by means of injection of voltage and current where the amplitude of both current and voltage and the phase angle between the voltage and current can be controlled. During the test the analog outputs of active and reactive power shall be monitored.

Procedure

1. Connect the test set for injection of voltage and current corresponding to the mode to be used in the application. If a three phase test set is available this could be used for all the modes. If a single phase current/voltage test set is available the test set should be connected to a selected input for one phase current and voltage.
   Use the formulas stated in Table 13 for the different calculation modes used.
   The set mode *Mode* can be found under **Main menu/Settings/IED Settings/Current/GUPPDUP/1:GUPPDUP/General**.

Set value: *Mode*	Formula used for complex power calculation
L1, L2, L3	$\overline{S} = \overline{U}_{L1} \cdot \overline{I}_{L1}^{\ *} + \overline{U}_{L2} \cdot \overline{I}_{L2}^{\ *} + \overline{U}_{L3} \cdot \overline{I}_{L3}^{\ *}$ (Equation 4) $\overline{S} = \overline{V}_{A} \cdot \overline{I}_{A}^{\ *} + \overline{V}_{B} \cdot \overline{I}_{B}^{\ *} + \overline{V}_{C} \cdot \overline{I}_{C}^{\ *}$ (Equation 4)
Arone	$\overline{S} = \overline{U}_{L1L2} \cdot \overline{I}_{L1}^{\ *} - \overline{U}_{L2L3} \cdot \overline{I}_{L3}^{\ *}$ (Equation 5)
PosSeq	$\overline{S} = 3 \cdot \overline{U}_{PosSeq} \cdot \overline{I}_{PosSeq}^{\ *}$ (Equation 6)
L1L2	$\overline{S} = \overline{U}_{L1L2} \cdot (\overline{I}_{L1}^{\ *} - \overline{I}_{L2}^{\ *})$ (Equation 7)
L2L3	$\overline{S} = \overline{U}_{L2L3} \cdot (\overline{I}_{L2}^{\ *} - \overline{I}_{L3}^{\ *})$ (Equation 8)
L3L1	$\overline{S} = \overline{U}_{L3L1} \cdot (\overline{I}_{L3}^{\ *} - \overline{I}_{L1}^{\ *})$ (Equation 9)

Table continues on next page

Set value: *Mode*	Formula used for complex power calculation
L1	$\bar{S} = 3 \cdot \bar{U}_{L1} \cdot \bar{I}_{L1}^{*}$ (Equation 10)
L2	$\bar{S} = 3 \cdot \bar{U}_{L2} \cdot \bar{I}_{L2}^{*}$ (Equation 11)
L3	$\bar{S} = 3 \cdot \bar{U}_{L3} \cdot \bar{I}_{L3}^{*}$ (Equation 12)

2. Adjust the injected current and voltage to the set values in % of *IBase* and *UBase* (converted to secondary current and voltage). The angle between the injected current and voltage shall be set equal to the set direction *Angle1*, angle for step 1 (equal to 0° for low forward power protection and equal to 180° for reverse power protection). Check that the monitored active power is equal to 100% of rated power and that the reactive power is equal to 0% of rated power.
3. Change the angle between the injected current and voltage to *Angle1* + 90°. Check that the monitored active power is equal to 0% of rated power and that the reactive power is equal to 100% of rated power.
4. Change the angle between the injected current and voltage back to 0°. Decrease the current slowly until the START1 signal, start of stage 1, is activated.
5. Increase the current to 100 % of *IBase*.
6. Switch the current off and measure the time for activation of TRIP1, trip of stage 1.
7. If a second stage is used: repeat points 2 - 6 for the second stage.

5.4.11.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.4.12 Directional over-power protection GOPPDOP

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.4.12.1 Verifying the settings

The overpower protection shall be set to values according to the real set values to be used. The test is made by means of injection of voltage and current where the amplitude of both current and voltage and the phase angle between the voltage and current can be controlled. During the test the analog outputs of active and reactive power shall be monitored.

Procedure

1. Connect the test set for injection of voltage and current corresponding to the mode to be used in the application. If a three phase test set is available this could be used for all the modes. If a single phase current/voltage test set is available the test set should be connected to a selected input for one phase current and voltage.
   Use the formulas stated in Table 13 for the different calculation modes used.
   The set mode *Mode* can be found under **Main menu/Settings/IED Settings/ CurrentGOPPDOP/1:GOPPDOP/General**.
2. Adjust the injected current and voltage to the set rated values in % of *IBase* and *UBase* (converted to secondary current and voltage). The angle between the injected current and voltage shall be set equal to the set direction *Angle1*, angle for step 1 (equal to 0° for low forward power protection and equal to 180° for reverse power protection). Check that the monitored active power is equal to 100% of rated power and that the reactive power is equal to 0% of rated power.
3. Change the angle between the injected current and voltage to *Angle1* + 90°. Check that the monitored active power is equal to 0% of rated power and that the reactive power is equal to 100% of rated power.
4. Change the angle between the injected current and voltage back to *Angle1* value. Increase the current slowly from 0 until the START1 signal, start of stage 1, is activated. Check the injected power and compare it to the set value *Power1*, power setting for stage 1 in % of *Sbase*.
5. Increase the current to 100 % of *IBase* and switch the current off.
6. Switch the current on and measure the time for activation of TRIP1, trip of stage 1.
7. If a second stage is used: repeat points 2 - 6 for the second stage.

5.4.12.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.5 Testing voltage protection functions

5.5.1 Two step undervoltage protection UV2PTUV

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.5.1.1 Verifying the setting

Verification of START-value and time delay to operate for Step1

Procedure

1. Check that the IED settings are appropriate, especially the START-value, the definite time delay and the 1 out of 3 operation mode.
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly decrease the voltage in one of the phases, until the START signal appears.
4. Note the operate value and compare it with the set value.
5. Increase the measured voltage to rated load conditions.
6. Check that the START signal resets.
7. Instantaneously decrease the voltage in one-phase to a value about 20% lower than the measured operate value.
8. Measure the time delay for the TRIP signal, and compare it with the set value.

Extended testing

Procedure

1. The test above can now be repeated for Step2.
2. The tests above can be repeated for 2 out of 3 and for 3 out of 3 operation mode.
3. The tests above can be repeated to check security, that is, the START and operate signals, that are not supposed to appear, - do not.
4. The tests above can be repeated to check the time to reset.
5. The tests above can be repeated to test the inverse time characteristic.

5.5.1.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.5.2 Two step overvoltage protection OV2PTOV

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.5.2.1 Verifying the settings

Procedure

1. Apply single phase voltage below the set value *U1>*.
2. Slowly increase the voltage until the ST1 signal appears.
3. Note the operate value and compare it with the set value.
4. Switch the applied voltage off.
5. Set and apply about 20% higher voltage than the measured operate value for one phase.
6. Measure the time delay for the TR1 signal and compare it with the set value.
7. Repeat the test for step 2.

5.5.2.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.5.3 Two step residual overvoltage protection ROV2PTOV

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.5.3.1 Verifying the settings

Procedure

1. Apply the single phase voltage either to a single phase voltage input or to a residual voltage input with the start value below the set value *U1>*
2. Slowly increase the value until ST1 appears
3. Note the operate value and compare it with the set value.
4. Switch the applied voltage off.
5. Set and apply about 20% higher voltage than the measured operate value for one phase.
6. Measure the time delay for the TR1 signal and compare it with the set value.
7. Repeat the test for step 2.

5.5.3.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.5.4 Loss of voltage check LOVPTUV

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.5.4.1 Measuring the operate limit of set values

Procedure

1. Check that the input logical signals BLOCK, CBOPEN and VTSU are logical zero.
2. Supply a three phase rated voltage in all three phases and note on the local HMI that the TRIP logical signal is equal to the logical 0.
3. Switch off the voltage in all three phases.
   After set *tTrip* time a TRIP signal appears on the corresponding binary output or on the local HMI. Note that TRIP at this time is a pulse signal, duration should be according to set *tPulse*.
4. Inject the measured voltages to their rated values for at least set *tRestore* time.
5. Activate the CBOPEN binary input.
6. Simultaneously disconnect all the three phase voltages from the IED.
   No TRIP signal should appear.
7. Inject the measured voltages to their rated values for at least set *tRestore* time.
8. Activate the VTSU binary input.
9. Simultaneously disconnect all the three phase voltages from the *tRestore*.

No TRIP signal should appear.

10. Reset the VTSU binary input.
11. Inject the measured voltages to their rated values for at least set *tRestore* time.
12. Activate the BLOCK binary input.
13. Simultaneously disconnect all the three phase voltages from the terminal.
    No TRIP signal should appear.
14. Reset the BLOCK binary input.
15. Continue to test another function or complete the test by setting the test mode to Off.

5.5.4.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.6 Testing frequency protection functions

5.6.1 Under frequency protection SAPTUF

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.6.1.1 Verifying the settings

Verification of START-value and time delay to operate

Procedure:

1. Check that the IED settings are appropriate, especially the START value and the definite time delay.
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly decrease the frequency of the applied voltage, until the START signal appears.
4. Note the operate value and compare it with the set value.
5. Increase the frequency until rated operating levels are reached.
6. Check that the START signal resets.
7. Instantaneously decrease the frequency of the applied voltage to a value about 20% lower than the operate value.
8. Measure the time delay of the TRIP signal, and compare it with the set value.

Extended testing

Procedure:

1. The test above can be repeated to check the time to reset.
2. The tests above can be repeated to test the voltage dependent inverse time characteristic.

Verification of the low voltage magnitude blocking

Procedure:

1.
2. Check that the IED settings are appropriate, especially the *StartFrequency*and the *tDelay* and the *MinValFreqMeas* in the SMAI preprocessing function.
3. Supply the IED with three-phase voltages at rated values.
4. Slowly decrease the magnitude of the applied voltage, until the BLKDMAGN signal appears.
5. Note the voltage magnitude value and compare it with the set value *MinValFreqMeas*.
6. Slowly decrease the frequency of the applied voltage, to a value below *StartFrequency*.
7. Check that the START signal not appears.
8. Wait for a time corresponding to *tDelay*, and check that the TRIP signal not appears.

5.6.1.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.6.2 Over frequency protection SAPTOF

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

5.6.2.1 Verifying the settings

Verification of START-value and time delay to operate

Procedure:

1. Check that you have appropriate settings in the IED, especially the START value and the definite time delay.
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly increase the frequency of the applied voltage, until the START signal appears.
4. Note the operate value and compare it with the set value.
5. Decrease the frequency to rated operating conditions.
6. Check that the START signal resets.
7. Instantaneously increase the frequency of the applied voltage to a value about 20% higher than the operate value.
8. Measure the time delay for the TRIP signal, and compare it with the set value.

Extended testing

Procedure:

1. The test above can be repeated to check the time to reset.

Verification of the low voltage magnitude blocking

Procedure:

1. Check that you have appropriate settings in the IED, especially the *StartFrequency*, *TtDelay* and the *MinValFreqMeas* in the SMAI preprocessing function
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly decrease the magnitude of the applied voltage, until the BLKDMAGN signal appears.
4. Note the voltage magnitude value and compare it with the set value, *MinValFreqMeas*.
5. Slowly increase the frequency of the applied voltage, to a value above *StartFrequency*.
6. Check that the START signal does not appear.
7. Wait for a time corresponding to *tDelay*, and make sure that the TRIP signal does not appear.

5.6.2.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.7 Testing secondary system supervision functions

5.7.1 Current circuit supervision CCSRDIF

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

Current circuit supervision (CCSRDIF) is conveniently tested with the same 3-phase test set as used when testing the measuring functions in the IED.

The condition for this procedure is that the setting of *IMinOp* is lower than the setting of *Ip>Block*

5.7.1.1 Verifying the settings

Procedure:

1. Check the input circuits and the operate value of the *IMinOp* current level detector by injecting current, one phase at a time
2. Check the phase current blocking function for all three phases by injection current, one phase at a time. The output signals shall reset with a delay of 1 second when the current exceeds 1.5**IBase*
3. Inject a current 0.9**IBase* to phase L1 and a current 0.15**IBase* to the reference current input IREFSMPL
4. Decrease slowly the current to the reference current input and check that blocking is obtained when the current is about 0.1**IBase*

5.7.1.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.7.2 Fuse failure supervision SDDRFUF

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

The verification is divided in two main parts. The first part is common to all fuse failure supervision options, and checks that binary inputs and outputs operate as expected according to actual configuration. In the second part the relevant set operate values are measured.

5.7.2.1 Checking that the binary inputs and outputs operate as expected

Procedure

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Connect the nominal dc voltage to the DISCPO binary input.
   - The signal BLKU should appear with almost no time delay.
   - No signals BLKZ and 3PH should appear on the IED.
   - Only the distance protection function operates.
   - No other undervoltage-dependent functions must operate.
3. Disconnect the dc voltage from the DISCPOS binary input terminal.
4. Connect the nominal dc voltage to the MCBOP binary input.
   - The BLKU and BLKZ signals should appear without any time delay.
   - No undervoltage-dependent functions must operate.
5. Disconnect the dc voltage from the MCBOP binary input terminal.
6. Disconnect one of the phase voltages and observe the logical output signals on the terminal binary outputs.
   BLKU and BLKZ signals should simultaneously appear.
7. After more than 5 seconds disconnect the remaining two phase voltages and all three currents.
   - There should be no change in the high status of the output signals BLKUV and BLKZ.
   - The signal 3PH will appear.
8. Establish normal voltage and current operating conditions simultaneously and observe the corresponding output signals.
   They should change to logical 0 as follows:
   - Signal 3PH after about 25 ms
   - Signal BLKU after about 50 ms
   - Signal BLKZ after about 200 ms

5.7.2.2 Measuring the operate value for the negative sequence function

Measure the operate value for the negative sequence function, if included in the IED.

Procedure

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Slowly decrease the measured voltage in one phase until the BLKU signal appears.
3. Record the measured voltage and calculate the corresponding negative-sequence voltage according to the equation.
   Observe that the voltages in the equation are phasors:

$$3 \cdot \overline{U_2} = \overline{U_{L1}} + a^2 \cdot \overline{U_{L2}} + a \cdot \overline{U_{L3}}$$

(Equation 13)

Where:

$\overline{U_{L1}}$ $\overline{U_{L2}}$ and $\overline{U_{L3}}$ = the measured phase voltages

$$a = 1 \cdot e^{j\frac{2 \cdot \pi}{3}} = -0,5 + j\frac{\sqrt{3}}{2}$$

4. Compare the result with the set value (consider that the set value *3U2>* is in percentage of the base voltage UBase) of the negative-sequence operating voltage.

5.7.2.3 Measuring the operate value for the zero sequence function

Measure the operate value for the zero sequence function, if included in the IED.

Procedure:

1. Simulate normal operating conditions with the three phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Slowly decrease the measured voltage in one phase until the BLKU signal appears.
3. Record the measured voltage and calculate the corresponding zero sequence voltage according to the equation.
   Observe that the voltages in the equation are phasors.

$$3 \cdot \overline{U_0} = \overline{U_{L1}} + \overline{U_{L2}} + \overline{U_{L3}}$$

(Equation 16)

Where:

$\overline{U_{L1}}$, $\overline{U_{L2}}$ and $\overline{U_{L3}}$ = the measured phase voltages.

4. Compare the result with the set value (consider that the set value *3U0>* is in percentage of the base voltage of the zero sequence operating voltage.

5.7.2.4 Checking the operation of the du/dt and di/dt based function

Check the operation of the du/dt and di/dt based function, if included in the IED.

Procedure

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Connect the nominal dc voltage to the CBCLOSED binary input.
3. Change the voltages and currents in all three phases simultaneously.
   The voltage change should be greater then set *DU>* and the current change should be less than the set *DI<*.
   - The BLKU and BLKZ signals appear without any time delay. The BLKZ signal will be activated , only if the internal deadline detection is not activated at the same time.
   - 3PH should appear after 5 seconds, if the remaining voltage levels are lower than the set *UDLD<VDLDPU* of the DLD function.
4. Apply normal conditions as in step 1 .
   The BLKU, BLKZ and 3PH signals should reset, if activated, see step 3 .
5. Change the voltages and currents in all three phases simultaneously.
   The voltage change should be greater then set *DU>* and the current change should be greater then the set *DI<*.
   The BLKU, BLKZ and 3PH signals should not appear.
6. Disconnect the dc voltage to the CBCLOSED binary input.
7. Apply normal conditions as in step 1 .
8. Repeat step 3 .
9. Connect the nominal voltages in all three phases and feed a current below the operate level in all three phases.
10. Keep the current constant. Disconnect the voltage in all three phases simultaneously.
    The BLKU, BLKZ and 3PH signals should not appear.

5.7.2.5 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.8 Testing control functions

The save of persistent parts of function block states/values to persistent memory uses an exponential back-off algorithm, which rapidly increases the time between save operations when there are frequent changes in such states/values. This means, that when e.g. a pulse counter is exercised periodically, the time between saves of its counter value to persistent memory will occur more and more seldom until there is one hour between save operations. If the IED is restarted by turning auxiliary power off and back on, then up to one hour of counter value increments will be lost. This back-off algorithm is necessary to avoid wearing out the FLASH memory that is used to save the states/values. When changing parameter values using LHMI or PST, there is a save of this type of memory before rebooting, so in this case normally no information is lost. When there are long periods of inactivity, the time between save operations decreases slowly again, until it reaches the minimum time between saves, which is approximately once per second. After commissioning is complete, the possible loss of information should not be an issue, since in normal operation an IED is switched off extremely seldom. But during certain tests, if the IED is rebooted, then data of this kind will sometimes revert back to old values.

5.8.1 Synchronizing and energizing check SESRSYN

This section contains instructions on how to test the synchronism and energizing check for single CB .

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

At commissioning and periodical checks, the functions shall be tested with the used settings. To test a specific function, it might be necessary to change some setting parameters, for example:

- *AutoEnerg = On/Off/DLLB/DBLL/Both*
- *ManEnerg = Off*
- *Operation = Off, On*
- Activation of the voltage selection function if applicable

The tests explained in the test procedures below describe the settings, which can be used as references during testing before the final settings are specified. *After testing, restore the equipment to the normal or desired settings.*

A secondary injection test set with the possibility to alter the phase angle by regulation of the resistive and reactive components is needed. The test set must also be able to generate different frequencies on different outputs.

The description below applies for a system with a nominal frequency of 50 Hz but can be directly transferred to 60 Hz. The synchronizing function can be set to use different phases, phase to earth or phase to phase. Use the set voltages instead of what is indicated below.

Figure 19 shows the general test connection principle, which can be used during testing. This description describes the test of the version intended for one bay.

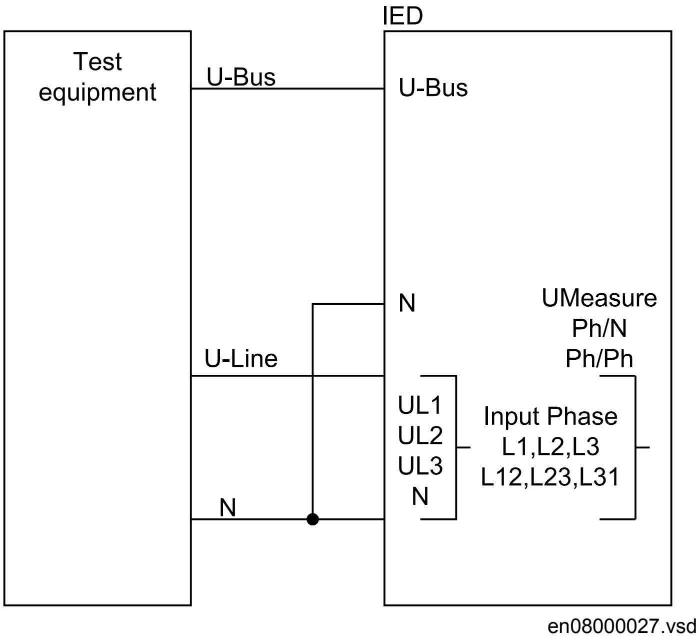

Figure 19: General test connection with three-phase voltage connected to the line side

5.8.1.1 Testing the synchronizing function

This section is applicable only if the synchronizing function is included.

The voltage inputs used are:

ULine	UL1, UL2 or UL3voltage inputs on the IED
U-Bus	U-Bus voltage input on the IED

Testing the frequency difference

The frequency difference is in the example set at 0.20 Hz on the local HMI, and the test should verify that operation is achieved when the *FreqDiffMax* frequency difference is lower than 0.20 Hz. The test procedure below will depend on the settings used.

Procedure:

1. Apply voltages U-Line = 100% *UBase*, f-line=50.0 Hz and U-Bus = 100% *UBase*, f-bus = 50.2 Hz.
2. Check that a closing pulse is submitted and at closing angle less than 2 degrees from phase equality. Modern test sets will evaluate this automatically.
3. Repeat with U-Bus = 80% *UBase*, f-bus=50.25 Hz to verify that the function does not operate when freqency difference is above limit.
4. Repeat with different frequency differences for example, 100 mH with f-bus nominal and line leading and for example 20 mHz (or just above *FreqDiffMin*) to verify that independent of frequency difference the closing pulse occurs within 2 degrees.
5. Verify that the closing command is not issued when the frequency difference is less than the set value *FreqDiffMin*.

5.8.1.2 Testing the synchrocheck

During test of the synchrocheck function for a single bay arrangement, these voltage inputs are used:

U-Line	UL1, UL2 or UL3 voltage input on the IED
U-Bus	U-Bus voltage input on the IED

Testing the voltage difference

Set the voltage difference at 15% *UBase* on the HMI, and the test should check that operation is achieved when the voltage difference *UDiff* is lower than 15% *UBase*.

The settings used in the test shall be final settings. The test shall be adapted to site setting values instead of values in the example below.

Test with no voltage difference between the inputs.

Test with a voltage difference higher than the set *UDiffSC*

1. Apply voltages U-Line (e.g.) = 80% *UBase* and U-Bus = 80% *UBase*.
2. Check that the AUTOSYOK and MANSYOK outputs are activated.
3. The test can be repeated with different voltage values to verify that the function operates within the set *UDiffSC values*. Check with both U1 and U2 respectively lower than the other.
4. Increase the U-Bus to 110% *UBase*, and the U-Line = 90% *UBase* and also the opposite condition.
5. Check that the two outputs for manual and auto synchronism are *not* activated.

Testing the phase angle difference

The phase angle differences *PhaseDiffM* and *PhaseDiffA* respectively are set to their final settings and the test should verify that operation is achieved when the phase angle difference is lower than this value both leading and lagging.

Test with no voltage difference

1. Apply voltages U-Line (e.g.) = 100% *UBase* and U-Bus = 100% *UBase*, with a phase difference equal to 0 degrees and a frequency difference lower than *FreqDiffA* and *FreqDiffM*
2. The test can be repeated with other *PhaseDiff* values to verify that the function operates for values lower than the set ones. By changing the phase angle on U1 connected to U-Bus, between ± dφ degrees. The user can check that the two outputs are activated for a *PhaseDiff* lower than the set value. It should not operate for other values. See figure 20.

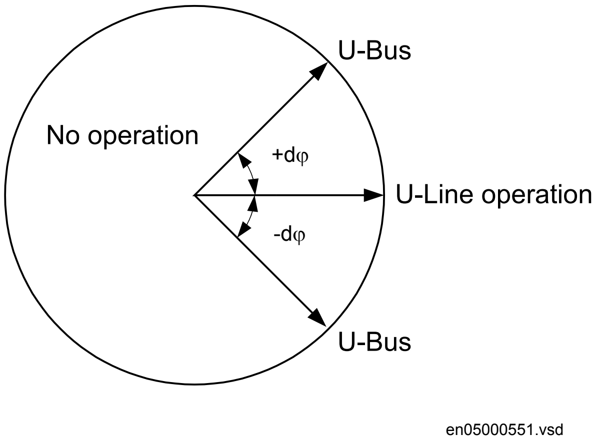

Figure 20: Test of phase difference.

3. Change the phase angle between +dφ and —dφ and verify that the two outputs are activated for phase differences between these values but not for phase differences outside, see figure 20.

Testing the frequency difference

The frequency difference test should verify that operation is achieved when the *FreqDiff* frequency difference is lower than the set value for manual and auto synchrocheck respectively and that operation is blocked when the frequency difference is bigger.

Test with *FreqDiff* = 0 mHz

Test with a frequency difference outside the set limits for manual and auto synchrocheck respectively.

1. Apply voltages U-Line equal to 100% *UBase* and U-Bus equal to 100% *UBase*, with a frequency difference equal to 0 mHz and a phase difference lower than the set value.
2. Check that the AUTOSYOK and MANSYOK outputs are activated.
3. Apply voltage to the U-Line equal to 100% *UBase* with a frequency equal to 50 Hz and voltage U-Bus equal to 100% *UBase*, with a frequency outside the set limit.
4. Check that the two outputs are *not* activated.
   The test can be repeated with different frequency values to verify that the function operates for values lower than the set ones. If a modern test set is used, the frequency can be changed continuously.

Testing the reference voltage

Procedure:

1. Use the same basic test connection as in figure 19.
   The *UDiff* between the voltage connected to U-bus and U-line should be 0%, so that the AUTOOK and MANOK outputs are activated first.
2. Change the U-Line voltage connection to U-Line2 without changing the setting on the local HMI.
3. Check that the two outputs are *not* activated.
4. The test can also be repeated by moving the U-line voltage to the U3PLN input.

5.8.1.3 Testing the energizing check

During test of the energizing check function for a single bay arrangement, these voltage inputs are used:

U-Line	UL1, UL2 or UL3 voltage input on the IED
U-Bus	U-Bus voltage input on the IED

General

At test of the energizing check function for the applicable bus, arrangement shall be done for the energizing check functions. The voltage is selected by activation of different inputs in the voltage selection logic.

The test shall be performed according to the settings for the station. Test the alternatives below that are applicable.

Testing the dead line live bus (DLLB)

The test should verify that the energizing check function operates for a low voltage on the U-Line and for a high voltage on the U-Bus. This corresponds to the energizing of a dead line to a live bus.

Procedure:

1. Apply a single-phase voltage 100% *UBase* to the U-Bus, and a single-phase voltage 30% *UBase* to the U-Line.
2. Check that the AUTOENOK and MANENOK outputs are activated.
3. Increase the U-Line to 60% *UBase* and U-Bus to be equal to 100% *UBase*.
   The outputs should *not* be activated.
   The test can be repeated with different values on the U-Bus and the U-Line.

Testing the dead bus live line (DBLL)

The test should verify that the energizing check function operates for a low voltage on the U-Bus and for a high voltage on the U-Line. This corresponds to an energizing of a dead bus from a live line.

Procedure:

1. Verify the local HMI settings *AutoEnerg* or *ManEnerg* to be *DBLL*.
2. Apply a single-phase voltage of 30% *UBase* to the U-Bus and a single-phase voltage of 100% *UBase* to the U-Line.
3. Check that the AUTOENOK and MANENOK outputs are activated.
4. Decrease the U-Line to 60% *UBase* and keep the U-Bus equal to 30% *UBase*.
   The outputs should *not* be activated.
5. The test can be repeated with different values on the U-Bus and the U-Line.

Testing both directions (DLLB or DBLL)

Procedure:

1. Verify the local HMI settings *AutoEnerg* or *ManEnerg* to be *Both*.
2. Apply a single-phase voltage of 30% *UBase* to the U-Line and a single-phase voltage of 100% *UBase* to the U-Bus.
3. Check that the AUTOENOK and MANENOK outputs are activated.
4. Change the connection so that the U-Line is equal to100% *UBase* and the U-Bus is equal to 30% *UBase*.
   The outputs should still be activated.
5. The test can be repeated with different values on the U-Bus and the U-Line.

Testing the dead bus dead line (DBDL)

The test should verify that the energizing check function operates for a low voltage on both the U-Bus and the U-Line, that is, closing of the breaker in a non-energized system. Test is valid only when this function is used.

Procedure:

1. Verify the local HMI setting *AutoEnerg* to be *Off* and *ManEnerg* to be *DBLL*.
2. Set the parameter *ManEnergDBDL* to *On*.
3. Apply a single-phase voltage of 30% *UBase* to the U-Bus and a single-phase voltage of 30% *UBase* to the U-Line.
4. Check that the MANENOK output is activated.
5. Increase the U-Bus to 80% *UBase* and keep the U-Line equal to 30% *UBase*. The outputs should *not* be activated.
6. Repeat the test with *ManEnerg* set to *DLLB* with different values on the U-Bus and the U-Line voltage.

5.8.1.4 Testing the voltage selection

Testing the voltage selection for single CB arrangements

This test should verify that the correct voltage is selected for the measurement in the energizing check function used in a double-bus arrangement. Apply a single-phase voltage of 30% *UBase* to the U-Line and a single-phase voltage of 100% *UBase* to the U-Bus.

If the UB1/2OK inputs for the fuse failure are used, they must be activated, during tests below. Also verify that deactivation prevents operation and gives an alarm.

Procedure

1. Connect the signals above to binary inputs and binary outputs.
2. Connect the voltage inputs to the analog inputs used for each bus or line depending of the type of busbar arrangement and verify that correct output signals are generated.

5.8.1.5 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.8.2 Autorecloser SMBRREC

Verification of the automatic reclosing function can be considered to consist of two parts; one part to verify the internal logic and timing of the function and one part to verify its interaction with the protection system. This section deals with verification of the auto-reclosing function itself. However, it is practical to start the auto-reclosing function by activating a protection function, for example, by secondary injection tests.

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

The purpose of verification before commissioning is to check that entered selections, setting parameters and configuration render the intended result. The function is flexible with many options and facilities. At commissioning only the selections and settings intended for use are verified. If one chooses to reduce some time settings in order to speed up verification, be careful to set the parameters at intended operational values at the end of the verification procedure. One such parameter is the *Reclaim* time and could result in a long delay of reclosing shots, for example, shot 2 and later ones.

The verification test is performed together with protection and trip functions. Figure 21 illustrates a suggested testing arrangement, where the circuit-breaker (CB) is simulated by an external bi-stable relay (BR), for example a relay type RXMVB2 or RXMD or Breaker Simulator of ABB. The following manual switches are used:

- Switch or push-button to close (SC)
- Switch or push-button to trip (ST)
- Switch for CB ready condition (SRY)

If no bi-stable relay or breaker simulator is available, replace it with two self-reset auxiliary relays and use a self-holding connection.

Use a secondary injection IED test set to operate the protection function. The test set shall be switched off when a trip signal is given or when the BR comes to open position to simulate real conditions.

The CB simulation can be made more elaborate, including simulation of the operating gear condition, CBREADY of either the type ready for a Close-Open (CO) cycle, or the type ready for an Open-Close -Open (OCO) cycle.

The CB condition CBREADY of a type, CO, shall be high (true) until a closing operation is performed. It then goes low (false) for a recharging time of about 5 - 10 s. After that it is high again.

A CB condition CBREADY of a type, OCO shall be high (true) before and during tripping (Start reclosing). During tripping it goes low for a recharging time, for example, 10 s. It may thus be low at the instant of reclosing. After each Open or Close operation it may need a recharging period before it goes high again.

In the example of CB simulation arrangement, the CBREADY condition is simulated by a manual switch, SRY.

Information and material for the verification:

- Protection or control unit, Intelligent electronic device (IED), configured and with settings entered.
- Configuration diagram for the IED
- Terminal diagram for the IED, or plant circuit diagram including the IED
- Technical reference manual for the IED
- IED test set for secondary injection
- Means to indicate, measure and record operation and times, e.g. an event recording facility
- A bi-stable relay (BR) or two auxiliary relays to simulate a CB
- Two push-buttons (SC, ST) to operate the BR and a change-over switch (SRY) to simulate CBREADY
- Possibly a switch simulation the Synchrocheck (SYNC) condition

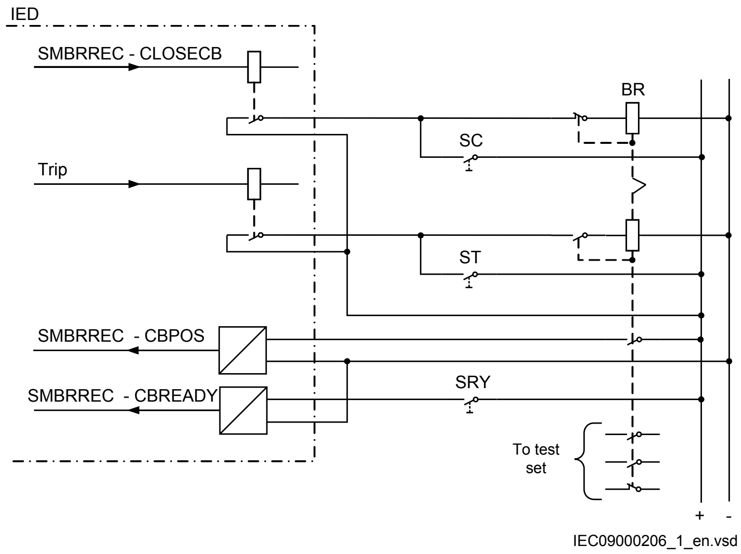

Figure 21: Simulating the CB operation by a bi-stable relay/breaker simulator and manual switches

5.8.2.1 Preparation of the verification

Procedure:

1. Check the function settings.
   In the HMI tree they are found under:
   Main menu/Settings/IED Settings/Control/SMBRREC(79,0->1)/ 1:SMBRREC
   If any timer settings are reduced to speed-up or facilitate the testing, they shall be set to normal after testing. A temporary label on the unit can be a reminder to restore normal settings after which a verification test should be performed.
2. Decide if a synchronization check (SYNC) shall be included in the test.

If SYNC as an internal function or external device is not operated by the injection, it may be connected as a permanent high signal or controlled by a switch.

3. Read and make notes of the reclosing operation counters.
   Local HMI tree:
   Main menu/Tests/Function status/Control/SMBRREC(79,0->1)/1:SMBRREC
   Possibly reset the counters to zero. Counters are reset in the RESET menu.
4. Make arrangements for the simulation of the CB, for example as in figure 21.
5. Make arrangements for indication, recording and time measurements.
   The signals for CBPOS, START, CLOSECB, READY and other relevant signals should preferably be arranged for event recording with time tagging. If that is not possible, other means of time measurement and recording should be arranged.

5.8.2.2 Switching the auto-reclosing function On and Off

Procedure:

1. Set the *Operation* setting to *Off* and check the state.
2. Set the *Operation* setting to *On* and check the state, including SETON and READY.
   The CB should be closed and ready.
3. If external control Off/On is connected, check that it works.
   Set *Operation* to *ExternalCtrl*, and use that control to switch On and Off, and check the state of the function

5.8.2.3 Verifying the auto-reclosing function

Select the test cases to be run according to what is applicable to the particular application. It can be, for example, three-phase single-shot reclosing or two-shot reclosing. Below a case with three-phase single-shot reclosing is described.

Procedure:

1. Set *Operation* = *On*.
2. If SYNC is not to be operated ensure that the SYNC input is activated. If the SYNC function is to be included, ensure that it is supplied with the appropriate AC quantities.
3. Simulate CB closed position by closing switch SC to make the BR relay pick-up.
4. Simulate CBREADY by closing the switch SRY, and leave it closed.
5. Inject AC quantities to give a trip to the BR and to the START input.
   Observe and preferably record the operation. The BR relay shall trip and reclose (pick-up). After reclosing, the SRY switch can be opened for about 5 s and then closed again.

The auto-reclosing open time and the sequence should be checked, for example in the event recording. Check also the operation indications (disturbance report) and the operation counters.
Main menu/Tests/Function status/SMBRREC(79,0->1)/1:SMBRREC
Should the operation not be as expected, this should be investigated. It could be caused by an inappropriate setting or missing condition such as CBREADY (or SYNC at three-phase reclosing).

6. Repeat the sequence by simulating a permanent fault.
   Shortly after the reclosing shot a new fault is applied. If a single-shot reclosing program is selected, there shall be one reclosing operation and then blocking of the auto-reclosing function for the set Reclaim time.
   Before a new reclosing sequence can be run, the CBREADY and CBPOS (CB closed) must be set manually.

5.8.2.4 Checking the reclosing conditions

When checking the influence of a releasing condition it is suggested to first run a sequence with the condition fulfilled. When the condition signal is removed, and a new sequence is run, it indicates that the result was due to the changed condition. In case of a blocking signal the procedure should be similar. Start without the blocking or inhibit signal, and then run a sequence with the blocking or inhibit signal added.

Checking the influence of the INHIBIT signal

Procedure:

1. Check that the auto-reclosing function is operative, for example, by making a reclosing shot without the INHIBIT signal.
2. Apply a fault and thereby a START signal. At the same time, or during the open time, apply a signal to the input INHIBIT.
3. Check that the reclosing sequence is interrupted and no reclosing takes place.

Check closing onto a fault

Procedure:

1. Check that the reclosing function is operative, e.g. by making a reclosing shot.
   Keep the CBREADY signal high.
2. Set the breaker simulating relay BR in Open position.
3. Close the BR relay and apply immediately a fault and thereby a START signal.
4. Check that no reclosing takes place.

Checking the influence of CB not ready for reclosing

Procedure:

1. Check that the auto-reclosing function is operative, e.g. by making a reclosing shot.

Keep the CB simulator BR closed. Remove the CBREADY signal by opening SRY.
2. Apply a fault and thereby a START signal.
3. Check that no reclosing takes place.

Checking the influence of synchrocheck (at three-phase reclosing)

Procedure:

1. Check that the auto-reclosing function is operative, for example, by making a three-phase reclosing shot with the synchrocheck condition.
   Remove the SYNC signal.
2. Apply a fault causing three-phase trip and thereby a START signal.
3. Wait for the tSync time out limit.
   Check that no reclosing is made.

Restoring equipment

After the tests, restore the equipment to normal or desired state. Check the following items in particular:

Procedure:

1. Check the operation counters.
   Reset the counters to zero, if that is the user's preference. The counter reset function is found in the HMI under:
   Main menu/Clear/Clear counters/SMBRREC(79,0->1)
2. Restore settings that may have been modified for the tests back to normal.
3. Disconnect the test switch, CB simulating arrangement and test circuits.
   Reconnect any links or connection terminals, which may have been opened for the tests.
4. Reset indications, alarms and disturbance recordings.
   Clearing of the disturbance report must be done via PCM600 using the disturbance handling tool.

5.8.2.5 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.9 Testing scheme communication functions

5.9.1 Scheme communication logic for distance or overcurrent protection ZCPSCH

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

Check the scheme logic during the secondary injection test of the impedance or overcurrent protection functions.

Activating of the different zones verifies that the CS signal is issued from the intended zones. The CS signal from the independent tripping zone must have a *tSendMin* minimum time.

Check the tripping function by activating the CR and CRG inputs with the overreaching zone used to achieve the CACC signal.

It is sufficient to activate the zones with only one type of fault with the secondary injection.

5.9.1.1 Testing permissive underreaching

Procedure

1. Activate the receive (CR) signal in the IED.
2. Apply healthy normal load conditions to the IED for at least two seconds.
3. Apply a fault condition within the permissive zone.
4. Check that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.
5. Check that other zones operate according to their zone timers and that the send (CS) signal is obtained only for the zone configured to generate the actual signal.
6. Deactivate the receive (CR) signal in the IED.
7. Check that the trip time complies with the zone timers and that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.

5.9.1.2 Testing permissive overreaching

Procedure

1. Activate the receive (CR) signal in the IED.
2. Apply healthy normal load conditions to the IED for at least two seconds.
3. Apply a fault condition within the permissive zone.
4. Check that correct trip outputs, external signals, and indication are obtained for the actual type of fault generated.
5. Check that the other zones operate according to their zone timer and that the send (CS) signal is obtained only for the zones that are configured to give the actual signal.
6. Deactivate the IED receive (CR) signal.
7. Apply healthy normal load conditions to the IED for at least two seconds.
8. Apply a fault condition within the permissive zone.
9. Check that trip time complies with the zone timers and that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.

5.9.1.3 Testing blocking scheme

Procedure

1. Deactivate the receive (CR) signal of the IED.
2. Apply healthy normal load conditions to the IED for at least two seconds.
3. Apply a fault condition within the forward directed zone used for scheme communication tripping.
4. Check that correct trip outputs and external signals are obtained for the type of fault generated and that the operate time complies with the *tCoord* timer (plus relay measuring time).
5. Check that the other zones operate according to their zone times and that a send (CS) signal is only obtained for the reverse zone.
6. Activate the IED receive (CR) signal.
7. Apply a fault condition in the forward directed zone used for scheme communication tripping.
8. Check that the no trip from scheme communication occurs.
9. Check that the trip time from the forward directed zone used for scheme communication tripping complies with the zone timer and that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.

5.9.1.4 Checking of unblocking logic

Check the unblocking function (if the function is required) when you check the communication scheme.

Command function with continuous unblocking (Unblock = 1).

Procedure

1. Activate the guard input signal (CRG) of the IED.
2. Using the scheme selected, check that a signal accelerated trip (TRIP) is obtained when the guard signal is deactivated.

5.9.1.5 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.9.2 Current reversal and weak-end infeed logic for distance protection ZCRWPSCH

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

The current reversal logic and the weak-end infeed functions are tested during the secondary injection test of the impedance or overcurrent protection zones together with the scheme communication logic for the distance protection function (ZCPSCH).

5.9.2.1 Current reversal logic

It is possible to check the delay of the CS send signal with *tDelayRev* by changing from a reverse to a forward fault.

By continuously activating the CR input and changing from a reverse to a forward fault, the delay *tDelayRev* can be checked.

Checking of current reversal

The reverse zone timer must not operate before the forward zone fault is applied. The user might need to block the reverse zone timer during testing of current reversal.

The forward zone timer must be set longer than 90 ms.

Procedure

1. Activate the receive (CRL) signal.
2. Set the healthy condition to an impedance at 50% of the reach of the reverse zone connected to IRVL.
3. After the start condition is obtained for reverse zone, apply a fault at 50% of the reach of the forward zone connected to WEIBLK1.
4. Check that correct trip outputs and external signals are obtained for the type of fault generated.
   The operation time should be about the *tDelayRev* setting longer than the carrier accelerated trip (TRIP) previously recorded for permissive scheme communication.
5. Restore the forward and reverse zone timer to its original setting.

5.9.2.2 Weak-end infeed logic

Weak-end infeed logic at permissive schemes

Procedure

1. Check the blocking of the echo with the injection of a CRL signal >40 ms after a reverse fault is applied.
2. Measure the duration of the echoed signal by applying a CRL receive signal.
3. Check the trip functions and the voltage level for trip by reducing a phase voltage and applying a CRL receive signal.

Testing conditions

Only one type of fault is sufficient, with ZCRWPSCH function. Apply three faults (one in each phase). For phase L1-N fault, set these parameters:

Table 14: Phase L1-N parameter values

Phase	I (Amps)	Phase-angle (Deg)	V (Volts)	Phase-angle (Deg)
L1	0	0	Set less than *UPN<*	0
L2	0	240	63	240
L3	0	120	63	120

Change all settings cyclically for other faults (L2-N and L3-N).

Weak-end infeed set for trip

Weak-end infeed set for echo

1. Apply input signals according table 14.
2. Activate the receive (CR) signal of the terminal.
3. After the relay has operated, turn off the input signals.
4. Check that trip, send signal, and indication are obtained.
   (note: a 200mS pulse)

5. Apply input signals according table 14.
6. Activate the receive (CR) signal of the terminal.
7. After the relay has operated turn off the input signals.
8. Check that the send signal is obtained.

5.9.2.3 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.9.3 Local acceleration logic ZCLCPLAL

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

The logic is checked during the secondary injection test of the impedance measuring zones.

5.9.3.1 Verifying the settings

Procedure

1. Provide the IED with conditions equivalent to normal load for at least two seconds.
2. Deactivate the conditions for accelerated function.
3. Apply a phase-to-earth fault at 100% of line impedance.
4. Check that the fault is tripped with the second zone time delay.
5. Provide the IED with conditions equivalent to normal load for at least two seconds.
6. Activate the condition for accelerated function either by the autorecloser or by the loss-of-load.
7. Apply a phase-to-earth fault at 100% of line impedance.
8. Check the fault is tripped “instantaneously”.

5.9.3.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/**

Tests/Function test modes/<Function group>/<Function>/<1:Function> for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.9.4 Scheme communication logic for residual overcurrent protection ECPSCH

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

Before testing the communication logic for residual overcurrent protection (ECPSCH), the four step residual overcurrent protection (EF4PTOC) has to be tested according to the corresponding instruction. Once this is done, continue with the instructions below.

If the current reversal and weak-end infeed logic for earth-fault protection is included, proceed with the testing according to the corresponding instruction after the testing the communication logic for residual overcurrent protection. The current reversal and weak-end-infeed functions shall be tested together with the permissive scheme.

5.9.4.1 Testing the directional comparison logic function

Blocking scheme

Procedure

1. Inject the polarizing voltage $3U_0$ at 5% of *UBase* where the current is lagging the voltage by 65°.
2. Inject current (65° lagging the voltage) in one phase at about 110% of the set operating current, and switch the current off with the switch.
3. Switch the fault current on and measure the operating time of the communication logic.
   Use the TRIP signal from the configured binary output to stop the timer.
4. Compare the measured time with the set value *tCoord.*
5. Activate the CR binary input.
6. Check that the CRL output is activated when the CR input is activated.
7. Switch the fault current on (110% of the set operating current) and wait longer than the set value *tCoord*.
   No TRIP signal should appear.
8. Switch the fault current off.
9. Reset the CR binary input.
10. Activate the BLOCK digital input.

11. Switch the fault current on (110% of the set operating current) and wait for a period longer than the set value *tCoord.*
    No TRIP signal should appear.
12. Switch the fault current and the polarizing voltage off.
13. Reset the BLOCK digital input.

Permissive scheme

Procedure

1. Inject the polarizing voltage $3U_0$ which is 5% of *UBase* where the current is lagging the voltage by 65°.
2. Inject current (65° lagging the voltage) into one phase at about 110% of the set operating current, and switch the current off with the switch.
3. Switch the fault current on, (110% of the set operating current) and wait longer than the set value *tCoord.*
   No TRIP signal should appear, and the CS binary output should be activated.
4. Switch the fault current off.
5. Activate the CR binary input.
6. Switch the fault current on (110% of the set operating current) and measure the operating time of the ECPSCH logic.
   Use the TRIP signal from the configured binary output to stop the timer.
7. Compare the measured time with the setting for *tCoord*.
8. Activate the BLOCK digital input.
9. Switch the fault current on (110% of the set operating current) and wait for a period longer than the set value tCoord.
   No TRIP signal should appear.
10. Switch the fault current and the polarizing voltage off.
11. Reset the CR binary input and the BLOCK digital input.

5.9.4.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.9.5 Current reversal and weak-end infeed logic for residual overcurrent protection ECRWPSCH

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

First, test the four step residual overcurrent protection (EF4PTOC) and then the current reversal and weak-end infeed logic according to the corresponding instructions. Then continue with the instructions below.

5.9.5.1 Testing the current reversal logic

Procedure

1. Inject the polarizing voltage *3U0* to 5% of *UBase* and the phase angle between voltage and current to 155°, the current leading the voltage.
2. Inject current (155° leading the voltage) in one phase to about 110% of the set operating current of the four step residual overcurrent protection (*IN>Dir*).
3. Check that the IRVL output is activated after the set time (*tPickUpRev*).
4. Abruptly reverse the current to 65° lagging the voltage, to operate the forward directional element.
5. Check that the IRVL output still is activated after the reversal with a time delay that complies with the setting (*tDelayRev*).
6. Switch off the polarizing voltage and the current.

5.9.5.2 Testing the weak-end infeed logic

If setting WEI=Echo

Procedure

1. Inject the polarizing voltage $3U_0$ to 5% of *UBase* and the phase angle between voltage and current to 155°, the current leading the voltage.
2. Inject current (155° leading the voltage) in one phase to about 110% of the setting operating current (*IN>Dir*).
3. Activate the CRL binary input.
   No ECHO and CS should appear.
4. Abruptly reverse the current to 65° lagging the voltage, to operate the forward directional element.
   No ECHO and CS should appear.
5. Switch off the current and check that the ECHO and CS appears on the corresponding binary output or on the local HMI unit, about 200 ms after resetting the directional element.
6. Switch off the CRL binary input.
7. Activate the BLOCK binary input.
8. Activate the CRL binary input.
   No ECHO and CS should appear.
9. Switch off the polarizing voltage and reset the BLOCK and CRL binary input.

If setting WEI=Echo & Trip

Procedure

1. Inject the polarizing voltage $3U_0$ to about 90% of the setting ($3U_0$) operating voltage.
2. Activate the CRL binary input.
   No ECHO, CS and TRWEI outputs should appear.
3. Increase the injected voltage to about 110% of the setting ($3U_0$) operating voltage.
4. Activate the CRL binary input.
5. Check that the ECHO, CS and TRWEI appears on the corresponding binary output or on the local HMI.
6. Reset the CRL binary input.
7. Activate the BLOCK binary input.
8. Activate the CRL binary input.
   No ECHO, CS and TRWEI outputs should appear.
9. Reset the CRL and BLOCK binary input.
10. Inject the polarizing voltage $3U_0$ to about 110% of the setting ($3U_0$) and adjust the phase angle between the voltage and current to 155°, the current leading the voltage.
11. Inject current (155° leading the voltage) in one phase to about 110% of the setting operating current (*IN>Dir*).
12. Activate the CRL binary input.
    No ECHO, CS and TRWEI should appear.
13. Abruptly reverse the current to 65° lagging the voltage, to operate the forward directional element.
    No ECHO, CS and TRWEI should appear.
14. Switch the current off and check that the ECHO, CS and TRWEI appears on the corresponding binary output or on the local HMI, about 200 ms after resetting the directional element.
15. Switch the polarizing voltage off and reset the CRL binary input.

5.9.5.3 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/ Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.10 Testing logic functions

5.10.1 Tripping logic SMPPTRC

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/ Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

This function is functionality tested together with other protection functions (earth-fault overcurrent protection, etc.) within the IED. It is recommended that the function is tested together with the autoreclosing function, or when a separate external unit is used for reclosing purposes. The testing is preferable done in conjunction with the protection system and autoreclosing function.

5.10.1.1 Three phase operating mode

Procedure:

1. Check that *AutoLock* and *TripLockout* are both set to *Off*.
2. Initiate a three-phase fault
   An adequate time interval between the faults should be considered, to overcome a reclaim time caused by the possible activation of the autorecloser (SMBRREC) function. The function must issue a three-phase trip in all cases, when trip is initiated by any protection or some other built-in or external function. The functional TRIP output signal must always appear.

5.10.1.2 Circuit breaker lockout

The following tests should be carried out when the built-in lockout function is used in addition to possible other tests, which depends on the complete configuration of a IED.

Procedure:

1. Check that *AutoLock* and *TripLockout* are both set to *Off*.
2. Initiate a three-phase fault
   The functional output TRIP should be active at each fault. The output CLLKOUT must not be set.
3. Activate the automatic lockout function, set *AutoLock* = *On* and repeat
   Beside the TRIP outputs, CLLKOUT should be set.
4. Reset the lockout signal by shortly thereafter activating the reset lockout (RSTLKOUT) signal.
5. Activate the TRIP signal lockout function, set *TripLockout* = *On* and repeat.
   The output TRIP must be active and stay active after each fault, CLLKOUT must be set.
6. Repeat.
   All functional outputs should reset.
7. Deactivate the TRIP signal lockout function, set *TripLockout* = *Off* and the automatic lockout function, set *AutoLock* = *Off*.

5.10.1.3 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.11 Testing monitoring functions

5.11.1 Event counter CNTGGIO

The function can be tested by connecting a binary input to the counter under test and from outside apply pulses to the counter. The speed of pulses must not exceed 10 per second. Normally the counter will be tested in connection with tests on the function that the counter is connected to, such as trip logic. When configured, test it together with the function which operates it. Trig the function and check that the counter result is the same the number of operations.

5.11.2 Fault locator RFLO

Prepare the IED for verification of settings as outlined in 4.1 "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under: **Main menu/Tests/Function status/<Function group>/<Function>/<1:Function>**. The Signal monitoring tool in PCM600 shows the same signals that are available on the LHMI.

The fault locator function depends on other functions to work properly, i.e. phase selection information from distance protection function and analog information supplied by the trip value recorder function. Check that proper binary start and phase selection signals are connected and voltage and current signals are configured (parameter settings)

The result is displayed on the local HMI or via PCM600. Distances to faults for the last 100 recorded disturbances can be found in the local HMI under the menu:

Main menu/Tests/Function status/Monitoring/LMBRFLO/1:LMBRFLO/Outputs

If PCM600 is used the result is displayed on the recording list after upload, including loop selection information.

Table 15: Test settings

Parameter:	Condition:
I	Higher than 30% I_r
Healthy conditions	U = 63,5 V, I = 0 A & ZF = 0°
Impedance |Z|	Test point Note: • $Z_x \leq$ (X0 + 2 · X1)/3 For single-phase faults • $Z_x \leq$ X1 For three and two phase faults • $Z_x \leq$ (X0 + 2 · X1 XM)/3 For single-phase fault with mutual zero-sequence current
Impedance angle ZΦ	Test angle • ZΦ arctan[(X0 + 2 · X1) / (R0 + 2R1)] For single-phase faults • ZΦ arctan(X1/R1) For two-phase faults

5.11.2.1 Measuring the operate limit

Procedure

1. Set the test point (|Z| fault impedance and ZΦ impedance phase angle) for a condition that meets the requirements in table 15.
2. Subject the IED to healthy normal load conditions for at least two seconds.
3. Apply a fault condition.
   Check that the distance-to-fault value displayed on the HMI complies with the following equations (the error should be less than five percent):

$$p = \frac{Z_x}{X1} \cdot 100$$

(Equation 18)

in % for two- and three-phase faults

$$p = \frac{3 \cdot Z_x}{X0 + 2 \cdot X1} \cdot 100$$

(Equation 19)

in % for single-phase-to-earth faults

$$p = \frac{3 \cdot Z_x}{X0 + 2 \cdot X1 \pm XM} \cdot 100$$

(Equation 20)

in % for single-phase-to-earth faults with mutual zero sequence current.

Where:

p = the expected value of a distance to fault in percent

Z_x = set test point on the test set

X0 = set zero-sequence reactance of a line

X1 = set positive-sequence reactance of a line

XM = set mutual zero-sequence impedance of a line

5.11.2.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Off* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No*, under **Main menu/Tests/Function test modes/<Function group>/<Function>/<1:Function>** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

5.12 Testing metering functions

5.12.1 Pulse counter PCGGIO

The test of Pulse counter PCGGIO function requires the Parameter Setting Tool (PST) in PCM600 or an appropriate connection to a local HMI with the necessary functionality. A known number of pulses with different frequencies are connected to the pulse counter input. The test should be performed with settings *Operation = Off/On* and the function blocked/deblocked. The pulse counter value is then checked in PCM600 or local HMI.

5.13 Exit test mode

The following procedure is used to return to normal operation.

Procedure

1. Navigate to the test mode folder.
2. Change the 'On' setting to 'Off'. Press the 'E' key and the left arrow key.
3. Answer 'YES', press the 'E' key and exit the menus.

Section 6 Troubleshooting

6.1 Fault tracing

6.1.1 Identifying hardware errors

1. Check the module with an error.
    - Check the general IED status in **Main menu/Diagnostics/IED status/ General** for a faulty hardware module.
    - Check the history of changes in internal event list in **Main menu/ Events**.
2. Inspect the IED visually.
    - Inspect the IED visually to find any physical error causes.
    - If you can find some obvious physical damage, contact ABB for repair or replacement actions.
3. Check whether the error is external or internal.
    - Check that the error is not caused by external origins.
    - Remove the wiring from the IED and test the input and output operation with an external test device.
    - If the problem remains, contact ABB for repair or replacement actions.

6.1.2 Identifying runtime errors

1. Check the error origin from IED's internal event list **Main menu/Diagnostics/ IED status/General**.
2. Reboot the IED and recheck the supervision events to see if the fault has cleared.
3. In case of persistent faults, contact ABB for corrective actions.

6.1.3 Identifying communication errors

Communication errors are normally communication interruptions or synchronization message errors due to communication link breakdown.

- Check the IEC61850 and DNP3 communication status in internal event list in **Main menu/Diagnostics/IED Status/General**.
- In case of persistent faults originating from IED's internal faults such as component breakdown, contact ABB for repair or replacement actions.

6.1.3.1 Checking the communication link operation

There are several different communication links on the product. First check that all communication ports that are used for communication are turned on.

1. Check the front communication port RJ-45.
   1.1. Check that the uplink LED is lit with a steady green light.
   The uplink LED is located on the LHMI above the RJ-45 communication port on the left. The port is used for direct electrical communication to a PC connected via a crossed-over Ethernet cable.
   1.2. Check the communication status of the front port via the LHMI in **Main menu/Test/Function status/Communication/1:DOSFRNT/Outputs**.
   Check that the *LINKUP* value is 1, that is, the communication is working. When the value is 0, there is no communication link.

> The rear port connector X0 is used for connecting an external HMI to the IED. If the *LINKUP* value is 0 for front port, there is no communication link via port X0. Do not use rear port connector X0 if the IED is equipped with an LHMI.

2. Check the communication status of the rear port X1 via the LHMI in **Main menu/Test/Function status/Communication/1:DOSLAN1/Outputs**.
   The X1 communication port on the rear side of the IED is for optical Ethernet via LC connector or electrical via RJ-45 connector of the IEC 61850-8-1 station bus communication.
   - Check that the *LINKUP* value is 1, that is, the communication is working. When the value is 0, there is no communication link.

6.1.3.2 Checking the time synchronization

- Select **Main menu/Diagnostics/IED status/General** and check the status of the time synchronization on **Time Synch**.
  The *Time synch* value is *Ready* when the synchronization is in order. Note that the time synchronization source has to be activated. Otherwise the value is always *Ready*.

6.1.4 Running the display test

To run the display test, either use the push buttons or start the test via the menu.

- Select **Main menu/Tests/LED test**.
- Press  or simultaneously 

and .

All the LEDs are tested by turning them on simultaneously. The LCD shows a set of patterns so that all the pixels are activated. After the test, the display returns to normal state.

6.2 Indication messages

6.2.1 Internal faults

When the Ready LED indicates an internal fault by flashing, the message associated with the fault is found in the internal event list in the LHMI menu **Main menu/Diagnostics/Internal events**. The message includes the date, time, description and signal state for the fault. The internal event list is not updated dynamically. The list is updated by leaving the **Internal events** menu and then selecting it again. The current status of the internal fault signals can also be checked via the LHMI in **Main menu/Diagnostics/IED status**.

Different actions are taken depending on the severity of the fault. After the fault is found to be permanent, the IED stays in internal fault mode. The IED continues to perform internal tests during the fault situation.

When a fault appears, the fault indication message is to be recorded and stated when ordering service.

Table 16: Internal fault indications

Fault indication	Additional information
Internal Fault Real Time Clock Error	Hardware error with the real time clock.
Internal Fault Runtime Exec. Error	One or more of the application threads are not working properly.
Internal Fault SW Watchdog Error	This signal will be activated when the terminal has been under too heavy load for at least 5 minutes.
Internal Fault Runtime App Error	One or more of the application threads are not in an expected state.
Internal Fault File System Error	A file system error has occurred.
Internal Fault TRM-Error	A TRM card error has occurred. The instance number is displayed at the end of the fault indication.
Internal Fault BIO-Error	A BIO card error has occurred. The instance number is displayed at the end of the fault indication.
Internal Fault COM-Error	A COM card error has occurred. The instance number is displayed at the end of the fault indication.
Internal Fault PSM-Error	A PSM card error has occurred. The instance number is displayed at the end of the fault indication.

6.2.2 Warnings

The warning message associated with the fault is found in the internal event list in the LHMI menu **Main menu/Diagnostics/Internal events**. The message includes the date, time, description and signal state for the fault. The current status of the internal fault signals can also be checked via the LHMI in **Main menu/Diagnostics/IED status/General**.

When a fault appears, the fault indication message is to be recorded and stated when ordering service.

Table 17: Warning indications

Warning indication	Additional information
Warning IEC 61850 Error	IEC 61850 has not succeeded in some actions such as reading the configuration file, startup etc.
Warning DNP3 Error	Error in DNP3 communication.

6.2.3 Additional indications

The additional indication messages do not activate internal fault or warning.

The messages are listed in the LHMI menu under the event list. The signal status data is found under the IED status and in the internal event list.

Table 18: Additional indications

Warning indication	Additional information
Time Synch Error	Source of the time synchronization is lost or time system has made a time reset.
BATTERY1 Error	BATTERY1 Error status.
Settings Changed	Settings have been changed.
Setting Groups Changed	Setting group has been changed.

6.3 Correction procedures

6.3.1 Resetting the configuration

The configuration is reset with PCM600.

For more information, see PCM600 documentation.

6.3.2 Changing and setting the password

The password can only be set with PCM600.

For more information, see PCM600 documentation.

6.3.3 Identifying IED application problems

Navigate to the appropriate menu in the LHMI to identify possible problems.

- Check that the function is on.
- Check that the correct setting group (1 to 4) is activated.
- Check the blocking.
- Check the mode.
- Check the measurement value.
- Check the connection to trip and disturbance recorder functions.
- Check the channel settings.

6.3.3.1 Inspecting the wiring

The physical inspection of wiring connections often reveals the wrong connection for phase currents or voltages. However, even though the phase current or voltage connections to IED terminals might be correct, wrong polarity of one or more measurement transformers can cause problems.

- Check the current or voltage measurements and their phase information from **Main menu/Measurements/Analog primary values** or **Analog secondary voltages**.
- Check that the phase information and phase shift between phases is correct.
- Correct the wiring if needed.
    - Change the parameter *Negation* in **Configuration/Analog modules/ 3PhaseAnalogGroup/1:SMAI_20_n** (n= the number of the SMAI used).

      Changing the *Negation* parameter is not recommended without special skills.

    - In PCM600, change the parameter *CTStarPointn* (n= the number on the current input) under the parameter settings for each current input.
- Check the actual state of the connected binary inputs.

- In LHMI, select **Main menu/Tests/Binary input values/Binary input modules**. Then navigate to the board with the actual binary input to be checked.
- With PCM600, right-click the product and select **Signal Monitoring**. Then navigate to the actual I/O board and to the binary input in question. The activated input signal is indicated with a yellow-lit diode.

- Measure output contacts using the voltage drop method of applying at least the minimum contact load given for the output relays in the technical data, for example 100 mA at 24 V AC/DC.

Output relays, especially power output relays, are designed for breaking high currents. Due to this, layers of high resistance may appear on the surface of the contacts. Do not determine proper functionality of connectivity or contact resistance by measuring with a regular hand-held ohm meter.

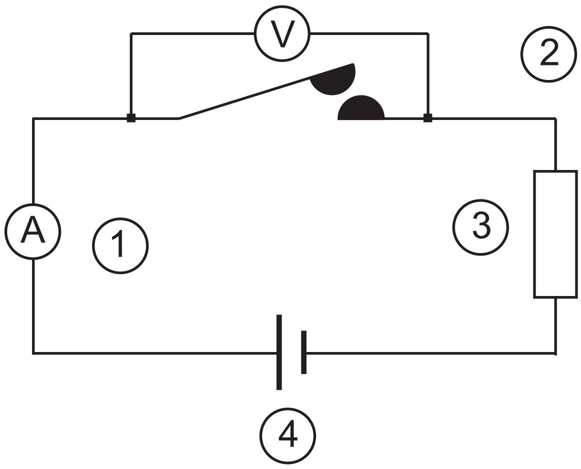

Figure 22: Testing output contacts using the voltage drop method

1 Contact current

2 Contact voltage drop

3 Load

4 Supply voltage

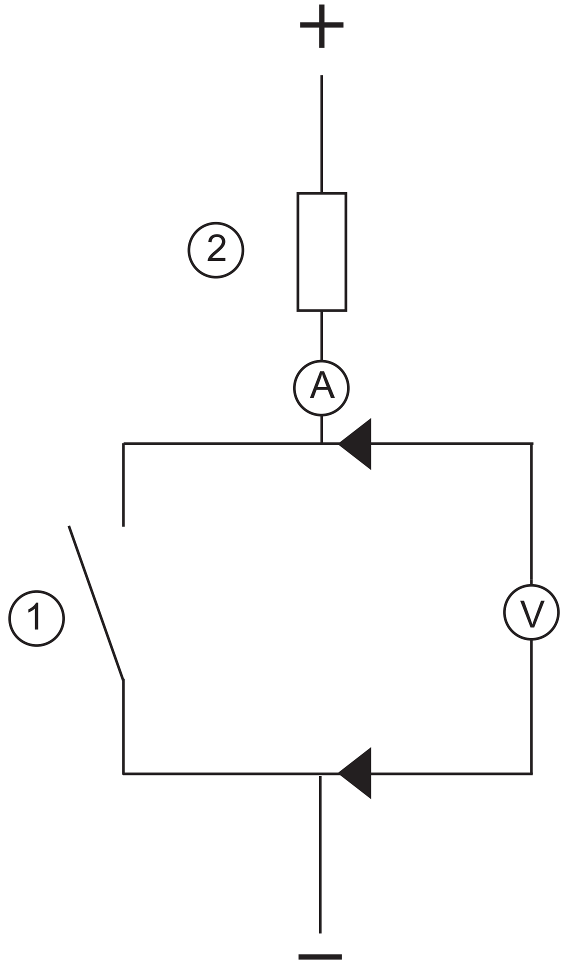

Figure 23: Testing a trip contact

1 Trip contact under test

2 Current limiting resistor

- To check the status of the output circuits driving the output relay via the LHMI, select **Main menu/Tests/Binary output values/Binary output modules** and then navigate to the board with the actual binary output to be checked.
- Test and change the relay state manually.
    1. To set the IED to test mode, select **Main menu/Tests/IED testmode1:TESTMODE/TestMode** and set the parameter to *On*.
    2. To operate or force the output relay to operate, select **Main menu/Tests/Forcing/Binary output values** and then navigate to the board with the actual binary output relay to be operated/forced.
    3. Select the BOn_PO to be operated/forced and use ↵ and ↑ or ↓ to operate the actual output relay.

In PCM600, only the result of these operations can be checked by right-clicking the product and selecting Signal Monitoring tool and then navigating to the actual I/O-board and the binary input in question. The activated output signal is indicated with a yellow-lit diode. Each BOn_PO is represented by two signals. The first signal in LHMI is the actual value 1 or 0 of the output, and in PCM600 a lit or dimmed diode. The second signal is the status Normal or Forced. Forced status is only achieved when the BO is set to *Forced* or operated on the LHMI.

Set the parameter *TestMode* to *Off* after completing these tests. The Start LED will stop flashing when the relay is no longer in test mode.

An initially high contact resistance will not cause problems as it will be reduced quickly by the electrical cleaning effect of fritting and thermal destruction of layers, bringing the contact resistance back to the mOhm range. As a result, practically the full voltage is available at the load.

Section 7 Glossary

AC	Alternating current
ACT	Application configuration tool within PCM600
A/D converter	Analog to digital converter
ADBS	Amplitude dead-band supervision
ANSI	American National Standards Institute
AR	Autoreclosing
ASCT	Auxiliary summation current transformer
ASD	Adaptive signal detection
AWG	American Wire Gauge standard
BR	External bi-stable relay
BS	British standard
CAN	Controller Area Network. ISO standard (ISO 11898) for serial communication
CB	Circuit breaker
CCITT	Consultative Committee for International Telegraph and Telephony. A United Nations sponsored standards body within the International Telecommunications Union.
CCVT	Capacitive Coupled Voltage Transformer
Class C	Protection Current Transformer class as per IEEE/ ANSI
CMPPS	Combined mega pulses per second
CO cycle	Close-open cycle
Co-directional	Way of transmitting G.703 over a balanced line. Involves two twisted pairs making it possible to transmit information in both directions
COMTRADE	Standard format according to IEC 60255-24
Contra-directional	Way of transmitting G.703 over a balanced line. Involves four twisted pairs of with two are used for transmitting data in both directions, and two pairs for transmitting clock signals
CPU	Central processor unit
CR	Carrier receive
CRC	Cyclic redundancy check
CS	Carrier send

CT	Current transformer
CVT	Capacitive voltage transformer
DAR	Delayed auto-reclosing
DARPA	Defense Advanced Research Projects Agency (The US developer of the TCP/IP protocol etc.)
DBDL	Dead bus dead line
DBLL	Dead bus live line
DC	Direct current
DFT	Discrete Fourier transform
DIP-switch	Small switch mounted on a printed circuit board
DLLB	Dead line live bus
DNP	Distributed Network Protocol as per IEEE/ANSI Std. 1379-2000
DR	Disturbance recorder
DRAM	Dynamic random access memory
DRH	Disturbance report handler
DSP	Digital signal processor
DTT	Direct transfer trip scheme
EHV network	Extra high voltage network
EIA	Electronic Industries Association
EMC	Electro magnetic compatibility
EMF	Electro motive force
EMI	Electro magnetic interference
EnFP	End fault protection
ESD	Electrostatic discharge
FOX 20	Modular 20 channel telecommunication system for speech, data and protection signals
FOX 512/515	Access multiplexer
FOX 6Plus	Compact, time-division multiplexer for the transmission of up to seven duplex channels of digital data over optical fibers
G.703	Electrical and functional description for digital lines used by local telephone companies. Can be transported over balanced and unbalanced lines
GCM	Communication interface module with carrier of GPS receiver module
GDE	Graphical display editor within PCM600

GI	General interrogation command
GIS	Gas insulated switchgear
GOOSE	Generic object oriented substation event
GPS	Global positioning system
HDLC protocol	High level data link control, protocol based on the HDLC standard
HFBR connector type	Plastic fiber connector
HMI	Human machine interface
HSAR	High speed auto reclosing
HV	High voltage
HVDC	High voltage direct current
IDBS	Integrating dead band supervision
IEC	International Electrical Committee
IEC 60044-6	IEC Standard, Instrument transformers – Part 6: Requirements for protective current transformers for transient performance
IEC 61850	Substation Automation communication standard
IEEE	Institute of Electrical and Electronics Engineers
IEEE 802.12	A network technology standard that provides 100 Mbits/s on twisted-pair or optical fiber cable
IEEE P1386.1	PCI Mezzanine card (PMC) standard for local bus modules. References the CMC (IEEE P1386, also known as Common mezzanine card) standard for the mechanics and the PCI specifications from the PCI SIG (Special Interest Group) for the electrical EMF Electro Motive Force.
IED	Intelligent electronic device
I-GIS	Intelligent gas insulated switchgear
Instance	When several occurrences of the same function are available in the IED they are referred to as instances of that function. One instance of a function is identical to another of the same kind but will have a different number in the IED user interfaces. The word instance is sometimes defined as an item of information that is representative of a type. In the same way an instance of a function in the IED is representative of a type of function.
IP	1. Internet protocol. The network layer for the TCP/IP protocol suite widely used on Ethernet networks. IP is a connectionless, best-effort packet switching protocol. It

	provides packet routing, fragmentation and re-assembly through the data link layer. 2. Ingression protection according to IEC standard
IP 20	Ingression protection, according to IEC standard, level 20
IP 40	Ingression protection, according to IEC standard, level 40
IP 54	Ingression protection, according to IEC standard, level 54
IRF	Internal fail signal
IRIG-B:	InterRange Instrumentation Group Time code format B, standard 200
ITU	International Telecommunications Union
LAN	Local area network
LIB 520	High voltage software module
LCD	Liquid crystal display
LDD	Local detection device
LED	Light emitting diode
MCB	Miniature circuit breaker
MCM	Mezzanine carrier module
MVB	Multifunction vehicle bus. Standardized serial bus originally developed for use in trains.
NCC	National Control Centre
OCO cycle	Open-close-open cycle
OCP	Overcurrent protection
OLTC	On load tap changer
OV	Over voltage
Overreach	A term used to describe how the relay behaves during a fault condition. For example a distance relay is over-reaching when the impedance presented to it is smaller than the apparent impedance to the fault applied to the balance point, i.e. the set reach. The relay “sees” the fault but perhaps it should not have seen it.
PCI	Peripheral component interconnect, a local data bus
PCM	Pulse code modulation
PCM600	Protection and control IED manager
PC-MIP	Mezzanine card standard
PISA	Process interface for sensors & actuators
PMC	PCI Mezzanine card
POTT	Permissive overreach transfer trip

Process bus	Bus or LAN used at the process level, that is, in near proximity to the measured and/or controlled components
PSM	Power supply module
PST	Parameter setting tool within PCM600
PT ratio	Potential transformer or voltage transformer ratio
PUTT	Permissive underreach transfer trip
RASC	Synchrocheck relay, COMBIFLEX
RCA	Relay characteristic angle
REVAL	Evaluation software
RFPP	Resistance for phase-to-phase faults
RFPE	Resistance for phase-to-earth faults
RISC	Reduced instruction set computer
RMS value	Root mean square value
RS422	A balanced serial interface for the transmission of digital data in point-to-point connections
RS485	Serial link according to EIA standard RS485
RTC	Real time clock
RTU	Remote terminal unit
SA	Substation Automation
SC	Switch or push-button to close
SCS	Station control system
SCT	System configuration tool according to standard IEC 61850
SMA connector	Subminiature version A, A threaded connector with constant impedance.
SMT	Signal matrix tool within PCM600
SMS	Station monitoring system
SNTP	Simple network time protocol – is used to synchronize computer clocks on local area networks. This reduces the requirement to have accurate hardware clocks in every embedded system in a network. Each embedded node can instead synchronize with a remote clock, providing the required accuracy.
SRY	Switch for CB ready condition
ST	Switch or push-button to trip
Starpoint	Neutral point of transformer or generator
SVC	Static VAr compensation
TC	Trip coil

TCS	Trip circuit supervision
TCP	Transmission control protocol. The most common transport layer protocol used on Ethernet and the Internet.
TCP/IP	Transmission control protocol over Internet Protocol. The de facto standard Ethernet protocols incorporated into 4.2BSD Unix. TCP/IP was developed by DARPA for internet working and encompasses both network layer and transport layer protocols. While TCP and IP specify two protocols at specific protocol layers, TCP/IP is often used to refer to the entire US Department of Defense protocol suite based upon these, including Telnet, FTP, UDP and RDP.
TNC connector	Threaded Neill Concelman, A threaded constant impedance version of a BNC connector
TPZ, TPY, TPX, TPS	Current transformer class according to IEC
Underreach	A term used to describe how the relay behaves during a fault condition. For example a distance relay is under-reaching when the impedance presented to it is greater than the apparent impedance to the fault applied to the balance point, i.e. the set reach. The relay does not “see” the fault but perhaps it should have seen it. See also Overreach.
U/I-PISA	Process interface components that deliver measured voltage and current values
UTC	Coordinated universal time. A coordinated time scale, maintained by the Bureau International des Poids et Mesures (BIPM), which forms the basis of a coordinated dissemination of standard frequencies and time signals. UTC is derived from International Atomic Time (TAI) by the addition of a whole number of "leap seconds" to synchronize it with Universal Time 1 (UT1), thus allowing for the eccentricity of the Earth"s orbit, the rotational axis tilt (23.5 degrees), but still showing the Earth"s irregular rotation, on which UT1 is based. The Coordinated Universal Time is expressed using a 24-hour clock and uses the Gregorian calendar. It is used for aeroplane and ship navigation, where it also sometimes known by the military name, "Zulu time". "Zulu" in the phonetic alphabet stands for "Z" which stands for longitude zero.
UV	Undervoltage
WEI	Weak end infeed logic
VT	Voltage transformer
X.21	A digital signalling interface primarily used for telecom equipment

$3I_O$	Three times zero-sequence current. Often referred to as the residual or the earth-fault current
$3U_O$	Three times the zero sequence voltage. Often referred to as the residual voltage or the neutral point voltage

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