



Relion® 650 series

Bay control REC650 ANSI Commissioning Manual



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ABB Inc.
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Toll Free: 1-800-HELP-365, menu option #8

ABB Inc.
3450 Harvester Road
Burlington, ON L7N 3W5, Canada
Toll Free: 1-800-HELP-365, menu option #8

ABB Mexico S.A. de C.V.
Paseo de las Americas No. 31 Lomas Verdes 3a secc.
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Phone: (+1) 440-585-7804, menu option #8

Conformity

This product complies with the directive of the Council of the European Communities on the approximation of the laws of the Member States relating to electromagnetic compatibility (EMC Directive 2004/108/EC) and concerning electrical equipment for use within specified voltage limits (Low-voltage directive 2006/95/EC). This conformity is the result of tests conducted by ABB in accordance with the product standards EN 50263 and EN 60255-26 for the EMC directive, and with the product standards EN 60255-1 and EN 60255-27 for the low voltage directive. The product is designed in accordance with the international standards of the IEC 60255 series and ANSI C37.90. The DNP protocol implementation in the IED conforms to "DNP3 Intelligent Electronic Device (IED) Certification Procedure Subset Level 2", available at www.dnp.org.

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The data, examples and diagrams in this manual are included solely for the concept or product description and are not to be deemed as a statement of guaranteed properties. All persons responsible for applying the equipment addressed in this manual must satisfy themselves that each intended application is suitable and acceptable, including that any applicable safety or other operational requirements are complied with. In particular, any risks in applications where a system failure and/or product failure would create a risk for harm to property or persons (including but not limited to personal injuries or death) shall be the sole responsibility of the person or entity applying the equipment, and those so responsible are hereby requested to ensure that all measures are taken to exclude or mitigate such risks.

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Safety information



Dangerous voltages can occur on the connectors, even though the auxiliary voltage has been disconnected.



Non-observance can result in death, personal injury or substantial property damage.



Only a competent electrician is allowed to carry out the electrical installation.



National and local electrical safety regulations must always be followed.



The frame of the IED has to be carefully grounded.



Whenever changes are made in the IED, measures should be taken to avoid inadvertent tripping.



The IED contains components which are sensitive to electrostatic discharge. Unnecessary touching of electronic components must therefore be avoided.

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Section 1 Introduction

1.1 This manual

The commissioning manual contains instructions on how to commission the IED. The manual can also be used by system engineers and maintenance personnel for assistance during the testing phase. The manual provides procedures for checking of external circuitry and energizing the IED, parameter setting and configuration as well as verifying settings by secondary injection. The manual describes the process of testing an IED in a substation which is not in service. The chapters are organized in chronological order in which the IED should be commissioned.

1.2 Intended audience

This manual addresses the personnel responsible for commissioning, maintenance and taking the IED in and out of normal service.

The commissioning personnel must have a basic knowledge of handling electronic equipment. The commissioning and maintenance personnel must be well experienced in using protection equipment, test equipment, protection functions and the configured functional logics in the IED.

1.3 Product documentation

1.3.1 Product documentation set

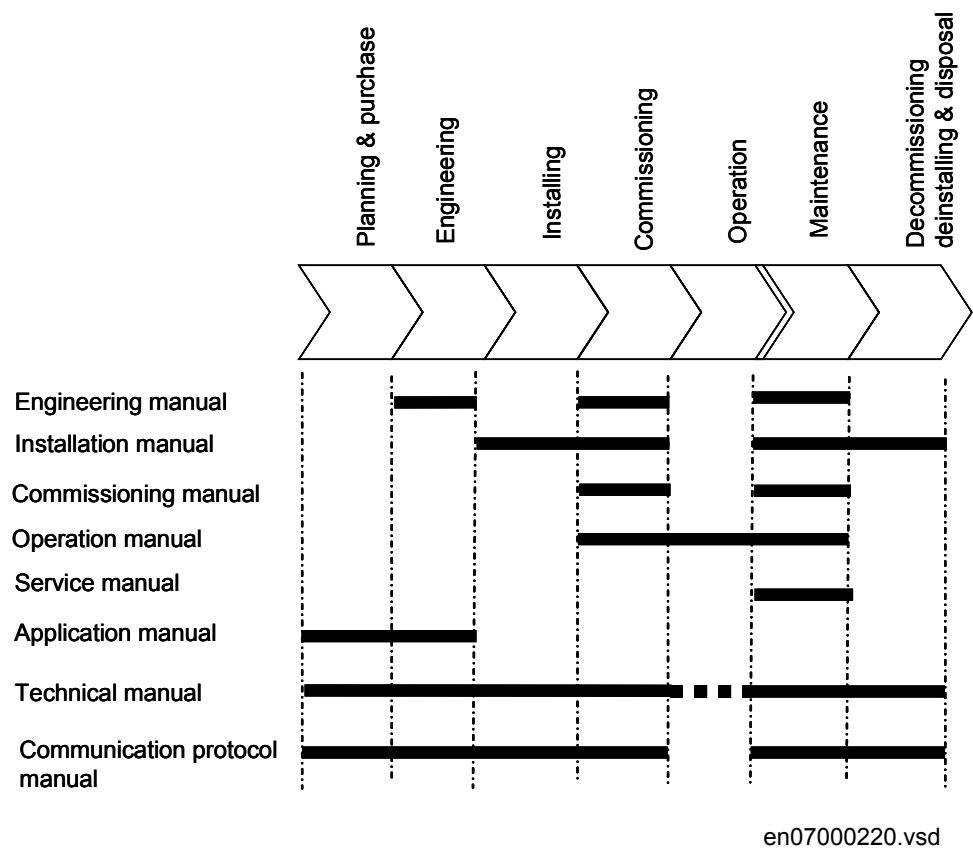


Figure 1: The intended use of manuals in different lifecycles

The engineering manual contains instructions on how to engineer the IEDs using the different tools in PCM600. The manual provides instructions on how to set up a PCM600 project and insert IEDs to the project structure. The manual also recommends a sequence for engineering of protection and control functions, LHMI functions as well as communication engineering for IEC 60870-5-103, IEC 61850 and DNP3.

The installation manual contains instructions on how to install the IED. The manual provides procedures for mechanical and electrical installation. The chapters are organized in chronological order in which the IED should be installed.

The commissioning manual contains instructions on how to commission the IED. The manual can also be used by system engineers and maintenance personnel for assistance

during the testing phase. The manual provides procedures for checking of external circuitry and energizing the IED, parameter setting and configuration as well as verifying settings by secondary injection. The manual describes the process of testing an IED in a substation which is not in service. The chapters are organized in chronological order in which the IED should be commissioned.

The operation manual contains instructions on how to operate the IED once it has been commissioned. The manual provides instructions for monitoring, controlling and setting the IED. The manual also describes how to identify disturbances and how to view calculated and measured power grid data to determine the cause of a fault.

The service manual contains instructions on how to service and maintain the IED. The manual also provides procedures for de-energizing, de-commissioning and disposal of the IED.

The application manual contains application descriptions and setting guidelines sorted per function. The manual can be used to find out when and for what purpose a typical protection function can be used. The manual can also be used when calculating settings.

The technical manual contains application and functionality descriptions and lists function blocks, logic diagrams, input and output signals, setting parameters and technical data sorted per function. The manual can be used as a technical reference during the engineering phase, installation and commissioning phase, and during normal service.

The communication protocol manual describes a communication protocol supported by the IED. The manual concentrates on vendor-specific implementations.

The point list manual describes the outlook and properties of the data points specific to the IED. The manual should be used in conjunction with the corresponding communication protocol manual.

1.3.2

Document revision history

Document revision/date	History
-/March 2012	First release
A/June 2012	Minor corrections made

1.3.3

Related documents

Documents related to REC650	Identity number
Application manual	1MRK 511 262-UUS
Technical manual	1MRK 511 263-UUS
Commissioning manual	1MRK 511 264-UUS
Product Guide	1MRK 511 265-BUS
Type test certificate	1MRK 511 265-TUS

650 series manuals	Identity number
Communication protocol manual, DNP3	1MRK 511 257-UUS
Communication protocol manual, IEC 61850–8–1	1MRK 511 258-UUS
Communication protocol manual, IEC 60870-5-103	1MRK 511 259-UUS
Cyber Security deployment guidelines	1MRK 511 268-UUS
Point list manual, DNP3	1MRK 511 260-UUS
Engineering manual	1MRK 511 261-UUS
Operation manual	1MRK 500 095-UUS
Installation manual	1MRK 514 015-UUS

1.4

Symbols and conventions

1.4.1

Symbols



The electrical warning icon indicates the presence of a hazard which could result in electrical shock.



The warning icon indicates the presence of a hazard which could result in personal injury.



The caution icon indicates important information or warning related to the concept discussed in the text. It might indicate the presence of a hazard which could result in corruption of software or damage to equipment or property.



The information icon alerts the reader of important facts and conditions.






The tip icon indicates advice on, for example, how to design your project or how to use a certain function.

Although warning hazards are related to personal injury, it is necessary to understand that under certain operational conditions, operation of damaged equipment may result in degraded process performance leading to personal injury or death. Therefore, comply fully with all warning and caution notices.

1.4.2

Document conventions

A particular convention may not be used in this manual.

- Abbreviations and acronyms in this manual are spelled out in the glossary. The glossary also contains definitions of important terms.
- Push button navigation in the LHMI menu structure is presented by using the push button icons.
To navigate between the options, use  and .
- HMI menu paths are presented in bold.
Select **Main menu/Settings**.
- LHMI messages are shown in Courier font.
To save the changes in non-volatile memory, select Yes and press .
- Parameter names are shown in italics.
The function can be enabled and disabled with the *Operation* setting.
- The ^ character in front of an input or output signal name in the function block symbol given for a function, indicates that the user can set an own signal name in PCM600.
- The * character after an input or output signal name in the function block symbol given for a function, indicates that the signal must be connected to another function block in the application configuration to achieve a valid application configuration.
- Dimensions are provided both in inches and mm. If it is not specifically mentioned then the dimension is in mm.

Section 2 Available functions



Note that not all functions included in the tables below have commissioning information available.

2.1 Back-up protection functions

IEC 61850/ Function block name	ANSI	Function description	Bay			
			REC650	REC650 (A01A) 1CBA	REC650 (A02A) 1CBAB	REC650 (A07A) BCAB
Current protection						
PHPIOC	50	Instantaneous phase overcurrent protection, 3–phase output	0–1	1	1	1
OC4PTOC	51	Four step phase overcurrent protection, 3–phase output	0–1	1	1	1
EFPIOC	50N	Instantaneous residual overcurrent protection	0–1	1	1	1
EF4PTOC	51N/67N	Four step residual overcurrent protection, zero/negative sequence direction	0–1	1	1	
SDEPSDE	67N	Sensitive directional residual overcurrent and power protection	0–1	1	1	1
LCPTTR	26	Thermal overload protection, one time constant, Celsius	0–1	1	1	1
LFPTTR	26	Thermal overload protection, one time constant, Fahrenheit	0–1	1	1	1
CCRBRF	50BF	Breaker failure protection, 3–phase activation and output	0–1	1	1	1
STBPTOC	50STB	Stub protection	0–1	1	1	1
CCRPLD	52PD	Pole discordance protection	0–1	1	1	1
BRCPTOC	46	Broken conductor check	0–1	1	1	1
GUPPDUP	37	Directional underpower protection	0–1	1	1	1
GOPPDOP	32	Directional overpower protection	0–1	1	1	1
DNSPTOC	46	Negative sequence based overcurrent function	0–1	1	1	1

Table continues on next page

IEC 61850/ Function block name	ANSI	Function description	Bay			
			REC650	REC650 (A01A) 1CBA	REC650 (A02A) 1CBAB	REC650 (A07A) BCAB
Voltage protection						
UV2PTUV	27	Two step undervoltage protection	0–1	1	1	1
OV2PTOV	59	Two step overvoltage protection	0–1	1	1	1
ROV2PTOV	59N	Two step residual overvoltage protection	0–1	1	1	1
LOVPTUV	27	Loss of voltage check	0–1	1	1	1
Frequency protection						
SAPTUF	81	Underfrequency function	0–2	2	2	2
SAPTOF	81	Overfrequency function	0–2	2	2	2
SAPFRC	81	Rate-of-change frequency protection	0–2	2	2	2

2.2 Control and monitoring functions

IEC 61850/Function block name	ANSI	Function description	Bay			
			REC650	REC650 (A01A) 1CBA	REC650 (A02A) 1CBAB	REC650 (A07A) BCAB
Control						
SESRSYN	25	Synchrocheck, energizing check, and synchronizing	0–1	1	1	1
SMBRREC	79	Autorecloser for 3–phase operation	0–1	1	1	1
APC8		Apparatus control for single bay, max 8 app. (1CB) incl. interlocking	1	1	1	1
SCILO	3	Logical node for interlocking	8	8	8	8
BB_ES	3	Interlocking for busbar earthing switch	3	3	3	3
A1A2_BS	3	Interlocking for bus-section breaker	2	2	2	2
A1A2_DC	3	Interlocking for bus-section disconnecter	3	3	3	3
ABC_BC	3	Interlocking for bus-coupler bay	1	1	1	1
BH_CONN	3	Interlocking for 1 1/2 breaker diameter	1	1	1	1
BH_LINE_A	3	Interlocking for 1 1/2 breaker diameter	1	1	1	1

Table continues on next page

IEC 61850/Function block name	ANSI	Function description	Bay			
			REC650	REC650 (A01A) 1CBA	REC650 (A02A) 1CBAB	REC650 (A07A) BCAB
BH_LINE_B	3	Interlocking for 1 1/2 breaker diameter	1	1	1	1
DB_BUS_A	3	Interlocking for double CB bay	1	1	1	1
DB_BUS_B	3	Interlocking for double CB bay	1	1	1	1
DB_LINE	3	Interlocking for double CB bay	1	1	1	1
ABC_LINE	3	Interlocking for line bay	1	1	1	1
AB_TRAFO	3	Interlocking for transformer bay	1	1	1	1
SCSWI		Switch controller	8	8	8	8
SXCBR		Circuit breaker	3	3	3	3
SXSWI		Circuit switch	7	7	7	7
POS_EVAL		Evaluation of position indication	8	8	8	8
SELGGIO		Select release	1	1	1	1
QCBAY		Bay control	1	1	1	1
LOCREM		Handling of LR-switch positions	1	1	1	1
LOCREMCTRL		LHMI control of Permitted Source To Operate (PSTO)	1	1	1	1
SLGGIO		Logic Rotating Switch for function selection and LHMI presentation	15	15	15	15
VSGGIO		Selector mini switch extension	20	20	20	20
DPGGIO		IEC 61850 generic communication I/O functions double point	16	16	16	16
SPC8GGIO		Single point generic control 8 signals	5	5	5	5
AUTOBITS		AutomationBits, command function for DNP3.0	3	3	3	3
I103CMD		Function commands for IEC60870-5-103	1	1	1	1
I103IEDCMD		IED commands for IEC60870-5-103	1	1	1	1
I103USRCMD		Function commands user defined for IEC60870-5-103	4	4	4	4
I103GENCMD		Function commands generic for IEC60870-5-103	50	50	50	50
I103POSCMD		IED commands with position and select for IEC60870-5-103	50	50	50	50
Secondary system supervision						
CCSRDIF	87	Current circuit supervision	0–1	1	1	1
SDDRFUF		Fuse failure supervision	0–1	1	1	1
TCSSCBR		Breaker close/trip circuit monitoring	3	3	3	3
Logic						

Table continues on next page

Section 2

Available functions

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IEC 61850/Function block name	ANSI	Function description	Bay			
			REC650	REC650 (A01A) 1CBA	REC650 (A02A) 1CBAB	REC650 (A07A) BCAB
SMPPTRC	94	Tripping logic, common 3-phase output	1	1	1	1
TMAGGIO		Trip matrix logic	12	12	12	12
OR		Configurable logic blocks, OR gate	283	283	283	283
INVERTER		Configurable logic blocks, Inverter gate	140	140	140	140
PULSETIMER		Configurable logic blocks, Pulse timer	40	40	40	40
GATE		Configurable logic blocks, Controllable gate	40	40	40	40
XOR		Configurable logic blocks, exclusive OR gate	40	40	40	40
LOOPDELAY		Configurable logic blocks, loop delay	40	40	40	40
TIMERSET		Configurable logic blocks, timer function block	40	40	40	40
AND		Configurable logic blocks, AND gate	280	280	280	280
SRMEMORY		Configurable logic blocks, set-reset memory flip-flop gate	40	40	40	40
RSMEMORY		Configurable logic blocks, reset-set memory flip-flop gate	40	40	40	40
ANDQT		Configurable logic Q/T, AND gate with quality and time	120	120	120	120
ORQT		Configurable logic Q/T, OR gate with quality and time	120	120	120	120
INVERTERQT		Configurable logic Q/T, inverter gate with quality and time	120	120	120	120
XORQT		Configurable logic Q/T, exclusive OR gate with quality and time	40	40	40	40
SRMEMORYQT		Configurable logic Q/T, set-reset with memory flip-flop gate with quality and time	40	40	40	40
RSMEMORYQT		Configurable logic Q/T, reset-set with memory flip-flop gate with quality and time	40	40	40	40
TIMERSETQT		Configurable logic Q/T, timer function block with quality and time	40	40	40	40
PULSETIMERQT		Configurable logic Q/T, pulse timer with quality and time	40	40	40	40
INVALIDQT		Configurable logic Q/T, used for invalidate data	12	12	12	12
INDCOMBSPQT		Configurable logic Q/T, single point indication logic signal combinator combining value with quality and time	20	20	20	20
INDEXTSPQT		Configurable logic Q/T, single point indication logic signal gate extracting value with quality and time	20	20	20	20

Table continues on next page

IEC 61850/Function block name	ANSI	Function description	Bay			
			REC650	REC650 (A01A) 1CBA	REC650 (A02A) 1CBAB	REC650 (A07A) BCAB
Q/T		Configurable logic blocks Q/T	0–1	20	20	20
FXDSIGN		Fixed signal function block	1	1	1	1
B16I		Boolean 16 to Integer conversion	16	16	16	16
B16IFCVI		Boolean 16 to Integer conversion with logic node representation	16	16	16	16
IB16A		Integer to Boolean 16 conversion	16	16	16	16
IB16FCVB		Integer to Boolean 16 conversion with logic node representation	16	16	16	16
Monitoring						
CVMMXN		Measurements	6	6	6	6
CMMXU		Phase current measurement	10	10	10	10
VMMXU		Phase-phase voltage measurement	6	6	6	6
CMSQI		Current sequence component measurement	6	6	6	6
VMSQI		Voltage sequence measurement	6	6	6	6
VNMMXU		Phase-neutral voltage measurement	6	6	6	6
AISVBAS		Function block for service values presentation of the analog inputs	1	1	1	1
TM_P_P2		Function block for service values presentation of primary analog inputs 600TRM	1	1	1	1
AM_P_P4		Function block for service values presentation of primary analog inputs 600AIM	1	1	1	1
TM_S_P2		Function block for service values presentation of secondary analog inputs 600TRM	1	1	1	1
AM_S_P4		Function block for service values presentation of secondary analog inputs 600AIM	1	1	1	1
CNTGGIO		Event counter	5	5	5	5
DRPRDRE		Disturbance report	1	1	1	1
AxRADR		Analog input signals	4	4	4	4
BxRBDR		Binary input signals	6	6	6	6
SPGGIO		IEC 61850 generic communication I/O functions	64	64	64	64
SP16GGIO		IEC 61850 generic communication I/O functions 16 inputs	16	16	16	16
MVGGIO		IEC 61850 generic communication I/O functions	16	16	16	16
MVEXP		Measured value expander block	66	66	66	66
LMBRFLO		Fault locator	1	1	1	

Table continues on next page

IEC 61850/Function block name	ANSI	Function description	Bay			
			REC650	REC650 (A01A) 1CBA	REC650 (A02A) 1CBAB	REC650 (A07A) BCAB
SPVNZBAT		Station battery supervision	0–1	1	1	1
SSIMG	63	Insulation gas monitoring function	0–1	1	1	1
SSIML	71	Insulation liquid monitoring function	0–1	1	1	1
SSCBR		Circuit breaker condition monitoring	0–1	1	1	1
I103MEAS		Measurands for IEC60870-5-103	1	1	1	1
I103MEASUSR		Measurands user defined signals for IEC60870-5-103	3	3	3	3
I103AR		Function status auto-recloser for IEC60870-5-103	1	1	1	1
I103EF		Function status ground-fault for IEC60870-5-103	1	1	1	1
I103FLTPROT		Function status fault protection for IEC60870-5-103	1	1	1	1
I103IED		IED status for IEC60870-5-103	1	1	1	1
I103SUPERV		Supervision status for IEC60870-5-103	1	1	1	1
I103USRDEF		Status for user defined signals for IEC60870-5-103	20	20	20	20
Metering						
PCGGIO		Pulse counter logic	16	16	16	16
ETPMTR		Function for energy calculation and demand handling	3	3	3	3

2.3 Communication

IEC 61850/Function block name	ANSI	Function description	Bay			
			REC650	REC650 (A01A) 1CBA	REC650 (A02A) 1CBAB	REC650 (A07A) BCAB
Station communication						
IEC61850-8-1		IEC 61850 communication protocol	1	1	1	1
DNPGEN		DNP3.0 for TCP/IP communication protocol	1	1	1	1
RS485DNP		DNP3.0 for EIA-485 communication protocol	1	1	1	1
CH1TCP		DNP3.0 for TCP/IP communication protocol	1	1	1	1

Table continues on next page

IEC 61850/Function block name	ANSI	Function description	Bay			
			REC650	REC650 (A01A) 1CBA	REC650 (A02A) 1CBAB	REC650 (A07A) BCAB
CH2TCP		DNP3.0 for TCP/IP communication protocol	1	1	1	1
CH3TCP		DNP3.0 for TCP/IP communication protocol	1	1	1	1
CH4TCP		DNP3.0 for TCP/IP communication protocol	1	1	1	1
OPTICALDNP		DNP3.0 for optical serial communication	1	1	1	1
MSTSERIAL		DNP3.0 for serial communication protocol	1	1	1	1
MST1TCP		DNP3.0 for TCP/IP communication protocol	1	1	1	1
MST2TCP		DNP3.0 for TCP/IP communication protocol	1	1	1	1
MST3TCP		DNP3.0 for TCP/IP communication protocol	1	1	1	1
MST4TCP		DNP3.0 for TCP/IP communication protocol	1	1	1	1
RS485GEN		RS485	1	1	1	1
OPTICALPROT		Operation selection for optical serial	1	1	1	1
RS485PROT		Operation selection for RS485	1	1	1	1
DNPFREC		DNP3.0 fault records for TCP/IP communication protocol	1	1	1	1
OPTICAL103		IEC60870-5-103 Optical serial communication	1	1	1	1
RS485103		IEC60870-5-103 serial communication for RS485	1	1	1	1
GOOSEINTLKRCV		Horizontal communication via GOOSE for interlocking	59	59	59	59
GOOSEBINRCV		GOOSE binary receive	4	4	4	4
ETHFRNT ETHLAN1 GATEWAY		Ethernet configuration of front port, LAN1 port and gateway	1	1	1	1
GOOSEDPRCV		GOOSE function block to receive a double point value	32	32	32	32
GOOSEINTRCV		GOOSE function block to receive an integer value	32	32	32	32
GOOSEMVRCV		GOOSE function block to receive a measurand value	16	16	16	16
GOOSESPRCV		GOOSE function block to receive a single point value	64	64	64	64

2.4 Basic IED functions

IEC 61850/Function block name	Function description	
Basic functions included in all products		
INTERRSIG	Self supervision with internal event list	1
SELSUPEVLST	Self supervision with internal event list	1
TIMESYNCHGEN	Time synchronization	1
SNTP	Time synchronization	1
DTSBEGIN, DTSEND, TIMEZONE	Time synchronization, daylight saving	1
IRIG-B	Time synchronization	1
SETGRPS	Setting group handling	1
ACTVGRP	Parameter setting groups	1
TESTMODE	Test mode functionality	1
CHNGLCK	Change lock function	1
TERMINALID	IED identifiers	1
PRODINF	Product information	1
SYSTEMTIME	System time	1
RUNTIME	IED Runtime comp	1
PRIMVAL	Primary system values	1
SMAI_20_1 - SMAI_20_12	Signal matrix for analog inputs	2
3PHSUM	Summation block 3 phase	12
GBASVAL	Global base values for settings	6
ATHSTAT	Authority status	1
ATHCHCK	Authority check	1
SPACOMMMAP	SPA communication mapping	1
FTPACCS	FTP access with password	1
DOSFRNT	Denial of service, frame rate control for front port	1
DOSLAN1	Denial of service, frame rate control for LAN1	1
DOSSCKT	Denial of service, socket flow control	1
SAFEFILECOPY	Safe file copy function	1
SPATD	Date and time via SPA protocol	1
BCSCONF	Basic communication system	1

Section 3 Starting up

3.1 Factory and site acceptance testing

Testing the proper IED operation is carried out at different occasions, for example:

- Acceptance testing
- Commissioning testing
- Maintenance testing

This manual describes the workflow and the steps to carry out the commissioning testing.

Factory acceptance testing (FAT) is typically done to verify that the IED and its corresponding configuration meet the requirements of the utility or industry. This test is the most complex and in depth, as it is done to familiarize the user with a new product or to verify a new configuration. The complexity of this testing depends on several factors, such as:

- New IED type
- New configuration
- Modified configuration

Site acceptance testing (SAT or commissioning testing) is typically done to verify that the installed IED is correctly set and connected to the power system. SAT requires that the acceptance testing has been performed and that the application configuration is verified.

Maintenance testing is a periodic verification that the IED is healthy and has correct settings, depending on changes in the power system. There are also other types of maintenance testing.

3.2 Commissioning checklist

Before starting up commissioning at site, check that the following items are available.

- Single line diagram
- Protection block diagram
- Circuit diagram
- Setting list and configuration

-
- RJ-45 Ethernet cable (CAT 5)
 - Three-phase test kit or other test equipment depending on the complexity of the configuration and functions to be tested.
 - PC with PCM600 installed along with the connectivity packages corresponding to the IEDs to be tested.
 - Administration rights on the PC, to set up IP addresses
 - Product documentation (engineering manual, installation manual, commissioning manual, operation manual, technical manual and communication protocol manual)

3.3 Checking the power supply

Check that the auxiliary supply voltage remains within the permissible input voltage range under all operating conditions. Check that the polarity is correct before powering the IED.

3.4 Energizing the IED

3.4.1 Checking the IED operation

Check all connections to external circuitry to ensure correct installation, before energizing the IED and carrying out the commissioning procedures.

Energize the power supply of the IED to pickup. This could be done in a number of ways, from energizing a whole cubicle to energizing a single IED. Set the IED time if no time synchronization source is configured. Check the self-supervision function in **Main menu/Diagnostics/Internal events** or **Main menu/Diagnostics/IED status/General** menu in local HMI to verify that the IED is functioning properly.

3.4.2 IED start-up sequence

The following sequence is expected when the IED is energized.

- The green Ready LED picks up flashing instantly and the ABB logo is shown on the LCD.
- After approximately 30 seconds, "Starting" is shown on the LCD.
- Within 90 seconds, the main menu is shown on the LCD and the green Ready LED shows a steady light, which indicates a successful pick up.



The pickup time depends on the size of the application configuration. Application configurations with less functionality have shorter pickup times.

If the green Ready LED continues to flash after pickup, the IED has detected an internal error. Navigate via **Main menu/Diagnostics/IED status/General** to investigate the error description.

3.5 Setting up communication between PCM600 and the IED

The communication between the IED and PCM600 is independent of the communication protocol used within the substation or to the NCC.

The communication media is always Ethernet and the used protocol is TCP/IP.

Each IED has an RJ-45 Ethernet interface connector on the front and on the rear side. The Ethernet connector can be used for communication with PCM600.

When an Ethernet-based station protocol is used, PCM600 communication can use the same Ethernet port and IP address.

To connect PCM600 to the IED, two basic variants must be considered.

- Direct point-to-point link between PCM600 and the IED front port.
- Indirect link via a station LAN or from remote via a network.

The physical connection and the IP address must be configured in both cases to enable communication.

The communication procedures are the same in both cases.

1. If needed, set the IP address for the IEDs.
2. Set up the PC or workstation for a direct link (point-to-point), or
3. Connect the PC or workstation to the LAN/WAN network.
4. Configure the IED IP addresses in the PCM600 project for each IED to match the IP addresses of the physical IEDs.

Setting up IP addresses

The IP address and the corresponding mask must be set via the LHMI for each available Ethernet interface in the IED. Each Ethernet interface has a default factory IP

address when the IED is delivered. This is not given when an additional Ethernet interface is installed or an interface is replaced.

- The default IP address for the IED front port is 10.1.150.3 and the corresponding subnetwork mask is 255.255.255.0, which can be set via the local HMI path **Main menu/Configuration/Communication/TCP-IP configuration/1:ETHFRNT**.
- The default IP address for the IED rear port is 192.168.1.10 and the corresponding subnetwork mask is 255.255.255.0, which can be set via the local HMI path **Main menu/Configuration/Communication/TCP-IP configuration/1:ETHLAN1 and Rear OEM - port CD**.



The front and rear port IP addresses cannot belong to the same subnet or communication will fail. It is recommended to change the IP address of the front port, if the front and rear port are set to the same subnet.

Setting up the PC or workstation for point-to-point access to IEDs front port

A special cable is needed to connect two physical Ethernet interfaces together without a hub, router, bridge or switch in between. The Tx and Rx signal wires must be crossed in the cable to connect Tx with Rx on the other side and vice versa. These cables are known as cross over cables. The maximum length should be about 2 m. The connector type is RJ-45.

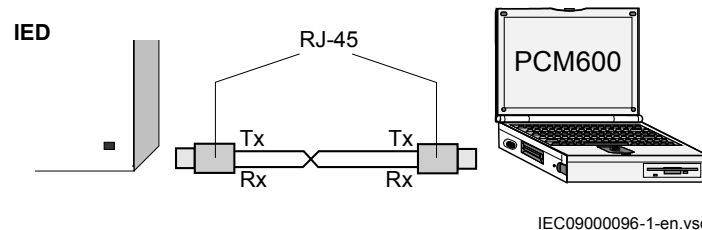


Figure 2: Point-to-point link between IED and PCM600 using a null-modem cable

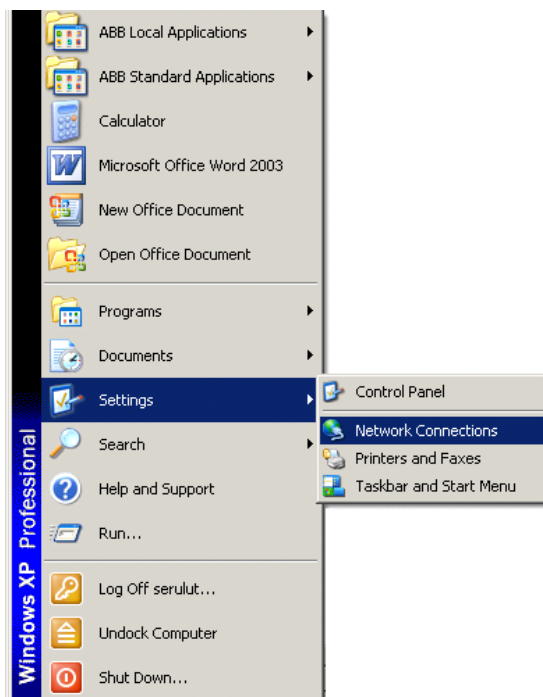
The following description is an example valid for standard PCs using Microsoft Windows operating system. The example is taken from a Laptop with one Ethernet interface.



Administrator rights are required to change the PC communication setup. Some PCs have the feature to automatically detect that Tx signals from the IED are received on the Tx pin on the PC. Thus, a straight (standard) Ethernet cable can be used.

When a PC is connected to the IED and the setting *DHCP*Server is set to *Enabled* via the local HMI path **Main menu/Configuration/Communication/TCP-IP configuration/1:ETHFRNT/DHCP**Server, the IEDs DHCP server for the front port assigns an IP address for the PC. The PC must be configured to obtain its IP address automatically as described in the following procedure.

1. Select **Network Connections** in the PC.



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Figure 3: *Select: Network connections*

2. Select **Properties** in the status window.

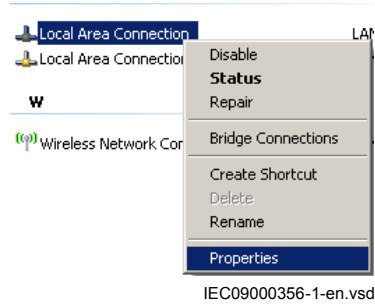


Figure 4: Right-click Local Area Connection and select Properties

3. Select the TCP/IP protocol from the list of configured components using this connection and click **Properties**.

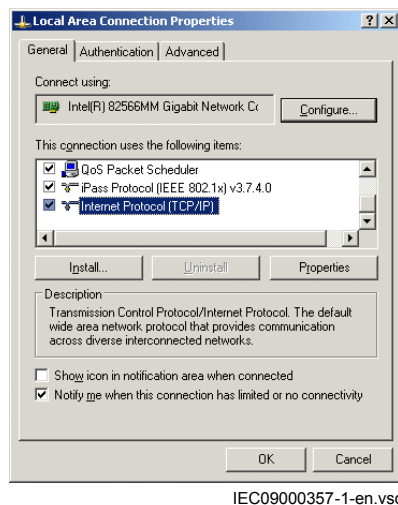


Figure 5: Select the TCP/IP protocol and open Properties

4. Select **Obtain an IP address automatically** if the parameter *DHCP*Server is set to *Enabled* in the IED.

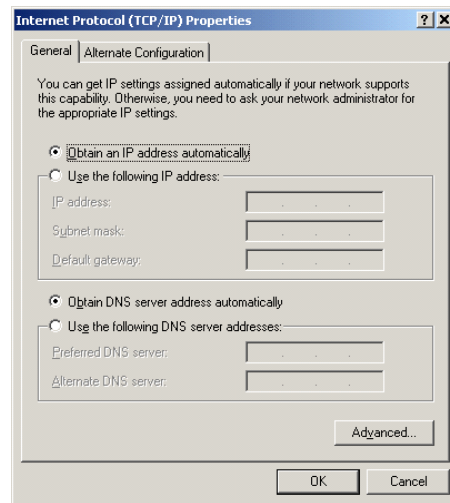
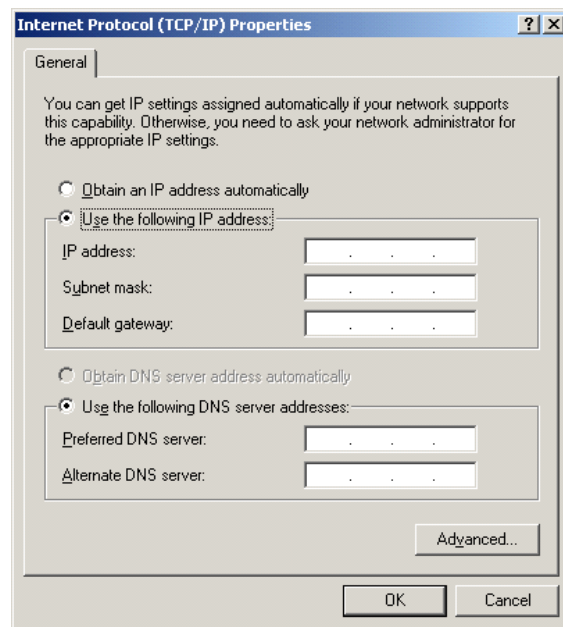


Figure 6: Select: Obtain an IP address automatically

5. Select **Use the following IP address** and define *IP address* and *Subnet mask* if the front port is used and if the *IP address* is not set to be obtained automatically by the IED, see [Figure 7](#). The IP address must be different from the IP address chosen for the IED.



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Figure 7: Select: Use the following IP address

6. Close all open windows and start PCM600.

Setting up the PC to access the IED via a network

This task depends on the used LAN/WAN network.



The PC and IED must belong to the same subnetwork for this set-up to work.

3.6

Writing an application configuration to the IED



Ensure that the IED includes the correct application configuration according to project specifications.

The application configuration is created using PCM600 and then written to the IED. Establish a connection between PCM600 and the IED when an application configuration must be written to the IED.

After writing an application configuration to the IED, the IED makes an application restart or a complete IED restart, when necessary.



The IED does not restart after reconfiguring IEC61850 (regardless of whether the protocol is enabled or disabled).



Be sure to set the correct technical key in both the IED and PCM600 to prevent writing an application configuration to a wrong IED.



See the engineering manual for information on how to create or modify an application configuration and how to write to the IED.

3.7 Checking CT circuits



Check that the wiring is in strict accordance with the supplied connection diagram.

- Primary injection test to verify the current ratio of the CT, the correct wiring up to the protection IED and correct phase sequence connection (that is A, B, C.)
- CT secondary loop resistance measurement to confirm that the current transformer secondary loop DC resistance is within specification and that there are no high resistance joints in the CT winding or wiring.
- Grounding check of the individual CT secondary circuits to verify that each three-phase set of main CTs is properly connected to the station ground and only at one electrical point.
- Insulation resistance check.



CT and VT connectors are pre-coded, and the CT and VT connector markings are different. For more information, see the installation manual.



Both the primary and the secondary sides must be disconnected from the line and the IED when plotting the excitation characteristics.

3.8 Checking VT circuits

Check that the wiring is in strict accordance with the supplied connection diagram.



Correct possible errors before continuing to test the circuitry.

Test the circuitry.

- Polarity check
- VT circuit voltage measurement (primary injection test)
- Grounding check
- Phase relationship
- Insulation resistance check

The polarity check verifies the integrity of circuits and the phase relationships. The check must be performed as close to the IED as possible.

The primary injection test verifies the VT ratio and the wiring all the way from the primary system to the IED. Injection must be performed for each phase-to-neutral circuit and each phase-to-phase pair. In each case, voltages in all phases and neutral are measured.

3.9 Checking the RTXP test switch

The RTXP test switch is designed to provide the means of safe testing of the IED. This is achieved by the electromechanical design of the test switch and test plug handle. When the test plug handle is inserted, it first blocks the trip and alarm circuits then it short circuits the CT secondary circuit and opens the VT secondary circuits making the IED available for secondary injection.

When pulled out, the test handle is mechanically stopped in half withdrawn position. In this position, the current and voltage enter the protection, but the alarm and trip circuits are still isolated. Before removing the test handle, check that no trip or alarms are present in the IED.

Not until the test handle is completely removed, the trip and alarm circuits are restored for operation.



By pulling in all cables, verify that the contact sockets have been crimped correctly and that they are fully inserted. Never do this with current circuits in service.

Current circuit

1. Verify that the contacts are of current circuit type.
2. Verify that the short circuit jumpers are located in the correct slots.

Voltage circuit

1. Verify that the contacts are of voltage circuit type.
2. Check that no short circuit jumpers are located in the slots dedicated for voltage.

Trip and alarm circuits

1. Check that the correct types of contacts are used.

3.10 Checking binary input and output circuits

3.10.1 Binary input circuits

Preferably, disconnect the binary input connector from the binary input cards. Check all connected signals so that both input level and polarity are in accordance with the IED specifications.

3.10.2 Binary output circuits

Preferably, disconnect the binary output connector from the binary output cards. Check all connected signals so that both load and polarity are in accordance with IED specifications.

3.11 Checking optical connections

Check that the Tx and Rx optical connections are correct.



An IED equipped with optical connections requires a minimum depth of 180 mm (7.2 inches) for plastic fiber cables and 275 mm (10.9

inches) for glass fiber cables. Check the allowed minimum bending radius from the optical cable manufacturer.

Section 4 Establishing connection and verifying the IEC 61850 station communication

4.1 Setting the station communication

To enable IEC 61850 station communication:

- The IEC 61850-8-1 station communication functionality must be on in the local HMI. Navigate to **Main menu/Configuration/Communication/Station communication/1:IEC61850-8-1** and set the *Operation* parameter to *Enabled*.
- To enable GOOSE communication the *Operation* parameter for the corresponding GOOSE function blocks (GOOSEBINRCV and GOOSEINTLKRCV) must be set to *Enabled* in the application configuration.
- To enable GOOSE communication via the front port the parameter *GOOSE* in **Main menu/Configuration/Communication/Station communication/IEC61850-8-1** must be set to *Front*. To enable GOOSE communication via rear port the parameter *GOOSE* must be set to *LAN1*.

4.2 Verifying the communication

Connect your PC to the substation network and ping the connected IED and the Substation Master PC, to verify that the communication is working (up to the transport layer).

The best way to verify the communication up to the application layer is to use protocol analyzer ITT600 connected to the substation bus, and monitor the communication.

Section 5 Testing IED operation

5.1 Preparing the IED to verify settings

If a test switch is included, start preparation by making the necessary connections to the test switch. This means connecting the test equipment according to a specific and designated IED terminal diagram.

Put the IED into the test mode to facilitate the test of individual functions and prevent unwanted operation caused by other functions. The test switch should then be connected to the IED.

Verify that analog input signals from the analog input module are measured and recorded correctly by injecting currents and voltages required by the specific IED.

To make testing even more effective, use PCM600. PCM600 includes the Signal monitoring tool, which is useful in reading the individual currents and voltages, their amplitudes and phase angles. In addition, PCM600 contains the Disturbance handling tool. The content of reports generated by the Disturbance handling tool can be configured which makes the work more efficient. For example, the tool may be configured to only show time tagged events and to exclude analog information and so on.

Check the disturbance report settings to ensure that the indications are correct.

For test functions and test and signal parameter names, see the technical manual. The correct initiation of the disturbance recorder is made on pickup and/or release or trip from a function. Also check that the wanted recordings of analogue (real and calculated) and binary signals are achieved.

The IEDs in the 650 series can have between 1 and 4 individual parameter setting groups prepared with full sets of different parameters for all functions. The purpose of these groups is to be able to handle different power system load conditions to optimize the parameters settings of the different functions for these different power systems conditions (for example summer/winter and day/night).



Parameters can be entered into different setting groups. Make sure to test functions for the same parameter setting group. If needed, repeat the tests for all different setting groups used. The difference between testing the first parameter setting group and the remaining is that there is no need for testing the connections.

During testing, observe that the right testing method, that corresponds to the actual parameters set in the activated parameter setting group, is used.

In the local HMI the sensitive directional earth fault protection SDEPSDE parameter group 4 is active indicated by the * next to #4 and the test of the SDEPSDE must be performed according to the instructions given for the setting *OpModeSel* and setting value *3I03V0Cosfi*.

Set and configure the function(s) before testing. Most functions are highly flexible and permit a choice of functional and tripping modes. The various modes are checked at the factory as part of the design verification. In certain cases, only modes with a high probability of coming into operation need to be checked when commissioned to verify the configuration and settings.

Requirements for testing the function.

- Calculated settings
- Valid configuration diagram for the IED
- Valid terminal diagram for the IED
- Technical manual
- Three-phase test equipment

Content of the technical manual.

- Application and functionality summaries
- Function blocks
- Logic diagrams
- Input and output signals
- A list of setting parameters
- Technical data for the function

The test equipment should be able to provide a three-phase supply of currents and three-phase voltage. The magnitude and angle of currents (and voltages) should be possible to vary. Check that the IED is prepared for test before starting the test session.

Consider the logic diagram of the function when performing the test.

The response from a test can be viewed in different ways.

- Binary output signals
- Service values in the local HMI (logical signal or phasors)
- A PC with PCM600 (configuration software) in debug mode



Do not switch off the auxiliary power supply to the IED before changes, for example, setting parameter or local/remote control state changes are saved.

A mechanism for limiting the number of writings per time period is included in the IED to prevent the flash memory to be worn out due to too many writings. As a consequence it may take up to an hour to save changes. If the auxiliary power is interrupted before a change is saved, that change is lost.

5.2 Activating test mode

Put the IED into the test mode before testing. The test mode blocks all protection functions and some of the control functions in the IED, and the individual functions to be tested can be unblocked to prevent unwanted operation caused by other functions. In this way, it is possible to test slower back-up measuring functions without the interference from faster measuring functions. Test mode is indicated when the yellow PickupLED flashes. The control functions not blocked when the IED enters test mode are listed in the following table:

Function Group	Function Name	Description
Control	DPGGIO	IEC 61850 generic communication I/O functions double point
Control	QCBAY	Bay control
Control	QCRSV	Bay reserve
Control	SCILO	Logical node for interlocking
Control	SCSWI	Switch controller
Control	SELGGIO	Select release
Control	SXCBR	Circuit breaker
Control	SXSWI	Circuit switch
Control	TCMYLTC	Tap changer control and supervision, 6 binary inputs
Control	VCTRRCV	Voltage control receiving block for GOOSE

Procedure

1. Select **Main menu/Tests/IED test mode/1:TESTMODE**
2. Set parameter *TestMode* to *Enabled*.
3. Save the changes.

As a consequence, the yellow pickupLED starts flashing as a reminder and remains flashing until the test mode is switched off.

5.3 Preparing the connection to the test equipment

The IED can be equipped with a test switch of type RTXP8, RTXP18 or RTXP24 or FT. The test switch and its associated test plug handles are a part of the COMBITEST or FT system of ABB, which provides secure and convenient testing of the IED.

When using the COMBITEST, preparations for testing are automatically carried out in the proper sequence, that is, for example, blocking of tripping circuits, short circuiting of CT's, opening of voltage circuits, making IED terminals available for secondary injection. Terminals 1 and 8, 1 and 18 as well as 1 and 12 of the test switches RTXP8, RTXP18 and RTXP24 respectively are not disconnected as they supply DC power to the protection IED. When FT switch is used for testing, care shall be exercised to open the tripping circuit, ahead of manipulating the CT fingers.

The RTXH test-plug handle leads may be connected to any type of test equipment or instrument. When a number of protection IEDs of the same type are tested, the test-plug handle only needs to be moved from the test switch of one protection IED to the test switch of the other, without altering the previous connections.

Use COMBITEST test system to prevent unwanted tripping when the handle is withdrawn, since latches on the handle secure it in the half withdrawn position. In this position, all voltages and currents are restored and any re-energizing transients are given a chance to decay before the trip circuits are restored. When the latches are released, the handle can be completely withdrawn from the test switch, restoring the trip circuits to the protection IED.

If a test switch is not used, perform measurement according to the provided circuit diagrams.



Never disconnect the secondary connection of a current transformer circuit without first short-circuiting the transformer's secondary winding. Operating a current transformer with the secondary winding open will cause a massive potential build up that may damage the transformer and cause personal injury.

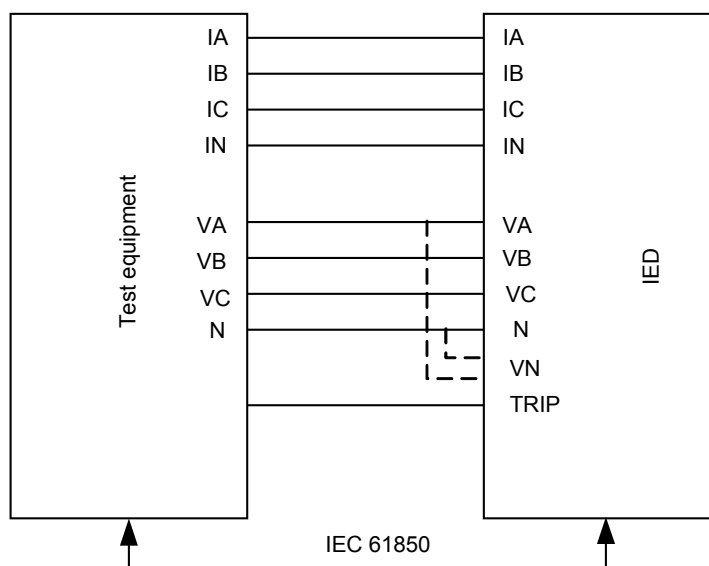
5.4 Connecting the test equipment to the IED

Connect the test equipment according to the IED specific connection diagram and the needed input and output signals for the function under test. An example of a connection is shown in figure [8](#).

Connect the current and voltage terminals. Pay attention to the current polarity. Make sure that the connection of input and output current terminals and the connection of the residual current conductor is correct. Check that the input and output logical signals in the logic diagram for the function under test are connected to the corresponding binary inputs and outputs of the IED under test.



To ensure correct results, make sure that the IED as well as the test equipment are properly grounded before testing.



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Figure 8: Connection example of the test equipment to the IED when test equipment is connected to the transformer input module

5.5 Releasing the function to be tested

Release or unblock the function to be tested. This is done to ensure that only the function or the chain of functions to be tested are in operation and that other functions are prevented from operating. Release the tested function(s) by setting the corresponding *Blocked* parameter under Function test modes to *No* in the local HMI.

When testing a function in this blocking feature, remember that not only the actual function must be activated, but the whole sequence of interconnected functions (from

measuring inputs to binary output contacts), including logic must be activated. Before starting a new test mode session, scroll through every function to ensure that only the function to be tested (and the interconnected ones) have the parameters *Blocked* and eventually *EvDisable* set to *No* and *Yes* respectively. Remember that a function is also blocked if the BLOCK input signal on the corresponding function block is active, which depends on the configuration. Ensure that the logical status of the BLOCK input signal is equal to 0 for the function to be tested. Event function blocks can also be individually blocked to ensure that no events are reported to a remote station during the test. This is done by setting the parameter *EvDisable* to *Yes*.



Any function is blocked if the corresponding setting in the local HMI under **Main menu/Tests/Function test modes** menu remains *Enabled*, that is, the parameter *Blocked* is set to *Yes* and the parameter *TestMode* under **Main menu/Tests/IED test mode** remains active. All functions that were blocked or released in a previous test mode session, that is, the parameter *Test mode* is set to *Enabled*, are reset when a new test mode session is started.

Procedure

1. Click the **Function test modes** menu.
The Function test modes menu is located in the local HMI under **Main menu/Tests/Function test modes**.
2. Browse to the function instance that needs to be released.
3. Set parameter *Blocked* for the selected function to *No*.

5.6 Verifying analog primary and secondary measurement

Verify that the connections are correct and that measuring and scaling is done correctly. This is done by injecting current and voltage to the IED.



Apply input signals as needed according to the actual hardware and the application configuration.

1. Inject a symmetrical three-phase voltage and current at rated value.
2. Compare the injected value with the measured values.

The voltage and current phasor menu in the local HMI is located under **Main menu/Measurements/Analog primary values** and **Main menu/Measurements/Analog secondary values**.

3. Compare the frequency reading with the set frequency and the direction of the power.

The frequency and active power are located under **Main menu/Tests/Function status/Monitoring/CVMMXN/1:CVMMXN/Outputs**. Then navigate to the bottom of the list to find the frequency.



Check both analog primary and secondary values, because then the CT and VT ratios entered into the IED are also checked.

These checks shall be repeated for Analog primary values.

4. Inject an unsymmetrical three-phase voltage and current, to verify that phases are correctly connected.

If some setting deviates, check the analog input settings under

Main menu/Configuration/Analog modules

Measured values such as current and voltages as well as active, reactive and apparent power, power factor phase angles as well as positive and negative and zero sequence currents and voltages are available in the local HMI under **Main menu/Tests/Function status/Monitoring**.

Navigate to the measurement function that contains the quantity to be checked.

Table 1: *Measurement functions*

Function	Quantity	Description
CMMXU	IA to IC	amplitude, range and angle
CMSQI	$3I_0$; I1 and I2	amplitude, range and angle
CVMMXN	S; P; Q; PF; I _{lag} ; I _{lead} ; U; I and f	amplitude, range and angle
VMMXU	VA_C to VC_A i.e. phase-to-phase	amplitude, range and angle
VMSQI	$3U_0$; U1 and U2	amplitude, range and angle
VNMMXU	VA to VC i.e. phase-to-neutral	amplitude, range and angle

Also the Signal Monitoring tool in PCM600 can be used to read the measured values. In many cases it is more convenient to use PCM600 since, among many things, reports on measured values can be exported from the Signal Monitoring tool to other tools (for example, MS Excel) for further analysis.

5.7 Testing protection functionality

Each protection function must be tested individually by secondary injection.

- Verify operating levels (trip) and timers.
- Verify alarm and blocking signals.
- Use the disturbance handling tool in PCM600 to evaluate that the protection function has received the correct data and responded correctly (signaling and timing).
- Use the event viewer tool in PCM600 to check that only expected events have occurred.

Section 6 Testing functionality

6.1 Testing disturbance report

6.1.1 Introduction

The following sub-functions are included in the disturbance report function:

- Disturbance recorder
- Event list
- Event recorder
- Trip value recorder
- Indications

If the disturbance report is enabled, then its sub-functions are also set up and so it is not possible to only disable these sub-functions. The disturbance report function is disabled (parameter *Operation = Disabled*) in PCM600 or the local HMI under **Main menu/Settings/IED Settings/Monitoring/Disturbance report/1:DRPRDRE**.

6.1.2 Disturbance report settings

When the IED is in test mode, the disturbance report can be made active or inactive. If the disturbance recorder is turned on during test mode, recordings will be made. When test mode is switched off all recordings made during the test session are cleared.

Setting *OpModeTest* for the control of the disturbance recorder during test mode are located on the local HMI under **Main menu/Settings/IED Settings/Monitoring/Disturbance report/1:DRPRDRE**.

6.2 Identifying the function to test in the technical reference manual

Use the technical manual to identify function blocks, logic diagrams, input and output signals, setting parameters and technical data.

6.3 Testing current protection functions

6.3.1 Instantaneous phase overcurrent protection 3-phase output PHPIOC (50)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for PHPIOC (50) are available on the local HMI under **Main menu/Tests/Function status/Current/PHPIOC(50,I>>)/1:PHPIOC**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

To verify the settings the following fault type should be tested:

- Phase-to-ground fault

Ensure that the maximum continuous current, supplied from the current source used for the test of the IED, does not exceed four times the rated current value of the IED.

6.3.1.1 Measuring the operate limit of set values

1. Inject a phase current into the IED with an initial value below that of the setting.
2. Increase the injected current until the TRIP signal appears.
3. Switch the fault current off.



Observe: Do not exceed the maximum permitted overloading of the current circuits in the IED.

4. Compare the measured operating current with the set value.

6.3.1.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/PHPIOC(50,I>>)/1:PHPIOC** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.2 Four step phase overcurrent protection 3-phase output OC4PTOC (51_67)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for OC4PTOC (51_67) are available on the local HMI under **Main menu/Tests/Function status/Current/OC4PTOC(51_67,4I>)/1:OC4PTOC**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.3.2.1 Verifying the settings

1. Connect the test set for current injection to the appropriate IED phases.
If there is any configuration logic that is used to enable or block any of the four available overcurrent steps, make sure that the step under test is enabled (for example, end fault protection).
Connect the symmetrical three-phase injection current into phases A, B and C.
2. Connect the test set for the appropriate three-phase voltage injection to the IED phases A, B and C. The protection shall be fed with a symmetrical three-phase voltage.
3. Set the injected polarizing voltage slightly larger than the set minimum polarizing voltage (default is 5% of V_{Base}) and set the injection current to lag the appropriate voltage by an angle of 55° if forward directional function is selected. If *1 out of 3* currents for operation is chosen: The voltage angle of phase A is the reference.
If reverse directional function is selected, set the injection current to lag the polarizing voltage by an angle equal to 235° (equal to $55^\circ + 180^\circ$).
4. Increase the injected current and note the operate value of the tested step of the function.
5. Decrease the current slowly and note the reset value.
6. If the test has been performed by injection of current in phase A, repeat the test, injecting current into phases B and C with polarizing voltage connected to phases B, respectively C (*1 out of 3* currents for operation).
7. If the test has been performed by injection of current in phases AB, repeat the test, injecting current into phases BC and CA with the appropriate phase angle of injected currents.
8. Block higher set stages when testing lower set stages by following the procedure described below.
9. Connect a trip output contact to a timer.
10. Set the injected current to 200% of the operate level of the tested stage, switch on the current and check the time delay.

For inverse time curves, check the operate time at a current equal to 110% of the operate current for *txMin*.

11. Check that all trip and pickup contacts operate according to the configuration (signal matrixes)
12. Reverse the direction of the injected current and check that the protection does not operate.
13. Repeat the above described tests for the higher set stages.
14. Finally check that pickup and trip information is stored in the event menu.



Verification of the non-directional phase overcurrent function is done as instructed above, without applying any polarizing voltage.

6.3.2.2

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/OC4PTOC(51_67,4I>)/1:OC4PTOC** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.3

Instantaneous residual overcurrent protection EFPIOC (50N)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for EFPIOC (50N) are available on the local HMI under **Main menu/Tests/Function status/Current/EFPIOC(50N,IN>>)/1:EFPIOC**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

To verify the settings the following fault type should be tested:

- Phase-to-ground fault

Ensure that the maximum continuous current, supplied from the current source used for the test of the IED, does not exceed four times the rated current value of the IED.

6.3.3.1

Measuring the operate limit of set values

1. Inject a phase current into the IED with an initial value below that of the setting.
2. Increase the injected current in the Ln or in the neutral (summed current input) phase until the TRIP signal appears.
3. Disable the fault current
Observe to not exceed the maximum permitted overloading of the current circuits in the IED
4. Compare the measured operating current with the set value.

6.3.3.2

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/EFPIOC(50N,IN>>)/1:EFPIOC** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.4

Four step residual overcurrent protection, zero or negative sequence direction EF4PTOC (51N/67N)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for EF4PTOC (51N/67N) are available on the local HMI under **Main menu/Tests/Function status/Current/EF4PTOC(51N/67N,4IN>)/X:EF4PTOC**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.3.4.1

Four step directional residual overcurrent protection

1. Connect the test set for single current injection to the appropriate IED terminals. Connect the injection current and voltage to terminals A and neutral.
2. Set the injected polarizing voltage slightly larger than the set minimum polarizing voltage (default 1% of V_n) and set the injection current to lag the voltage by an angle equal to the set reference characteristic angle (*AngleRCA*), if the forward directional function is selected.
If reverse directional function is selected, set the injection current to lag the polarizing voltage by an angle equal to $RCA + 180^\circ$.
3. Increase the injected current and note the value at which the studied step of the function operates.
4. Decrease the current slowly and note the reset value.

5. If the test has been performed by injection of current in phase A, repeat the test, injecting current into terminals B and C with a polarizing voltage connected to terminals B, respectively C.
6. Block lower set steps when testing higher set steps according to the instructions that follow.
7. Connect a trip output contact to a timer.
8. Set the injected current to 200% of the operate level of the tested step, switch on the current and check the time delay.
For inverse time curves, check the operate time at a current equal to 110% of the operate current for *txMin*.
9. Check that all trip and trip contacts operate according to the configuration (signal matrixes)
10. Reverse the direction of the injected current and check that the step does not operate.
11. Check that the protection does not operate when the polarizing voltage is zero.
12. Repeat the above described tests for the higher set steps.
13. Finally, check that pickup and trip information is stored in the event menu.

6.3.4.2 Four step non-directional residual overcurrent protection

1. Do as described in ["Four step directional residual overcurrent protection"](#), but without applying any polarizing voltage.

6.3.4.3 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/EF4PTOC(51N67N,4IN>)/X:EF4PTOC** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.5 Sensitive directional residual overcurrent and power protection SDEPSDE (67N)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for SDEPSDE (67N) are available on the local HMI under **Main menu/Tests/Function status/Current/SDEPSDE(67N,IN<->)/1:SDEPSDE**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.3.5.1

Measuring the operate and time limit for set values

Operation mode $3I_0 \cdot \cos\varphi$

Procedure

1. Set the polarizing voltage to $1.2 \cdot VNRelPU$ and the phase angle between voltage and current to the set characteristic angle (*RCADir*), the current lagging the voltage. Take setting *RCAComp* into consideration if not equal to 0.
2. Measure that the operate current of the set directional element is equal to the *INcosPhiPU* setting.
The I Dir ($I_0 \cos(\text{Angle})$) function activates the BFI_3P and STDIRIN output.
3. Measure with angles $\varphi = RCADir \pm 45^\circ$ that the measuring element operates when $I_0 \cos(RCADir - \varphi) = I_0 \cos(\pm 45) = INcosPhiPU$.
4. Compare the result with the set value.
Take the set characteristic into consideration, see figure 9 and figure 10.
5. Measure the operate time of the timer by injecting a current two times the set *INcosPhiPU* value and the polarizing voltage $1.2 \cdot VNRelPU$.

$$T_{inv} = kSN \cdot Sref / 3I_{0test} \cdot \cos(\varphi)$$

(Equation 1)

6. Compare the result with the expected value.
The expected value depends on whether definite or inverse time was selected.
7. Set the polarizing voltage to zero and increase until the boolean output signal VNREL is activated, which is visible in the Application Configuration in PCM600 when the IED is in online mode. Compare the voltage with the set value *VNRelPU*.
8. Continue to test another function or complete the test by setting the test mode to *Disabled*.

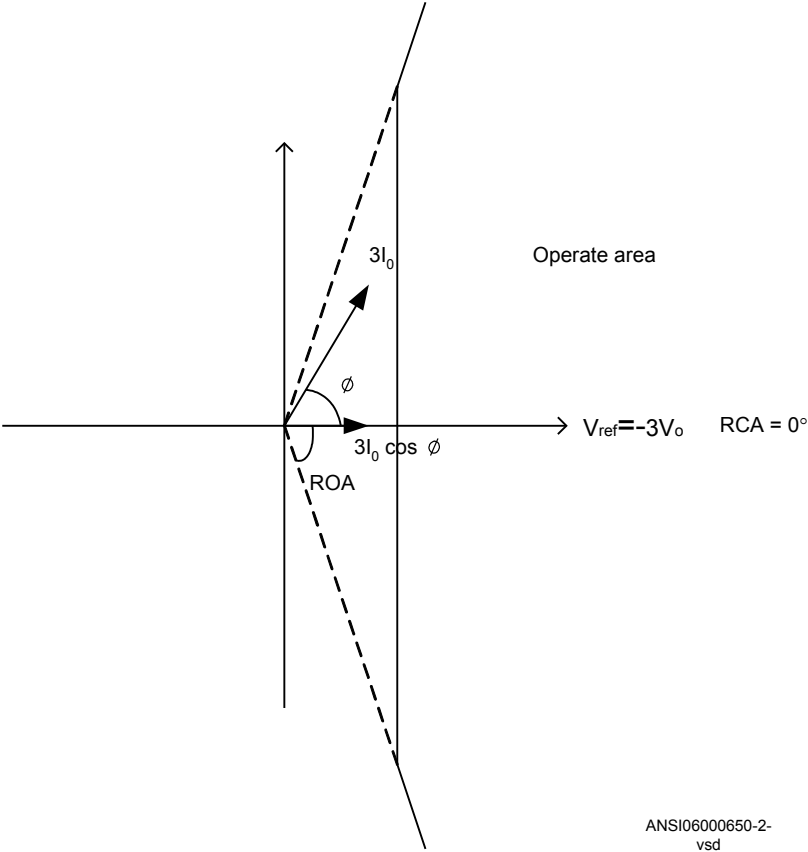


Figure 9: Characteristic with ROADir restriction

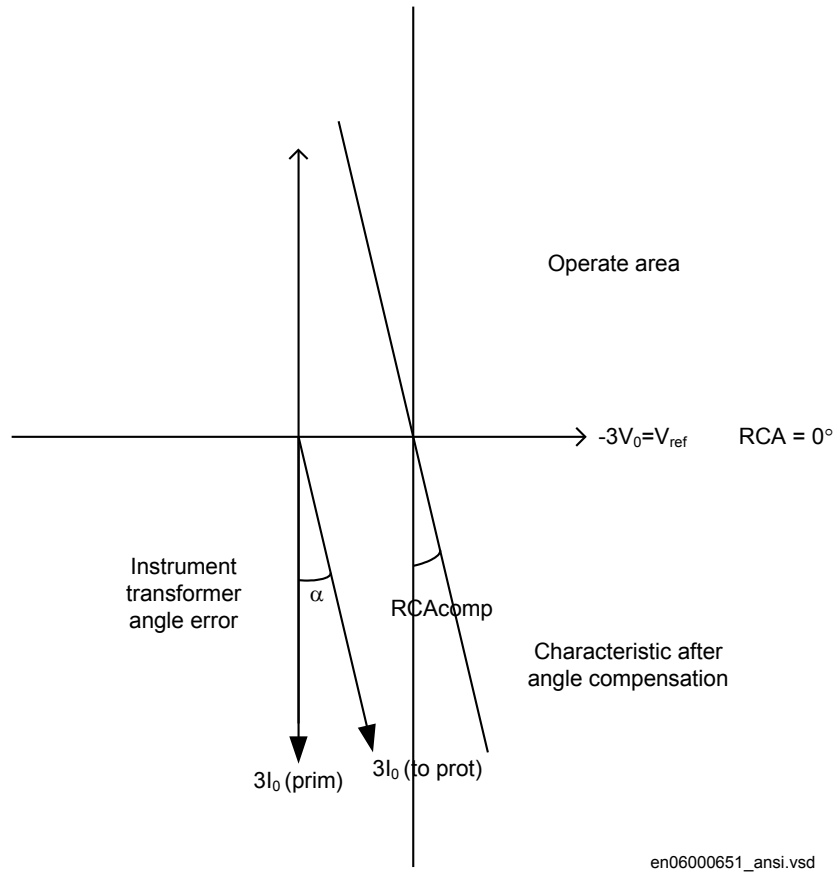


Figure 10: Explanation of RCAcomp

Operation mode $3I_0 \cdot 3V_0 \cdot \cos\phi$

1. Set the polarizing voltage to $1.2 \cdot VNRelPU$ and the phase angle between voltage and current to the set characteristic angle ($RCADir$), the current lagging the voltage.
2. Measure that the operate power is equal to the SN_PU setting for the set directional element.
Note that for pick-up, both the injected current and voltage must be greater than the set values $INRelPU$ and $VNRelPU$ respectively.
The function activates the BFI_3P and STDIRIN outputs.
3. Measure with angles $\phi = RCADir \pm 45^\circ$ that the measuring element operates when $3I_0 \cdot 3V_0 \cdot \cos(RCADir - \phi) = 3I_0 \cdot 3V_0 \cdot \cos(\pm 45) = SN_PU$.
4. Compare the result with the set value. Take the set characteristic into consideration, see figure 9 and figure 10.

5. Measure the operate time of the timer by injecting $1.2 \cdot VNRelPU$ and a current to get two times the set SN_PU operate value.

$$T_{inv} = kSN \cdot S_{ref} / 3I_{0test} \cdot 3V_{0test} \cdot \cos(\varphi)$$

(Equation 2)

6. Compare the result with the expected value.
The expected value depends on whether definite or inverse time was selected.
7. Continue to test another function or complete the test by setting the test mode to *Disabled*.

Operation mode $3I_0$ and φ

1. Set the polarizing voltage to $1.2 \cdot VNRelPU$ and the phase angle between voltage and current to the set characteristic angle ($RCADir$), the current lagging the voltage.
2. Measure that the operate power is equal to the $INRelPU$ setting for the set directional element.



Note that for pickup, both the injected current and voltage must be greater than the set values $INRelPU$ and $VNRelPU$ respectively.

The function activates the BFI_3P and STDIRIN output.

3. Measure with angles φ around $RCADir$ +/- $ROADir$.
4. Compare the result with the set values, refer to figure 11 for example characteristic.
5. Measure the operate time of the timer by injecting a current to get two times the set SN_PU operate value.

$$T_{inv} = kSN \cdot S_{ref} / 3I_{0test} \cdot 3V_{0test} \cdot \cos(\varphi)$$

(Equation 3)

6. Compare the result with the expected value.
The expected value depends on whether definite or inverse time was selected.
7. Continue to test another function or complete the test by setting the test mode to *Disabled*.

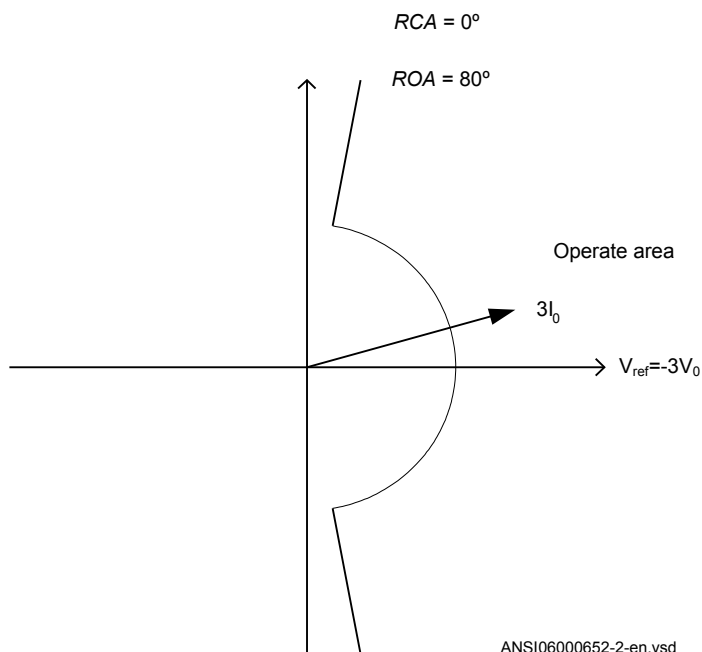


Figure 11: Example characteristic

Non-directional ground fault current protection

Procedure

1. Measure that the operate current is equal to the *INNonDirPU* setting.
The function activates the BFI_3P and STDIRIN output.
2. Measure the operate time of the timer by injecting a current of 200% of the operate value.
3. Compare the result with the expected value.
The expected value depends on whether definite time *tINNonDir* or inverse time was selected.
4. Continue to test another function or complete the test by setting the test mode to *Disabled*.

Residual overvoltage release and protection

Procedure

1. Measure that the operate voltage is equal to the *VN_PU* setting.
The function activates the BFI_3P and STUN signals.
2. Measure the operate time by injecting a voltage 1.2 times set *VN_PU* operate value.
3. Compare the result with the set *tVN* operate value.

4. Inject a voltage $0.8 \cdot V_{NRelPU}$ and a current high enough to operate the directional function at the chosen angle.
5. Increase the voltage until the directional function is released.
6. Compare the measured value with the set V_{NRelPU} operate value.

6.3.5.2

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/SDEPSDE(67N,IN<->)/1:SDEPSDE** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.6

Thermal overload protection, one time constant, Fahrenheit/Celsius LFPTTR/LCPTTR (26)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for LFPTTR/LCPTTR (26) are available on the local HMI under , **Main menu/Tests/Function status/Current/LFPTTR(26,T>)/1:LFPTTR**, **Main menu/Tests/Function status/Current/LCPTTR(26,T>)/1:LCPTTR**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

Check that the input logical signal BLOCK is logical zero and that on the local HMI, the logical signal TRIP, PICKUP and ALARM are equal to logical zero.

6.3.6.1

Testing the protection without external temperature compensation (NonComp)

1. Quickly set the measured current (fault current) in one phase to about 300% of I_{Ref} (to minimise the trip time), and switch the current off.
2. Reset the thermal memory on the local HMI under **Main menu/Clear/Clear temperature constant/LFPTTR(26,8>)/1:LFPTTR**, **Main menu/Clear/Clear temperature constant/LCPTTR(26,8>)/1:LCPTTR**
3. Switch the fault current on and take note of the temperature, available on the local HMI under **Main menu/Tests/Function status/Current/LFPTTR(26,8>)/1:LFPTTR/Outputs/TEMP**, **Main menu/Tests/Function status/Current/LCPTTR(26,8>)/1:LCPTTR/Outputs/TEMP**
4. Check the time until the alarm limit has reached the *AlarmTemp* level during injection.

-
- Monitor the signal ALARM until it appears on the corresponding binary output or on the local HMI.
 - 5. Compare the measured temperature with the setting.
 - 6. Measure the LFPTTR/LCPTTR (26) protection trip time.
Use the TRIP signal from the configured binary output to stop the timer.
 - 7. Take the TEMP readings.
Compare with the setting of *TripTemp*.
 - 8. Activate the BLOCK binary input.
The signals ALARM, RI and TRIP should disappear.
 - 9. Reset the BLOCK binary input.
 - 10. Check the reset limit (TdReset).
Monitor the signal PICKUP until it disappears on the corresponding binary output or on the local HMI, take the TEMP readings and compare with the setting of *ReclTemp*.
 - 11. Compare the measured trip time with the setting according to the formula.
 - 12. Reset the thermal memory.
 - 13. Continue to test another function or end the test by changing the test mode setting to *Disabled*.

6.3.6.2

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/LFPTTR(26,T>)/1:LFPTTR**, **Main menu/Tests/Function test modes/Current/LCPTTR(26,T>)/1:LCPTTR** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.7

Breaker failure protection, phase segregated activation and output CCRBRF (50BF)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for CCRBRF (50BF) are available on the local HMI under **Main menu/Tests/Function status/Current/CCRBRF(50BF)/1:CCRBRF**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

The Breaker failure protection, 3-phase activation and output function CCRBRF (50BF) should normally be tested in conjunction with some other function that provides an initiate signal. An external INITIATE signal can also be used.

To verify the settings in the most common back-up trip mode *1 out of 3*, it is sufficient to test phase-to-ground faults.

At mode *2 out of 4* the phase current setting, *Pickup_PH* can be checked by single-phase injection where the return current is connected to the summated current input. The value of residual (ground fault) current *IN* set lower than *Pickup_PH* is easiest checked in back-up trip mode *1 out of 4*.

6.3.7.1

Checking the phase current operate value, *Pickup_PH*

Check the current level *IP* > where setting *FunctionMode=Current* and setting *BuTripMode=1 out of 3* or *2 out of 4* as set under **Main menu/Settings/IED Settings/Current/CCRBRF(50BF)/1:CCRBRF**.

1. Apply the fault condition, including INITIATION of CCRBRF (50BF), with a current below set *Pickup_PH*.
2. Repeat the fault condition and increase the current in steps until a trip occurs.
3. Compare the result with the set *Pickup_PH*.
4. Disconnect AC and INITIATE input signals.



Note! If *NoIPickupcheck* or *Retrip off* is set, only back-up trip can be used to check set *Pickup_PH*.

6.3.7.2

Checking the residual (ground fault) current operate value *Pickup_N* set below *Pickup_PH*

Check the low set *Pickup_N* current where setting *FunctionMode = Current* and setting *BuTripMode = 1 out of 4* as set under **Main menu/Settings/IED Settings/Current/CCRBRF(50BF)/1:CCRBRF**.

1. Apply the fault condition, including INITIATION of CCRBRF (50BF), with a current just below set *IN* > *Pickup_N*.
2. Repeat the fault condition and increase the current in steps until trip appears.
3. Compare the result with the set *Pickup_N*.
4. Disconnect AC and INITIATION input signals.

6.3.7.3

Checking the re-trip and back-up times

The check of the set times can be made in connection with the check of operate values above.

Choose the applicable function and trip mode, such as *FunctionMode = Current* and setting *RetripMode = No CBPos*. Check as set under **Main menu/Settings/IED Settings/Current/CCRBRF(50BF)/1:CCRBRF**.

1. Apply the fault condition, including initiation of CCRBRF (50BF), well above the set current value. Measure time from INITIATION of CCRBRF (50BF).
2. Check the re-trip $t1$ and back-up trip times $t2$.
3. Disconnect AC and INITIATE input signals.

6.3.7.4

Verifying the re-trip mode

Choose the mode below, which corresponds to the actual case.

In the cases below it is assumed that *FunctionMode = Current* as set under **Main menu/Settings/IED Settings/Current/CCRBRF(50BF)/1:CCRBRF**.

Checking the case without re-trip, *RetripMode = Retrip Off*

1. Set *RetripMode = Retrip Off*.
2. Apply the fault condition, including initiation of CCRBRF (50BF), well above the set current value.
3. Verify that no re-trip, but back-up trip is achieved after set time.
4. Disconnect AC and INITIATE input signals.

Checking the re-trip with current check, *RetripMode = CB Pos Check*

1. Set *RetripMode = CB Pos Check*.
2. Apply the fault condition, including initiation of CCRBRF (50BF), well above the set current value.
3. Verify that re-trip is achieved after set time $t1$ and back-up trip after time $t2$
4. Apply the fault condition, including initiation of CCRBRF (50BF), with current below set current value.
5. Verify that no re-trip, and no back-up trip is obtained.
6. Disconnect AC and INITIATE input signals.

Checking re-trip without current check, *RetripMode = No CBPos Check*

1. Set *RetripMode = No CBPos Check*.
2. Apply the fault condition, including initiation of CCRBRF (50BF), without any current.
3. Verify that re-trip is achieved after set time $t1$, and back-up trip after time $t2$.

4. Apply the fault condition, including initiation of CCRBRF (50BF), with current below set current value.
5. Verify that re-trip is achieved after set time $t1$, but no back-up trip is obtained.
6. Disconnect AC and INITIATE input signals.

6.3.7.5

Verifying the back-up trip mode

In the cases below it is assumed that *FunctionMode* = *Current* is selected.

Checking that back-up tripping is not achieved at normal CB tripping

Use the actual tripping modes. The case below applies to re-trip with current check.

1. Apply the fault condition, including initiation of CCRBRF (50BF), with phase current well above set value IP .
2. Interrupt the current, with a margin before back-up trip time, $t2$. It may be made at issue of re-trip command.
3. Check that re-trip is achieved, if selected, but no back-up trip is obtained.
4. Disconnect AC and INITIATE input signals.

The normal mode *BuTripMode* = *1 out of 3* should have been verified in the tests above. In applicable cases the modes *1 out of 4* and *2 out of 4* can be checked. Choose the mode below, which corresponds to the actual case.

Checking the case *BuTripMode* = *1 out of 4*

It is assumed that the ground-fault current setting *Pickup_N* is below phase current setting *Pickup_{PH}*.

1. Set *BuTripMode* = *1 out of 4*.
2. Apply the fault condition, including initiation of CCRBRF (50BF), with one-phase current below set *Pickup_{PH}* but above *Pickup_N*. The residual ground-fault should then be above set *Pickup_N*.
3. Verify that back-up trip is achieved after set time. If selected, re-trip should also appear.
4. Disconnect AC and INITIATE input signals.

Checking the case *BuTripMode* = *2 out of 4*

The ground-fault current setting *Pickup_N* may be equal to or below phase-current setting *Pickup_{PH}*.

1. Set *BuTripMode* = 2 out of 4.
2. Apply the fault condition, including initiation of CCRBRF (50BF), with one-phase current above set *Pickup_PH* and residual (ground fault) above set *Pickup_N*.
3. Verify that back-up trip is achieved after set time. If selected, re-trip should also appear.
4. Apply the fault condition, including initiation of CCRBRF (50BF), with at least one-phase current below set *Pickup_PH* and residual (ground fault) above set *Pickup_N*. The current may be arranged by feeding three- (or two-) phase currents with equal phase angle (I0-component) below *Pickup_PH*, but of such value that the residual (ground fault) current ($3I_0$) will be above set value *Pickup_N*.
5. Verify that back-up trip is not achieved.
6. Disconnect AC and INITIATE input signals.

6.3.7.6

Verifying the case *RetripMode* = *Contact*

It is assumed that re-trip without current check is selected, *RetripMode* = *Contact*.

1. Set *FunctionMode* = *Contact*
2. Apply input signal for CB closed to input 52a_A (B or C).
3. Apply input signal, for initiation of CCRBRF (50BF).
4. Verify that phase selection re-trip and back-up trip are achieved after set times.
5. Disconnect the trip signal. Keep the CB closed signal.
6. Apply input signal, for initiation of CCRBRF (50BF).
7. Arrange disconnection of CB closed signal well before set back-up trip time t_2 .
8. Verify that back-up trip is not achieved.
9. Disconnect injected AC and INITIATE input signals.

6.3.7.7

Verifying the function mode *Current&Contact*

To be made only when *FunctionMode* = *Current&Contact* is selected.

Checking the case with fault current above set value *Pickup_PH*

The operation shall be as in *FunctionMode* = *Current*.

1. Set *FunctionMode* = *Current&Contact*.
2. Leave the inputs for CB close inactivated. These signals should not influence.
3. Apply the fault condition, including initiation of CCRBRF (50BF), with current above the set *Pickup_PH* value.
4. Check that the re-trip, if selected, and back-up trip commands are achieved.
5. Disconnect injected AC and INITIATE input signals.

Checking the case with fault current below set value *Pickup_BlkCont*

The case shall simulate a case where the fault current is very low and operation will depend on CB position signal from CB auxiliary contact. It is suggested that re-trip without current check is used, setting *RetripMode* = *No CBPos Check*.

1. Set *FunctionMode* = *Current&Contact*.
2. Apply input signal for CB closed to relevant input or inputs 52a_A (B or C)
3. Apply the fault condition with input signal(s) for initiation of CCRBRF (50BF). The value of current should be below the set value *Pickup_BlkCont*
4. Verify that phase selection re-trip (if selected) and back-up trip are achieved after set times. Failure to trip is simulated by keeping the signal(s) CB closed activated.
5. Disconnect the AC and the INITIATE signal(s). Keep the CB closed signal(s).
6. Apply the fault and the initiation again. The value of current should be below the set value *Pickup_BlkCont*.
7. Arrange disconnection of BC closed signal(s) well before set back-up trip time *t2*. It simulates a correct CB tripping.
8. Verify that back-up trip is not achieved. Re-trip can appear for example, due to selection "Re-trip without current check".
9. Disconnect injected AC and INITIATE input signals.

6.3.7.8

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/CCRBRF(50BF)/X:CCRBRF** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.8

Stub protection STBPTOC (50STB)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Logical signals for STBPTOC (50STB) protection are available on the local HMI under **Main menu/Tests/Function status/Current/STBPTOC(50STB)/1:STBPTOC/Inputs or Outputs**

Values of the logical signals for STBPTOC (50STB) are available on the local HMI under **Main menu/Tests/Function status/Current/STBPTOC(50STB,I>)/1:STBPTOC**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.3.8.1 Measuring the operate limit of set values

1. Check that the input logical signals BLOCK and ENABLE and the output logical signal TRIP are all logical zero.
2. Activate the input ENABLE on the STBPTOC (50STB) function block
3. For a short while inject a current (fault current) in one phase to about 110% of the set operating current, and switch the current off.
Observe to not exceed the maximum permitted overloading of the current circuits in the IED.
4. Switch the fault current on and measure the operating time of STBPTOC (50STB).
Use the TRIP signal from the configured binary output to stop the timer. The operation should be instantaneously.
5. Activate the input BLOCK on the STBPTOC (50STB) function block.
6. Switch on the fault current (110% of the setting).
No TRIP signal should appear.
7. Switch off the fault current.
8. For a short while inject a current (fault current) in same phase to about 90% of the set operating current, and switch the current off.
9. Switch the fault current on.
No TRIP signal should appear.
10. Switch off the fault current.
11. Reset the ENABLE binary input.
12. Switch the fault current on.
No TRIP signal should appear.
13. Disable the fault current.

6.3.8.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/STBPTOC(50STB,I>)/1:STBPTOC** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.9 Pole discrepancy protection CCRPLD (52PD)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for CCRPLD (52PD) are available on the local HMI under **Main menu/Tests/Function status/Current/CCRPLD(52PD)/X:CCRPLD**.

The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.3.9.1 Verifying the settings

1. When CCRPLD (52PD) is set for external, set setting *ContSel* to *Enable* under **Main menu/Settings/IED Settings/Current/CCRPLD/1:CCRPLD** to activate the logic that detects pole discordance when external pole discordance signaling is used (input EXTPDIND) in the application configuration.
2. Activate the input EXTPDIND on CCRPLD (52PD) function block, and measure the operating time of CCRPLD (52PD).
3. Compare the measured time with the set value *tTrip*.
4. Reset the EXTPDIND input.
5. When CCRPLD (52PD) is set for unsymmetrical current detection with CB monitoring, set setting *CurrSel* under **Main menu/Settings/IED Settings/Current/CCRPLD/1:CCRPLD** to *Enable*.
Use the TRIP signal from the configured binary output to stop the timer.
6. Repeat point 4 and 5 using OPENCMD instead of CLOSECMD. Set all three currents to 110% of *CurrRelLevel*. Activate CLOSECMD.
NO TRIP signal should appear due to symmetrical condition.

6.3.9.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/CCRPLD(52PD)/X:CCRPLD** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.10 Broken conductor check BRCPTOC (46)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for BRCPTOC (46) are available on the local HMI under **Main menu/Tests/Function status/Current/BRCPTOC(46,lub)/X:BRCPTOC**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.3.10.1 Measuring the operate and time limit of set values

1. Check that the input logical signal BLOCK to the BRCPTOC (46) function block is logical zero and note on the local HMI that the output signal TRIP from the BRCPTOC (46) function block is equal to the logical 0.
2. Set the measured current (fault current) in one phase to about 110% of the set operating current $IP>$.
Observe to not exceed the maximum permitted overloading of the current circuits in the terminal.
3. Switch on the fault current and measure the operating time of BRCPTOC (46).
TRIP is controlled by Gate 13 in the configuration.
Use the TRIP signal from the configured binary output to stop the timer.
4. Compare the measured time with the set value $tOper$.
5. Activate the BLOCK binary input.
6. Switch on the fault current (110% of the setting) and wait longer than the set value $tOper$.
No TRIP signal should appear.
7. Switch off the fault current.
8. Set the measured current (fault current) in same phase to about 90% of the set operating current $IP>$. Switch off the current.
9. Switch on the fault current and wait longer than the set value $tOper$.
No TRIP signal should appear.
10. Switch off the fault current.

6.3.10.2

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/BRCPTOC(46,lub)/X:BRCPTOC** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.11

Directional underpower protection GUPPDUP (37)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for GUPPDUP (37) are available on the local HMI under **Main menu/Tests/Function status/Current/GUPPDUP(37,P<)/1:GUPPDUP**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.3.11.1

Verifying the settings

The underpower protection shall be set to values according to the real set values to be used.

The test is made by means of injection of voltage and current where the amplitude of both current and voltage and the phase angle between the voltage and current can be controlled. During the test, the analog outputs of active and reactive power shall be monitored.

1. Connect the test set for injection of voltage and current corresponding to the mode to be used in the application. If a three-phase test set is available this could be used for all the modes. If a single-phase current/voltage test set is available the test set should be connected to a selected input for one-phase current and voltage. Use the formulas stated in [Table 2](#) for the different calculation modes used. The set mode *Mode* can be found on the local HMI under **Main menu/Settings/IED Settings/Current/GUPPDUP(37,P<)/1:GUPPDUP/General**.

Table 2: Calculation modes

Set value: <i>Mode</i>	Formula used for complex power calculation
A, B, C	$\bar{S} = \bar{V}_A \cdot \bar{I}_A^* + \bar{V}_B \cdot \bar{I}_B^* + \bar{V}_C \cdot \bar{I}_C^*$ <p>(Equation 4)</p>
Arone	$\bar{S} = \bar{V}_{AB} \cdot \bar{I}_A^* - \bar{V}_{BC} \cdot \bar{I}_C^*$ <p>(Equation 5)</p>
PosSeq	$\bar{S} = 3 \cdot \bar{V}_{PosSeq} \cdot \bar{I}_{PosSeq}^*$ <p>(Equation 6)</p>
AB	$\bar{S} = \bar{V}_{AB} \cdot (\bar{I}_A^* - \bar{I}_B^*)$ <p>(Equation 7)</p>
BC	$\bar{S} = \bar{V}_{BC} \cdot (\bar{I}_B^* - \bar{I}_C^*)$ <p>(Equation 8)</p>
Table continues on next page	

Set value: <i>Mode</i>	Formula used for complex power calculation
CA	$\bar{S} = \bar{V}_{CA} \cdot (\bar{I}_C^* - \bar{I}_A^*)$ <p>(Equation 9)</p>
A	$\bar{S} = 3 \cdot \bar{V}_A \cdot \bar{I}_A^*$ <p>(Equation 10)</p>
B	$\bar{S} = 3 \cdot \bar{V}_B \cdot \bar{I}_B^*$ <p>(Equation 11)</p>
C	$\bar{S} = 3 \cdot \bar{V}_C \cdot \bar{I}_C^*$ <p>(Equation 12)</p>

- Adjust the injected current and voltage to the set values in % of *IBase* and *VBase* (converted to secondary current and voltage). The angle between the injected current and voltage shall be set equal to the set direction *Angle1*, angle for stage 1 (equal to 0° for low forward power protection and equal to 180° for reverse power protection). Check that the monitored active power is equal to 100% of rated power and that the reactive power is equal to 0% of rated power.
- Change the angle between the injected current and voltage to *Angle1* + 90°. Check that the monitored active power is equal to 0% of rated power and that the reactive power is equal to 100% of rated power.
- Change the angle between the injected current and voltage back to 0°. Decrease the current slowly until the PICKUP1 signal, pickup of stage 1, is activated.
- Increase the current to 100% of *IBase*.
- Switch the current off and measure the time for activation of TRIP1, trip of stage 1.
- If a second stage is used, repeat steps [2](#) to [6](#) for the second stage.

6.3.11.2

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/GUPPDUP(37,P<)/1:GUPPDUP** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.3.12 Directional overpower protection GOPPDOP (32)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for GOPPDOP (32) are available on the local HMI under **Main menu/Tests/Function status/Current/GOPPDOP(32,P>)/X:GOPPDOP**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.3.12.1 Verifying the settings

The overpower protection shall be set to values according to the real set values to be used. The test is made by means of injection of voltage and current where the amplitude of both current and voltage and the phase angle between the voltage and current can be controlled. During the test the analog outputs of active and reactive power shall be monitored.

1. Connect the test set for injection of voltage and current corresponding to the mode to be used in the application. If a three phase test set is available this could be used for all the modes. If a single phase current/voltage test set is available the test set should be connected to a selected input for one phase current and voltage. Use the formulas stated in [Table 2](#) for the different calculation modes used. The set mode *Mode* can be found under **Main menu/Settings/IED Settings/Current/GOPPDOP(32,P>)/1:GOPPDOP/General**.
2. Adjust the injected current and voltage to the set rated values in % of *IBase* and *VBase* (converted to secondary current and voltage). The angle between the injected current and voltage shall be set equal to the set direction *Angle1*, angle for stage 1 (equal to 0° for low forward power protection and equal to 180° for reverse power protection). Check that the monitored active power is equal to 100% of rated power and that the reactive power is equal to 0% of rated power.
3. Change the angle between the injected current and voltage to *Angle1* + 90°. Check that the monitored active power is equal to 0% of rated power and that the reactive power is equal to 100% of rated power.
4. Change the angle between the injected current and voltage back to *Angle1* value. Increase the current slowly from 0 until the PICKUP1 signal, pickup of stage 1, is activated. Check the injected power and compare it to the set value *Power1*, power setting for stage 1 in % of *Sbase*.
5. Increase the current to 100% of *IBase* and switch the current off.
6. Switch the current on and measure the time for activation of TRIP1, trip of stage 1.
7. If a second stage is used, repeat steps [2](#) to [6](#) for the second stage.

6.3.12.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Current/GOPPDOP(32,P>)/X:GOPPDOP** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.4 Testing voltage protection functions

6.4.1 Two step undervoltage protection UV2PTUV (27)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for UV2PTUV (27) are available on the local HMI under **Main menu/Tests/Function status/Voltage/UV2PTUV(27,2U<)/1:UV2PTUV**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.4.1.1 Verifying the setting

Verification of PICKUP value and time delay to operate for Step1

1. Check that the IED settings are appropriate, especially the PICKUP value, the definite time delay and the *1 out of 3* operation mode.
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly decrease the voltage in one of the phases, until the PICKUP signal appears.
4. Note the operate value and compare it with the set value.
5. Increase the measured voltage to rated load conditions.
6. Check that the PICKUP signal resets.
7. Instantaneously decrease the voltage in one phase to a value about 20% lower than the measured operate value.
8. Measure the time delay for the TRIP signal, and compare it with the set value.

Extended testing

1. The test above can now be repeated for step [2](#).
2. The tests above can be repeated for *2 out of 3* and for *3 out of 3* operation mode.
3. The tests above can be repeated to check security, that is, the PICKUP and operate signals, that are not supposed to appear, - do not.
4. The tests above can be repeated to check the time to reset.
5. The tests above can be repeated to test the inverse time characteristic.

6.4.1.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Voltage/UV2PTUV(27,2U<)/1:UV2PTUV** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.4.2 Two step overvoltage protection OV2PTOV (59)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for OV2PTOV (59) are available on the local HMI under **Main menu/Tests/Function status/Voltage/OV2PTOV(59,2U>)/1:OV2PTOV**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.4.2.1 Verifying the settings

1. Apply single-phase voltage below the set value *Pickup1*.
2. Slowly increase the voltage until the PU_ST1 signal appears.
3. Note the operate value and compare it with the set value.
4. Switch the applied voltage off.
5. Set and apply about 20% higher voltage than the measured operate value for one phase.
6. Measure the time delay for the TRST1 signal and compare it with the set value.
7. Repeat the test for step [2](#).

6.4.2.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Voltage/OV2PTOV(59,2U>)/1:OV2PTOV** for the function, or

for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.4.3 Two step residual overvoltage protection ROV2PTOV (59N)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for ROV2PTOV (59N) are available on the local HMI under **Main menu/Tests/Function status/Voltage/ROV2PTOV(59N,2UN>)/1:ROV2PTOV**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.4.3.1 Verifying the settings

1. Apply the single-phase voltage either to a single-phase voltage input or to a residual voltage input with the pickup value below the set value *Pickup1*.
2. Slowly increase the value until PU_ST1 appears.
3. Note the operate value and compare it with the set value.
4. Switch the applied voltage off.
5. Set and apply about 20% higher voltage than the measured operate value for one phase.
6. Measure the time delay for the TRST1 signal and compare it with the set value.
7. Repeat the test for step [2](#).

6.4.3.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Voltage/ROV2PTOV(59N,2UN>)/1:ROV2PTOV** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.4.4 Loss of voltage check LOVPTUV (27)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for LOVPTUV (27) are available on the local HMI under **Main menu/Tests/Function status/Voltage/LOVPTUV(27,U<)/1:LOVPTUV**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.4.4.1 Measuring the operate limit of set values

1. Check that the input logical signals BLOCK, CBOPEN and BLKU are logical zero.
2. Supply a three-phase rated voltage in all three phases and note on the local HMI that the TRIP logical signal is equal to the logical 0.
3. Switch off the voltage in all three phases.
After set t_{Trip} time a TRIP signal appears on the corresponding binary output or on the local HMI.



Note that TRIP at this time is a pulse signal, duration should be according to set t_{Pulse} .

4. Inject the measured voltages at rated values for at least set $t_{Restore}$ time.
5. Activate the CBOPEN binary input.
6. Simultaneously disconnect all the three-phase voltages from the IED.
No TRIP signal should appear.
7. Inject the measured voltages at rated values for at least set $t_{Restore}$ time.
8. Activate the BLKU binary input.
9. Simultaneously disconnect all the three-phase voltages from the IED.
No TRIP signal should appear.
10. Reset the BLKU binary input.
11. Inject the measured voltages at rated values for at least set $t_{Restore}$ time.
12. Activate the BLOCK binary input.
13. Simultaneously disconnect all the three-phase voltages from the IED.
No TRIP signal should appear.
14. Reset the BLOCK binary input.

6.4.4.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Voltage/LOVPTUV(27,U<)/1:LOVPTUV** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.5 Testing frequency protection functions

6.5.1 Underfrequency protection SAPTUF (81)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for SAPTUF (81) are available on the local HMI under **Main menu/Tests/Function status/Frequency/SAPTUF(81,f<)/X:SAPTUF**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.5.1.1 Verifying the settings

Verification of PICKUP value and time delay to operate

1. Check that the IED settings are appropriate, especially the PICKUP value and the definite time delay.
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly decrease the frequency of the applied voltage, until the PICKUP signal appears.
4. Note the operate value and compare it with the set value.
5. Increase the frequency until rated operating levels are reached.
6. Check that the PICKUP signal resets.
7. Instantaneously decrease the frequency of the applied voltage to a value about 20% lower than the operate value.
8. Measure the time delay of the TRIP signal, and compare it with the set value.

Extended testing

1. The test above can be repeated to check the time to reset.
2. The tests above can be repeated to test the frequency dependent inverse time characteristic.

Verification of the low voltage magnitude blocking

1. Check that the IED settings are appropriate, especially the *PUFrequency* and the *tDelay* and the *MinValFreqMeas* in the SMAI preprocessing function.
2. Supply the IED with three-phase voltages at rated values.
3. Slowly decrease the magnitude of the applied voltage, until the BLKDMAGN signal appears.
4. Note the voltage magnitude value and compare it with the SMAI set value *MinValFreqMeas*.

5. Slowly decrease the frequency of the applied voltage, to a value below *PUFrequency*.
6. Check that the PICKUP signal does not appear.
7. Wait for a time corresponding to *tDelay*, and check that the TRIP signal does not appear.

6.5.1.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Frequency/SAPTUF(81,f<)/X:SAPTUF** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.5.2 Overfrequency protection SAPTOF (81)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for SAPTOF (81) are available on the local HMI under **Main menu/Tests/Function status/Frequency/SAPTOF(81,f>)/X:SAPTOF**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.5.2.1 Verifying the settings

Verification of PICKUP value and time delay to operate

1. Check that the settings in the IED are appropriate, especially the PICKUP value and the definite time delay.
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly increase the frequency of the applied voltage, until the PICKUP signal appears.
4. Note the operate value and compare it with the set value.
5. Decrease the frequency to rated operating conditions.
6. Check that the PICKUP signal resets.
7. Instantaneously increase the frequency of the applied voltage to a value about 20% higher than the operate value.
8. Measure the time delay for the TRIP signal, and compare it with the set value.

Extended testing

1. The test above can be repeated to check the time to reset.

Verification of the low voltage magnitude blocking

1. Check that you have appropriate settings in the IED, especially the *PUFrequency*, *TtDelay* and the *MinValFreqMeas* in the SMAI preprocessing function
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly decrease the magnitude of the applied voltage, until the BLKDMAGN signal appears.
4. Note the voltage magnitude value and compare it with the set value, *MinValFreqMeas*.
5. Slowly increase the frequency of the applied voltage, to a value above *PUFrequency*.
6. Check that the PICKUP signal does not appear.
7. Wait for a time corresponding to *tDelay*, and make sure that the TRIP signal does not appear.

6.5.2.2

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Frequency/SAPTOF(81,f>)/X:SAPTOF** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.5.3

Rate-of-change frequency protection SAPFRC (81)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for SAPFRC (81) are available on the local HMI under **Main menu/Tests/Function status/Frequency/SAPFRC(81,df/dt)/X:SAPFRC**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

6.5.3.1

Verifying the settings

PICKUP value and time delay to operate

1. Check that the appropriate settings are available in the IED, especially the PICKUP value and the definite time delay. Set *PickupFreqgrad*, to a rather small negative value.
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly decrease the frequency of the applied voltage, with an increasing rate-of-change that finally exceeds the setting of *PickupFreqgrad*, and check that the PICKUP signal appears.
4. Note the operate value and compare it with the set value.
5. Increase the frequency to rated operating conditions, and zero rate-of-change.
6. Check that the PICKUP signal resets.
7. Instantaneously decrease the frequency of the applied voltage to a value about 20% lower than the nominal value.
8. Measure the time delay for the TRIP signal, and compare it with the set value.

Extended testing

1. The test above can be repeated to check a positive setting of *PickupFreqGrad*.
2. The tests above can be repeated to test the RESTORE signal, when the frequency recovers from a low value.

6.5.3.2

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Frequency/SAPFRC(81,df/dt)/X:SAPFRC** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.6

Testing secondary system supervision functions

6.6.1

Current circuit supervision CCSRDIF (87)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for CCSRDIF (87) are available on the local HMI under **Main menu/Tests/Function status/Secondary system supervision/CCSRDIF(87,IND)/X:CCSRDIF**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

The Current circuit supervision function CCSRDIF (87) is conveniently tested with the same three-phase test set as used when testing the measuring functions in the IED.

The condition for this procedure is that the setting of *IMinOp* is lower than the setting of *Pickup_Block*.

6.6.1.1 Verifying the settings

1. Check the input circuits and the operate value of the *IMinOp* current level detector by injecting current, one phase at a time.
2. Check the phase current blocking function for all three phases by injecting current, one phase at a time. The output signals shall reset with a delay of 1 second when the current exceeds $1.5 \cdot I_{Base}$.
3. Inject a current $0.1 \cdot I_{Base}$ to the reference current input IREFSMPL.
4. Increase slowly the current in one of the phases current input and check that FAIL output is obtained when the current is about $0.9 \cdot I_{Base}$.

6.6.1.2 Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Secondary system supervision/CCSRDIF(87,INd)/X:CCSRDIF** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.6.2 Fuse failure supervision SDDRFUF

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for SDDRFUF are available on the local HMI under **Main menu/Tests/Function status/Secondary system supervision/SDDRFUF/1:SDDRFUF**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

The verification is divided in two main parts. The first part is common to all fuse failure supervision options, and checks that binary inputs and outputs operate as expected according to actual configuration. In the second part the relevant set operate values are measured.

6.6.2.1 Checking that the binary inputs and outputs operate as expected

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Connect the nominal dc voltage to the 89bS binary input.
 - The signal BLKV should appear with almost no time delay.
 - No signals BLKZ and 3PH should appear on the IED.
 - Only the distance protection function can operate.
 - Undervoltage-dependent functions must not operate.
3. Disconnect the dc voltage from the 89b binary input terminal.
4. Connect the nominal dc voltage to the MCBOP binary input.
 - The BLKV and BLKZ signals should appear without any time delay.
 - All undervoltage-dependent functions must be blocked.
5. Disconnect the dc voltage from the MCBOP binary input terminal.
6. Disconnect one of the phase voltages and observe the logical output signals on the binary outputs of the IED.

BLKV and BLKZ signals should appear simultaneously whether the BLKV and BLKZ reset depends on the setting *Sealln* “on” or “off”. If “on” no reset, if “off” reset.
7. After more than 5 seconds disconnect the remaining two-phase voltages and all three currents.
 - There should be no change in the high status of the output signals BLKV and BLKZ.
 - The signal 3PH will appear.
8. Establish normal voltage and current operating conditions simultaneously and observe the corresponding output signals.

They should change to logical 0 as follows:

 - Signal 3PH after about 25ms
 - Signal BLKV after about 50ms
 - Signal BLKZ after about 200ms

6.6.2.2 Measuring the operate value for the negative sequence function

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Slowly decrease the measured voltage in one phase until the BLKV signal appears.
3. Record the measured voltage and calculate the corresponding negative-sequence voltage according to the equation.

Observe that the voltages in the equation are phasors.

$$3 \cdot \overline{V_2} = \overline{V_A} + a^2 \cdot \overline{V_B} + a \cdot \overline{V_C}$$

(Equation 13)

Where:

$$\overline{V_A}, \overline{V_B} \text{ and } \overline{V_C}$$

= the measured phase voltages

$$a = 1 \cdot e^{j \frac{2 \cdot \pi}{3}} = -0,5 + j \frac{\sqrt{3}}{2}$$

4. Compare the result with the set value (consider that the set value $3V2PU$ is in percentage of the base voltage $VBase$) of the negative-sequence operating voltage.

6.6.2.3

Measuring the operate value for the zero-sequence function

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Slowly decrease the measured voltage in one phase until the BLKV signal appears.
3. Record the measured voltage and calculate the corresponding zero-sequence voltage according to the equation.

Observe that the voltages in the equation are phasors.

$$3 \cdot \overline{V_0} = \overline{V_A} + \overline{V_B} + \overline{V_C}$$

(Equation 16)

Where:

$$\overline{V_A}, \overline{V_B} \text{ and } \overline{V_C}$$

= the measured phase voltages.

4. Compare the result with the set value (consider that the set value $3V0Pickup$ is in percentage of the base voltage of the zero-sequence operating voltage.

6.6.2.4

Checking the operation of the dv/dt and di/dt based function

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Change the voltages in all three phases simultaneously.
The voltage change must be greater than the set value for $DVPU$ and the current constant below the set value for $DIPU$.

- The BLKV and BLKZ signals appear without any time delay. The BLKZ signal will be activated only if the internal deadline detection is not activated at the same time.
 - 3PH should appear after 5 seconds, if the remaining voltage levels are lower than the set *VDLDP*U of the DLD function.
3. Apply normal conditions as in step 1.
The BLKV, BLKZ and 3PH signals should reset, if activated, see step 1 and 2.
 4. Change the voltages in all three phases simultaneously.
The voltage change must be greater than the set value for *DVPU* and the current constant above the set value for *DIPU*.
The BLKV, BLKZ and 3PH signals should not appear.
 5. Repeat step 2.
 6. Connect the nominal voltages in all three phases and feed a current below the operate level in all three phases.
 7. Keep the current constant. Disconnect the voltage in all three phases simultaneously.
 8. To test the internal signals *ifDeltaU* and *ifDeltaI*, change the magnitude of the voltage and current for phase 1 to a value greater than the set value for *DVPU* and *DIPU*.
 9. Check that the start output signals STDUL1 and STDIL1 and the general start signals STDU or STDI are activated.
 10. Check that the start output signals for the current and voltage phases 2 and 3 are activated by changing the magnitude of the voltage and current for phases 2 and 3.

6.6.2.5

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Secondary system supervision/SDDRFUF/1:SDDRFUF** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.7

Testing control functions



During periods of frequent counter state/value changes, if the auxiliary power to the IED is interrupted, it is possible that this information will be lost.

6.7.1 Synchrocheck, energizing check, and synchronizing SESRSYN (25)

This section contains instructions on how to test the synchrocheck/synchronism check, energizing check, and synchronizing function SESRSYN (25) for single, double and breaker-and-a-half arrangements.

This section contains instructions on how to test the synchrocheck/synchronism check and energizing check for single CB with or without the synchronizing function.

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for SESRSYN (25) are available on the local HMI under **Main menu/Tests/Function status/Control/SESRSYN(25,SYNC)/X:SESRSYN**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

At commissioning and periodical checks, the functions shall be tested with the used settings. To test a specific function, it might be necessary to change some setting parameters, for example:

- *AutoEnerg = Disabled/DLLB/DBLL/Both*
- *ManEnerg = Disabled*
- *Operation = Disabled/Enabled*
- Activation of the voltage selection function if applicable

The tests explained in the test procedures below describe the settings, which can be used as references during testing before the final settings are specified. After testing, restore the equipment to the normal or desired settings.

A secondary injection test set with the possibility to alter the phase angle and amplitude of the voltage is needed. The test set must also be able to generate different frequencies on different outputs.



The description below applies for a system with a nominal frequency of 50 Hz but can be directly applicable to 60 Hz. SESRSYN (25) can be set to use different phases, phase to ground or phase to phase. Use the set voltages instead of what is indicated below.

Figure [12](#) shows the general test connection principle, which can be used during testing. This description describes the test of the version intended for one bay.

Figure 13 shows the general test connection for a breaker-and-a-half diameter with one-phase voltage connected to the line side.

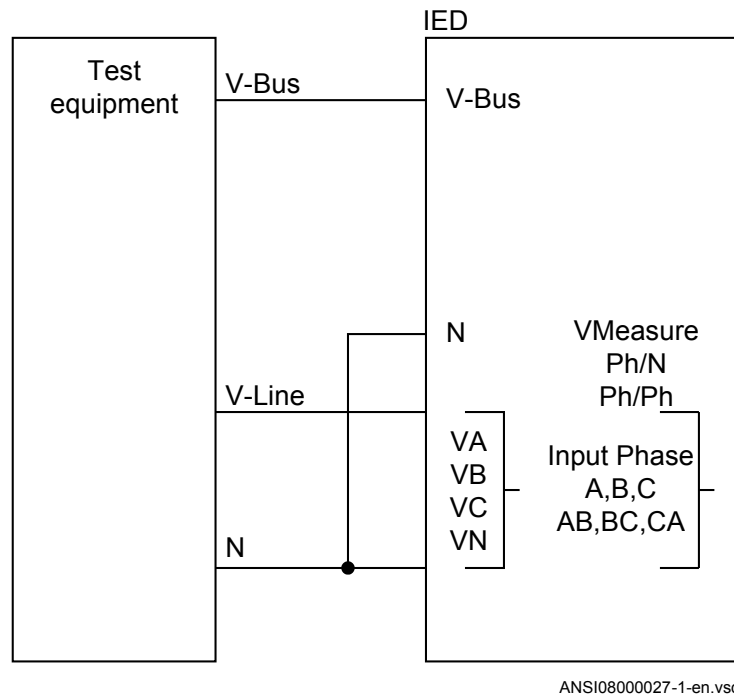


Figure 12: General test connection with three-phase voltage connected to the line side

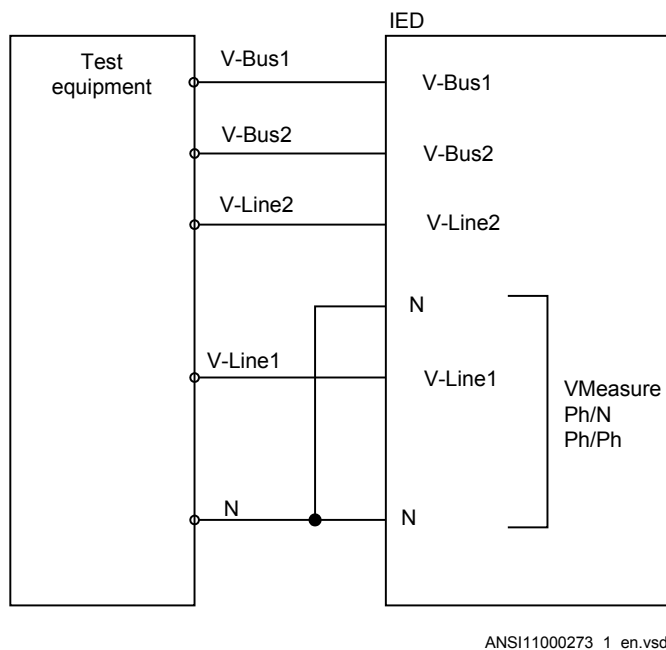


Figure 13: General test connection for a breaker-and-a-half diameter with one-phase voltage connected to the line side

6.7.1.1

Testing the synchronizing function

This section is applicable only if the synchronizing function is included.

The voltage inputs used are:

V-Line	VA, VB or VC line 1 voltage inputs on the IED
V-Bus	Bus voltage input on the IED

Testing the frequency difference

The frequency difference is in the example set at 0.20 Hz on the local HMI, and the test should verify that operation is achieved when the *FreqDiffMax* frequency difference is lower than 0.20 Hz. The test procedure below will depend on the settings used. Input STARTSYN must be activated during the test.

1. Apply voltages

- 1.1. V-Line = 100% *GblBaseSelLine* and f-Line = 50.0 Hz
- 1.2. V-Bus = 100% *GblBaseSelBus* and f-Bus = 50.2Hz
2. Check that a closing pulse is submitted at a closing angle less than 2 degrees from phase equality. Modern test sets will evaluate this automatically.
3. Repeat with
 - 3.1. V-Bus = 80% *GblBaseSelBus* and f-Bus = 50.25 Hz, to verify that the function does not operate when frequency difference is above limit.
4. Repeat with different frequency differences for example, 100 mHz with f-Bus nominal and line leading and for example 20 mHz (or just above *FreqDiffMin*) to verify that independent of frequency difference the closing pulse occurs within 2 degrees.
5. Verify that the closing command is not issued when the frequency difference is less than the set value *FreqDiffMin*.

6.7.1.2

Testing the synchrocheck check

During the test of SESRSYN (25) for a single bay arrangement, these voltage inputs are used:

V-Line	VA, VB or VC line 1 voltage inputs on the IED
V-Bus	Bus voltage input on the IED

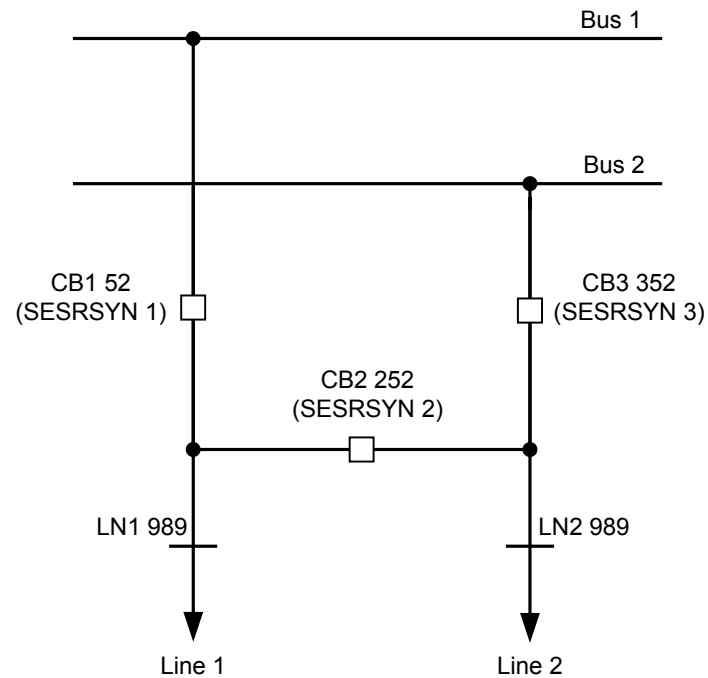
Voltage inputs

At test of the SESRSYN (25) function for a breaker-and-a-half diameter the following alternative voltage inputs can be used for the three SESRSYN (SESRSYN 1, SESRSYN 2, SESRSYN 3) functions. These three SESRSYN functions can either be in one, two or three different IEDs. Table 3 describes the scenario when SESRSYN 1, SESRSYN 2 and SESRSYN 3 all are in the same IED. If SESRSYN 3 is in another IED, Bus1 will be considered as Bus2 and Line2 as Line1. The voltage is selected by activation of different inputs in the voltage selection logic as shown in table 3 and figure 14.

Table 3: Voltage selection logic

SESRSYN	CBConfig setting	Section to be synchronized	Activated B1QCLD input on IED from	Activated B2QCLD input on IED from	Activated LN1QCLD input on IED from	Activated LN2QCLD input on IED from	Indication from SESRSYN on IED
SESRSYN 1 (Operates on CB1 52)	breaker-and-a-half bus CB	Bus1 – Line1			LN1 989		B1SEL, LN1SEL
		Bus1 – Line2	CB2 252			LN2 989	B1SEL, LN2SEL
		Bus1 – Bus2	CB2 252	CB3 352			B1SEL, B2SEL
Table continues on next page							

SESRSYN	CBConfig setting	Section to be synchronized	Activated B1QCLD input on IED from	Activated B2QCLD input on IED from	Activated LN1QCLD input on IED from	Activated LN2QCLD input on IED from	Indication from SESRSYN on IED
SESRSYN 2 (Operates on CB2 252)	Tie CB	Line1 – Line2			LN1 989	LN2 989	LN1SEL, LN2SEL
		Bus1 – Line2	CB1 52			LN2 989	B1SEL, LN2SEL
		Bus2 – Line1		CB3 352	LN1 989		B2SEL, LN1SEL
		Bus1 – Bus2	CB1 52	CB3 352			B1SEL, B2SEL
SESRSYN 3 (Operates on CB3 352)	breaker-and-a-half bus alt. CB (mirrored)	Bus2 – Line2				LN2 989	B2SEL, LN2SEL
		Bus2 – Line1	CB2 252		LN1 989		B2SEL, LN1SEL
		Bus2 – Bus1	CB2 252	CB1 52			B1SEL, B2SEL



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Figure 14: Voltage selection logic

Testing the voltage difference

Set the voltage difference to 0.15 p.u. on the local HMI, and the test should check that operation is achieved when the voltage difference *VDiffSC* is lower than 0.15 p.u.

The settings used in the test shall be final settings. The test shall be adapted to site setting values instead of values in the example below.

Test with no voltage difference between the inputs.

Test with a voltage difference higher than the set *VDiffSC*.

1. Apply voltages V-Line (for example) = 80% *GblBaseSelLine* and V-Bus = 80% *GblBaseSelBus*
2. Check that the AUTOSYOK and MANSYOK outputs are activated.
3. The test can be repeated with different voltage values to verify that the function operates within the set *VDiffSC*. Check with both V-Line and V-Bus respectively lower than the other.
4. Increase the V-Bus to 110% *GblBaseSelBus*, and the V-Line = 90% *GblBaseSelLine* and also the opposite condition.
5. Check that the two outputs for manual and auto synchronism are not activated.

Testing the phase angle difference

The phase angle differences *PhaseDiffM* and *PhaseDiffA* respectively are set to their final settings and the test should verify that operation is achieved when the phase angle difference is lower than this value both leading and lagging.

Test with no voltage difference.

1. Apply voltages V-Line (for example) = 100% *GblBaseSelLine* and V-Bus = 100% *GblBaseSelBus*, with a phase difference equal to 0 degrees and a frequency difference lower than *FreqDiffA* and *FreqDiffM*.
2. Check that the AUTOSYOK and MANSYOK outputs are activated.
The test can be repeated with other phase difference values to verify that the function operates for values lower than the set ones, *PhaseDiffM* and *PhaseDiffA*. By changing the phase angle on the voltage connected to V-Bus, between $\pm d\phi$ degrees, the user can check that the two outputs are activated for a phase difference lower than the set value. It should not operate for other values. See figure [15](#).

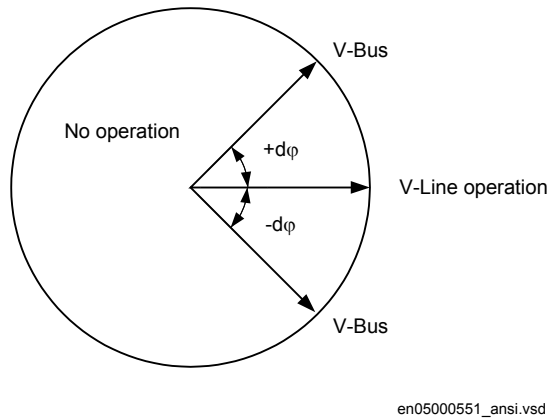


Figure 15: Test of phase difference

3. Change the phase angle between $+d\phi$ and $-d\phi$ and verify that the two outputs are activated for phase differences between these values but not for phase differences outside, see figure 15.

Testing the frequency difference

The frequency difference test should verify that operation is achieved when the *FreqDiffA* and *FreqDiffM* frequency difference is lower than the set value for manual and auto synchronizing check, *FreqDiffA* and *FreqDiffM* respectively and that operation is blocked when the frequency difference is greater.

Test with frequency difference = 0 mHz

Test with a frequency difference outside the set limits for manual and auto synchronizing check respectively.

1. Apply voltages V-Line equal to 100% *GblBaseSelLine* and V-Bus equal to 100% *GblBaseSelBus*, with a frequency difference equal to 0 mHz and a phase difference lower than the set value.
2. Check that the AUTOSYOK and MANSYOK outputs are activated.
3. Apply voltage to the V-Line equal to 100% *GblBaseSelLine* with a frequency equal to 50 Hz and voltage V-Bus equal to 100% *GblBaseSelBus*, with a frequency outside the set limit.
4. Check that the two outputs are not activated. The test can be repeated with different frequency values to verify that the function operates for values lower than the set ones. If a modern test set is used, the frequency can be changed continuously.

Testing the reference voltage

1. Use the same basic test connection as in figure 12.

- The voltage difference between the voltage connected to V-Bus and V-Line should be 0%, so that the AUTOSYOK and MANSYOK outputs are activated first.
2. Change the V-Line voltage connection to V-Line2 without changing the setting on the local HMI. Check that the two outputs are not activated.

6.7.1.3 Testing the energizing check

During the test of the energizing check function for a single bay arrangement, these voltage inputs are used:

V-Line	VA, VB or VC line1 voltage inputs on the IED
V-Bus	Bus voltage input on the IED

General

When testing the energizing check function for the applicable bus, arrangement shall be done for the energizing check functions. The voltage is selected by activation of different inputs in the voltage selection logic.

Live voltage level is fixed to 80% *UBase* and dead voltage level to fixed 40% *UBase*.

The test shall be performed according to the settings for the station. Test the alternatives below that are applicable.

Testing the dead line live bus (DLLB)

The test should verify that the energizing check function operates for a low voltage on the V-Line and for a high voltage on the V-Bus. This corresponds to the energizing of a dead line to a live bus.

1. Apply a single-phase voltage 100% *GblBaseSelBus* to the V-Bus, and a single-phase voltage 30% *GblBaseSelLine* to the V-Line.
2. Check that the AUTOENOK and MANENOK outputs are activated after set *tAutoEnerg* respectively *tManEnerg*.
3. Increase the V-Line to 60% *GblBaseSelLine* and V-Bus to be equal to 100% *GblBaseSelBus*. The outputs should not be activated.
4. The test can be repeated with different values on the V-Bus and the V-Line.

Testing the dead bus live line (DBLL)

The test should verify that the energizing check function operates for a low voltage on the V-Bus and for a high voltage on the V-Line. This corresponds to an energizing of a dead bus to a live line.

1. Verify the settings *AutoEnerg* or *ManEnerg* to be *DBLL*.
2. Apply a single-phase voltage of 30% *GblBaseSelBus* to the V-Bus and a single-phase voltage of 100% *GblBaseSelLine* to the V-Line.
3. Check that the AUTOENOK and MANENOK outputs are activated after set *tAutoEnerg* respectively *tManEnerg*.
4. Decrease the V-Line to 60% *GblBaseSelLine* and keep the V-Bus equal to 30% *GblBaseSelBus*. The outputs should not be activated.
5. The test can be repeated with different values on the V-Bus and the V-Line.

Testing both directions (DLLB or DBLL)

1. Verify the local HMI settings *AutoEnerg* or *ManEnerg* to be *Both*.
2. Apply a single-phase voltage of 30% *GblBaseSelLine* to the V-Line and a single-phase voltage of 100% *GblBaseSelBus* to the V-Bus.
3. Check that the AUTOENOK and MANENOK outputs are activated after set *tAutoEnerg* respectively *tManEnerg*.
4. Change the connection so that the V-Line is equal to 100% *GblBaseSelLine* and the V-Bus is equal to 30% *GblBaseSelBus*. The outputs should still be activated.
5. The test can be repeated with different values on the V-Bus and the V-Line.

Testing the dead bus dead line (DBDL)

The test should verify that the energizing check function operates for a low voltage on both the V-Bus and the V-Line, that is, closing of the breaker in a non-energized system. Test is valid only when this function is used.

1. Verify the local HMI setting *AutoEnerg* to be *Disabled* and *ManEnerg* to be *DBLL*.
2. Set the parameter *ManEnergDBDL* to *Enabled*.
3. Apply a single-phase voltage of 30% *GblBaseSelBus* to the V-Bus and a single-phase voltage of 30% *GblBaseSelLine* to the V-Line.
4. Check that the MANENOK output is activated after set *tManEnerg*.
5. Increase the V-Bus to 80% *GblBaseSelBus* and keep the V-Line equal to 30% *GblBaseSelLine*. The outputs should not be activated.
6. Repeat the test with *ManEnerg* set to *DLLB* with different values on the V-Bus and the V-Line voltage.

6.7.1.4

Testing the voltage selection

Testing the voltage selection for single CB arrangements

This test should verify that the correct voltage is selected for the measurement in the SESRSYN function used in a double-bus arrangement. Apply a single-phase voltage of 30% *GblBaseSelLine* to the V-Line and a single-phase voltage of 100% *GblBaseSelBus* to the V-Bus.

If the VB1OK or VB2OK inputs for the fuse failure are used, they must be activated, during tests below. Also verify that deactivation prevents operation and gives an alarm.

1. Connect the signals above to binary inputs and binary outputs.
2. Connect the voltage inputs to the analog inputs used for each bus or line depending of the type of busbar arrangement and verify that correct output signals are generated.

6.7.1.5

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Control/SESRSYN(25,SYNC)/X:SESRSYN** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.7.2

Autorecloser for 3-phase operation SMBRREC (79)

Verification of the Autorecloser for 3-phase operation function SMBRREC (79) for three-phase reclosing attempts can be considered to consist of two parts.

- One part to verify the internal logic and timing of the function
- One part to verify its interaction with the protection system

This section deals with verification of SMBRREC (79) itself. However, it is practical to initiate SMBRREC (79) by activating a protection function, for example, by secondary injection tests.

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for SMBRREC (79) are available on the local HMI under **Main menu/Tests/Function status/Control/SMBRREC(79,0->1)/X:SMBRREC**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

The purpose of verification before commissioning is to check that entered selections, setting parameters and configuration render the intended result. The function is flexible with many options and facilities. At commissioning only the selections and settings intended for use are verified. If one chooses to reduce some time settings in order to speed up verification, be careful to set the parameters at intended operational values at the end of the verification procedure. One such parameter is the *tReset* time that must be timed out before a new test sequence can be performed.

The verification test is performed together with protection and trip functions. Figure 16 illustrates a suggested testing arrangement, where the circuit-breaker (CB) is simulated by an external bi-stable relay (BR), for example a relay type RXMVB2 or RXMD or Breaker Simulator of ABB. The following manual switches are used:

- Switch or push-button to close (SC)
- Switch or push-button to trip (ST)
- Switch for CB ready condition (SRY)

If no bi-stable relay or breaker simulator is available, replace it with two self-reset auxiliary relays and use a self-holding connection.

Use a secondary injection IED test set to operate the protection function. The test set shall be switched off when a trip signal is given or when the BR comes to open position to simulate real conditions.

The CB simulation can be made more elaborate, including simulation of the operating gear condition, CBREADY of either the type ready for a Close-Open (CO) cycle, or the type ready for an Open-Close -Open (OCO) cycle.

The CB condition CBREADY of a type, CO, shall be high (true) until a closing operation is performed. It then goes low (false) for a recharging time of about 5 - 10s. After that it is high again.

A CB condition CBREADY of a type, OCO shall be high (true) before and during tripping (Initiate reclosing). During tripping it goes low for a recharging time, for example, 10s. It may thus be low at the instant of reclosing. After each Open or Close operation it may need a recharging period before it goes high again.

In the example of CB simulation arrangement, the CBREADY condition is simulated by a manual switch, SRY.

Information and material for the verification:

- Protection or control unit, IED, configured and with settings entered.
- Configuration diagram for the IED
- Terminal diagram for the IED, or plant circuit diagram including the IED
- Technical reference manual for the IED
- IED test set for secondary injection

- Means to indicate, measure and record operation and times, for example an event recording facility
- A bi-stable relay (BR) or two auxiliary relays to simulate a CB
- Two push-buttons (SC, ST) to operate the BR and a change-over switch (SRY) to simulate CBREADY
- Possibly a switch simulation the synchronizing check SESRSYN (25) condition

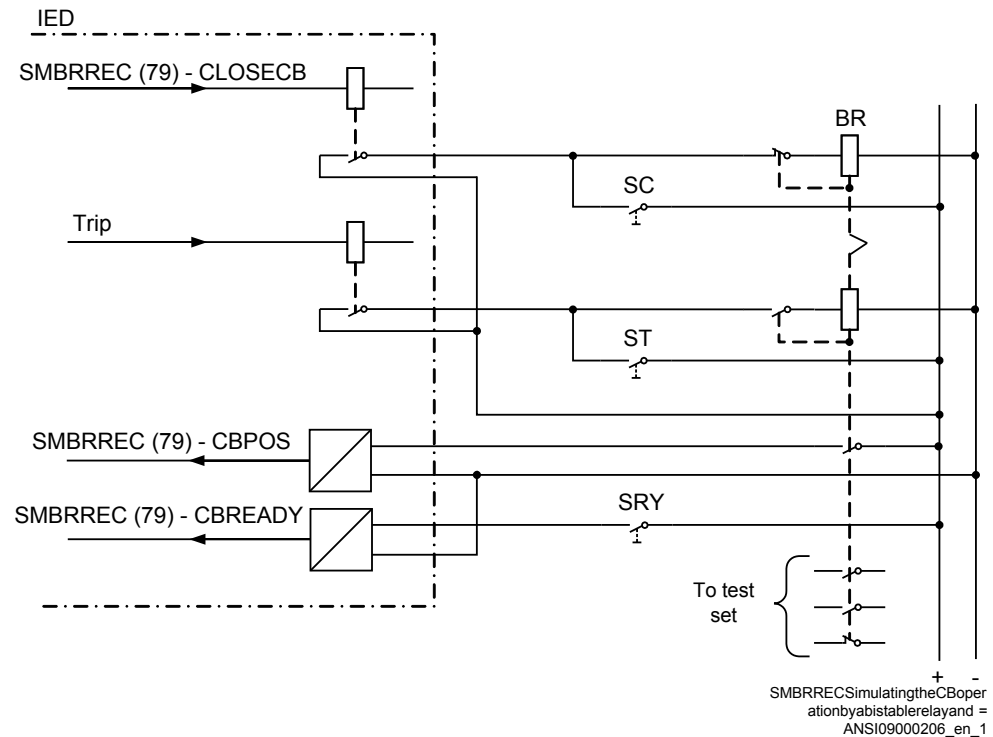


Figure 16: Simulating the CB operation by a bi-stable relay/breaker simulator and manual switches

6.7.2.1

Preparation of the verification

1. Check the function settings on the local HMI under **Main menu/Settings/IED Settings/Control/SMBRREC(79,0->1)/1:SMBRREC**
If any timer settings are reduced to speed up or facilitate the testing, they shall be set to normal after testing. A temporary label on the IED can be a reminder to restore normal settings after which a verification test should be performed.
2. Decide if a synchronizing check function SESRSYN (25) shall be included in the test.

If SESRSYN (25) as an internal function or external device is not operated by the injection, input signal SYNC must be connected as a permanent high signal or controlled by a switch.

3. Read and make notes of the reclosing operation counters on the local HMI under **Main menu/Tests/Function status/Control/SMBRREC(79,0->1)/1:SMBRREC**
Possibly reset the counters to *Zero*. Counters are reset in the reset menu.
4. Make arrangements for the simulation of the CB, for example as in figure [16](#).
5. Make arrangements for indication, recording and time measurements.
The signals for ,52a, RI, CLOSECMD, READY and other relevant signals should preferably be arranged for event recording with time tagging. If that is not possible, other means of time measurement and recording should be arranged.

6.7.2.2

Switching the autorecloser for 3-phase operation function to *Enabled* and *Disabled*

1. Set the *Operation* setting to *Disabled* and check the state.
2. Set the *Operation* setting to *Enabled* and check the state, including SETON and READY.
The CB should be closed and ready.
3. If external control Disabled/Enabled is connected, check that it works.
Set *Operation* to *ExternalCtrl* and use that control to switch Enabled and Disabled, and check the state of the function.

6.7.2.3

Verifying the autorecloser for 3-phase operation function SMBRREC (79)

Select the test cases to be run according to what is applicable to the particular application. It can be, for example, three-phase single-shot reclosing or two-shot reclosing. Below, a case with three-phase single-shot reclosing is described.

1. Set *Operation* = *Enabled*.
2. If the autorecloser for 3-phase operation function SMBRREC(79) is not to be operated, ensure that the SMBRREC input is activated. If SMBRREC(79) is to be included, ensure that it is supplied with the appropriate AC quantities.
3. Simulate CB closed position by closing switch SC to make the BR relay pick-up.
4. Simulate CBREADY by closing the switch SRY, and leave it closed.
5. Inject AC quantities to give a trip to the BR and to the RI input.
Observe and preferably record the operation. The BR relay shall trip and reclose (pick-up). After reclosing, the SRY switch can be opened for about 5s and then closed again.
The autoreclosing open time and the sequence should be checked, for example in the event recording. Check also the operation indications (disturbance report) and

the operation counters on the local HMI under **Main menu/Tests/Function status/SMBRREC(79,0->1)/1:SMBRREC**

Should the operation not be as expected, this should be investigated. It could be caused by an inappropriate setting or missing condition such as CBREADY.

6. Repeat the sequence by simulating a permanent fault.
Shortly after the reclosing shot, a new fault is applied. If a single-shot reclosing program is selected, there shall be one reclosing operation and then blocking of SMBRREC (79) for the set Reset time.
Before a new reclosing sequence can be run, the CBREADY and 52a (CB closed) must be set manually.

6.7.2.4

Checking the reclosing conditions

When checking the influence of a releasing condition it is suggested to first run a sequence with the condition fulfilled. When the condition signal is removed, and a new sequence is run, it indicates that the result was due to the changed condition. In case of a blocking signal the procedure should be similar. Start without the blocking or inhibit signal, and then run a sequence with the blocking or inhibit signal added.

Checking the influence of the INHIBIT signal

1. Check that the autorecloser function SMBRREC (79) is operative, for example, by making a reclosing shot without the INHIBIT signal.
2. Apply a fault and thereby a RI signal. At the same time, or during the open time, apply a signal to the input INHIBIT.
3. Check that the reclosing sequence is interrupted and no reclosing takes place.

Check closing onto a fault

1. Check that the autorecloser function SMBRREC (79) is operative, for example by making a reclosing shot.
Keep the CBREADY signal high.
2. Set the breaker simulating relay BR in Open position.
3. Close the BR relay and apply immediately a fault and thereby a RI signal.
4. Check that no reclosing takes place.

Checking the influence of CB not ready for reclosing

1. Check that the autorecloser function SMBRREC (79) is operative, for example by making a reclosing shot.
Keep the CB simulator BR closed. Remove the CBREADY signal by opening SRY.
2. Apply a fault and thereby a RI signal.
3. Check that no reclosing takes place.

Checking the influence of synchronizing check (at three-pole reclosing)

1. Check that the autorecloser for 3-phase operation function SMBRREC (79) is operative, for example, by making a three-pole reclosing shot with the synchronizing check condition.
Remove the SMBRREC (25) signal.
2. Apply a fault causing three-phase trip and thereby a RI signal.
3. Wait for the t_{Sync} time out limit.
Check that no reclosing is made.

Restoring equipment

After the tests, restore the equipment to normal or desired state. Check the following items in particular:

1. Check the operation counters.
Reset the counters to zero, if that is the user's preference. The counter reset function is found on the local HMI under **Main menu/Clear/Clear counters/SMBRREC(79,0->1)**
2. Restore settings that may have been modified for the tests back to normal.
3. Disconnect the test switch, CB simulating arrangement and test circuits.
Reconnect any links or connection terminals, which may have been opened for the tests.
4. Reset indications, alarms and disturbance recordings.
Clearing of the disturbance report must be done via the Disturbance Handling in PCM600.

6.7.2.5

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Control/SMBRREC(79,0->1)/X:SMBRREC** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.7.3

Interlocking

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals are available on the local HMI under **Main menu/Tests/Function status/Control/<Function>/<1:Function>**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

The interlocking function consists of a bay-level part and a station-level part. The interlocking is delivery specific and is realized by bay-to-bay communication over the station bus. For that reason, test the function in a system, that is, either in a complete delivery system as an acceptance test (FAT/SAT) or as parts of that system.

6.7.4 Apparatus control APC

The apparatus control function consists of four types of function blocks, which are connected in a delivery-specific way between bays and to the station level. For that reason, test the total function in a system, that is, either in a complete delivery system as an acceptance test (FAT/SAT) or as parts of that system.



If a block/unblock command is sent from remote to function, while the IED is shut down, this command will not be recognized after the start up, thus the command that was sent prior to the shut down is used. In such cases, where there is a mismatch, the user is advised to make a complete cycle of block/unblock operations to align the statuses.

6.8 Testing logic functions

6.8.1 Tripping logic, common 3-phase output SMPPTRC (94)

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for SMPPTRC (94) are available on the local HMI under **Main menu/Tests/Function status/Logic/SMPPTRC(94,1->0)/X:SMPPTRC**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

This function is functionality tested together with other protection functions (ground-fault overcurrent protection, and so on) within the IED. It is recommended that the function is tested together with the autorecloser function, regardless of whether the autorecloser function is integrated or external.

6.8.1.1 Three-phase operating mode

1. Check that *AutoLock* and *TripLockout* are both set to *Disabled*.
2. Initiate a three-phase fault.

An adequate time interval between the faults should be considered, to overcome areset time caused by the possible activation of the Autorecloser function, SMBRREC (79). The function must issue a three-pole trip in all cases, when a trip is initiated by any protection function, either integrated or external. The functional TRIP output signal must always appear.

6.8.1.2

Circuit breaker lockout

The following tests should be carried out when the built-in lockout function is used in addition to possible other tests, which depends on the complete configuration of an IED.

1. Check that *AutoLock* and *TripLockout* are both set to *Disabled*.
2. Initiate a three-phase fault.
The functional output TRIP should be active at each fault. The output CLLKOUT must not be activated.
3. Activate the automatic lockout function, set *AutoLock* = *Enabled* and repeat.
Besides the TRIP outputs, CLLKOUT should be set.
4. Reset the lockout signal by activating the reset lockout (RSTLKOUT) signal.
5. Activate the TRIP signal lockout function, set *TripLockout* = *Enable* and repeat.
The output TRIP must be active and stay active after each fault. CLLKOUT must be activated.
6. Reset the lockout.
All functional outputs should reset.
7. Deactivate the TRIP signal lockout function, set *TripLockout* = *Disabled* and the automatic lockout function, set *AutoLock* = *Disabled* if not used.

6.8.1.3

Completing the test

Continue to test another function or end the testing by setting the parameter *TestMode* to *Disabled* under **Main menu/Tests/IED test mode/1:TESTMODE**. If another function is tested, then set the parameter *Blocked* to *No* under **Main menu/Tests/Function test modes/Logic/SMPPTRC(94,1->0)/X:SMPPTRC** for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter *Blocked* to *Yes*, for each individual function that has been tested.

6.9

Testing monitoring functions

6.9.1

Event counter CNTGGIO

The event counter function CNTGGIO can be tested by connecting a binary input to the counter under test and applying pulses to the counter. The speed of pulses must not

exceed 10 per second. Normally the counter will be tested in connection with tests on the function that the counter is connected to, such as trip logic. When configured, test it together with the function that operates it. Trig the function and check that the counter result corresponds with the number of operations.

6.9.2 Fault locator LMBRFLO

Prepare the IED for verification of settings as outlined in [5.1 "Preparing the IED to verify settings"](#).

Values of the logical signals for LMBRFLO are available on the local HMI under **Main menu/Tests/Function status/Monitoring/LMBRFLO/1:LMBRFLO**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

The Fault locator function LMBRFLO depends on other functions to work properly, that is, phase selection information from distance protection function and analog information supplied by the trip value recorder function. Check that proper binary initiate (pickup or tripping) and phase selection signals are connected and voltage and current signals are configured (parameter settings).



The fault locator LMBRFLO function, supports kilometer and mile for the line length unit. The fault distance will be presented with the same unit as the line length and is mapped to IEC61850 -8-1 communication protocol, where the fault distance is supposed to be in kilometer (km). Select the line length unit to kilometer for compliance with IEC61850.

The result is displayed on the local HMI or via PCM600. Distances to faults for the last 100 recorded disturbances can be found on the local HMI under **Main menu/Tests/Function status/Monitoring/LMBRFLO/1:LMBRFLO/Outputs**

If PCM600 is used, the result is displayed on the recording list after upload, including loop selection information.

Table 4: *Test settings*

Parameter	Condition
I	
Healthy conditions	V = 69.3 V, I = 0 A & ZF = 0°
Impedance Z	Test point Note: <ul style="list-style-type: none"> • $Z_x \leq (X_0 + 2 \cdot X_1)/3$ For single-phase faults • $Z_x \leq X_1$ For three and two phase faults • $Z_x \leq (X_0 + 2 \cdot X_1 \cdot X_M)/3$ For single-phase fault with mutual zero-sequence current
Impedance angle ZΦ	Test angle <ul style="list-style-type: none"> • $Z\Phi \arctan[(X_0 + 2 \cdot X_1) / (R_0 + 2R_1)]$ For single-phase faults • $Z\Phi \arctan(X_1/R_1)$ For two-phase faults

6.9.2.1

Measuring the operate limit

1. Set the test point (|Z| fault impedance and ZΦ impedance phase angle) for a condition that meets the requirements in table 4.
2. Subject the IED to healthy normal load conditions for at least two seconds.
3. Apply a fault condition.
Check that the distance-to-fault value displayed on the HMI complies with equations 18, 19 and 20

$$p = \frac{Z_x}{X_1} \cdot 100$$

(Equation 18)

in % for two- and three-pole faults

$$p = \frac{3 \cdot Z_x}{X_0 + 2 \cdot X_1} \cdot 100$$

(Equation 19)

in % for single-phase-to-ground faults

$$p = \frac{3 \cdot Z_x}{X_0 + 2 \cdot X_1 \pm X_M} \cdot 100$$

(Equation 20)

in % for single-phase-to-ground faults with mutual zero sequence current.

Where:

- p = the expected value of a distance to fault in percent
- Z_x = set test point on the test set
- X0 = set zero-sequence reactance of a line
- X1 = set positive-sequence reactance of a line
- XM = set mutual zero-sequence impedance of a line

6.9.2.2 Completing the test

6.10 Testing metering functions

6.10.1 Pulse counter PCGGIO

The test of the Pulse counter function PCGGIO requires the Parameter Setting tool in PCM600 or an appropriate connection to the local HMI with the necessary functionality. A known number of pulses with different frequencies are connected to the pulse counter input. The test should be performed with settings *Operation = Enable* or *Operation = Disable* and the function blocked or unblocked. The pulse counter value is then checked in PCM600 or on the local HMI.

6.11 Exit test mode

The following procedure is used to return to normal operation.

1. Navigate to the test mode folder.
2. Change the *Enable* setting to *Disable*. Press the 'E' key and the left arrow key.
3. Answer *YES*, press the 'E' key and exit the menus.

Section 7 Commissioning and maintenance of the fault clearing system

7.1 Commissioning and maintenance of the fault clearing system

About this chapter

This chapter discusses maintenance tests and other periodic maintenance measures.

7.1.1 Commissioning tests

During commissioning all protection functions shall be verified with the setting values used at each plant. The commissioning tests must include verification of all circuits by highlighting the circuit diagrams and the configuration diagrams for the used functions.

Further, the settings for protection functions are tested and recorded carefully as outlined for the future periodic maintenance tests.

The final testing includes primary verification of all directional functions where load currents is checked on the local HMI and in PCM600. The magnitudes and angles of all currents and voltages should be checked and the symmetry verified.

Directional functions have information about the measured direction and, for example, measured impedance. These values must be checked and verified as correct with the export or import of power available.

Finally, final trip tests must be performed. This involves activation of protection functions or tripping outputs with the circuit breaker closed and the tripping of the breaker verified. When several breakers are involved, each breaker must be checked individually and it must be verified that the other involved breakers are not tripped at the same time.

7.1.2 Periodic maintenance tests

The periodicity of all tests depends on several factors, for example the importance of the installation, environmental conditions, simple or complex equipment, static or electromechanical IEDs, and so on.

The normal maintenance practices of the user should be followed. However, ABB's recommendation is as follows:

Every second to third year

- Visual inspection of all equipment.
- Removal of dust on ventilation louvres and IEDs if necessary.
- Periodic maintenance test for protection IEDs of object where no redundant protections are provided.

Every four to six years

- Periodic maintenance test for protection IEDs of objects with redundant protection system.



First maintenance test should always be carried out after the first half year of service.



When protection IEDs are combined with built-in control, the test interval can be increased drastically, up to for instance 15 years, because the IED continuously reads service values, operates the breakers, and so on.

7.1.2.1

Visual inspection

Prior to testing, the protection IEDs should be inspected to detect any visible damage that may have occurred (for example, dirt or moisture deposits, overheating). Should burned contacts be observed when inspecting the IEDs, a diamond file or an extremely fine file can be used to polish the contacts. Emery cloth or similar products must not be used as insulating grains of abrasive may be deposited on the contact surfaces and cause failure.

Make sure that all IEDs are equipped with covers.

7.1.2.2

Maintenance tests

To be made after the first half year of service, then with the cycle as proposed above and after any suspected maloperation or change of the IED setting.

Testing of protection IEDs shall preferably be made with the primary circuit de-energized. The IED cannot protect the circuit during testing. Trained personnel may

test one IED at a time on live circuits where redundant protection is installed and de-energization of the primary circuit is not allowed.

ABB protection IEDs are preferably tested by aid of components from the COMBITEST testing system or FT test systems described in information B03-9510 E. Main components are RTXP 8/18/24 test switch located to the left in each protection IED and RTXH 8/18/24 test handle, which is inserted in test switch at secondary testing. All necessary operations such as opening of trip circuits, short-circuiting of current circuits and opening of voltage circuits are automatically performed in the right order to allow for simple and safe secondary testing even with the object in service.

Important components of FT test system are FT1, FTx, FT19, FT19RS, FR19RX switches and assemblies as well as FT-1 test plug.

Preparation

Before starting maintenance testing, the test engineers should scrutinize applicable circuit diagrams and have the following documentation available:

- Test instructions for protection IEDs to be tested
- Test records from previous commissioning and maintenance tests
- List of valid settings
- Blank test records to fill in measured values

Recording

It is of utmost importance to carefully record the test results. Special test sheets covering the frequency of test, date of test and achieved test values should be used. IED setting list and protocols from previous tests should be available and all results should be compared for differences. At component failures, spare equipment is used and set to the requested value. A note of the exchange is made and the new measured values are recorded. Test records for several years of testing should be stored in a common file for a station, or a part of a station, to give a simple overview of the period of testing and achieved test values. These test records are valuable when analysis of service disturbances shall be done.

Secondary injection

The periodic maintenance test is done by secondary injection from a portable test set. Each protection shall be tested according to the secondary injection test information for the specific protection IED. Only the setting values adopted shall be checked for each protection function. If the discrepancy between obtained value and requested set value is too big the setting should be adjusted, the new value recorded and a note should be made in the test record.

Alarm test

When inserting the test handle of RTXP or using FT plugs, the alarm and event signalling is normally blocked. This is done in the IED by setting the event reporting to

Disabled during the test. This can be done when the test handle is inserted or the IED is set to test mode from the local HMI. At the end of the secondary injection test it should be checked that the event and alarm signalling is correct by activating the events and performing some selected tests.

Self supervision check

Once secondary testing has been completed, it should be checked that no self-supervision signals are activated continuously or sporadically. Especially check the time synchronization system, GPS or other, and communication signals, both station communication and remote communication.

Trip circuit check

When the protection IED undergoes an operational check, a tripping pulse is normally obtained on one or more of the output contacts and preferably on the test switch. The healthy circuit is of utmost importance for the protection operation. If the circuit is not provided with a continuous trip-circuit supervision, it is possible to check that circuit is really closed when the test-plug handle has been removed by using a high-ohmic voltmeter and measuring between the plus and the trip output on the panel. The measurement is then done through the tripping magnet of the circuit breaker and therefore the complete tripping circuit is checked.



Note that the breaker must be closed.



Please observe that the test system does not provide built-in security during this test. If the instrument should be set on Amp instead of Volts, the circuit breaker naturally is tripped, therefore, great care is necessary.

Trip circuit from trip IEDs to circuit breaker is often supervised by trip-circuit supervision. It can then be checked that a circuit is healthy by opening tripping output terminals in the cubicle. When the terminal is opened, an alarm shall be achieved on the signal system after a delay of some seconds.



Remember to close the circuit directly after the test and tighten the terminal carefully.

Measurement of service currents

After a maintenance test it is recommended to measure the service currents and service voltages recorded by the protection IED. The service values are checked on the local

HMI or in PCM600. Ensure that the correct values and angles between voltages and currents are recorded. Also check the direction of directional functions such as Distance and directional overcurrent functions.

For transformer differential protection, the achieved differential current value is dependent on the tap changer position and can vary between less than 1% up to perhaps 10% of rated current. For line differential functions, the capacitive charging currents can normally be recorded as a differential current.

The zero-sequence current to ground-fault protection IEDs should be measured. The current amounts normally very small but normally it is possible to see if the current circuit is "alive".

The neutral-point voltage to an ground-fault protection IED is checked. The voltage is normally 0.1 to 1V secondary. However, voltage can be considerably higher due to harmonics. Normally a CVT secondary can have around 2.5 - 3% third-harmonic voltage.

Restoring

Maintenance is very important to improve the availability of the protection system by detecting failures before the protection is required to operate. There is however little point in testing healthy equipment and then putting it back into service with an open terminal, with a removed fuse or open miniature circuit breaker with an open connection, wrong setting, and so on.

Thus a list should be prepared of all items disturbed during test so that all can be put back into service quickly and without overlooking something. It should be put back into service item by item and signed by the responsible engineer.

Section 8 Troubleshooting

8.1 Fault tracing

8.1.1 Identifying hardware errors

1. Check the module with an error.
 - Check the general IED status in **Main menu/Diagnostics/IED status/General** for a faulty hardware module.
 - Check the history of changes in internal event list in **Main menu/Diagnostics/Internal Events**.
2. Inspect the IED visually.
 - Inspect the IED visually to find any physical error causes.
 - If you can find some obvious physical damage, contact ABB for repair or replacement actions.
3. Check whether the error is external or internal.
 - Check that the error is not caused by external origins.
 - Remove the wiring from the IED and test the input and output operation with an external test device.
 - If the problem remains, contact ABB for repair or replacement actions.

8.1.2 Identifying runtime errors

1. Check the error origin from IED's internal event list **Main menu/Diagnostics/IED status/General**.
2. Reboot the IED and recheck the supervision events to see if the fault has cleared.
3. In case of persistent faults, contact ABB for corrective actions.

8.1.3 Identifying communication errors

Communication errors are normally communication interruptions or synchronization message errors due to communication link breakdown.

- Check the IEC61850 and DNP3 communication status in internal event list in **Main menu/Diagnostics/IED Status/General**.
- In case of persistent faults originating from IED's internal faults such as component breakdown, contact ABB for repair or replacement actions.

8.1.3.1

Checking the communication link operation

There are several different communication links on the product. First check that all communication ports that are used for communication are turned on.

1. Check the front communication port RJ-45.
 - 1.1. Check that the uplink LED is lit with a steady green light.
The uplink LED is located on the LHMI above the RJ-45 communication port on the left. The port is used for direct electrical communication to a PC connected via a crossed-over Ethernet cable.
 - 1.2. Check the communication status of the front port via the LHMI in **Main menu/Test/Function status/Communication/1:DOSFRNT/Outputs**.
Check that the *LINKUP* value is 1, that is, the communication is working. When the value is 0, there is no communication link.



The rear port connector X0 is used for connecting an external HMI to the IED. If the *LINKUP* value is 0 for front port, there is no communication link via port X0. Do not use rear port connector X0 if the IED is equipped with an LHMI.

2. Check the communication status of the rear port X1 via the LHMI in **Main menu/Test/Function status/Communication/1:DOSLAN1/Outputs**.
The X1 communication port on the rear side of the IED is for optical Ethernet via LC connector or electrical via RJ-45 connector of the IEC 61850-8-1 station bus communication.
 - Check that the *LINKUP* value is 1, that is, the communication is working. When the value is 0, there is no communication link.

8.1.3.2

Checking the time synchronization



- Select **Main menu/Diagnostics/IED status/General** and check the status of the time synchronization on **Time Synch**.
The *Time synch* value is *Normal* when the synchronization is in order.



Note that the time synchronization source has to be activated. Otherwise the value is always *Normal*.

8.1.4 Running the display test

To run the display test, either use the push buttons or start the test via the menu.

- Select **Main menu/Tests/LED test**.
- Press simultaneously  and .

All the LEDs are tested by turning them on simultaneously. The display shows a set of patterns so that all the pixels are activated. After the test, the display returns to normal state.

8.2 Indication messages

8.2.1 Internal faults

When the Ready LED indicates an internal fault by flashing, the message associated with the fault is found in the internal event list in the LHMI menu **Main menu/Diagnostics/Internal events**. The message includes the date, time, description and signal state for the fault. The internal event list is not updated dynamically. The list is updated by leaving the **Internal events** menu and then selecting it again. The current status of the internal fault signals can also be checked via the LHMI in **Main menu/Diagnostics/IED status**.

Different actions are taken depending on the severity of the fault. If the fault is found to be permanent, the IED stays in internal fault mode. The IED continues to perform internal tests during the fault situation.

When a fault appears, the fault indication message is to be recorded and stated when requesting support or service.

Table 5: *Internal fault indications*

Fault indication	Additional information
Internal Fault Real Time Clock Error	Hardware error with the real time clock.
Internal Fault Runtime Exec. Error	One or more of the application threads are not working properly.
Internal Fault SW Watchdog Error	This signal will be activated when the terminal has been under too heavy load for at least 5 minutes.
Internal Fault Runtime App Error	One or more of the application threads are not in an expected state.
Internal Fault File System Error	A file system error has occurred.
Internal Fault TRM-Error	A TRM card error has occurred. The instance number is displayed at the end of the fault indication.
Internal Fault COM-Error	A COM card error has occurred. The instance number is displayed at the end of the fault indication.
Internal Fault PSM-Error	A PSM card error has occurred. The instance number is displayed at the end of the fault indication.

8.2.2

Warnings

The warning message associated with the fault is found in the internal event list in the LHMI menu **Main menu/Diagnostics/Internal events**. The message includes the date, time, description and signal state for the fault. The current status of the internal fault signals can also be checked via the LHMI in **Main menu/Diagnostics/IED status/General**.

When a fault appears, record the fault indication message and state it when ordering service.

Table 6: *Warning indications*

Warning indication	Additional information
Warning IEC 61850 Error	IEC 61850 has not succeeded in some actions such as reading the configuration file, startup etc.
Warning DNP3 Error	Error in DNP3 communication.

8.2.3 Additional indications

The additional indication messages do not activate internal fault or warning.

The messages are listed in the LHMI menu under the event list. The signal status data is found under the IED status and in the internal event list.

Table 7: *Additional indications*

Warning indication	Additional information
Time Synch Error	Source of the time synchronization is lost or time system has made a time reset.
BATTERY1 Error	Auxiliary power is disconnected.
Settings Changed	Settings have been changed.
Setting Groups Changed	Setting group has been changed.

8.3 Correction procedures

8.3.1 Changing and setting the password

The password can only be set with PCM600.



For more information, see PCM600 documentation.

8.3.2 Identifying IED application problems

Navigate to the appropriate menu in the LHMI to identify possible problems.

- Check that the function is on.
- Check that the correct setting group (1 to 4) is activated.
- Check the blocking.
- Check the mode.
- Check the measurement value.
- Check the connection to trip and DFR functions.
- Check the channel settings.

8.3.2.1

Inspecting the wiring

The physical inspection of wiring connections often reveals the wrong connection for phase currents or voltages. However, even though the phase current or voltage connections to IED terminals might be correct, wrong polarity of one or more measurement transformers can cause problems.

- Check the current or voltage measurements and their phase information from **Main menu/Measurements/Analog primary values** or **Analog secondary voltages**.
- Check that the phase information and phase shift between phases is correct.
- Correct the wiring if needed.
 - Change the parameter *Negation* in **Configuration/Analog modules/3PhaseAnalogGroup/1:SMAI_20_n** (n= the number of the SMAI used).



Changing the *Negation* parameter is not recommended without special skills.

- In PCM600, change the parameter *CTStarPointn* (n= the number on the current input) under the parameter settings for each current input.
- Check the actual state of the connected binary inputs.
 - In LHMI, select **Main menu/Tests/Binary input values/Binary input modules**. Then navigate to the board with the actual binary input to be checked.
 - With PCM600, right-click the product and select **Signal Monitoring**. Then navigate to the actual I/O board and to the binary input in question. The activated input signal is indicated with a yellow-lit diode.
- Measure output contacts using the voltage drop method of applying at least the minimum contact load given for the output relays in the technical data, for example 100 mA at 24 V AC/DC.



Output relays, especially power output relays, are designed for breaking high currents. Due to this, layers of high resistance may appear on the surface of the contacts. Do not determine proper functionality of connectivity or contact resistance by measuring with a regular hand-held ohm meter.

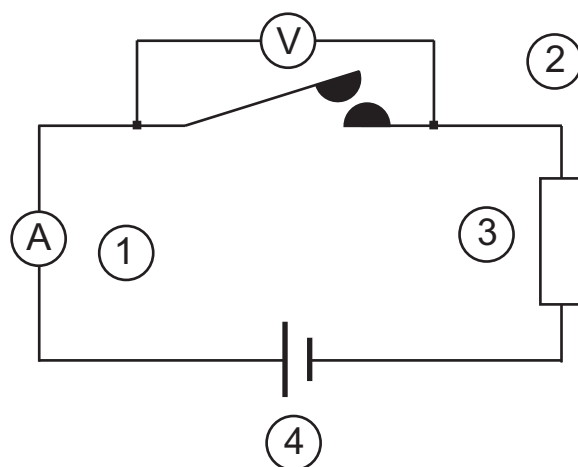


Figure 17: Testing output contacts using the voltage drop method

- 1 Contact current
- 2 Contact voltage drop
- 3 Load
- 4 Supply voltage

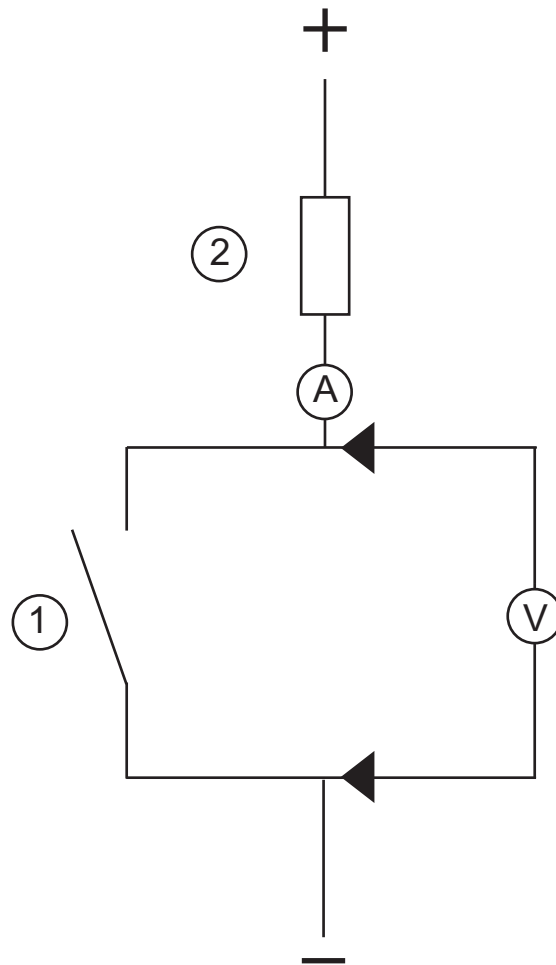





Figure 18: Testing a trip contact

- 1 Trip contact under test
- 2 Current limiting resistor

- To check the status of the output circuits driving the output relay via the LHMI, select **Main menu/Tests/Binary output values/Binary output modules** and then navigate to the board with the actual binary output to be checked.
- Test and change the relay state manually.
 1. To set the IED to test mode, select **Main menu/Tests/IED testmode1:TESTMODE/TestMode** and set the parameter to *enable*.
 2. To operate or force the output relay to operate, select and then navigate to the board with the actual binary output relay to be operated/forced.
 3. Select the BOn_PO to be operated/forced and use  and  or  to operate the actual output relay.

In PCM600, only the result of these operations can be checked by right-clicking the product and selecting Signal Monitoring tool and then navigating to the actual I/O-board and the binary input in question. The activated output signal is indicated with a yellow-lit diode. Each BOn_PO is represented by two signals. The first signal in LHMI is the actual value 1 or 0 of the output, and in PCM600 a lit or dimmed diode. The second signal is the status Normal or Forced. Forced status is only achieved when the BO is set to *Forced* or operated on the LHMI.



Set the parameter *TestMode* to *disable* after completing these tests. The Pickup LED stops flashing when the relay is no longer in test mode.

An initially high contact resistance does not cause problems as it is reduced quickly by the electrical cleaning effect of fritting and thermal destruction of layers, bringing the contact resistance back to the mOhm range. As a result, practically the full voltage is available at the load.

Section 9 Glossary

AC	Alternating current
ACT	Application configuration tool within PCM600
A/D converter	Analog-to-digital converter
ADBS	Amplitude deadband supervision
AI	Analog input
ANSI	American National Standards Institute
AR	Autoreclosing
ASCT	Auxiliary summation current transformer
ASD	Adaptive signal detection
AWG	American Wire Gauge standard
BI	Binary input
BOS	Binary outputs status
BR	External bistable relay
BS	British Standards
CAN	Controller Area Network. ISO standard (ISO 11898) for serial communication
CB	Circuit breaker
CCITT	Consultative Committee for International Telegraph and Telephony. A United Nations-sponsored standards body within the International Telecommunications Union.
CCVT	Capacitive Coupled Voltage Transformer
Class C	Protection Current Transformer class as per IEEE/ ANSI
CMPPS	Combined megapulses per second
CMT	Communication Management tool in PCM600
CO cycle	Close-open cycle
Codirectional	Way of transmitting G.703 over a balanced line. Involves two twisted pairs making it possible to transmit information in both directions

COMTRADE	Standard Common Format for Transient Data Exchange format for Disturbance recorder according to IEEE/ANSI C37.111, 1999 / IEC60255-24
Contra-directional	Way of transmitting G.703 over a balanced line. Involves four twisted pairs, two of which are used for transmitting data in both directions and two for transmitting clock signals
CPU	Central processor unit
CR	Carrier receive
CRC	Cyclic redundancy check
CROB	Control relay output block
CS	Carrier send
CT	Current transformer
CVT or CCVT	Capacitive voltage transformer
DAR	Delayed autoreclosing
DARPA	Defense Advanced Research Projects Agency (The US developer of the TCP/IP protocol etc.)
DBDL	Dead bus dead line
DBLL	Dead bus live line
DC	Direct current
DFC	Data flow control
DFT	Discrete Fourier transform
DHCP	Dynamic Host Configuration Protocol
DIP-switch	Small switch mounted on a printed circuit board
DI	Digital input
DLLB	Dead line live bus
DNP	Distributed Network Protocol as per IEEE/ANSI Std. 1379-2000
DR	Disturbance recorder
DRAM	Dynamic random access memory
DRH	Disturbance report handler
DSP	Digital signal processor
DTT	Direct transfer trip scheme
EHV network	Extra high voltage network
EIA	Electronic Industries Association

EMC	Electromagnetic compatibility
EMF	(Electric Motive Force)
EMI	Electromagnetic interference
EnFP	End fault protection
EPA	Enhanced performance architecture
ESD	Electrostatic discharge
FCB	Flow control bit; Frame count bit
FOX 20	Modular 20 channel telecommunication system for speech, data and protection signals
FOX 512/515	Access multiplexer
FOX 6Plus	Compact time-division multiplexer for the transmission of up to seven duplex channels of digital data over optical fibers
G.703	Electrical and functional description for digital lines used by local telephone companies. Can be transported over balanced and unbalanced lines
GCM	Communication interface module with carrier of GPS receiver module
GDE	Graphical display editor within PCM600
GI	General interrogation command
GIS	Gas-insulated switchgear
GOOSE	Generic object-oriented substation event
GPS	Global positioning system
HDLC protocol	High-level data link control, protocol based on the HDLC standard
HFBR connector type	Plastic fiber connector
HMI	Human-machine interface
HSAR	High speed autoreclosing
HV	High-voltage
HVDC	High-voltage direct current
IDBS	Integrating deadband supervision
IEC	International Electrical Committee
IEC 60044-6	IEC Standard, Instrument transformers – Part 6: Requirements for protective current transformers for transient performance

IEC 61850	Substation automation communication standard
IEC 61850-8-1	Communication protocol standard
IEEE	Institute of Electrical and Electronics Engineers
IEEE 802.12	A network technology standard that provides 100 Mbits/s on twisted-pair or optical fiber cable
IEEE P1386.1	PCI Mezzanine Card (PMC) standard for local bus modules. References the CMC (IEEE P1386, also known as Common Mezzanine Card) standard for the mechanics and the PCI specifications from the PCI SIG (Special Interest Group) for the electrical EMF (Electromotive force).
IEEE 1686	Standard for Substation Intelligent Electronic Devices (IEDs) Cyber Security Capabilities
IED	Intelligent electronic device
I-GIS	Intelligent gas-insulated switchgear
Instance	When several occurrences of the same function are available in the IED, they are referred to as instances of that function. One instance of a function is identical to another of the same kind but has a different number in the IED user interfaces. The word "instance" is sometimes defined as an item of information that is representative of a type. In the same way an instance of a function in the IED is representative of a type of function.
IP	1. Internet protocol. The network layer for the TCP/IP protocol suite widely used on Ethernet networks. IP is a connectionless, best-effort packet-switching protocol. It provides packet routing, fragmentation and reassembly through the data link layer. 2. Ingression protection, according to IEC standard
IP 20	Ingression protection, according to IEC standard, level IP20- Protected against solid foreign objects of 12.5mm diameter and greater.
IP 40	Ingression protection, according to IEC standard, level IP40- Protected against solid foreign objects of 1mm diameter and greater.
IP 54	Ingression protection, according to IEC standard, level IP54-Dust-protected, protected against splashing water.
IRF	Internal failure signal
IRIG-B:	InterRange Instrumentation Group Time code format B, standard 200
ITU	International Telecommunications Union

LAN	Local area network
LIB 520	High-voltage software module
LCD	Liquid crystal display
LDD	Local detection device
LED	Light-emitting diode
MCB	Miniature circuit breaker
MCM	Mezzanine carrier module
MVB	Multifunction vehicle bus. Standardized serial bus originally developed for use in trains.
NCC	National Control Centre
OCO cycle	Open-close-open cycle
OCF	Overcurrent protection
OLTC	On-load tap changer
OV	Over-voltage
Overreach	A term used to describe how the relay behaves during a fault condition. For example, a distance relay is overreaching when the impedance presented to it is smaller than the apparent impedance to the fault applied to the balance point, that is, the set reach. The relay “sees” the fault but perhaps it should not have seen it.
PCI	Peripheral component interconnect, a local data bus
PCM	Pulse code modulation
PCM600	Protection and control IED manager
PC-MIP	Mezzanine card standard
PMC	PCI Mezzanine card
POR	Permissive overreach
POTT	Permissive overreach transfer trip
Process bus	Bus or LAN used at the process level, that is, in near proximity to the measured and/or controlled components
PSM	Power supply module
PST	Parameter setting tool within PCM600
PT ratio	Potential transformer or voltage transformer ratio
PUTT	Permissive underreach transfer trip
RASC	Synchrocheck relay, COMBIFLEX

RCA	Relay characteristic angle
RFPP	Resistance for phase-to-phase faults Resistance for phase-to-ground faults
RISC	Reduced instruction set computer
RMS value	Root mean square value
RS422	A balanced serial interface for the transmission of digital data in point-to-point connections
RS485	Serial link according to EIA standard RS485
RTC	Real-time clock
RTU	Remote terminal unit
SA	Substation Automation
SBO	Select-before-operate
SC	Switch or push button to close
SCS	Station control system
SCADA	Supervision, control and data acquisition
SCT	System configuration tool according to standard IEC 61850
SDU	Service data unit
SMA connector	Subminiature version A, A threaded connector with constant impedance.
SMT	Signal matrix tool within PCM600
SMS	Station monitoring system
SNTP	Simple network time protocol – is used to synchronize computer clocks on local area networks. This reduces the requirement to have accurate hardware clocks in every embedded system in a network. Each embedded node can instead synchronize with a remote clock, providing the required accuracy.
SRY	Switch for CB ready condition
ST	Switch or push button to trip
Starpoint	Neutral/Wye point of transformer or generator
SVC	Static VAr compensation
TC	Trip coil
TCS	Trip circuit supervision
TCP	Transmission control protocol. The most common transport layer protocol used on Ethernet and the Internet.

TCP/IP	Transmission control protocol over Internet Protocol. The de facto standard Ethernet protocols incorporated into 4.2BSD Unix. TCP/IP was developed by DARPA for Internet working and encompasses both network layer and transport layer protocols. While TCP and IP specify two protocols at specific protocol layers, TCP/IP is often used to refer to the entire US Department of Defense protocol suite based upon these, including Telnet, FTP, UDP and RDP.
TNC connector	Threaded Neill-Concelman, a threaded constant impedance version of a BNC connector
TPZ, TPY, TPX, TPS	Current transformer class according to IEC
UMT	User management tool
Underreach	A term used to describe how the relay behaves during a fault condition. For example, a distance relay is underreaching when the impedance presented to it is greater than the apparent impedance to the fault applied to the balance point, that is, the set reach. The relay does not "see" the fault but perhaps it should have seen it. See also Overreach.
UTC	Coordinated Universal Time. A coordinated time scale, maintained by the Bureau International des Poids et Mesures (BIPM), which forms the basis of a coordinated dissemination of standard frequencies and time signals. UTC is derived from International Atomic Time (TAI) by the addition of a whole number of "leap seconds" to synchronize it with Universal Time 1 (UT1), thus allowing for the eccentricity of the Earth's orbit, the rotational axis tilt (23.5 degrees), but still showing the Earth's irregular rotation, on which UT1 is based. The Coordinated Universal Time is expressed using a 24-hour clock, and uses the Gregorian calendar. It is used for aeroplane and ship navigation, where it is also sometimes known by the military name, "Zulu time." "Zulu" in the phonetic alphabet stands for "Z", which stands for longitude zero.
UV	Undervoltage
WEI	Weak end infeed logic
VT	Voltage transformer
X.21	A digital signalling interface primarily used for telecom equipment
3I₀	Three times zero-sequence current. Often referred to as the residual or the -fault current

$3V_0$

Three times the zero sequence voltage. Often referred to as the residual voltage or the neutral point voltage

Contact us

ABB Inc.

1021 Main Campus Drive
Raleigh, NC 27606, USA
Phone Toll Free: 1-800-HELP-365,
menu option #8

ABB Inc.

3450 Harvester Road
Burlington, ON L7N 3W5, Canada
Phone Toll Free: 1-800-HELP-365,
menu option #8

ABB Mexico S.A. de C.V.

Paseo de las Americas No. 31 Lomas
Verdes 3a secc.
53125, Naucalpan, Estado De Mexico,
MEXICO
Phone (+1) 440-585-7804, menu
option #8