

Design verification part I

Manufacturer of the ASSEMBLY

Company stamp

Customer:

Order number:

Project:

Type:

Low-voltage switchgear and controlgear ASSEMBLIES

- Power Switchgear Combination (PSC) Design verification pursuant to IEC 61439-2 / VDE
- Sub-distribution board (DBO) Design verification pursuant to IEC 61439-3 / VDE

ASSEMBLY rating data:

(Required data from step 1: Collecting the requisite data)

Rated voltage:	_____ V	Short-circuit withstand strength
Rated frequency:	_____ Hz	I_{cc} : _____ kA
Network system:	<input type="checkbox"/> TN <input type="checkbox"/> TT <input type="checkbox"/> IT	I_{cw} : _____ kA
Rated current of the ASSEMBLY I_{nA} :	_____ A	I_{pk} : _____ kA
Rated impulse withstand voltage (U_{imp}):	_____ kV	

(enter only applicable values)

Verified for (see Annex – Part II):

Design verification performed:

Place / Date

Name and signature of the performer

Place / Date

Name and signature of the tester