

**Event list for REX**

Document ID: 1MRS752000-RTI  
 Revision: F  
 Description: REX521 event list file (max set)

Channel Code	Values	Weighting	Default	IEC address	GI table	DB name	Event State	Event Reason	RX	Scada address
Technical data of measuring channels										
/*CH001me / Rev C MEAS */										
0 E32	-	1	0	99	0 -	-	-	-	-	2099
0 E33	-	2	0	99	0 -	-	-	-	-	2099
0 E34	-	4	0	99	0 -	-	-	-	-	2099
0 E35	-	8	0	99	0 -	-	-	-	-	2099
0 E36	-	16	0	99	0 -	-	-	-	-	2099
0 E37	-	32	0	99	0 -	-	-	-	-	2099
0 E38	-	64	0	99	0 -	-	-	-	-	2099
0 E39	-	128	0	99	0 -	-	-	-	-	2099
0 E40	-	256	0	99	0 -	-	-	-	-	2099
0 E41	-	512	0	99	0 -	-	-	-	-	2099
0 E42	-	1024	0	99	0 -	-	-	-	-	2099
0 E43	-	2048	0	99	0 -	-	-	-	-	2099
0 E44	-	4096	0	99	0 -	-	-	-	-	2099
0 E46	-	16384	0	99	0 -	-	-	-	-	2099
0 E47	-	32768	0	99	0 -	-	-	-	-	2099
0 E48	-	65536	0	99	0 -	-	-	-	-	2099
0 E49	-	131072	0	99	0 -	-	-	-	-	2099
0 E50	EV_NODAT	262144	1	25	0 -	-	Startup	Module	CMS	2025
0 E51	EV_NODAT	524288	1	26	0 -	-	Overflow	Event buffer	ALA	2026
Default mask=786432										
General parameters for control commands										
/*CTRL / Rev D CTRL */										
2 E0	EV_2BIT_1	1	1	0	1	F002V005	Disable	Recent control position	LRS	2200
2 E1	EV_2BIT_1	2	1	0	0	F002V005	Local	Recent control position	LRS	2200
2 E2	EV_2BIT_1	4	1	0	0	F002V005	Remote	Recent control position	LRS	2200
2 E3	-	8	0	99	-	-	-	-	-	2299
2 E4	EV_1BIT	16	1	1	1	F002V004	Inactive	Interlocking bypass mode	INH	2201
2 E5	EV_1BIT	32	1	1	0	F002V004	Active	Interlocking bypass mode	INH	2201
Default mask=55										
Time management										
/*TMA / Rev A TMA */										
5 E0	EV_NODAT	1	0	0	0 -	-	-	Impermissible deviation of time	ALA	2500
5 E1	EV_NODAT	2	0	1	0 -	-	-	Real Time Clock operation failure	ALA	2501
Default mask=0										

## General parameters

/\*GP / Rev D GP \*/

6 E0	-	1	0	99	0 -	-	-	-	2699
6 E1	EV_NODAT	2	0	1	0 -	-	DSP will be restarted	-	2601
	Default mask=0								

## Configuration specific parameters - constant part

/\*CONFIG2 / Rev C CONFIG2 \*/

10 E0	EV_NODAT	1	1	0	0 -	Reset	TRIP signal of MasterTrip	TRP	3000
10 E1	EV_NODAT	2	1	0	0 -	Activated	TRIP signal of MasterTrip	TRP	3000
10 E2	EV_NODAT	4	1	1	0 -	Reset	TRIP signal of ExternalTrip	TRP	3001
10 E3	EV_NODAT	8	1	1	0 -	Activated	TRIP signal of ExternalTrip	TRP	3001
10 E4	-	16	0	99	0 -	-	-	-	3099
10 E5	EV_NODAT	32	1	2	0 -	Unsuccessful	Manual control	INS	3002
10 E6	EV_NODAT	64	1	3	1 F010V020	Reset	HSP01 Lockout	INS	3003
10 E7	EV_NODAT	128	1	3	0 F010V020	Activated	HSP01 Lockout	INS	3003
10 E8	-	256	0	99	0 -	-	-	-	3099
10 E9	EV_NODAT	512	1	4	0 -	Activated	Interlocking	INS	3004
10 E10	EV_NODAT	1024	1	5	1 F010V021	Reset	External Lockout	INS	3005
10 E11	EV_NODAT	2048	1	5	0 F010V021	Activated	External Lockout	INS	3005
10 E12	EV_NODAT	4096	0	6	0 F010V030	Reset	Indication reset request	-	3006
10 E13	EV_NODAT	8192	0	6	0 F010V030	Activated	Indication reset request	-	3006
	Default mask=3823								

## Digital input polling

/\*DIPO / Rev A DIPO \*/

11 E0	EV_NODAT	1	0	1	1 F011I001	Reset	Digital input 1	INS	3101
11 E1	EV_NODAT	2	0	1	0 F011I001	Activated	Digital input 1	INS	3101
11 E2	EV_NODAT	4	0	2	1 F011I002	Reset	Digital input 2	INS	3102
11 E3	EV_NODAT	8	0	2	0 F011I002	Activated	Digital input 2	INS	3102
11 E4	EV_NODAT	16	0	3	1 F011I003	Reset	Digital input 3	INS	3103
11 E5	EV_NODAT	32	0	3	0 F011I003	Activated	Digital input 3	INS	3103
11 E6	EV_NODAT	64	0	4	1 F011I004	Reset	Digital input 4	INS	3104
11 E7	EV_NODAT	128	0	4	0 F011I004	Activated	Digital input 4	INS	3104
11 E8	EV_NODAT	256	0	5	1 F011I005	Reset	Digital input 5	INS	3105
11 E9	EV_NODAT	512	0	5	0 F011I005	Activated	Digital input 5	INS	3105
11 E10	EV_NODAT	1024	0	6	1 F011I006	Reset	Digital input 6	INS	3106
11 E11	EV_NODAT	2048	0	6	0 F011I006	Activated	Digital input 6	INS	3106
11 E12	EV_NODAT	4096	0	7	1 F011I007	Reset	Digital input 7	INS	3107
11 E13	EV_NODAT	8192	0	7	0 F011I007	Activated	Digital input 7	INS	3107
11 E14	EV_NODAT	16384	0	8	1 F011I008	Reset	Digital input 8	INS	3108
11 E15	EV_NODAT	32768	0	8	0 F011I008	Activated	Digital input 8	INS	3108
11 E16	EV_NODAT	65536	0	9	1 F011I009	Reset	Digital input 9	INS	3109
11 E17	EV_NODAT	131072	0	9	0 F011I009	Activated	Digital input 9	INS	3109
	Default mask=0								

Digital output handling  
/\*DOHA / Rev B DOHA \*/

12 E0	EV_NODAT	1	1	1	0 F012V017	OFF	Test mode	INS	3201
12 E1	EV_NODAT	2	1	1	0 F012V017	ON	Test mode	INS	3201
12 E2	EV_NODAT	4	0	2	1 F012O001	Reset	Output relay 1	INS	3202
12 E3	EV_NODAT	8	0	2	0 F012O001	Activated	Output relay 1	INS	3202
12 E4	EV_NODAT	16	0	3	1 F012O002	Reset	Output relay 2	INS	3203
12 E5	EV_NODAT	32	0	3	0 F012O002	Activated	Output relay 2	INS	3203
12 E6	EV_NODAT	64	0	4	1 F012O003	Reset	Output relay 3	INS	3204
12 E7	EV_NODAT	128	0	4	0 F012O003	Activated	Output relay 3	INS	3204
12 E8	EV_NODAT	256	0	5	1 F012O004	Reset	Output relay 4	INS	3205
12 E9	EV_NODAT	512	0	5	0 F012O004	Activated	Output relay 4	INS	3205
12 E10	EV_NODAT	1024	0	6	1 F012O005	Reset	Output relay 5	INS	3206
12 E11	EV_NODAT	2048	0	6	0 F012O005	Activated	Output relay 5	INS	3206
12 E12	EV_NODAT	4096	0	7	1 F012O006	Reset	Output relay 6	INS	3207
12 E13	EV_NODAT	8192	0	7	0 F012O006	Activated	Output relay 6	INS	3207
Default mask=3									

Testing and self-supervision  
/\*TESU / Rev A TESU \*/

13 E0	EV_NODAT	1	0	1	1 F013I032	Reset	ACFail	INS	3301
13 E1	EV_NODAT	2	0	1	0 F013I032	Activated	ACFail	INS	3301
Default mask=0									

## Error handling

/\*ERHA / Rev C ERHA \*/

24 E0	EV_NODAT	1	1	0	0	-	-	Relay HSPO1 fault	ALA	4400
24 E1	EV_NODAT	2	1	1	0	-	-	Relay PO1 fault	ALA	4401
24 E2	EV_NODAT	4	1	2	0	-	-	Relay PO2 fault	ALA	4402
24 E3	EV_NODAT	8	1	3	0	-	-	Relay PO3 fault	ALA	4403
24 E4	EV_NODAT	16	1	4	0	-	-	Relay SO1 fault	ALA	4404
24 E5	EV_NODAT	32	1	5	0	-	-	Relay SO2 fault	ALA	4405
24 E6	EV_NODAT	64	1	6	0	-	-	Relay control fault	ALA	4406
24 E7	EV_NODAT	128	1	7	0	-	-	Relay test fault	ALA	4407
24 E8	EV_NODAT	256	1	8	0	-	-	NOV error	ALA	4408
24 E9	EV_NODAT	512	1	9	0	-	-	RAM error	ALA	4409
24 E10	EV_NODAT	1024	1	10	0	-	-	Overtemperature fault	ALA	4410
24 E11	EV_NODAT	2048	1	11	0	-	-	Voltage low 24V fault	ALA	4411
24 E12	EV_NODAT	4096	1	12	0	-	-	Voltage high 24V fault	ALA	4412
24 E13	EV_NODAT	8192	1	13	0	-	-	Voltage low +15V fault	ALA	4413
24 E14	EV_NODAT	16384	1	14	0	-	-	Voltage high +15V fault	ALA	4414
24 E15	EV_NODAT	32768	1	15	0	-	-	Voltage low -15V fault	ALA	4415
24 E16	EV_NODAT	65536	1	16	0	-	-	Voltage high -15V fault	ALA	4416
24 E17	EV_NODAT	131072	1	17	0	-	-	A/D conversion fault	ALA	4417
24 E18	EV_NODAT	262144	1	18	0	-	-	Channel stuck	ALA	4418
24 E19	EV_NODAT	524288	1	19	0	-	-	Unspecified	ALA	4419
24 E20	EV_NODAT	1048576	1	20	0	-	-	EEPROM update	ALA	4420
24 E21	EV_NODAT	2097152	1	21	0	-	-	Hardware mismatch	ALA	4421
24 E22	EV_NODAT	4194304	1	22	0	-	-	EEPROM error	ALA	4422
24 E23	EV_NODAT	8388608	1	23	0	-	-	Start up error	ALA	4423
24 E24	EV_NODAT	16777216	1	24	0	-	-	HMI error	ALA	4424
24 E25	-	33554432	0	99	0	-	-	-	-	4499
24 E26	-	67108864	0	99	0	-	-	-	-	4499
24 E27	-	134217728	0	99	0	-	-	-	-	4499
24 E28	EV_NODAT	268435456	1	28	0	-	Deactivated	IRF test	INS	4428
24 E29	EV_NODAT	536870912	1	28	0	-	Activated	IRF test	INS	4428
24 E30	EV_INT32	1073741824	1	29	1	F024V015	Deactivated	IRF error	INS	4429
24 E31	EV_INT32	2147483648	1	29	0	F024V015	Activated	IRF error	INS	4429

Default mask=4060086271

## Graphical MMI module (6x16 and 4x8)

/\*MMI / Rev D MMI \*/

27 E0	EV_NODAT	1	0	0	0	-	OFF	Backlight	-	4700
27 E1	EV_NODAT	2	0	0	0	-	ON	Backlight	-	4700
27 E2	EV_NODAT	4	0	1	0	-	-	Password changed	-	4701
27 E3	EV_NODAT	8	0	2	0	-	-	Setting done	-	4702
27 E4	EV_NODAT	16	0	3	0	-	-	Language changed	-	4703

Default mask=0

## 3-phase non-directional overcurrent function, low-set stage

/\*100031 / Rev D NOC3Low \*/

31 E0	EV_3BIT_1	1	1	0	0	F031O001	Reset	START signal from 3l> stage	STR	5100
31 E1	EV_3BIT_1	2	1	0	0	F031O001	Activated	START signal from 3l> stage	STR	5100
31 E2	EV_3BIT_1	4	1	1	0	F031O002	Reset	TRIP signal from 3l> stage	TRP	5101
31 E3	EV_3BIT_1	8	1	1	0	F031O002	Activated	TRIP signal from 3l> stage	TRP	5101
31 E4	EV_3BIT_1	16	1	2	0	F031O003	Reset	CBFP signal from 3l> stage	ALA	5102
31 E5	EV_3BIT_1	32	1	2	0	F031O003	Activated	CBFP signal from 3l> stage	ALA	5102
31 E6	EV_NODAT	64	0	3	0	F031I004	Reset	BS1 signal of 3l> stage	BLK	5103
31 E7	EV_NODAT	128	0	3	0	F031I004	Activated	BS1 signal of 3l> stage	BLK	5103
31 E8	EV_NODAT	256	0	4	0	F031I005	Reset	BS2 signal of 3l> stage	BLK	5104
31 E9	EV_NODAT	512	0	4	0	F031I005	Activated	BS2 signal of 3l> stage	BLK	5104
31 E10	EV_NODAT	1024	0	5	0	-	Off	Test mode of 3l> stage	INS	5105
31 E11	EV_NODAT	2048	0	5	0	-	On	Test mode of 3l> stage	INS	5105

Default mask=63

## 3-phase non-directional overcurrent function, high-set stage

/\*100032 / Rev C NOC3High \*/

32 E0	EV_3BIT_1	1	1	0	0	F032O002	Reset	START signal from 3l>> stage	STR	5200
32 E1	EV_3BIT_1	2	1	0	0	F032O002	Activated	START signal from 3l>> stage	STR	5200
32 E2	EV_3BIT_1	4	1	1	0	F032O003	Reset	TRIP signal from 3l>> stage	TRP	5201
32 E3	EV_3BIT_1	8	1	1	0	F032O003	Activated	TRIP signal from 3l>> stage	TRP	5201
32 E4	EV_3BIT_1	16	1	2	0	F032O004	Reset	CBFP signal from 3l>> stage	ALA	5202
32 E5	EV_3BIT_1	32	1	2	0	F032O004	Activated	CBFP signal from 3l>> stage	ALA	5202
32 E6	EV_3BIT_1	64	0	3	0	F032O001	Reset	BSOUT signal from 3l>> stage	BLK	5203
32 E7	EV_3BIT_1	128	0	3	0	F032O001	Activated	BSOUT signal from 3l>> stage	BLK	5203
32 E8	EV_NODAT	256	0	4	0	F032I004	Reset	BS1 signal of 3l>> stage	BLK	5204
32 E9	EV_NODAT	512	0	4	0	F032I004	Activated	BS1 signal of 3l>> stage	BLK	5204
32 E10	EV_NODAT	1024	0	5	0	F032I005	Reset	BS2 signal of 3l>> stage	BLK	5205
32 E11	EV_NODAT	2048	0	5	0	F032I005	Activated	BS2 signal of 3l>> stage	BLK	5205
32 E12	EV_NODAT	4096	0	6	0	-	Off	Test mode of 3l>> stage	INS	5206
32 E13	EV_NODAT	8192	0	6	0	-	On	Test mode of 3l>> stage	INS	5206

Default mask=63

## 3-phase non-directional overcurrent protection function, instantaneous stage

/\*100033 / Rev C NOC3Inst \*/

33 E0	EV_3BIT_1	1	1	0	0	F033O002	Reset	START signal from 3l>>> stage	STR	5300
33 E1	EV_3BIT_1	2	1	0	0	F033O002	Activated	START signal from 3l>>> stage	STR	5300
33 E2	EV_3BIT_1	4	1	1	0	F033O003	Reset	TRIP signal from 3l>>> stage	TRP	5301
33 E3	EV_3BIT_1	8	1	1	0	F033O003	Activated	TRIP signal from 3l>>> stage	TRP	5301
33 E4	EV_3BIT_1	16	1	2	0	F033O004	Reset	CBFP signal from 3l>>> stage	ALA	5302
33 E5	EV_3BIT_1	32	1	2	0	F033O004	Activated	CBFP signal from 3l>>> stage	ALA	5302
33 E6	EV_3BIT_1	64	0	3	0	F033O001	Reset	BSOUT signal from 3l>>> stage	BLK	5303
33 E7	EV_3BIT_1	128	0	3	0	F033O001	Activated	BSOUT signal from 3l>>> stage	BLK	5303
33 E8	EV_NODAT	256	0	4	0	F033I004	Reset	BS1 signal of 3l>>> stage	BLK	5304
33 E9	EV_NODAT	512	0	4	0	F033I004	Activated	BS1 signal of 3l>>> stage	BLK	5304
33 E10	EV_NODAT	1024	0	5	0	F033I005	Reset	BS2 signal of 3l>>> stage	BLK	5305
33 E11	EV_NODAT	2048	0	5	0	F033I005	Activated	BS2 signal of 3l>>> stage	BLK	5305
33 E12	EV_NODAT	4096	0	6	0	-	Off	Test mode of 3l>>> stage	INS	5306
33 E13	EV_NODAT	8192	0	6	0	-	On	Test mode of 3l>>> stage	INS	5306

Default mask=63

## Three-phase transformer inrush and motor start-up current detector

/\*100034 / Rev D Inrush3 \*/

34 E0	EV_NODAT	1	1	0	0	F034O001	Reset	START signal from 3l2f> stage	STR	5400
34 E1	EV_NODAT	2	1	0	0	F034O001	Activated	START signal from 3l2f> stage	STR	5400
34 E2	EV_NODAT	4	0	1	0	-	Off	Test mode of 3l2f> stage	INS	5401
34 E3	EV_NODAT	8	0	1	0	-	On	Test mode of 3l2f> stage	INS	5401

Default mask=3

## Three-phase directional O/C function, low-set stage l&gt; -&gt;

/\*100035 / Rev F DOC6Low \*/

35 E0	EV_3BIT_1	1	1	0	0	F035O002	Reset	START signal from 3l> -> stage	STR	5500
35 E1	EV_3BIT_1	2	1	0	0	F035O002	Activated	START signal from 3l> -> stage	STR	5500
35 E2	EV_3BIT_1	4	1	1	0	F035O003	Reset	TRIP signal from 3l> -> stage	TRP	5501
35 E3	EV_3BIT_1	8	1	1	0	F035O003	Activated	TRIP signal from 3l> -> stage	TRP	5501
35 E4	EV_3BIT_1	16	1	2	0	F035O004	Reset	CBFP signal from 3l> -> stage	ALA	5502
35 E5	EV_3BIT_1	32	1	2	0	F035O004	Activated	CBFP signal from 3l> -> stage	ALA	5502
35 E6	EV_NODAT	64	0	3	0	F035O001	Reset	DIRECTION signal of 3l> -> stage	ALA	5503
35 E7	EV_NODAT	128	0	3	0	F035O001	Activated	DIRECTION signal of 3l> -> stage	ALA	5503
35 E8	EV_NODAT	256	0	4	0	F035I016	Reset	BS1 signal of 3l> -> stage	BLK	5504
35 E9	EV_NODAT	512	0	4	0	F035I016	Activated	BS1 signal of 3l> -> stage	BLK	5504
35 E10	EV_NODAT	1024	0	5	0	F035I017	Reset	BS2 signal of 3l> -> stage	BLK	5505
35 E11	EV_NODAT	2048	0	5	0	F035I017	Activated	BS2 signal of 3l> -> stage	BLK	5505
35 E12	EV_NODAT	4096	0	6	0	-	Off	Test mode of 3l> -> stage	INS	5506
35 E13	EV_NODAT	8192	0	6	0	-	On	Test mode of 3l> -> stage	INS	5506

Default mask=63

Three-phase directional O/C function, high-set stage l>> ->  
/\*100036 / Rev F DOC6High \*/

36 E0	EV_3BIT_1	1	1	0	0	F036O003	Reset	START signal from 3l>> -> stage	STR	5600
36 E1	EV_3BIT_1	2	1	0	0	F036O003	Activated	START signal from 3l>> -> stage	STR	5600
36 E2	EV_3BIT_1	4	1	1	0	F036O004	Reset	TRIP signal from 3l>> -> stage	TRP	5601
36 E3	EV_3BIT_1	8	1	1	0	F036O004	Activated	TRIP signal from 3l>> -> stage	TRP	5601
36 E4	EV_3BIT_1	16	1	2	0	F036O005	Reset	CBFP signal from 3l>> -> stage	ALA	5602
36 E5	EV_3BIT_1	32	1	2	0	F036O005	Activated	CBFP signal from 3l>> -> stage	ALA	5602
36 E6	EV_3BIT_1	64	0	3	0	F036O002	Reset	BSOUT signal from 3l>> -> stage	BLK	5603
36 E7	EV_3BIT_1	128	0	3	0	F036O002	Activated	BSOUT signal from 3l>> -> stage	BLK	5603
36 E8	EV_NODAT	256	0	4	0	F036O001	Reset	DIRECTION signal of 3l>> -> stage	ALA	5604
36 E9	EV_NODAT	512	0	4	0	F036O001	Activated	DIRECTION signal of 3l>> -> stage	ALA	5604
36 E10	EV_NODAT	1024	0	5	0	F036I016	Reset	BS1 signal of 3l>> -> stage	BLK	5605
36 E11	EV_NODAT	2048	0	5	0	F036I016	Activated	BS1 signal of 3l>> -> stage	BLK	5605
36 E12	EV_NODAT	4096	0	6	0	F036I017	Reset	BS2 signal of 3l>> -> stage	BLK	5606
36 E13	EV_NODAT	8192	0	6	0	F036I017	Activated	BS2 signal of 3l>> -> stage	BLK	5606
36 E14	EV_NODAT	16384	0	7	0	-	Off	Test mode of 3l>> -> stage	INS	5607
36 E15	EV_NODAT	32768	0	7	0	-	On	Test mode of 3l>> -> stage	INS	5607

Default mask=63

Non-directional earth-fault protection function, low-set stage  
/\*100038 / Rev E NEF1Low \*/

38 E0	EV_NODAT	1	1	0	0	F038O001	Reset	START signal from lo> stage	STR	5800
38 E1	EV_NODAT	2	1	0	0	F038O001	Activated	START signal from lo> stage	STR	5800
38 E2	EV_NODAT	4	1	1	0	F038O002	Reset	TRIP signal from lo> stage	TRP	5801
38 E3	EV_NODAT	8	1	1	0	F038O002	Activated	TRIP signal from lo> stage	TRP	5801
38 E4	EV_NODAT	16	1	2	0	F038O003	Reset	CBFP signal from lo> stage	ALA	5802
38 E5	EV_NODAT	32	1	2	0	F038O003	Activated	CBFP signal from lo> stage	ALA	5802
38 E6	EV_NODAT	64	0	3	0	F038I002	Reset	BS1 signal of lo> stage	BLK	5803
38 E7	EV_NODAT	128	0	3	0	F038I002	Activated	BS1 signal of lo> stage	BLK	5803
38 E8	EV_NODAT	256	0	4	0	F038I003	Reset	BS2 signal of lo> stage	BLK	5804
38 E9	EV_NODAT	512	0	4	0	F038I003	Activated	BS2 signal of lo> stage	BLK	5804
38 E10	EV_NODAT	1024	0	5	0	-	Off	Test mode of lo> stage	INS	5805
38 E11	EV_NODAT	2048	0	5	0	-	On	Test mode of lo> stage	INS	5805

Default mask=63

## Non-directional earth-fault protection function, high-set stage

/\*100039 / Rev C NEF1High \*/

39 E0	EV_NODAT	1	1	0	0 F039O001	Reset	START signal from lo>> stage	STR	5900
39 E1	EV_NODAT	2	1	0	0 F039O001	Activated	START signal from lo>> stage	STR	5900
39 E2	EV_NODAT	4	1	1	0 F039O002	Reset	TRIP signal from lo>> stage	TRP	5901
39 E3	EV_NODAT	8	1	1	0 F039O002	Activated	TRIP signal from lo>> stage	TRP	5901
39 E4	EV_NODAT	16	1	2	0 F039O003	Reset	CBFP signal from lo>> stage	ALA	5902
39 E5	EV_NODAT	32	1	2	0 F039O003	Activated	CBFP signal from lo>> stage	ALA	5902
39 E6	EV_NODAT	64	0	3	0 F039I002	Reset	BS1 signal of lo>> stage	BLK	5903
39 E7	EV_NODAT	128	0	3	0 F039I002	Activated	BS1 signal of lo>> stage	BLK	5903
39 E8	EV_NODAT	256	0	4	0 F039I003	Reset	BS2 signal of lo>> stage	BLK	5904
39 E9	EV_NODAT	512	0	4	0 F039I003	Activated	BS2 signal of lo>> stage	BLK	5904
39 E10	EV_NODAT	1024	0	7	0 -	Off	Test mode of lo>> stage	INS	5907
39 E11	EV_NODAT	2048	0	7	0 -	On	Test mode of lo>> stage	INS	5907

Default mask=63

## Directional earth-fault protection function, low-set stage

/\*100040 / Rev E DEF2Low \*/

40 E0	EV_NODAT	1	1	0	0 F040O001	Reset	START signal from lo> ->	STR	6000
40 E1	EV_NODAT	2	1	0	0 F040O001	Activated	START signal from lo> ->	STR	6000
40 E2	EV_NODAT	4	1	1	0 F040O002	Reset	TRIP signal from lo> ->	TRP	6001
40 E3	EV_NODAT	8	1	1	0 F040O002	Activated	TRIP signal from lo> ->	TRP	6001
40 E4	EV_NODAT	16	1	2	0 F040O003	Reset	CBFP signal from lo> ->	ALA	6002
40 E5	EV_NODAT	32	1	2	0 F040O003	Activated	CBFP signal from lo> ->	ALA	6002
40 E6	EV_NODAT	64	0	3	0 F040I005	Reset	BS1 signal of lo> ->	BLK	6003
40 E7	EV_NODAT	128	0	3	0 F040I005	Activated	BS1 signal of lo> ->	BLK	6003
40 E8	EV_NODAT	256	0	4	0 F040I006	Reset	BS2 signal of lo> ->	BLK	6004
40 E9	EV_NODAT	512	0	4	0 F040I006	Activated	BS2 signal of lo> ->	BLK	6004
40 E10	EV_NODAT	1024	0	5	0 -	Off	Test mode of lo> ->	INS	6005
40 E11	EV_NODAT	2048	0	5	0 -	On	Test mode of lo> ->	INS	6005

Default mask=63

## Directional earth-fault protection function, high-set stage

/\*100041 / Rev E DEF2High \*/

41 E0	EV_NODAT	1	1	0	0 F041O001	Reset	START signal from lo>> ->	STR	6100
41 E1	EV_NODAT	2	1	0	0 F041O001	Activated	START signal from lo>> ->	STR	6100
41 E2	EV_NODAT	4	1	1	0 F041O002	Reset	TRIP signal from lo>> ->	TRP	6101
41 E3	EV_NODAT	8	1	1	0 F041O002	Activated	TRIP signal from lo>> ->	TRP	6101
41 E4	EV_NODAT	16	1	2	0 F041O003	Reset	CBFP signal from lo>> ->	ALA	6102
41 E5	EV_NODAT	32	1	2	0 F041O003	Activated	CBFP signal from lo>> ->	ALA	6102
41 E6	EV_NODAT	64	0	3	0 F041I005	Reset	BS1 signal of lo>> ->	BLK	6103
41 E7	EV_NODAT	128	0	3	0 F041I005	Activated	BS1 signal of lo>> ->	BLK	6103
41 E8	EV_NODAT	256	0	4	0 F041I006	Reset	BS2 signal of lo>> ->	BLK	6104
41 E9	EV_NODAT	512	0	4	0 F041I006	Activated	BS2 signal of lo>> ->	BLK	6104
41 E10	EV_NODAT	1024	0	5	0 -	Off	Test mode of lo>> ->	INS	6105
41 E11	EV_NODAT	2048	0	5	0 -	On	Test mode of lo>> ->	INS	6105

Default mask=63

## Directional earth-fault protection function, instantaneous stage

/\*100042 / Rev E DEF2Inst \*/

42 E0	EV_NODAT	1	1	0	0 F042O001	Reset	START signal from lo>>> ->	STR	6200
42 E1	EV_NODAT	2	1	0	0 F042O001	Activated	START signal from lo>>> ->	STR	6200
42 E2	EV_NODAT	4	1	1	0 F042O002	Reset	TRIP signal from lo>>> ->	TRP	6201
42 E3	EV_NODAT	8	1	1	0 F042O002	Activated	TRIP signal from lo>>> ->	TRP	6201
42 E4	EV_NODAT	16	1	2	0 F042O003	Reset	CBFP signal from lo>>> ->	ALA	6202
42 E5	EV_NODAT	32	1	2	0 F042O003	Activated	CBFP signal from lo>>> ->	ALA	6202
42 E6	EV_NODAT	64	0	3	0 F042I005	Reset	BS1 signal of lo>>> ->	BLK	6203
42 E7	EV_NODAT	128	0	3	0 F042I005	Activated	BS1 signal of lo>>> ->	BLK	6203
42 E8	EV_NODAT	256	0	4	0 F042I006	Reset	BS2 signal of lo>>> ->	BLK	6204
42 E9	EV_NODAT	512	0	4	0 F042I006	Activated	BS2 signal of lo>>> ->	BLK	6204
42 E10	EV_NODAT	1024	0	5	0 -	Off	Test mode of lo>>> ->	INS	6205
42 E11	EV_NODAT	2048	0	5	0 -	On	Test mode of lo>>> ->	INS	6205

Default mask=63

## Residual overvoltage protection, low-set stage Uo&gt;

/\*100044 / Rev D ROV1Low \*/

44 E0	EV_NODAT	1	1	0	0 F044O001	Reset	START signal from Uo> stage	STR	6400
44 E1	EV_NODAT	2	1	0	0 F044O001	Activated	START signal from Uo> stage	STR	6400
44 E2	EV_NODAT	4	1	1	0 F044O002	Reset	TRIP signal from Uo> stage	TRP	6401
44 E3	EV_NODAT	8	1	1	0 F044O002	Activated	TRIP signal from Uo> stage	TRP	6401
44 E4	EV_NODAT	16	0	2	0 F044I002	Reset	BS1 signal of Uo> stage	BLK	6402
44 E5	EV_NODAT	32	0	2	0 F044I002	Activated	BS1 signal of Uo> stage	BLK	6402
44 E6	EV_NODAT	64	0	3	0 F044I003	Reset	BS2 signal of Uo> stage	BLK	6403
44 E7	EV_NODAT	128	0	3	0 F044I003	Activated	BS2 signal of Uo> stage	BLK	6403
44 E8	EV_NODAT	256	0	4	0 -	Off	Test mode of Uo> stage	INS	6404
44 E9	EV_NODAT	512	0	4	0 -	On	Test mode of Uo> stage	INS	6404

Default mask=15

## Residual overvoltage protection, high-set stage Uo&gt;&gt;

/\*100045 / Rev D ROV1High \*/

45 E0	EV_NODAT	1	1	0	0 F045O001	Reset	START signal from Uo>> stage	STR	6500
45 E1	EV_NODAT	2	1	0	0 F045O001	Activated	START signal from Uo>> stage	STR	6500
45 E2	EV_NODAT	4	1	1	0 F045O002	Reset	TRIP signal from Uo>> stage	TRP	6501
45 E3	EV_NODAT	8	1	1	0 F045O002	Activated	TRIP signal from Uo>> stage	TRP	6501
45 E4	EV_NODAT	16	0	2	0 F045I002	Reset	BS1 signal of Uo>> stage	BLK	6502
45 E5	EV_NODAT	32	0	2	0 F045I002	Activated	BS1 signal of Uo>> stage	BLK	6502
45 E6	EV_NODAT	64	0	3	0 F045I003	Reset	BS2 signal of Uo>> stage	BLK	6503
45 E7	EV_NODAT	128	0	3	0 F045I003	Activated	BS2 signal of Uo>> stage	BLK	6503
45 E8	EV_NODAT	256	0	4	0 -	Off	Test mode of Uo>> stage	INS	6504
45 E9	EV_NODAT	512	0	4	0 -	On	Test mode of Uo>> stage	INS	6504

Default mask=15

## Residual overvoltage protection, instantaneous stage Uo&gt;&gt;&gt;

/\*100046 / Rev D ROV1Inst \*/

46 E0	EV_NODAT	1	1	0	0 F046O001	Reset	START signal from Uo>>> stage	STR	6600
46 E1	EV_NODAT	2	1	0	0 F046O001	Activated	START signal from Uo>>> stage	STR	6600
46 E2	EV_NODAT	4	1	1	0 F046O002	Reset	TRIP signal from Uo>>> stage	TRP	6601
46 E3	EV_NODAT	8	1	1	0 F046O002	Activated	TRIP signal from Uo>>> stage	TRP	6601
46 E4	EV_NODAT	16	0	2	0 F046I002	Reset	BS1 signal of Uo>>> stage	BLK	6602
46 E5	EV_NODAT	32	0	2	0 F046I002	Activated	BS1 signal of Uo>>> stage	BLK	6602
46 E6	EV_NODAT	64	0	3	0 F046I003	Reset	BS2 signal of Uo>>> stage	BLK	6603
46 E7	EV_NODAT	128	0	3	0 F046I003	Activated	BS2 signal of Uo>>> stage	BLK	6603
46 E8	EV_NODAT	256	0	4	0 -	Off	Test mode of Uo>>> stage	INS	6604
46 E9	EV_NODAT	512	0	4	0 -	On	Test mode of Uo>>> stage	INS	6604

Default mask=15

## Three-phase thermal overload protection for cables

/\*100047 / Rev E TOL3Cab \*/

47 E0	EV_NODAT	1	1	0	0 F047O001	Reset	START signal from 3lth>	STR	6700
47 E1	EV_NODAT	2	1	0	0 F047O001	Activated	START signal from 3lth>	STR	6700
47 E2	EV_NODAT	4	1	1	0 F047O002	Reset	TRIP signal from 3lth>	TRP	6701
47 E3	EV_NODAT	8	1	1	0 F047O002	Activated	TRIP signal from 3lth>	TRP	6701
47 E4	EV_NODAT	16	1	2	0 -	Reset	CBFP signal from 3lth>	ALA	6702
47 E5	EV_NODAT	32	1	2	0 -	Activated	CBFP signal from 3lth>	ALA	6702
47 E6	EV_NODAT	64	0	3	0 F047O003	Reset	Current alarm from 3lth>	ALA	6703
47 E7	EV_NODAT	128	0	3	0 F047O003	Activated	Current alarm from 3lth>	ALA	6703
47 E8	EV_NODAT	256	0	4	0 F047I010	Reset	BLOCK signal of 3lth>	BLK	6704
47 E9	EV_NODAT	512	0	4	0 F047I010	Activated	BLOCK signal of 3lth>	BLK	6704
47 E10	EV_FLOAT_1	1024	0	5	1 F047O005	-	Calculated temperature of the conductor	TMP	6705
47 E11	EV_INT32_1	2048	0	6	0 F047O006	-	Cooling time for the successful reclosure	TIM	6706
47 E12	EV_NODAT	4096	0	7	0 -	Off	Test mode of 3lth>	INS	6707
47 E13	EV_NODAT	8192	0	7	0 -	On	Test mode of 3lth>	INS	6707
47 E14	EV_NODAT	16384	0	8	0 F047O008	Reset	Sensor error signal from 3lth>	ALA	6708
47 E15	EV_NODAT	32768	0	8	0 F047O008	Activated	Sensor error signal from 3lth>	ALA	6708
47 E16	EV_INT32_1	65536	0	9	0 F047O007	-	Predicted trip time from 3lth>	TIM	6709

Default mask=63

## Three-phase thermal overload protection for devices.

/\*100048 / Rev E TOL3Dev \*/

48 E0	EV_NODAT	1	1	0	0 F048O001	Reset	START signal from 3lthdev>	STR	6800
48 E1	EV_NODAT	2	1	0	0 F048O001	Activated	START signal from 3lthdev>	STR	6800
48 E2	EV_NODAT	4	1	1	0 F048O002	Reset	TRIP signal from 3lthdev>	TRP	6801
48 E3	EV_NODAT	8	1	1	0 F048O002	Activated	TRIP signal from 3lthdev>	TRP	6801
48 E4	EV_NODAT	16	1	2	0 -	Reset	CBFP signal from 3lthdev>	ALA	6802
48 E5	EV_NODAT	32	1	2	0 -	Activated	CBFP signal from 3lthdev>	ALA	6802
48 E6	EV_NODAT	64	0	3	0 F048I010	Reset	BLOCK signal of 3lthdev>	BLK	6803
48 E7	EV_NODAT	128	0	3	0 F048I010	Activated	BLOCK signal of 3lthdev>	BLK	6803
48 E8	EV_FLOAT	256	0	4	1 F048O003	-	Calculated temperature	TMP	6804
48 E9	EV_INT32	512	0	5	0 F048O006	-	Cooling time for the successful restart	TIM	6805
48 E10	EV_NODAT	1024	0	6	0 -	Off	Test mode of 3lthdev>	INS	6806
48 E11	EV_NODAT	2048	0	6	0 -	On	Test mode of 3lthdev>	INS	6806
48 E12	EV_NODAT	4096	0	7	0 F048O009	Reset	Sensor error signal from 3lthdev>	ALA	6807
48 E13	EV_NODAT	8192	0	7	0 F048O009	Activated	Sensor error signal from 3lthdev>	ALA	6807
48 E14	EV_NODAT	16384	1	8	0 -	Reset	START from 3lthdev> <= STATOR	STR	6808
48 E15	EV_NODAT	32768	1	8	0 -	Activated	START from 3lthdev> <= STATOR	STR	6808
48 E16	EV_NODAT	65536	1	9	0 -	Reset	TRIP from 3lthdev> <= STATOR	TRP	6809
48 E17	EV_NODAT	131072	1	9	0 -	Activated	TRIP from 3lthdev> <= STATOR	TRP	6809
48 E18	EV_NODAT	262144	1	10	0 -	Reset	START from 3lthdev> <= ROTOR	STR	6810
48 E19	EV_NODAT	524288	1	10	0 -	Activated	START from 3lthdev> <= ROTOR	STR	6810
48 E20	EV_NODAT	1048576	1	11	0 -	Reset	TRIP from 3lthdev> <= ROTOR	TRP	6811
48 E21	EV_NODAT	2097152	1	11	0 -	Activated	TRIP from 3lthdev> <= ROTOR	TRP	6811
48 E22	EV_INT32	4194304	0	12	0 F048O007	-	Predicted trip time from 3lthdev>	TIM	6812

Default mask=4177983

## Phase discontinuity protection DI&gt;

/\*100051 / Rev D CUB3Low \*/

51 E0	EV_NODAT	1	1	0	0 F051O001	Reset	START signal from DI> stage	STR	7100
51 E1	EV_NODAT	2	1	0	0 F051O001	Activated	START signal from DI> stage	STR	7100
51 E2	EV_NODAT	4	1	1	0 F051O002	Reset	TRIP signal from DI> stage	TRP	7101
51 E3	EV_NODAT	8	1	1	0 F051O002	Activated	TRIP signal from DI> stage	TRP	7101
51 E4	EV_NODAT	16	1	2	0 F051O003	Reset	CBFP signal from DI> stage	ALA	7102
51 E5	EV_NODAT	32	1	2	0 F051O003	Activated	CBFP signal from DI> stage	ALA	7102
51 E6	EV_NODAT	64	0	3	0 F051I005	Reset	BS1 signal of DI> stage	BLK	7103
51 E7	EV_NODAT	128	0	3	0 F051I005	Activated	BS1 signal of DI> stage	BLK	7103
51 E8	EV_NODAT	256	0	4	0 F051I006	Reset	BS2 signal of DI> stage	BLK	7104
51 E9	EV_NODAT	512	0	4	0 F051I006	Activated	BS2 signal of DI> stage	BLK	7104
51 E10	EV_NODAT	1024	0	5	0 -	Off	Test mode of DI> stage	INS	7105
51 E11	EV_NODAT	2048	0	5	0 -	On	Test mode of DI> stage	INS	7105

Default mask=63

## Three-phase start-up supervision for motors

/\*100054 / Rev G MotStart \*/

54 E0	EV_NODAT	1	1	0	0 F054O001	Reset	START signal from Is2t n<	STR	7400
54 E1	EV_NODAT	2	1	0	0 F054O001	Activated	START signal from Is2t n<	STR	7400
54 E2	EV_NODAT	4	1	1	0 F054O002	Reset	TRIP signal from Is2t n<	TRP	7401
54 E3	EV_NODAT	8	1	1	0 F054O002	Activated	TRIP signal from Is2t n<	TRP	7401
54 E4	EV_NODAT	16	1	2	0 F054O003	Reset	STALL signal from Is2t n<	STL	7402
54 E5	EV_NODAT	32	1	2	0 F054O003	Activated	STALL signal from Is2t n<	STL	7402
54 E6	EV_NODAT	64	0	3	0 -	Off	Test mode of Is2t n<	INS	7403
54 E7	EV_NODAT	128	0	3	0 -	On	Test mode of Is2t n<	INS	7403
Default mask=63									

## Phase reversal protection

/\*100055 / Rev C PREV3 \*/

55 E0	EV_NODAT	1	1	0	0 F055O001	Reset	START signal from 3I()	STR	7500
55 E1	EV_NODAT	2	1	0	0 F055O001	Activated	START signal from 3I()	STR	7500
55 E2	EV_NODAT	4	1	1	0 F055O002	Reset	TRIP signal from 3I()	TRP	7501
55 E3	EV_NODAT	8	1	1	0 F055O002	Activated	TRIP signal from 3I()	TRP	7501
55 E4	EV_NODAT	16	0	2	0 F055I004	Reset	BLOCK signal of 3I()	BLK	7502
55 E5	EV_NODAT	32	0	2	0 F055I004	Activated	BLOCK signal of 3I()	BLK	7502
55 E6	EV_NODAT	64	0	3	0 -	Off	Test mode of 3I()	INS	7503
55 E7	EV_NODAT	128	0	3	0 -	On	Test mode of 3I()	INS	7503
Default mask=15									

## Three-phase overvoltage protection, low-set stage

/\*100062 / Rev E OV3Low \*/

62 E0	EV_3BIT_2	1	1	0	0 F062O001	Reset	START signal from 3U> stage	STR	8200
62 E1	EV_3BIT_2	2	1	0	0 F062O001	Activated	START signal from 3U> stage	STR	8200
62 E2	EV_3BIT_2	4	1	1	0 F062O002	Reset	TRIP signal from 3U> stage	TRP	8201
62 E3	EV_3BIT_2	8	1	1	0 F062O002	Activated	TRIP signal from 3U> stage	TRP	8201
62 E4	EV_NODAT	16	0	2	0 F062I004	Reset	BS1 signal of 3U> stage	BLK	8202
62 E5	EV_NODAT	32	0	2	0 F062I004	Activated	BS1 signal of 3U> stage	BLK	8202
62 E6	EV_NODAT	64	0	3	0 F062I005	Reset	BS2 signal of 3U> stage	BLK	8203
62 E7	EV_NODAT	128	0	3	0 F062I005	Activated	BS2 signal of 3U> stage	BLK	8203
62 E8	EV_NODAT	256	0	4	0 -	Off	Test mode of 3U> stage	INS	8204
62 E9	EV_NODAT	512	0	4	0 -	On	Test mode of 3U> stage	INS	8204
Default mask=15									

## Three-phase overvoltage protection, high-set stage

/\*100063 / Rev D OV3High \*/

63 E0	EV_3BIT_2	1	1	0	0 F063O001	Reset	START signal from 3U>> stage	STR	8300
63 E1	EV_3BIT_2	2	1	0	0 F063O001	Activated	START signal from 3U>> stage	STR	8300
63 E2	EV_3BIT_2	4	1	1	0 F063O002	Reset	TRIP signal from 3U>> stage	TRP	8301
63 E3	EV_3BIT_2	8	1	1	0 F063O002	Activated	TRIP signal from 3U>> stage	TRP	8301
63 E4	EV_NODAT	16	0	2	0 F063I004	Reset	BS1 signal of 3U>> stage	BLK	8302
63 E5	EV_NODAT	32	0	2	0 F063I004	Activated	BS1 signal of 3U>> stage	BLK	8302
63 E6	EV_NODAT	64	0	3	0 F063I005	Reset	BS2 signal of 3U>> stage	BLK	8303
63 E7	EV_NODAT	128	0	3	0 F063I005	Activated	BS2 signal of 3U>> stage	BLK	8303
63 E8	EV_NODAT	256	0	4	0 -	Off	Test mode of 3U>> stage	INS	8304
63 E9	EV_NODAT	512	0	4	0 -	On	Test mode of 3U>> stage	INS	8304

Default mask=15

## Three-phase undervoltage protection, low-set stage

/\*100064 / Rev D UV3Low \*/

64 E0	EV_3BIT_2	1	1	0	0 F064O001	Reset	START signal from 3U< stage	STR	8400
64 E1	EV_3BIT_2	2	1	0	0 F064O001	Activated	START signal from 3U< stage	STR	8400
64 E2	EV_3BIT_2	4	1	1	0 F064O002	Reset	TRIP signal from 3U< stage	TRP	8401
64 E3	EV_3BIT_2	8	1	1	0 F064O002	Activated	TRIP signal from 3U< stage	TRP	8401
64 E4	EV_NODAT	16	0	2	0 F064I004	Reset	BS1 signal of 3U< stage	BLK	8402
64 E5	EV_NODAT	32	0	2	0 F064I004	Activated	BS1 signal of 3U< stage	BLK	8402
64 E6	EV_NODAT	64	0	3	0 F064I005	Reset	BS2 signal of 3U< stage	BLK	8403
64 E7	EV_NODAT	128	0	3	0 F064I005	Activated	BS2 signal of 3U< stage	BLK	8403
64 E8	EV_NODAT	256	0	4	0 -	Off	Test mode of 3U< stage	INS	8404
64 E9	EV_NODAT	512	0	4	0 -	On	Test mode of 3U< stage	INS	8404

Default mask=15

## Three-phase undervoltage protection, high-set stage

/\*100065 / Rev D UV3High \*/

65 E0	EV_3BIT_2	1	1	0	0 F065O001	Reset	START signal from 3U<< stage	STR	8500
65 E1	EV_3BIT_2	2	1	0	0 F065O001	Activated	START signal from 3U<< stage	STR	8500
65 E2	EV_3BIT_2	4	1	1	0 F065O002	Reset	TRIP signal from 3U<< stage	TRP	8501
65 E3	EV_3BIT_2	8	1	1	0 F065O002	Activated	TRIP signal from 3U<< stage	TRP	8501
65 E4	EV_NODAT	16	0	2	0 F065I004	Reset	BS1 signal of 3U<< stage	BLK	8502
65 E5	EV_NODAT	32	0	2	0 F065I004	Activated	BS1 signal of 3U<< stage	BLK	8502
65 E6	EV_NODAT	64	0	3	0 F065I005	Reset	BS2 signal of 3U<< stage	BLK	8503
65 E7	EV_NODAT	128	0	3	0 F065I005	Activated	BS2 signal of 3U<< stage	BLK	8503
65 E8	EV_NODAT	256	0	4	0 -	Off	Test mode of 3U<< stage	INS	8504
65 E9	EV_NODAT	512	0	4	0 -	On	Test mode of 3U<< stage	INS	8504

Default mask=15

## Synchro-check/voltage check function stage 1, SCVCS1

/\*100070 / Rev K SCVCS1 \*/

70 E0	EV_NODAT	1	1	0	0 F070O001	Reset	Synchro-check in progress	ALA	9000
70 E1	EV_NODAT	2	1	0	0 F070O001	Activated	Synchro-check in progress	ALA	9000
70 E2	EV_NODAT	4	1	1	0 F070O002	Reset	Closing permission given	ALA	9001
70 E3	EV_NODAT	8	1	1	0 F070O002	Activated	Closing permission given	ALA	9001
70 E4	EV_NODAT	16	1	2	0 F070O003	Reset	Alarm; CB closing failed	ALA	9002
70 E5	EV_NODAT	32	1	2	0 F070O003	Activated	Alarm; CB closing failed	ALA	9002
70 E6	EV_NODAT	64	1	3	0 F070O004	Reset	Alarm; command signal too long	ALA	9003
70 E7	EV_NODAT	128	1	3	0 F070O004	Activated	Alarm; command signal too long	ALA	9003

Default mask=255

## Underfrequency or overfrequency protection stage 1

/\*100072 / Rev G Freq1St1 \*/

72 E0	EV_NODAT	1	1	0	0 F072O001	Reset	START1 signal from f>,f< St1	STR	9200
72 E1	EV_NODAT	2	1	0	0 F072O001	Activated	START1 signal from f>,f< St1	STR	9200
72 E2	EV_NODAT	4	1	1	0 F072O002	Reset	TRIP1 signal from f>,f< St1	TRP	9201
72 E3	EV_NODAT	8	1	1	0 F072O002	Activated	TRIP1 signal from f>,f< St1	TRP	9201
72 E4	EV_NODAT	16	1	2	0 F072O003	Reset	START2 signal from f>,f< St1	STR	9202
72 E5	EV_NODAT	32	1	2	0 F072O003	Activated	START2 signal from f>,f< St1	STR	9202
72 E6	EV_NODAT	64	1	3	0 F072O004	Reset	TRIP2 signal from f>,f< St1	TRP	9203
72 E7	EV_NODAT	128	1	3	0 F072O004	Activated	TRIP2 signal from f>,f< St1	TRP	9203
72 E8	EV_NODAT	256	0	4	0 F072I004	Reset	BS1 signal of f>,f< St1	BLK	9204
72 E9	EV_NODAT	512	0	4	0 F072I004	Activated	BS1 signal of f>,f< St1	BLK	9204
72 E10	EV_NODAT	1024	0	5	0 F072I005	Reset	BS2 signal of f>,f< St1	BLK	9205
72 E11	EV_NODAT	2048	0	5	0 F072I005	Activated	BS2 signal of f>,f< St1	BLK	9205
72 E12	EV_NODAT	4096	0	6	0 -	Off	Test mode of f>,f< St1	INS	9206
72 E13	EV_NODAT	8192	0	6	0 -	On	Test mode of f>,f< St1	INS	9206

Default mask=255

## Underfrequency or overfrequency protection stage 2

/\*100073 / Rev G Freq1St2 \*/

73 E0	EV_NODAT	1	1	0	0	F073O001	Reset	START1 signal from f>,f< St2	STR	9300
73 E1	EV_NODAT	2	1	0	0	F073O001	Activated	START1 signal from f>,f< St2	STR	9300
73 E2	EV_NODAT	4	1	1	0	F073O002	Reset	TRIP1 signal from f>,f< St2	TRP	9301
73 E3	EV_NODAT	8	1	1	0	F073O002	Activated	TRIP1 signal from f>,f< St2	TRP	9301
73 E4	EV_NODAT	16	1	2	0	F073O003	Reset	START2 signal from f>,f< St2	STR	9302
73 E5	EV_NODAT	32	1	2	0	F073O003	Activated	START2 signal from f>,f< St2	STR	9302
73 E6	EV_NODAT	64	1	3	0	F073O004	Reset	TRIP2 signal from f>,f< St2	TRP	9303
73 E7	EV_NODAT	128	1	3	0	F073O004	Activated	TRIP2 signal from f>,f< St2	TRP	9303
73 E8	EV_NODAT	256	0	4	0	F073I004	Reset	BS1 signal of f>,f< St2	BLK	9304
73 E9	EV_NODAT	512	0	4	0	F073I004	Activated	BS1 signal of f>,f< St2	BLK	9304
73 E10	EV_NODAT	1024	0	5	0	F073I005	Reset	BS2 signal of f>,f< St2	BLK	9305
73 E11	EV_NODAT	2048	0	5	0	F073I005	Activated	BS2 signal of f>,f< St2	BLK	9305
73 E12	EV_NODAT	4096	0	6	0	-	Off	Test mode of f>,f< St2	INS	9306
73 E13	EV_NODAT	8192	0	6	0	-	On	Test mode of f>,f< St2	INS	9306

Default mask=255

## Negative-phase-sequence (NPS) protection, low-set stage

/\*100077 / Rev G NPS3Low \*/

77 E0	EV_NODAT	1	1	0	0	F077O001	Reset	START signal from I2> stage	STR	9700
77 E1	EV_NODAT	2	1	0	0	F077O001	Activated	START signal from I2> stage	STR	9700
77 E2	EV_NODAT	4	1	1	0	F077O002	Reset	TRIP signal from I2> stage	TRP	9701
77 E3	EV_NODAT	8	1	1	0	F077O002	Activated	TRIP signal from I2> stage	TRP	9701
77 E4	EV_NODAT	16	1	2	0	F077O004	Reset	CBFP signal from I2> stage	ALA	9702
77 E5	EV_NODAT	32	1	2	0	F077O004	Activated	CBFP signal from I2> stage	ALA	9702
77 E6	EV_NODAT	64	0	3	0	F077O003	Reset	BLOCK_OUT signal from I2> st.	OUT	9703
77 E7	EV_NODAT	128	0	3	0	F077O003	Activated	BLOCK_OUT signal from I2> st.	OUT	9703
77 E8	EV_NODAT	256	0	4	0	F077I006	Reset	BLOCK signal of I2> stage	BLK	9704
77 E9	EV_NODAT	512	0	4	0	F077I006	Activated	BLOCK signal of I2> stage	BLK	9704
77 E10	EV_NODAT	1024	0	5	0	-	Off	Test mode of I2> stage	INS	9705
77 E11	EV_NODAT	2048	0	5	0	-	On	Test mode of I2> stage	INS	9705

Default mask=63

## Negative phase sequence (NPS) protection, high-set stage

/\*100078 / Rev G NPS3High \*/

78 E0	EV_NODAT	1	1	0	0	F078O001	Reset	START signal from I2>> stage	STR	9800
78 E1	EV_NODAT	2	1	0	0	F078O001	Activated	START signal from I2>> stage	STR	9800
78 E2	EV_NODAT	4	1	1	0	F078O002	Reset	TRIP signal from I2>> stage	TRP	9801
78 E3	EV_NODAT	8	1	1	0	F078O002	Activated	TRIP signal from I2>> stage	TRP	9801
78 E4	EV_NODAT	16	1	2	0	F078O004	Reset	CBFP signal from I2>> stage	ALA	9802
78 E5	EV_NODAT	32	1	2	0	F078O004	Activated	CBFP signal from I2>> stage	ALA	9802
78 E6	EV_NODAT	64	0	3	0	F078O003	Reset	BLOCK_OUT signal from I2>> st.	OUT	9803
78 E7	EV_NODAT	128	0	3	0	F078O003	Activated	BLOCK_OUT signal from I2>> st.	OUT	9803
78 E8	EV_NODAT	256	0	4	0	F078I006	Reset	BLOCK signal of I2>> stage	BLK	9804
78 E9	EV_NODAT	512	0	4	0	F078I006	Activated	BLOCK signal of I2>> stage	BLK	9804
78 E10	EV_NODAT	1024	0	5	0	-	Off	Test mode of I2>> stage	INS	9805
78 E11	EV_NODAT	2048	0	5	0	-	On	Test mode of I2>> stage	INS	9805

Default mask=63

## Auto-reclosure function AR5Func (86)

/\*100080 / Rev D AR5Func \*/

80 E0	EV_NODAT	1	1	0	0	-	End	Auto-reclosing sequence	ARC	10000
80 E1	EV_NODAT	2	1	0	0	-	Started	Auto-reclosing sequence	ARC	10000
80 E2	EV_1BIT	4	0	1	0	-	-	AR (shots 1...5) initiated by AR1	ARC	10001
80 E3	EV_1BIT	8	0	2	0	-	-	AR (shots 1...5) initiated by AR2	ARC	10002
80 E4	EV_1BIT	16	0	3	0	-	-	AR (shots 1...5) initiated by AR3	ARC	10003
80 E5	EV_1BIT	32	0	4	0	-	-	AR (shots 1...5) initiated by AR4	ARC	10004
80 E6	EV_NODAT	64	1	5	0	-	Reset	DEF.TRIP alarm	TRP	10005
80 E7	EV_NODAT	128	1	5	0	-	Activated	DEF.TRIP alarm	TRP	10005
80 E8	EV_1BIT	256	0	6	0	-	-	DEF.TRIP alarm activated by AR1	TRP	10006
80 E9	EV_1BIT	512	0	7	0	-	-	DEF.TRIP alarm activated by AR2	TRP	10007
80 E10	EV_1BIT	1024	0	8	0	-	-	DEF.TRIP alarm activated by AR3	TRP	10008
80 E11	EV_1BIT	2048	0	9	0	-	-	DEF.TRIP alarm activated by AR4	TRP	10009
80 E12	EV_1BIT	4096	0	10	0	-	-	AR sequence successful	ARC	10010
80 E13	EV_1BIT	8192	0	11	0	-	-	AR sequence initiated by AR1 successful	ARC	10011
80 E14	EV_1BIT	16384	0	12	0	-	-	AR sequence initiated by AR2 successful	ARC	10012
80 E15	EV_1BIT	32768	0	13	0	-	-	AR sequence initiated by AR3 successful	ARC	10013
80 E16	EV_1BIT	65536	0	14	0	-	-	AR sequence initiated by AR4 successful	ARC	10014
80 E17	EV_1BIT	131072	0	15	0	-	-	Forced shot increment by the signal	ARC	10015
80 E18	EV_NODAT	262144	1	16	0	-	Open	Breaker position	POS	10016
80 E19	EV_NODAT	524288	1	16	0	-	Close	Breaker position	POS	10016
80 E20	EV_NODAT	1048576	1	17	0	-	Open	Manual/remote CB control	INS	10017
80 E21	EV_NODAT	2097152	1	17	0	-	Close	Manual/remote CB control	INS	10017
80 E22	EV_NODAT	4194304	0	18	0	F080O001	Reset	OPEN output	INS	10018
80 E23	EV_NODAT	8388608	0	18	0	F080O001	Activated	OPEN output	INS	10018
80 E24	EV_NODAT	16777216	0	19	0	F080O002	Reset	CLOSE output	INS	10019
80 E25	EV_NODAT	33554432	0	19	0	F080O002	Activated	CLOSE output	INS	10019
80 E26	EV_1BIT	67108864	1	20	0	-	-	CB opening failed via auto-recloser	INS	10020
80 E27	EV_1BIT	134217728	1	21	0	-	-	CB closing failed via auto-recloser	INS	10021
80 E28	EV_1BIT	268435456	1	22	0	-	-	CB closing inhibited	INH	10022
80 E29	EV_1BIT	536870912	1	23	0	-	-	Attempt to execute without open/close selection	INS	10023
80 E30	EV_NODAT	1073741824	1	24	0	-	Reset	Maintenance monitor alarm	ALA	10024
80 E31	EV_NODAT	2147483648	1	24	0	-	Activated	Maintenance monitor alarm	ALA	10024
80 E32	EV_NODAT	1	1	26	0	-	Reset	Initiation signal AR1...4 activated >2 min	INS	10026
80 E33	EV_NODAT	2	1	26	0	-	Activated	Initiation signal AR1...4 activated >2 min	INS	10026
80 E34	EV_NODAT	4	1	27	1	F080S004	Not in use	Auto-reclosure	INS	10027
80 E35	EV_NODAT	8	1	27	0	F080S004	In use	Auto-reclosure	INS	10027
80 E36	EV_1BIT	16	1	28	0	-	-	AR interrupted by the signal ARINH	INS	10028
80 E37	EV_1BIT	32	1	29	0	-	-	AR interrupted by CB close during the sequence	INS	10029
80 E38	EV_1BIT	64	1	30	0	-	-	AR interrupted by CB open during the sequence	INS	10030
80 E39	EV_1BIT	128	0	31	0	-	-	AR interrupted by Frequent Operation Counter	INS	10031
80 E40	EV_NODAT	256	0	32	0	F080O016	Elapsed	Discriminating time td	INS	10032

80 E41	EV_NODAT	512	0	32	0 F080O016	Started	Discriminating time td	INS	10032
80 E42	EV_NODAT	1024	0	33	0 -	Elapsed	Reclaim time tr	INS	10033
80 E43	EV_NODAT	2048	0	33	0 -	Started or restarted	Reclaim time tr	INS	10033
80 E44	EV_NODAT	4096	0	34	0 F080O014	Reset	LOCKOUT	INS	10034
80 E45	EV_NODAT	8192	0	34	0 F080O014	Activated	LOCKOUT	INS	10034
Default mask=4231790914									
Auto-reclosure function AR5Func (86)									
/*100080 / Rev D AR5Func */									
81 E0	EV_NODAT	1	0	0	0 -	Concluded	Auto-reclose shot 1	ARC	10100
81 E1	EV_NODAT	2	1	0	0 -	In progress	Auto-reclose shot 1	ARC	10100
81 E2	EV_1BIT	4	0	1	0 -	-	AR shot 1 initiated via AR1	ARC	10101
81 E3	EV_1BIT	8	0	2	0 -	-	AR shot 1 initiated via AR2	ARC	10102
81 E4	EV_1BIT	16	0	3	0 -	-	AR shot 1 initiated via AR3	ARC	10103
81 E5	EV_1BIT	32	0	4	0 -	-	AR shot 1 initiated via AR4	ARC	10104
81 E6	EV_1BIT	64	0	5	0 -	-	AR shot 1 successful	ARC	10105
Default mask=2									
Auto-reclosure function AR5Func (86)									
/*100080 / Rev D AR5Func */									
82 E0	EV_NODAT	1	0	0	0 -	Concluded	Auto-reclose shot 2	ARC	10200
82 E1	EV_NODAT	2	1	0	0 -	In progress	Auto-reclose shot 2	ARC	10200
82 E2	EV_1BIT	4	0	1	0 -	-	AR shot 2 initiated via AR1	ARC	10201
82 E3	EV_1BIT	8	0	2	0 -	-	AR shot 2 initiated via AR2	ARC	10202
82 E4	EV_1BIT	16	0	3	0 -	-	AR shot 2 initiated via AR3	ARC	10203
82 E5	EV_1BIT	32	0	4	0 -	-	AR shot 2 initiated via AR4	ARC	10204
82 E6	EV_1BIT	64	0	5	0 -	-	AR shot 2 successful	ARC	10205
Default mask=2									
Auto-reclosure function AR5Func (86)									
/*100080 / Rev D AR5Func */									
83 E0	EV_NODAT	1	0	0	0 -	Concluded	Auto-reclose shot 3	ARC	10300
83 E1	EV_NODAT	2	1	0	0 -	In progress	Auto-reclose shot 3	ARC	10300
83 E2	EV_1BIT	4	0	1	0 -	-	AR shot 3 initiated via AR1	ARC	10301
83 E3	EV_1BIT	8	0	2	0 -	-	AR shot 3 initiated via AR2	ARC	10302
83 E4	EV_1BIT	16	0	3	0 -	-	AR shot 3 initiated via AR3	ARC	10303
83 E5	EV_1BIT	32	0	4	0 -	-	AR shot 3 initiated via AR4	ARC	10304
83 E6	EV_1BIT	64	0	5	0 -	-	AR shot 3 successful	ARC	10305
Default mask=2									
Auto-reclosure function AR5Func (86)									
/*100080 / Rev D AR5Func */									
84 E0	EV_NODAT	1	0	0	0 -	Concluded	Auto-reclose shot 4	ARC	10400
84 E1	EV_NODAT	2	1	0	0 -	In progress	Auto-reclose shot 4	ARC	10400
84 E2	EV_1BIT	4	0	1	0 -	-	AR shot 4 initiated via AR1	ARC	10401
84 E3	EV_1BIT	8	0	2	0 -	-	AR shot 4 initiated via AR2	ARC	10402
84 E4	EV_1BIT	16	0	3	0 -	-	AR shot 4 initiated via AR3	ARC	10403
84 E5	EV_1BIT	32	0	4	0 -	-	AR shot 4 initiated via AR4	ARC	10404
84 E6	EV_1BIT	64	0	5	0 -	-	AR shot 4 successful	ARC	10405
Default mask=2									

## Auto-reclosure function AR5Func (86)

/\*100080 / Rev D AR5Func \*/

85 E0	EV_NODAT	1	0	0	0 -	Concluded	Auto-reclose shot 5	ARC	10500
85 E1	EV_NODAT	2	1	0	0 -	In progress	Auto-reclose shot 5	ARC	10500
85 E2	EV_1BIT	4	0	1	0 -	-	AR shot 5 initiated via AR1	ARC	10501
85 E3	EV_1BIT	8	0	2	0 -	-	AR shot 5 initiated via AR2	ARC	10502
85 E4	EV_1BIT	16	0	3	0 -	-	AR shot 5 initiated via AR3	ARC	10503
85 E5	EV_1BIT	32	0	4	0 -	-	AR shot 5 initiated via AR4	ARC	10504
85 E6	EV_1BIT	64	0	5	0 -	-	AR shot 5 successful	ARC	10505
Default mask=2									

## Auto-reclosure function AR5Func (86)

/\*100080 / Rev D AR5Func \*/

86 E0	EV_1BIT	1	1	0	0 -	-	Final trip	TRP	10600
86 E1	EV_1BIT	2	0	1	0 -	-	Final trip via AR1	ARC	10601
86 E2	EV_1BIT	4	0	2	0 -	-	Final trip via AR2	ARC	10602
86 E3	EV_1BIT	8	0	3	0 -	-	Final trip via AR3	ARC	10603
86 E4	EV_1BIT	16	0	4	0 -	-	Final trip via AR4	ARC	10604
Default mask=1									

## 3-phase non-directional undercurrent protection, stage 1

/\*100088 / Rev F NUC3St1 \*/

88 E0	EV_3BIT_1	1	1	0	0 F088O001	Reset	START signal from 3l< stage 1	STR	10800
88 E1	EV_3BIT_1	2	1	0	0 F088O001	Activated	START signal from 3l< stage 1	STR	10800
88 E2	EV_3BIT_1	4	1	1	0 F088O002	Reset	TRIP signal from 3l< stage 1	TRP	10801
88 E3	EV_3BIT_1	8	1	1	0 F088O002	Activated	TRIP signal from 3l< stage 1	TRP	10801
88 E4	EV_3BIT_1	16	1	2	0 F088O003	Reset	CBFP signal from 3l< stage 1	ALA	10802
88 E5	EV_3BIT_1	32	1	2	0 F088O003	Activated	CBFP signal from 3l< stage 1	ALA	10802
88 E6	EV_3BIT_1	64	1	3	1 F088O004	Reset	ALARM signal from 3l< stage 1	ALA	10803
88 E7	EV_3BIT_1	128	1	3	0 F088O004	Activated	ALARM signal from 3l< stage 1	ALA	10803
88 E8	EV_NODAT	256	0	4	0 F088I004	Reset	External BLOCK signal of 3l< stage 1	BLK	10804
88 E9	EV_NODAT	512	0	4	0 F088I004	Activated	External BLOCK signal of 3l< stage 1	BLK	10804
88 E10	EV_NODAT	1024	0	5	0 F088I005	Reset	Internal BLOCK signal of 3l< stage 1	BLK	10805
88 E11	EV_NODAT	2048	0	5	0 F088I005	Activated	Internal BLOCK signal of 3l< stage 1	BLK	10805
88 E12	EV_NODAT	4096	0	6	0 -	Off	Test mode of 3l< stage 1	INS	10806
88 E13	EV_NODAT	8192	0	6	0 -	On	Test mode of 3l< stage 1	INS	10806
Default mask=255									

## Non-directional earth-fault protection function, instantaneous stage

/\*100090 / Rev D NEF1Inst \*/

90 E0	EV_NODAT	1	1	0	0 F090O001	Reset	START signal from lo>>> stage	STR	11000
90 E1	EV_NODAT	2	1	0	0 F090O001	Activated	START signal from lo>>> stage	STR	11000
90 E2	EV_NODAT	4	1	1	0 F090O002	Reset	TRIP signal from lo>>> stage	TRP	11001
90 E3	EV_NODAT	8	1	1	0 F090O002	Activated	TRIP signal from lo>>> stage	TRP	11001
90 E4	EV_NODAT	16	1	2	0 F090O003	Reset	CBFP signal from lo>>> stage	ALA	11002
90 E5	EV_NODAT	32	1	2	0 F090O003	Activated	CBFP signal from lo>>> stage	ALA	11002
90 E6	EV_NODAT	64	0	3	0 F090I002	Reset	BS1 signal of lo>>> stage	BLK	11003
90 E7	EV_NODAT	128	0	3	0 F090I002	Activated	BS1 signal of lo>>> stage	BLK	11003
90 E8	EV_NODAT	256	0	4	0 F090I003	Reset	BS2 signal of lo>>> stage	BLK	11004
90 E9	EV_NODAT	512	0	4	0 F090I003	Activated	BS2 signal of lo>>> stage	BLK	11004
90 E10	EV_NODAT	1024	0	5	0 -	Off	Test mode of lo>>> stage	INS	11005
90 E11	EV_NODAT	2048	0	5	0 -	On	Test mode of lo>>> stage	INS	11005
Default mask=63									

## Phase sequence voltage protection, stage 1

/\*100112 / Rev D PSV3St1 \*/

112 E0	EV_NODAT	1	1	0	0 F112O001	Reset	U1U2<>_1 START U2>	STR	13200
112 E1	EV_NODAT	2	1	0	0 F112O001	Activated	U1U2<>_1 START U2>	STR	13200
112 E2	EV_NODAT	4	1	1	0 F112O001	Reset	U1U2<>_1 START U1<	STR	13201
112 E3	EV_NODAT	8	1	1	0 F112O001	Activated	U1U2<>_1 START U1<	STR	13201
112 E4	EV_NODAT	16	1	2	0 F112O001	Reset	U1U2<>_1 START U1>	STR	13202
112 E5	EV_NODAT	32	1	2	0 F112O001	Activated	U1U2<>_1 START U1>	STR	13202
112 E6	EV_NODAT	64	1	3	0 F112O002	Reset	U1U2<>_1 TRIP U2>	TRP	13203
112 E7	EV_NODAT	128	1	3	0 F112O002	Activated	U1U2<>_1 TRIP U2>	TRP	13203
112 E8	EV_NODAT	256	1	4	0 F112O002	Reset	U1U2<>_1 TRIP U1<	TRP	13204
112 E9	EV_NODAT	512	1	4	0 F112O002	Activated	U1U2<>_1 TRIP U1<	TRP	13204
112 E10	EV_NODAT	1024	1	5	0 F112O002	Reset	U1U2<>_1 TRIP U1>	TRP	13205
112 E11	EV_NODAT	2048	1	5	0 F112O002	Activated	U1U2<>_1 TRIP U1>	TRP	13205
112 E12	EV_NODAT	4096	0	6	0 F112I004	Reset	U1U2<>_1 BLOCK	BLK	13206
112 E13	EV_NODAT	8192	0	6	0 F112I004	Activated	U1U2<>_1 BLOCK	BLK	13206
112 E14	EV_NODAT	16384	0	7	0 -	Off	Test mode of U1U2<>_1	INS	13207
112 E15	EV_NODAT	32768	0	7	0 -	On	Test mode of U1U2<>_1	INS	13207
Default mask=4095									

## Fuse failure supervision

/\*100118 / Rev D FuseFail \*/

118 E0	EV_NODAT	1	1	0	0	F118O001	Reset	Fuse failure	ALA	13800
118 E1	EV_NODAT	2	1	0	0	F118O001	Activated	Fuse failure	ALA	13800
118 E2	EV_NODAT	4	1	1	1	F118I002	Open	MCB position	POS	13801
118 E3	EV_NODAT	8	1	1	0	F118I002	Closed	MCB position	POS	13801
118 E4	EV_NODAT	16	0	2	0	F118I001	Reset	FUSEF blocked	BLK	13802
118 E5	EV_NODAT	32	0	2	0	F118I001	Activated	FUSEF blocked	BLK	13802
118 E6	EV_NODAT	64	0	3	0	F118V002	Off	Test mode of FUSEF	INS	13803
118 E7	EV_NODAT	128	0	3	0	F118V002	On	Test mode of FUSEF	INS	13803

Default mask=15

## Circuit breaker 1 (2 state inputs / 2 control outputs)

/\*100120 / Rev C COCB1 \*/

120 E0	EV_2BIT_1	1	1	0	1	F120V001	Open (10)	Breaker 1 position	POS	14000
120 E1	EV_2BIT_1	2	1	0	0	F120V001	Close (01)	Breaker 1 position	POS	14000
120 E2	EV_2BIT_1	4	1	0	0	F120V001	Faulty (11)	Breaker 1 position	POS	14000
120 E3	EV_2BIT_1	8	1	0	0	F120V001	Middle (00)	Breaker 1 position	POS	14000
120 E4	EV_1BIT	16	1	1	1	F120V031	Enabled	Breaker 1 open command	INS	14001
120 E5	EV_1BIT	32	1	1	0	F120V031	Disabled	Breaker 1 open command	INS	14001
120 E6	EV_1BIT	64	1	2	1	F120V030	Enabled	Breaker 1 close command	INS	14002
120 E7	EV_1BIT	128	1	2	0	F120V030	Disabled	Breaker 1 close command	INS	14002
120 E8	EV_1BIT	256	0	3	0	F120V034	Inactive	Breaker 1 invalid state	INS	14003
120 E9	EV_1BIT	512	1	3	0	F120V034	Active	Breaker 1 invalid state	INS	14003
120 E10	EV_NODAT	1024	1	4	0	-	Completed	Breaker 1 command sequence	INS	14004
120 E11	EV_NODAT	2048	1	4	0	-	Started	Breaker 1 command sequence	INS	14004
120 E12	EV_NODAT	4096	0	5	0	-	Deactivated	Breaker 1 open output	INS	14005
120 E13	EV_NODAT	8192	1	5	0	-	Activated	Breaker 1 open output	INS	14005
120 E14	EV_NODAT	16384	0	6	0	-	Deactivated	Breaker 1 close output	INS	14006
120 E15	EV_NODAT	32768	1	6	0	-	Activated	Breaker 1 close output	INS	14006
120 E16	EV_NODAT	65536	0	7	0	F120O003	Normal	Breaker 1 opening time	INS	14007
120 E17	EV_NODAT	131072	1	7	0	F120O003	Alarm	Breaker 1 opening time	INS	14007
120 E18	EV_NODAT	262144	0	8	0	F120O004	Normal	Breaker 1 closing time	INS	14008
120 E19	EV_NODAT	524288	1	8	0	F120O004	Alarm	Breaker 1 closing time	INS	14008
120 E20	EV_NODAT	1048576	0	9	0	F120O005	Normal	Breaker 1 inactive time	INS	14009
120 E21	EV_NODAT	2097152	1	9	0	F120O005	Alarm	Breaker 1 inactive time	INS	14009
120 E22	EV_NODAT	4194304	0	10	0	F120O006	Normal	Breaker 1 cycle count	INS	14010
120 E23	EV_NODAT	8388608	1	10	0	F120O006	Alarm	Breaker 1 cycle count	INS	14010
120 E24	EV_NODAT	16777216	0	11	0	-	Nack	Breaker 1 command status	INS	14011
120 E25	EV_NODAT	33554432	0	11	0	-	Ack	Breaker 1 command status	INS	14011
120 E26	EV_1BIT	67108864	0	12	1	F120V035	Inactive	Breaker 1 control blocking	INS	14012
120 E27	EV_1BIT	134217728	1	12	0	F120V035	Active	Breaker 1 control blocking	INS	14012
120 E28	EV_NODAT	268435456	0	13	0	-	Unsuccessful	Breaker 1 command status	CMS	14013

Default mask=145403647

## Object indication 1 (2 state inputs)

/\*100127 / Rev C COIND1 \*/

127 E0	EV_2BIT_1	1	1	0	1	F127V001	Open (10)	Indication 1 position	POS	14700
127 E1	EV_2BIT_1	2	1	0	0	F127V001	Close (01)	Indication 1 position	POS	14700
127 E2	EV_2BIT_1	4	1	0	0	F127V001	Faulty (11)	Indication 1 position	POS	14700
127 E3	EV_2BIT_1	8	1	0	0	F127V001	Middle (00)	Indication 1 position	POS	14700
127 E4	-	16	0	99	-	-	-	-	-	14799
127 E5	-	32	0	99	-	-	-	-	-	14799
127 E6	-	64	0	99	-	-	-	-	-	14799
127 E7	-	128	0	99	-	-	-	-	-	14799
127 E8	EV_1BIT	256	0	1	0	F127V034	Inactive	Indication 1 invalid state	INS	14701
127 E9	EV_1BIT	512	1	1	0	F127V034	Active	Indication 1 invalid state	INS	14701

Default mask=527

## Object indication 2 (2 state inputs)

/\*100128 / Rev C COIND2 \*/

128 E0	EV_2BIT_1	1	1	0	1	F128V001	Open (10)	Indication 2 position	POS	14800
128 E1	EV_2BIT_1	2	1	0	0	F128V001	Close (01)	Indication 2 position	POS	14800
128 E2	EV_2BIT_1	4	1	0	0	F128V001	Faulty (11)	Indication 2 position	POS	14800
128 E3	EV_2BIT_1	8	1	0	0	F128V001	Middle (00)	Indication 2 position	POS	14800
128 E4	-	16	0	99	-	-	-	-	-	14899
128 E5	-	32	0	99	-	-	-	-	-	14899
128 E6	-	64	0	99	-	-	-	-	-	14899
128 E7	-	128	0	99	-	-	-	-	-	14899
128 E8	EV_1BIT	256	0	1	0	F128V034	Inactive	Indication 2 invalid state	INS	14801
128 E9	EV_1BIT	512	1	1	0	F128V034	Active	Indication 2 invalid state	INS	14801

Default mask=527

## Object indication 3 (2 state inputs)

/\*100129 / Rev C COIND3 \*/

129 E0	EV_2BIT_1	1	1	0	1	F129V001	Open (10)	Indication 3 position	POS	14900
129 E1	EV_2BIT_1	2	1	0	0	F129V001	Close (01)	Indication 3 position	POS	14900
129 E2	EV_2BIT_1	4	1	0	0	F129V001	Faulty (11)	Indication 3 position	POS	14900
129 E3	EV_2BIT_1	8	1	0	0	F129V001	Middle (00)	Indication 3 position	POS	14900
129 E4	-	16	0	99	-	-	-	-	-	14999
129 E5	-	32	0	99	-	-	-	-	-	14999
129 E6	-	64	0	99	-	-	-	-	-	14999
129 E7	-	128	0	99	-	-	-	-	-	14999
129 E8	EV_1BIT	256	0	1	0	F129V034	Inactive	Indication 3 invalid state	INS	14901
129 E9	EV_1BIT	512	1	1	0	F129V034	Active	Indication 3 invalid state	INS	14901

Default mask=527

## Logic control position selector

/\*100142 / Rev B COLOCAT \*/

142 E0	EV_1BIT	1	0	0	0	F142V001	Inactive	Logic position setting	INS	16200
142 E1	EV_1BIT	2	1	0	0	F142V001	Active	Logic position setting	INS	16200

Default mask=2

## Alarm 1 (MMI, remote)

/\*100162 / Rev C MMIALAR1 \*/

162 E0	EV_NODAT	1	0	0	1	F162I001	Inactive	Alarm 1 status	INS	18200
162 E1	EV_NODAT	2	1	0	0	F162I001	Active	Alarm 1 status	INS	18200
162 E2	-	4	0	99	-	-	-	-	-	18299
162 E3	EV_NODAT	8	0	1	0	-	-	Alarm 1 acknowledgement	INS	18201
Default mask=2										

## Alarm 2 (MMI, remote)

/\*100163 / Rev C MMIALAR2 \*/

163 E0	EV_NODAT	1	0	0	1	F163I001	Inactive	Alarm 2 status	INS	18300
163 E1	EV_NODAT	2	1	0	0	F163I001	Active	Alarm 2 status	INS	18300
163 E2	-	4	0	99	-	-	-	-	-	18399
163 E3	EV_NODAT	8	0	1	0	-	-	Alarm 2 acknowledgement	INS	18301
Default mask=2										

## Alarm 3 (MMI, remote)

/\*100164 / Rev C MMIALAR3 \*/

164 E0	EV_NODAT	1	0	0	1	F164I001	Inactive	Alarm 3 status	INS	18400
164 E1	EV_NODAT	2	1	0	0	F164I001	Active	Alarm 3 status	INS	18400
164 E2	-	4	0	99	-	-	-	-	-	18499
164 E3	EV_NODAT	8	0	1	0	-	-	Alarm 3 acknowledgement	INS	18401
Default mask=2										

## Alarm 4 (MMI, remote)

/\*100165 / Rev C MMIALAR4 \*/

165 E0	EV_NODAT	1	0	0	1	F165I001	Inactive	Alarm 4 status	INS	18500
165 E1	EV_NODAT	2	1	0	0	F165I001	Active	Alarm 4 status	INS	18500
165 E2	-	4	0	99	-	-	-	-	-	18599
165 E3	EV_NODAT	8	0	1	0	-	-	Alarm 4 acknowledgement	INS	18501
Default mask=2										

## Alarm 5 (MMI, remote)

/\*100166 / Rev C MMIALAR5 \*/

166 E0	EV_NODAT	1	0	0	1	F166I001	Inactive	Alarm 5 status	INS	18600
166 E1	EV_NODAT	2	1	0	0	F166I001	Active	Alarm 5 status	INS	18600
166 E2	-	4	0	99	-	-	-	-	-	18699
166 E3	EV_NODAT	8	0	1	0	-	-	Alarm 5 acknowledgement	INS	18601
Default mask=2										

## Alarm 6 (MMI, remote)

/\*100167 / Rev C MMIALAR6 \*/

167 E0	EV_NODAT	1	0	0	1	F167I001	Inactive	Alarm 6 status	INS	18700
167 E1	EV_NODAT	2	1	0	0	F167I001	Active	Alarm 6 status	INS	18700
167 E2	-	4	0	99	-	-	-	-	-	18799
167 E3	EV_NODAT	8	0	1	0	-	-	Alarm 6 acknowledgement	INS	18701
Default mask=2										

## Alarm 7 (MMI, remote)

/\*100168 / Rev C MMIALAR7 \*/

168 E0	EV_NODAT	1	0	0	1 F168I001	Inactive	Alarm 7 status	INS	18800
168 E1	EV_NODAT	2	1	0	0 F168I001	Active	Alarm 7 status	INS	18800
168 E2	-	4	0	99 -	-	-	-	-	18899
168 E3	EV_NODAT	8	0	1	0 -	-	Alarm 7 acknowledgement	INS	18801
Default mask=2									

## Alarm 8 (MMI, remote)

/\*100169 / Rev C MMIALAR8 \*/

169 E0	EV_NODAT	1	0	0	1 F169I001	Inactive	Alarm 8 status	INS	18900
169 E1	EV_NODAT	2	1	0	0 F169I001	Active	Alarm 8 status	INS	18900
169 E2	-	4	0	99 -	-	-	-	-	18999
169 E3	EV_NODAT	8	0	1	0 -	-	Alarm 8 acknowledgement	INS	18901
Default mask=2									

## Supervision Function of the Energizing Current Input Circuit

/\*100181 / Rev C CMCU3 \*/

181 E0	EV_NODAT	1	1	0	1 F181O001	Off	Current input circuit alarm	ALA	20100
181 E1	EV_NODAT	2	1	0	0 F181O001	On	Current input circuit alarm	ALA	20100
Default mask=3									

## Supervision of the Energizing Voltage Input Circuit

/\*100182 / Rev D CMVO3 \*/

182 E0	EV_NODAT	1	1	0	1 F182O001	Off	Input voltage circuit alarm	ALA	20200
182 E1	EV_NODAT	2	1	0	0 F182O001	On	Input voltage circuit alarm	ALA	20200
Default mask=3									

## Operate Time Counter 1 for the Used Operate Time (motors)

/\*100184 / Rev B CMTIME1 \*/

184 E0	EV_NODAT	1	0	0	1 F184O001	Reset	Accumulated time 1 alarm	ALA	20400
184 E1	EV_NODAT	2	1	0	0 F184O001	Activated	Accumulated time 1 alarm	ALA	20400
184 E2	EV_NODAT	4	0	1	0 F184I001	Inactive	Accumulated time 1 measurement	INS	20401
184 E3	EV_NODAT	8	1	1	0 F184I001	Active	Accumulated time 1 measurement	INS	20401
Default mask=10									

## Circuit Breaker Electric Wear 1

/\*100187 / Rev C CMBWEAR1 \*/

187 E0	EV_NODAT	1	0	0	1 F187O001	Reset	Breaker 1 electric wear alarm	ALA	20700
187 E1	EV_NODAT	2	1	0	0 F187O001	Activated	Breaker 1 electric wear alarm	ALA	20700
Default mask=2									

## Trip Circuit Supervision 1

/\*100191 / Rev B CMTCS1 \*/

191 E0	EV_NODAT	1	0	0	1 F191O001	Reset	Trip circuit superv. 1 alarm	ALA	21100
191 E1	EV_NODAT	2	1	0	0 F191O001	Activated	Trip circuit superv. 1 alarm	ALA	21100
191 E2	EV_NODAT	4	0	1	0 F191I002	Inactive	Trip circuit superv. 1 block	BLK	21101
191 E3	EV_NODAT	8	1	1	0 F191I002	Active	Trip circuit superv. 1 block	BLK	21101
Default mask=10									

## Three-phase current measurement

/\*100200 / Rev D MECU3A \*/

200 E0	EV_FLOAT	1	0	1	1 F200I001	High warning reset	IL1	CUR	22001
200 E1	EV_FLOAT;IQ=HW	2	0	1	0 F200I001	High warning activated	IL1	CUR	22001
200 E2	EV_FLOAT	4	0	2	1 F200I002	High warning reset	IL2	CUR	22002
200 E3	EV_FLOAT;IQ=HW	8	0	2	0 F200I002	High warning activated	IL2	CUR	22002
200 E4	EV_FLOAT	16	0	3	1 F200I003	High warning reset	IL3	CUR	22003
200 E5	EV_FLOAT;IQ=HW	32	0	3	0 F200I003	High warning activated	IL3	CUR	22003
200 E6	EV_FLOAT	64	0	1	0 F200I001	High alarm reset	IL1	CUR	22001
200 E7	EV_FLOAT;IQ=HA	128	0	1	0 F200I001	High alarm activated	IL1	CUR	22001
200 E8	EV_FLOAT	256	0	2	0 F200I002	High alarm reset	IL2	CUR	22002
200 E9	EV_FLOAT;IQ=HA	512	0	2	0 F200I002	High alarm activated	IL2	CUR	22002
200 E10	EV_FLOAT	1024	0	3	0 F200I003	High alarm reset	IL3	CUR	22003
200 E11	EV_FLOAT;IQ=HA	2048	0	3	0 F200I003	High alarm activated	IL3	CUR	22003
200 E12	EV_FLOAT	4096	0	1	0 F200I001	Low warning reset	IL1	CUR	22001
200 E13	EV_FLOAT;IQ=LW	8192	0	1	0 F200I001	Low warning activated	IL1	CUR	22001
200 E14	EV_FLOAT	16384	0	2	0 F200I002	Low warning reset	IL2	CUR	22002
200 E15	EV_FLOAT;IQ=LW	32768	0	2	0 F200I002	Low warning activated	IL2	CUR	22002
200 E16	EV_FLOAT	65536	0	3	0 F200I003	Low warning reset	IL3	CUR	22003
200 E17	EV_FLOAT;IQ=LW	131072	0	3	0 F200I003	Low warning activated	IL3	CUR	22003
200 E18	EV_FLOAT	262144	0	1	0 F200I001	Low alarm reset	IL1	CUR	22001
200 E19	EV_FLOAT;IQ=LA	524288	0	1	0 F200I001	Low alarm activated	IL1	CUR	22001
200 E20	EV_FLOAT	1048576	0	2	0 F200I002	Low alarm reset	IL2	CUR	22002
200 E21	EV_FLOAT;IQ=LA	2097152	0	2	0 F200I002	Low alarm activated	IL2	CUR	22002
200 E22	EV_FLOAT	4194304	0	3	0 F200I003	Low alarm reset	IL3	CUR	22003
200 E23	EV_FLOAT;IQ=LA	8388608	0	3	0 F200I003	Low alarm activated	IL3	CUR	22003
200 E24	-	0	0	99 -	-	-	-	-	22099
200 E25	EV_FLOAT	33554432	0	1	0 F200I001	Delta	IL1	CUR	22001
200 E26	-	0	0	99 -	-	-	-	-	22099
200 E27	EV_FLOAT	134217728	0	2	0 F200I002	Delta	IL2	CUR	22002
200 E28	-	0	0	99 -	-	-	-	-	22099
200 E29	EV_FLOAT	536870912	0	3	0 F200I003	Delta	IL3	CUR	22003
	Default mask=0								

## Neutral current measurement

/\*100201 / Rev D MECU1A \*/

201 E0	EV_FLOAT	1	0	1	1 F201I001	High warning reset	lo	CUR	22101
201 E1	EV_FLOAT;IQ=HW	2	0	1	0 F201I001	High warning activated	lo	CUR	22101
201 E2	EV_FLOAT	4	0	1	0 F201I001	High alarm reset	lo	CUR	22101
201 E3	EV_FLOAT;IQ=HA	8	0	1	0 F201I001	High alarm activated	lo	CUR	22101
201 E4	-	0	0	99 -	-	-	-	-	22199
201 E5	EV_FLOAT	32	0	1	0 F201I001	Delta	lo	CUR	22101
	Default mask=0								

## Three-phase voltage measurement

/\*100204 / Rev E MEVO3A \*/

204 E0	EV_FLOAT	1	0	1	1 F204I001	High warning reset	UL1	VOL	22401
204 E1	EV_FLOAT;IQ=HW	2	0	1	0 F204I001	High warning activated	UL1	VOL	22401
204 E2	EV_FLOAT	4	0	2	1 F204I002	High warning reset	UL2	VOL	22402
204 E3	EV_FLOAT;IQ=HW	8	0	2	0 F204I002	High warning activated	UL2	VOL	22402
204 E4	EV_FLOAT	16	0	3	1 F204I003	High warning reset	UL3	VOL	22403
204 E5	EV_FLOAT;IQ=HW	32	0	3	0 F204I003	High warning activated	UL3	VOL	22403
204 E6	EV_FLOAT	64	0	1	0 F204I001	High alarm reset	UL1	VOL	22401
204 E7	EV_FLOAT;IQ=HA	128	0	1	0 F204I001	High alarm activated	UL1	VOL	22401
204 E8	EV_FLOAT	256	0	2	0 F204I002	High alarm reset	UL2	VOL	22402
204 E9	EV_FLOAT;IQ=HA	512	0	2	0 F204I002	High alarm activated	UL2	VOL	22402
204 E10	EV_FLOAT	1024	0	3	0 F204I003	High alarm reset	UL3	VOL	22403
204 E11	EV_FLOAT;IQ=HA	2048	0	3	0 F204I003	High alarm activated	UL3	VOL	22403
204 E12	EV_FLOAT	4096	0	1	0 F204I001	Low warning reset	UL1	VOL	22401
204 E13	EV_FLOAT;IQ=LW	8192	0	1	0 F204I001	Low warning activated	UL1	VOL	22401
204 E14	EV_FLOAT	16384	0	2	0 F204I002	Low warning reset	UL2	VOL	22402
204 E15	EV_FLOAT;IQ=LW	32768	0	2	0 F204I002	Low warning activated	UL2	VOL	22402
204 E16	EV_FLOAT	65536	0	3	0 F204I003	Low warning reset	UL3	VOL	22403
204 E17	EV_FLOAT;IQ=LW	131072	0	3	0 F204I003	Low warning activated	UL3	VOL	22403
204 E18	EV_FLOAT	262144	0	1	0 F204I001	Low alarm reset	UL1	VOL	22401
204 E19	EV_FLOAT;IQ=LA	524288	0	1	0 F204I001	Low alarm activated	UL1	VOL	22401
204 E20	EV_FLOAT	1048576	0	2	0 F204I002	Low alarm reset	UL2	VOL	22402
204 E21	EV_FLOAT;IQ=LA	2097152	0	2	0 F204I002	Low alarm activated	UL2	VOL	22402
204 E22	EV_FLOAT	4194304	0	3	0 F204I003	Low alarm reset	UL3	VOL	22403
204 E23	EV_FLOAT;IQ=LA	8388608	0	3	0 F204I003	Low alarm activated	UL3	VOL	22403
204 E24	-	0	0	99	0 -	-	-	-	22499
204 E25	EV_FLOAT	33554432	0	1	0 F204I001	Delta	UL1	VOL	22401
204 E26	-	0	0	99	0 -	-	-	-	22499
204 E27	EV_FLOAT	134217728	0	2	0 F204I002	Delta	UL2	VOL	22402
204 E28	-	0	0	99	0 -	-	-	-	22499
204 E29	EV_FLOAT	536870912	0	3	0 F204I003	Delta	UL3	VOL	22403
204 E30	-	0	0	99	0 -	-	-	-	22499
204 E31	-	0	0	99	0 -	-	-	-	22499
204 E32	EV_FLOAT	1	0	4	1 F204I001	High warning reset	U12	VOL	22404
204 E33	EV_FLOAT;IQ=HW	2	0	4	0 F204I001	High warning activated	U12	VOL	22404
204 E34	EV_FLOAT	4	0	5	1 F204I002	High warning reset	U23	VOL	22405
204 E35	EV_FLOAT;IQ=HW	8	0	5	0 F204I002	High warning activated	U23	VOL	22405
204 E36	EV_FLOAT	16	0	6	1 F204I003	High warning reset	U31	VOL	22406
204 E37	EV_FLOAT;IQ=HW	32	0	6	0 F204I003	High warning activated	U31	VOL	22406
204 E38	EV_FLOAT	64	0	4	0 F204I001	High alarm reset	U12	VOL	22404
204 E39	EV_FLOAT;IQ=HA	128	0	4	0 F204I001	High alarm activated	U12	VOL	22404
204 E40	EV_FLOAT	256	0	5	0 F204I002	High alarm reset	U23	VOL	22405
204 E41	EV_FLOAT;IQ=HA	512	0	5	0 F204I002	High alarm activated	U23	VOL	22405
204 E42	EV_FLOAT	1024	0	6	0 F204I003	High alarm reset	U31	VOL	22406
204 E43	EV_FLOAT;IQ=HA	2048	0	6	0 F204I003	High alarm activated	U31	VOL	22406
204 E44	EV_FLOAT	4096	0	4	0 F204I001	Low warning reset	U12	VOL	22404

204 E45	EV_FLOAT;IQ=LW	8192	0	4	0	F204I001	Low warning activated	U12	VOL	22404
204 E46	EV_FLOAT	16384	0	5	0	F204I002	Low warning reset	U23	VOL	22405
204 E47	EV_FLOAT;IQ=LW	32768	0	5	0	F204I002	Low warning activated	U23	VOL	22405
204 E48	EV_FLOAT	65536	0	6	0	F204I003	Low warning reset	U31	VOL	22406
204 E49	EV_FLOAT;IQ=LW	131072	0	6	0	F204I003	Low warning activated	U31	VOL	22406
204 E50	EV_FLOAT	262144	0	4	0	F204I001	Low alarm reset	U12	VOL	22404
204 E51	EV_FLOAT;IQ=LA	524288	0	4	0	F204I001	Low alarm activated	U12	VOL	22404
204 E52	EV_FLOAT	1048576	0	5	0	F204I002	Low alarm reset	U23	VOL	22405
204 E53	EV_FLOAT;IQ=LA	2097152	0	5	0	F204I002	Low alarm activated	U23	VOL	22405
204 E54	EV_FLOAT	4194304	0	6	0	F204I003	Low alarm reset	U31	VOL	22406
204 E55	EV_FLOAT;IQ=LA	8388608	0	6	0	F204I003	Low alarm activated	U31	VOL	22406
204 E56	-	0	0	99	0	-	-	-	-	22499
204 E57	EV_FLOAT	33554432	0	4	0	F204I001	Delta	U12	VOL	22404
204 E58	-	0	0	99	0	-	-	-	-	22499
204 E59	EV_FLOAT	134217728	0	5	0	F204I002	Delta	U23	VOL	22405
204 E60	-	0	0	99	0	-	-	-	-	22499
204 E61	EV_FLOAT	536870912	0	6	0	F204I003	Delta	U31	VOL	22406

Default mask=0

## Residual voltage measurement

/\*100205 / Rev F MEVO1A \*/

205 E0	EV_FLOAT	1	0	1	1	F205I001	High warning reset	Uo	VOL	22501
205 E1	EV_FLOAT;IQ=HW	2	0	1	0	F205I001	High warning activated	Uo	VOL	22501
205 E2	EV_FLOAT	4	0	1	0	F205I001	High alarm reset	Uo	VOL	22501
205 E3	EV_FLOAT;IQ=HA	8	0	1	0	F205I001	High alarm activated	Uo	VOL	22501
205 E4	-	0	0	99	-	-	-	-	-	22599
205 E5	EV_FLOAT	32	0	1	0	F205I001	Delta	Uo	VOL	22501

Default mask=0

## Three-phase voltage measurement (LV-side)

/\*100206 / Rev C MEVO3B \*/

206 E0	EV_FLOAT	1	0	1	1	F206I001	High warning reset	UL1	VOL	22601
206 E1	EV_FLOAT;IQ=HW	2	0	1	0	F206I001	High warning activated	UL1	VOL	22601
206 E2	EV_FLOAT	4	0	2	1	F206I002	High warning reset	UL2	VOL	22602
206 E3	EV_FLOAT;IQ=HW	8	0	2	0	F206I002	High warning activated	UL2	VOL	22602
206 E4	EV_FLOAT	16	0	3	1	F206I003	High warning reset	UL3	VOL	22603
206 E5	EV_FLOAT;IQ=HW	32	0	3	0	F206I003	High warning activated	UL3	VOL	22603
206 E6	EV_FLOAT	64	0	1	0	F206I001	High alarm reset	UL1	VOL	22601
206 E7	EV_FLOAT;IQ=HA	128	0	1	0	F206I001	High alarm activated	UL1	VOL	22601
206 E8	EV_FLOAT	256	0	2	0	F206I002	High alarm reset	UL2	VOL	22602
206 E9	EV_FLOAT;IQ=HA	512	0	2	0	F206I002	High alarm activated	UL2	VOL	22602
206 E10	EV_FLOAT	1024	0	3	0	F206I003	High alarm reset	UL3	VOL	22603
206 E11	EV_FLOAT;IQ=HA	2048	0	3	0	F206I003	High alarm activated	UL3	VOL	22603
206 E12	EV_FLOAT	4096	0	1	0	F206I001	Low warning reset	UL1	VOL	22601
206 E13	EV_FLOAT;IQ=LW	8192	0	1	0	F206I001	Low warning activated	UL1	VOL	22601
206 E14	EV_FLOAT	16384	0	2	0	F206I002	Low warning reset	UL2	VOL	22602
206 E15	EV_FLOAT;IQ=LW	32768	0	2	0	F206I002	Low warning activated	UL2	VOL	22602
206 E16	EV_FLOAT	65536	0	3	0	F206I003	Low warning reset	UL3	VOL	22603
206 E17	EV_FLOAT;IQ=LW	131072	0	3	0	F206I003	Low warning activated	UL3	VOL	22603

206 E18	EV_FLOAT	262144	0	1	0 F206I001	Low alarm reset	UL1	VOL	22601
206 E19	EV_FLOAT;IQ=LA	524288	0	1	0 F206I001	Low alarm activated	UL1	VOL	22601
206 E20	EV_FLOAT	1048576	0	2	0 F206I002	Low alarm reset	UL2	VOL	22602
206 E21	EV_FLOAT;IQ=LA	2097152	0	2	0 F206I002	Low alarm activated	UL2	VOL	22602
206 E22	EV_FLOAT	4194304	0	3	0 F206I003	Low alarm reset	UL3	VOL	22603
206 E23	EV_FLOAT;IQ=LA	8388608	0	3	0 F206I003	Low alarm activated	UL3	VOL	22603
206 E24	-	0	0	99	0 -	-	-	-	22699
206 E25	EV_FLOAT	33554432	0	1	0 F206I001	Delta	UL1	VOL	22601
206 E26	-	0	0	99	0 -	-	-	-	22699
206 E27	EV_FLOAT	134217728	0	2	0 F206I002	Delta	UL2	VOL	22602
206 E28	-	0	0	99	0 -	-	-	-	22699
206 E29	EV_FLOAT	536870912	0	3	0 F206I003	Delta	UL3	VOL	22603
206 E30	-	0	0	99	0 -	-	-	-	22699
206 E31	-	0	0	99	0 -	-	-	-	22699
206 E32	EV_FLOAT	1	0	4	1 F206I001	High warning reset	U12	VOL	22604
206 E33	EV_FLOAT;IQ=HW	2	0	4	0 F206I001	High warning activated	U12	VOL	22604
206 E34	EV_FLOAT	4	0	5	1 F206I002	High warning reset	U23	VOL	22605
206 E35	EV_FLOAT;IQ=HW	8	0	5	0 F206I002	High warning activated	U23	VOL	22605
206 E36	EV_FLOAT	16	0	6	1 F206I003	High warning reset	U31	VOL	22606
206 E37	EV_FLOAT;IQ=HW	32	0	6	0 F206I003	High warning activated	U31	VOL	22606
206 E38	EV_FLOAT	64	0	4	0 F206I001	High alarm reset	U12	VOL	22604
206 E39	EV_FLOAT;IQ=HA	128	0	4	0 F206I001	High alarm activated	U12	VOL	22604
206 E40	EV_FLOAT	256	0	5	0 F206I002	High alarm reset	U23	VOL	22605
206 E41	EV_FLOAT;IQ=HA	512	0	5	0 F206I002	High alarm activated	U23	VOL	22605
206 E42	EV_FLOAT	1024	0	6	0 F206I003	High alarm reset	U31	VOL	22606
206 E43	EV_FLOAT;IQ=HA	2048	0	6	0 F206I003	High alarm activated	U31	VOL	22606
206 E44	EV_FLOAT	4096	0	4	0 F206I001	Low warning reset	U12	VOL	22604
206 E45	EV_FLOAT;IQ=LW	8192	0	4	0 F206I001	Low warning activated	U12	VOL	22604
206 E46	EV_FLOAT	16384	0	5	0 F206I002	Low warning reset	U23	VOL	22605
206 E47	EV_FLOAT;IQ=LW	32768	0	5	0 F206I002	Low warning activated	U23	VOL	22605
206 E48	EV_FLOAT	65536	0	6	0 F206I003	Low warning reset	U31	VOL	22606
206 E49	EV_FLOAT;IQ=LW	131072	0	6	0 F206I003	Low warning activated	U31	VOL	22606
206 E50	EV_FLOAT	262144	0	4	0 F206I001	Low alarm reset	U12	VOL	22604
206 E51	EV_FLOAT;IQ=LA	524288	0	4	0 F206I001	Low alarm activated	U12	VOL	22604
206 E52	EV_FLOAT	1048576	0	5	0 F206I002	Low alarm reset	U23	VOL	22605
206 E53	EV_FLOAT;IQ=LA	2097152	0	5	0 F206I002	Low alarm activated	U23	VOL	22605
206 E54	EV_FLOAT	4194304	0	6	0 F206I003	Low alarm reset	U31	VOL	22606
206 E55	EV_FLOAT;IQ=LA	8388608	0	6	0 F206I003	Low alarm activated	U31	VOL	22606
206 E56	-	0	0	99	0 -	-	-	-	22699
206 E57	EV_FLOAT	33554432	0	4	0 F206I001	Delta	U12	VOL	22604
206 E58	-	0	0	99	0 -	-	-	-	22699
206 E59	EV_FLOAT	134217728	0	5	0 F206I002	Delta	U23	VOL	22605
206 E60	-	0	0	99	0 -	-	-	-	22699
206 E61	EV_FLOAT	536870912	0	6	0 F206I003	Delta	U31	VOL	22606

Default mask=0

## Three-Phase Power and Energy Measurement

/\*100207 / Rev G MEPE7 \*/

207 E0	EV_FLOAT	1	0	1	1 F207I001	High warning reset	P3	APW	22701
207 E1	EV_FLOAT;IQ=HW	2	0	1	0 F207I001	High warning activated	P3	APW	22701
207 E2	EV_FLOAT	4	0	1	0 F207I001	High alarm reset	P3	APW	22701
207 E3	EV_FLOAT;IQ=HA	8	0	1	0 F207I001	High alarm activated	P3	APW	22701
207 E4	EV_FLOAT	16	0	2	1 F207I002	High warning reset	Q3	RPW	22702
207 E5	EV_FLOAT;IQ=HW	32	0	2	0 F207I002	High warning activated	Q3	RPW	22702
207 E6	EV_FLOAT	64	0	2	0 F207I002	High alarm reset	Q3	RPW	22702
207 E7	EV_FLOAT;IQ=HA	128	0	2	0 F207I002	High alarm activated	Q3	RPW	22702
207 E8	EV_FLOAT	256	0	1	0 F207I001	Low warning reset	P3	APW	22701
207 E9	EV_FLOAT;IQ=LW	512	0	1	0 F207I001	Low warning activated	P3	APW	22701
207 E10	EV_FLOAT	1024	0	1	0 F207I001	Low alarm reset	P3	APW	22701
207 E11	EV_FLOAT;IQ=LA	2048	0	1	0 F207I001	Low alarm activated	P3	APW	22701
207 E12	EV_FLOAT	4096	0	2	0 F207I002	Low warning reset	Q3	RPW	22702
207 E13	EV_FLOAT;IQ=LW	8192	0	2	0 F207I002	Low warning activated	Q3	RPW	22702
207 E14	EV_FLOAT	16384	0	2	0 F207I002	Low alarm reset	Q3	RPW	22702
207 E15	EV_FLOAT;IQ=LA	32768	0	2	0 F207I002	Low alarm activated	Q3	RPW	22702
207 E16	-	0	0	99 -	-	-	-	-	22799
207 E17	EV_FLOAT	131072	0	1	0 F207I001	Delta	P3	APW	22701
207 E18	-	0	0	99 -	-	-	-	-	22799
207 E19	EV_FLOAT	524288	0	2	0 F207I002	Delta	Q3	RPW	22702
207 E20	-	0	0	99 -	-	-	-	-	22799
207 E21	EV_FLOAT	2097152	0	3	0 -	Delta	S3	TPW	22703
207 E22	-	0	0	99 -	-	-	-	-	22799
207 E23	EV_FLOAT	8388608	0	4	1 F207I003	Delta	DPF	DPF	22704
207 E24	-	0	0	99 -	-	-	-	-	22799
207 E25	EV_FLOAT	33554432	0	5	0 F207V414	Delta	Active energy	RFE	22705
207 E26	-	0	0	99 -	-	-	-	-	22799
207 E27	EV_FLOAT	134217728	0	6	0 F207V415	Delta	Active reverse energy	RRE	22706
207 E28	-	0	0	99 -	-	-	-	-	22799
207 E29	EV_FLOAT	536870912	0	7	0 F207V416	Delta	Reactive energy	AFE	22707
207 E30	-	0	0	99 -	-	-	-	-	22799
207 E31	EV_FLOAT	2147483648	0	8	0 F207V417	Delta	Reactive reverse energy	ARE	22708

Default mask=0

## System frequency measurement

/\*100208 / Rev D MEFR1 \*/

208 E0	EV_FLOAT	1	0	1	1	F208I001	High warning reset	Frequency	FRQ	22801
208 E1	EV_FLOAT;IQ=HW	2	0	1	0	F208I001	High warning activated	Frequency	FRQ	22801
208 E2	EV_FLOAT	4	0	1	0	F208I001	High alarm reset	Frequency	FRQ	22801
208 E3	EV_FLOAT;IQ=HA	8	0	1	0	F208I001	High alarm activated	Frequency	FRQ	22801
208 E4	EV_FLOAT	16	0	1	0	F208I001	Low warning reset	Frequency	FRQ	22801
208 E5	EV_FLOAT;IQ=LW	32	0	1	0	F208I001	Low warning activated	Frequency	FRQ	22801
208 E6	EV_FLOAT	64	0	1	0	F208I001	Low alarm reset	Frequency	FRQ	22801
208 E7	EV_FLOAT;IQ=LA	128	0	1	0	F208I001	Low alarm activated	Frequency	FRQ	22801
208 E8	-	0	0	99	-	-	-	-	-	22899
208 E9	EV_FLOAT	512	0	1	0	F208I001	Delta	Frequency	FRQ	22801
Default mask=0										

## General measurement 1

/\*100213 / Rev C MEAI1 \*/

213 E0	EV_FLOAT	1	0	1	1	F213I001	High warning reset	AI1 value	VOL	23301
213 E1	EV_FLOAT;IQ=HW	2	0	1	0	F213I001	High warning activated	AI1 value	VOL	23301
213 E2	EV_FLOAT	4	0	1	0	F213I001	High alarm reset	AI1 value	VOL	23301
213 E3	EV_FLOAT;IQ=HA	8	0	1	0	F213I001	High alarm activated	AI1 value	VOL	23301
213 E4	EV_FLOAT	16	0	1	0	F213I001	Low warning reset	AI1 value	VOL	23301
213 E5	EV_FLOAT;IQ=LW	32	0	1	0	F213I001	Low warning activated	AI1 value	VOL	23301
213 E6	EV_FLOAT	64	0	1	0	F213I001	Low alarm reset	AI1 value	VOL	23301
213 E7	EV_FLOAT;IQ=LA	128	0	1	0	F213I001	Low alarm activated	AI1 value	VOL	23301
213 E8	EV_FLOAT	256	0	1	0	F213I001	Value is valid	AI1 value	VOL	23301
213 E9	EV_FLOAT;IQ=IV	512	0	1	0	F213I001	Value is invalid	AI1 value	VOL	23301
213 E10	-	1024	0	99	0	-	-	-	-	23399
213 E11	EV_FLOAT	2048	0	1	0	F213I001	Delta	AI1 value	VOL	23301
Default mask=0										

Transient disturbance recorder for 16 analogue channels  
/\*100225 / Rev K MEDREC16 \*/

225 E0	EV_NODAT	1	1	0	0	-	Not full	Recorder memory	CMS	24500
225 E1	EV_NODAT	2	1	0	0	-	Full	Recorder memory	CMS	24500
225 E2	-	0	0	99	0	-	-	-	-	24599
225 E3	EV_NODAT	8	1	1	0	-	On	Overwrite of recording	CMS	24501
225 E4	-	0	0	99	0	-	-	-	-	24599
225 E5	-	0	0	99	0	-	-	-	-	24599
225 E6	-	0	0	99	0	-	-	-	-	24599
225 E7	EV_NODAT	128	1	4	0	-	Failed	Manual trigger	CMS	24504
225 E8	-	0	0	99	0	-	-	-	-	24599
225 E9	EV_NODAT	512	1	5	0	-	On	New recording made	INZ	24505
225 E10	-	0	0	99	0	-	-	-	-	24599
225 E11	-	0	0	99	0	-	-	-	-	24599
225 E12	-	0	0	99	0	-	-	-	-	24599
225 E13	-	0	0	99	0	-	-	-	-	24599
225 E14	-	0	0	99	0	-	-	-	-	24599
225 E15	-	0	0	99	0	-	-	-	-	24599
225 E16	-	0	0	99	0	-	-	-	-	24599
225 E17	-	0	0	99	0	-	-	-	-	24599
225 E18	-	0	0	99	0	-	-	-	-	24599
225 E19	-	0	0	99	0	-	-	-	-	24599
225 E20	-	0	0	99	0	-	-	-	-	24599
225 E21	-	0	0	99	0	-	-	-	-	24599
225 E22	-	0	0	99	0	-	-	-	-	24599
225 E23	-	0	0	99	0	-	-	-	-	24599
225 E24	-	0	0	99	0	-	-	-	-	24599
225 E25	-	0	0	99	0	-	-	-	-	24599
225 E26	-	0	0	99	0	-	-	-	-	24599
225 E27	-	0	0	99	0	-	-	-	-	24599
225 E28	-	0	0	99	0	-	-	-	-	24599
225 E29	-	0	0	99	0	-	-	-	-	24599
225 E30	-	0	0	99	0	-	-	-	-	24599
225 E31	EV_NODAT	2147483648	1	3	0	-	On	Recorder triggered	INZ	24503
Default mask=2147484299										

Current waveform distortion measurement  
/\*100512 / Rev E PQCU3H \*/

512 E0	EV_NODAT	1	1	0	0	F512O001	Reset	PQ 3Inf:Harmonic limit	ALA	53200
512 E1	EV_NODAT	2	1	0	0	F512O001	Exceeded	PQ 3Inf:Harmonic limit	ALA	53200
512 E2	EV_INT16	4	1	1	0	F512O002	Exceeded	PQ 3Inf:Cumulative limit	ALA	53201
512 E3	EV_NODAT	8	1	2	0	-	On	PQ 3Inf:Obs. period near end	OUT	53202
512 E4	EV_NODAT	16	1	3	0	-	On	PQ 3Inf:Obs. period ended	OUT	53203
Default mask=31										

Voltage waveform distortion measurement

/\*100513 / Rev E PQVO3H \*/

513 E0	EV_NODAT	1	1	0	0 F513O001	Reset	PQ 3Unf:Harmonic limit	ALA	53300
513 E1	EV_NODAT	2	1	0	0 F513O001	Exceeded	PQ 3Unf:Harmonic limit	ALA	53300
513 E2	EV_INT16	4	1	1	0 F513O002	Exceeded	PQ 3Unf:Cumulative limit	ALA	53301
513 E3	EV_NODAT	8	1	2	0 -	On	PQ 3Unf:Obs. period near end	OUT	53302
513 E4	EV_NODAT	16	1	3	0 -	On	PQ 3Unf:Obs. period ended	OUT	53303
Default mask=31									