System Description

Advant Controller 31
Intelligent Decentralized Automation System

Hardware

ABB Schalt- und Steuerungstechnik
Regulations
Concerning the Setting up of Installations

Apart from the basic "Regulations for the Setting up of Power Installations" DIN VDE* 0100 and for "The Rating of Creepage Distances and Clearances" DIN VDE 0110 Part 1 and Part 2, the regulations "The Equipment of Power Installations with Electrical Components" DIN VDE 0160 in conjunction with DIN VDE 0660 Part 500 have to be taken into consideration.

Further attention has to be paid to DIN VDE 0113 Part 1 and Part 200 in case of the control of working and processing machines. If operating elements are to be mounted near parts with dangerous contact voltage DIN VDE 0106 Part 100 is additionally relevant.

If the protection against direct contact according to DIN VDE 0160 is required, this has to be ensured by the user (e.g. by incorporating the elements in a switch-gear cabinet). The devices are designed for pollution severity 2 in accordance with DIN VDE 0110 Part 1. If higher pollution is expected, the devices must be installed in appropriate housings.

The user has to guarantee that the devices and the components belonging to them are mounted following these regulations. For operating the machines and installations, other national and international relevant regulations, concerning prevention of accidents and using technical working means, also have to be met.

Devices of Advant Controller 31 Series (AC31) are designed according to IEC 1131 Part 2. Meeting this regulation, they are classified in overvoltage category II which is in conformance with DIN VDE 0110 Part 2.

For the direct connection of AC31 devices, which are powered with or coupled to AC line voltages of overvoltage category III, appropriate protection measures corresponding to overvoltage category II according to IEC-Report 664/1980 and DIN VDE 0110 Part 1 are to install.

Equivalent standards:

DIN VDE 0110 Part 1 ⇔ IEC 664
DIN VDE 0113 Part 1 ⇔ EN 60204 Part 1
DIN VDE 0660 Part 500 ⇔ EN 60439–1 ⇔ IEC 439–1

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* VDE stands for "Association of German Electrical Engineers".

ABB Schalt- und Steuerungstechnik GmbH Heidelberg
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9 unused

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### 1.1 System data

#### Operating and environmental conditions

<table>
<thead>
<tr>
<th>VOLTAGES</th>
<th>DESCRIPTION</th>
<th>LIMITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>24 V DC</td>
<td>Process and supply voltage absolute limits</td>
<td>19.2 V ... 30 V incl. ripple</td>
</tr>
<tr>
<td></td>
<td>ripple</td>
<td>≤ 5 %</td>
</tr>
<tr>
<td>120 V AC</td>
<td>Line voltage frequency</td>
<td>120 V AC (+ 10 %, - 15 %)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>50 Hz (+ 5 %, - 5 %) or 60 Hz (+ 5 %, - 5 %)</td>
</tr>
<tr>
<td>230 V AC</td>
<td>Line voltage frequency</td>
<td>230 V AC (+ 10 %, - 15 %)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>50 Hz (+ 5 %, - 5 %) or 60 Hz (+ 5 %, - 5 %)</td>
</tr>
</tbody>
</table>

#### Allowed interruptions of power supply

- **DC supply**: interruption ≤ 10 ms, time between 2 interruptions ≥ 1 s
- **AC supply**: interruption ≤ 0.5 periods, time between 2 interruptions ≥ 1 s

<table>
<thead>
<tr>
<th>TEMPERATURE</th>
<th>DESCRIPTION</th>
<th>LIMITS</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>operating</td>
<td>0 °C ... + 55 °C</td>
</tr>
<tr>
<td></td>
<td>storage</td>
<td>- 25 °C ... + 75 °C</td>
</tr>
<tr>
<td></td>
<td>transport</td>
<td>- 25 °C ... + 75 °C</td>
</tr>
</tbody>
</table>

#### Humidity

50...95 %, without condensation

#### Air pressure

- **operation**: > 800 hPa/≤ 2000 m
- **storage**: > 660 hPa/≤ 3500 m

#### Creepage distances and clearances

The creepage distances and clearances meet Overvoltage category II, pollution degree 2

#### Insulation test voltages

- 230 V circuits (mains, 230 V inputs/outputs) against other circuitry: 2500 V
- 120 V circuits (mains) against other circuitry: 1500 V
- 24 V circuits (supply, 24 V inputs/outputs), when electrically isolated against other circuitry: 500 V
- CS31 bus against other circuitry: 500 V

#### Electromagnetic compatibility

- **Immunity**
  - against electrostatic discharge (ESD) according to EN 61000-4-2
    - electrostatic voltage in case of air discharge: 8 kV
    - electrostatic voltage in case of contact discharge: 4 kV
  - immunity against the influence of radiated interference (CW radiated) according to EN 61000-4-5
    - test field strength: 10 V/m
Immunity against transient interference voltages (burst) according to EN 61000-4-4:
- supply voltage units (AC/DC) 2 kV
- digital inputs/outputs (24 V DC) 1 kV
- digital inputs/outputs (120/230 V AC) 2 kV
- analog inputs/outputs 1 kV
- CS31-system bus 2 kV
- serial interfaces (COM) 0.5 kV
- ARCnet 0.5 kV

Immunity against the influence of line-conducted interferences (CW conducted) according to EN 61000-4-6:
- test voltage 10 V

Radio disturbance according to EN 55011 radio interference level A and according to EN 55022 radio interference level A (only for communication modules):
- Immunity against transient interference voltages (burst) 2 kV
- Immunity against the influence of line-conducted interferences (CW conducted) 10 V

Mechanical data

Wiring method / terminals
- for plug-in base ECZ screw-type terminals for normal and Phillips-head screwdrivers, conductor cross section max. 2 x 2.5 mm²
- for removable terminal blocks (big) screw-type terminals for normal screwdrivers, conductor cross section max. 2.5 mm²
- for removable terminal blocks (small) screw-type terminals for normal screwdrivers, conductor cross section max. 1.5 mm²

Degree of protection
IP 20

Housing
according to UL 94

Vibration resistance all three axes
- 10 Hz...57 Hz continuous 0.0375 mm peak 0.075 mm
- 57 Hz...150 Hz continuous 0.5 g peak 1.0 g

Shock test all three axes
15 g, 11 ms, half-sinusoidal

Mounting of the modules
- DIN rail according to DIN EN 50022, width 35 mm, depth 15 mm
- only for plug-in base ECZ: depths 7.5 mm and 15 mm
- mounting with screws screws with a diameter of 4 mm

Interfaces
- between the basic unit and the input/output modules, EIA RS-485 (CS31 system bus)
- for the programming units and the connection to a terminal, 9-pole D-SUB, female EIA RS-232
1.2 CS31 system bus

Wiring

Bus line

- construction: 2 cores, twisted, with common shield
- conductor cross section: > 0.22 mm² (24 AWG)
- recommendation: 0.5 mm², corresponds to Ø 0.8 mm
- twisting rate: > 10 per meter (symmetrically twisted)
- core insulation: polyethylene (PE)
- resistance per core: ≤ 100 Ω / km
- characteristic impedance: approx. 120 Ω (100…150 Ω)
- Capacitance between the cores: < 55 nF / km (if higher, the max. bus length must be reduced)
- terminating resistors: 120 Ω ¼ W at both line ends
- bus length: max. 500 m

Remarks

Commonly used telephone cables with PE insulation and a core diameter of ≥ 0.8 mm are normally good.

Cables with PVC core insulation and a core diameter of ≥ 0.8 mm can be used up to a length of approx. 250 m. In this case, the bus terminating resistor is approx. 100 Ω.
Bus configurations
A CS31 system bus always contains only one bus master (basic unit or coupler) which controls all actions on the bus. Up to 31 slaves can be connected to the bus, e.g. remote modules or slave-configured basic units.

Besides the wiring instructions shown below, the wiring and earthing instructions provided with the descriptions of the modules are valid additionally.

**Fig. 1.2-1:** Bus configuration for CS31 system bus (bus master at one end of the bus line)

**Fig. 1.2-2:** Bus configuration for CS31 system bus (bus master within the bus line)

This configuration is only possible for bus masters that do not contain internal bus terminating resistors. Therefore, this configuration is not allowed with the couplers 07 CS 61 and 35 CS 91.
Wiring with spur lines is not allowed.

**Earthing**

In order to avoid disturbance, the cable shields must be earthed directly.

**Case a:**

Several switch-gear cabinets: If it can be guaranteed that no potential differences can occur between the switch-gear cabinets by means of current-carrying metal connections (earthing bars, steel constructions etc.), the direct earthing is chosen.

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**Fig. 1.2-3:** Wiring with spur lines is not allowed.

**Fig. 1.2-4:** Direct earthing
Case b:
Several switch-gear cabinets: If potential differences can occur between the switch-gear cabinets, the capacitive earthing is chosen in order to avoid circulating currents on the cable shields.

![Diagram of earthing concept with several switch-gear cabinets](image)

Fig. 1.2-5: Earthing concept with several switch-gear cabinets: direct earthing of cable shields when cables enter the first switch-gear cabinet (containing the master), and capacitive earthing at the modules.

Note: The total length of the earthing connections shield-module and module-earthing bar must be as short as possible (max. 25 cm). The conductor cross section must be at least 2.5 mm².

VDE 0160 requires, that the shield must be earthed directly at least once per system.

1.3 Hints for project planning
see volume 5
1.4 Bus cycle time and data security

Bus cycle time

In the following, the bus cycle time t_B is introduced. The reaction time from terminal to terminal is the sum of several delays and is described with the basic units. The bus cycle time consist of:

- Base time 2 ms
  During this time the basic unit performs a diagnosis and looks for new remote modules.

- Bus transmission time per module, depends on the type of module (see next page).

- Equation for the bus cycle time of the AC31 modules:
  Bus cycle time t_B = sum of the bus transmission times of the modules + base time (2 ms)
<table>
<thead>
<tr>
<th>Module</th>
<th>Bus transmission time in µs</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Digital input modules</strong></td>
<td></td>
</tr>
<tr>
<td>ICSI 08 D1</td>
<td>323</td>
</tr>
<tr>
<td>ICSI 08 E1</td>
<td>323</td>
</tr>
<tr>
<td>ICSI 08 E3</td>
<td>323</td>
</tr>
<tr>
<td>ICSI 08 E4</td>
<td>323</td>
</tr>
<tr>
<td>ICSI 16 D1</td>
<td>387</td>
</tr>
<tr>
<td>ICSI 16 E1</td>
<td>387</td>
</tr>
<tr>
<td><strong>Digital output modules</strong></td>
<td></td>
</tr>
<tr>
<td>ICSO 08 R1</td>
<td>260</td>
</tr>
<tr>
<td>ICSO 08 Y1</td>
<td>260</td>
</tr>
<tr>
<td>ICSO 16 N1</td>
<td>340</td>
</tr>
<tr>
<td><strong>Digital input/output modules</strong></td>
<td></td>
</tr>
<tr>
<td>ICSC 08 L1</td>
<td>387</td>
</tr>
<tr>
<td>ICFC 16 L1</td>
<td>516</td>
</tr>
<tr>
<td>ICSK 20 F1</td>
<td>452</td>
</tr>
<tr>
<td>ICSK 20 N1</td>
<td>452</td>
</tr>
<tr>
<td>07 DC 91 / ICDG 32 L1</td>
<td>516/590</td>
</tr>
<tr>
<td>07 DI 92</td>
<td>516</td>
</tr>
<tr>
<td>07 DC 92</td>
<td>750/516</td>
</tr>
<tr>
<td><strong>IP65-I/O modules</strong></td>
<td></td>
</tr>
<tr>
<td>07 DK 93-I</td>
<td>387</td>
</tr>
<tr>
<td>07 DO 93-I</td>
<td>260</td>
</tr>
<tr>
<td>07 DI 93-I</td>
<td>387</td>
</tr>
<tr>
<td><strong>Analog modules</strong></td>
<td></td>
</tr>
<tr>
<td>ICSM 06 A6</td>
<td>1162</td>
</tr>
<tr>
<td>ICSE 08 A6</td>
<td>1355</td>
</tr>
<tr>
<td>ICSE 08 B5</td>
<td>1355</td>
</tr>
<tr>
<td>ICSA 04 B5</td>
<td>700</td>
</tr>
<tr>
<td>ICST 08 A8</td>
<td>1355</td>
</tr>
<tr>
<td>ICST 08 A9</td>
<td>1355</td>
</tr>
<tr>
<td>07 AI 91 / ICDT 08 B5</td>
<td>1355</td>
</tr>
<tr>
<td>07 AC 91</td>
<td>2500</td>
</tr>
<tr>
<td><strong>Couplers</strong></td>
<td></td>
</tr>
<tr>
<td>ICBG32L7</td>
<td>516</td>
</tr>
<tr>
<td>ICBG64L7</td>
<td>750</td>
</tr>
<tr>
<td><strong>High-speed counter</strong></td>
<td></td>
</tr>
<tr>
<td>ICSF 08 D1</td>
<td>1300</td>
</tr>
<tr>
<td><strong>Safety-related modules</strong></td>
<td></td>
</tr>
<tr>
<td>07 DI 90-S / 07 EB 90-S</td>
<td>590</td>
</tr>
<tr>
<td>07 DO 90-S / 07 AB 90-S</td>
<td>750</td>
</tr>
<tr>
<td>07 AI 90-S / 07 EA 90-S</td>
<td>1050</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Module</th>
<th>Bus transmission time in µs</th>
</tr>
</thead>
<tbody>
<tr>
<td>Keyboard/LED controller module</td>
<td></td>
</tr>
<tr>
<td>07 TC 90/91, TCK 64 16 E/A</td>
<td>387</td>
</tr>
<tr>
<td>32 E/A</td>
<td>750</td>
</tr>
<tr>
<td><strong>Basic units as slaves</strong></td>
<td></td>
</tr>
<tr>
<td>07 KR 31 / 07 KT 31</td>
<td>516 * 1)</td>
</tr>
<tr>
<td>07 KR 91 / KT 92 / KT 93 / KT 94</td>
<td>750 * 2)</td>
</tr>
<tr>
<td>* = default</td>
<td></td>
</tr>
<tr>
<td><strong>Typ. settings</strong></td>
<td></td>
</tr>
<tr>
<td>1) Send 2 bytes + receive 2 bytes (1 word)</td>
<td>516</td>
</tr>
<tr>
<td>2) Send 4 bytes + receive 4 bytes (2 words)</td>
<td>750</td>
</tr>
<tr>
<td>Send 8 bytes + receive 8 bytes (4 words)</td>
<td>1300</td>
</tr>
<tr>
<td>Send 12 bytes + receive 12 bytes (6 words)</td>
<td>1850</td>
</tr>
<tr>
<td>Send 8 words + receive 8 words</td>
<td>2500</td>
</tr>
</tbody>
</table>
For system bus-compatible modules from other companies, the bus cycle times are provided with the modules.

Example: 8 modules of 07 DI 92 are used. For one 07 DI 92 a bus transmission time of 516 µs is given in the table. The bus cycle time is now calculated as 
\[ t_{\text{ci}} = 8 \cdot 516 \mu s + 2 \text{ ms} = 6.1 \text{ ms}. \]

Data security
The transmission protocol serves for max. 31 slaves (remote I/O modules) plus one master - the basic unit.

During the initialization cycle the bus master searches for all the slaves and gets in this way the number and types of the found modules.

All telegrams terminate with a CRC8 check word. Error security of the bus is Hamming distance 4.

All the messages have the following format:

Request of the basic unit:

<table>
<thead>
<tr>
<th>Address No.</th>
<th>Data</th>
<th>CRC8</th>
</tr>
</thead>
</table>

Answer of an I/O module:

<table>
<thead>
<tr>
<th>Data</th>
<th>CRC8</th>
</tr>
</thead>
</table>

In every cycle, the bus master addresses all existing modules one after the other, performs diagnostic functions and checks for new installed modules. In this way diagnosis is carried out continuously, the networking is always checked for correct function and new installed modules are detected quickly.

If a basic unit or a module detects a difference between a received CRC and its self-calculated CRC, the concerned telegram is ignored.

A bus error exists when 10 messages are wrong in sequence. For reactions see the descriptions of the basic units and the couplers.

1.5 Replacing modules on the CS31 system bus

- Check the DIL switch settings.
- After replacing a module, the new module will be adopted into the bus cycle by the basic unit or by the coupler automatically. If a module is replaced while the system is running, some error flags may remain set. They can, for instance, be reset with power off/on of the basic unit.

1.6 Number of user data

The following table shows, how many user data (in bytes) the modules send to the master or receive from the master.

<table>
<thead>
<tr>
<th>Module</th>
<th>sends</th>
<th>receives</th>
<th>type</th>
</tr>
</thead>
<tbody>
<tr>
<td>ICSI 08...</td>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>ICSI 16...</td>
<td>2</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>ICSO 08...</td>
<td>0</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>ICSO 16...</td>
<td>0</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>ICSC 08...</td>
<td>1</td>
<td>1</td>
<td>4</td>
</tr>
<tr>
<td>ICSC 16...</td>
<td>2</td>
<td>2</td>
<td>4</td>
</tr>
<tr>
<td>ICSK 20...</td>
<td>2</td>
<td>1</td>
<td>4</td>
</tr>
<tr>
<td>07 DC 91 (ICDG 32 L1) *</td>
<td>2</td>
<td>2 4</td>
<td></td>
</tr>
<tr>
<td></td>
<td>3</td>
<td>2</td>
<td>4</td>
</tr>
<tr>
<td>07 DI 92</td>
<td>4</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>07 DC 92 *</td>
<td>4</td>
<td>4</td>
<td>4</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>4</td>
<td>2</td>
</tr>
<tr>
<td>07 DK 93-I</td>
<td>1</td>
<td>1</td>
<td>4</td>
</tr>
<tr>
<td>07 DO 93-I</td>
<td>0</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>07 DI 93-I</td>
<td>2</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>07 TC 91 / 07 TC 90 (TCK 64) *</td>
<td>2</td>
<td>2 4</td>
<td></td>
</tr>
<tr>
<td></td>
<td>4</td>
<td>4</td>
<td>4</td>
</tr>
<tr>
<td>ICSE 08...</td>
<td>16</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>ICST 08...</td>
<td>16</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>07 AI 91 (ICDT 08 B5)</td>
<td>16</td>
<td>0 1</td>
<td></td>
</tr>
<tr>
<td>ICSA 04 B5</td>
<td>0</td>
<td>8</td>
<td>3</td>
</tr>
<tr>
<td>ICSM 06 A6</td>
<td>8</td>
<td>4</td>
<td>5</td>
</tr>
<tr>
<td>07 AC 91 12 bits *</td>
<td>16 16</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td></td>
<td>16</td>
<td>16</td>
<td>5</td>
</tr>
<tr>
<td></td>
<td>16</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td></td>
<td>0</td>
<td>16</td>
<td>3</td>
</tr>
<tr>
<td>ICSF 08 D1</td>
<td>10</td>
<td>6</td>
<td>5</td>
</tr>
<tr>
<td>07 AI 90-S (07 EA 90-S)</td>
<td>12</td>
<td>0 1</td>
<td></td>
</tr>
<tr>
<td>07 DO 90-S (07 AB 90-S)</td>
<td>4</td>
<td>4 4</td>
<td></td>
</tr>
<tr>
<td>07 DI 90-S (07 EB 90-S)</td>
<td>5</td>
<td>0 0</td>
<td></td>
</tr>
</tbody>
</table>

* depends on configuration

Types:
0 = Digital input
1 = Analog input word
2 = Digital output
3 = Analog output word
4 = Digital input/output
5 = Analog input/output word
4 Digital modules

4.1 Digital input modules
4.1.1 07 DI 92: Digital input module, 32 inputs 24 V DC, electrically isolated in groups ............... 4.1.1-1

4.2 Digital output modules

4.3 Digital input/output modules
4.3.1 07 DC 91: Digital input/output module, 16 inputs, 8 outputs
8 configurable inputs/outputs, 24 V DC ................................................................. 4.3.1-1
4.3.2 07 DC 92: Digital input/output module, 32 configurable inputs/outputs, 24 V DC,
outputs can be loaded with 500 mA ................................................................. 4.3.2-1
4.3.3 07 TC 90: Keyboard controller, 32 switches/keys and 32 LEDs controllable ....................... 4.3.3-1
4.3.4 07 TC 91: Keyboard controller, 32 switches/keys and 32 LEDs controllable ....................... 4.3.4-1

4.4 Digital input and output modules with degree of protection IP67
4.4.1 07 DI 93-I: Digital input module, 16 channels 24 V DC, degree of protection IP67 ..................... 4.4.1-1
4.4.2 07 DO 93-I: Digital output module, 8 channels 24 V DC, 2 A, degree of protection IP67 ............ 4.4.2-1
4.4.3 07 DK 93-I: Digital input/output module, 8 input channels 24 V DC,
4 output channels 24 V DC/2 A, degree of protection IP67 ..................................... 4.4.3-1
4.1.1 Digital Input Module 07 DI 92

32 digital inputs 24 V DC, electrically isolated in groups, CS31 system bus

Fig. 4.1.1-1: Digital Input Module 07 DI 92

Contents

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Display and operating elements on the front panel .......... 4.1.1-1
Electrical connection ........................................ 4.1.1-1
Addressing ...................................................... 4.1.1-3
Input/output configuration .................................. 4.1.1-3
Normal operation ............................................ 4.1.1-3
Displays ........................................................ 4.1.1-3
Technical data ................................................ 4.1.1-4
Dimensions for installation .................................... 4.1.1-6

Intended purpose

The digital input module 07 DI 92 is used as a remote module on the CS31 system bus. It contains 32 inputs, 24 V DC, in 4 groups with the following features:

- The 4 groups of the inputs are electrically isolated from each other and from the rest of the unit.
- The module occupies two digital addresses for inputs on the CS31 system bus.

The unit works with a supply voltage of 24 V DC.

The system bus connection is electrically isolated from the rest of the unit.

Display and operating elements on the front panel

1 32 green LEDs to indicate the signal status of the inputs
2 Red LED for error messages
3 Test button

Electrical connection

The module can be mounted on a DIN rail (15 mm high) or with 4 screws. The following illustration shows the electrical connection of the input module.
Advant Controller 31
Input Unit

DC-IN 5W
24 V 0V PE
24 25 26 27 28
X4

Inputs 24V DC
En+1,00... En+1,07
29 30 31 32 33 34 35 36 37 38
X5

Inputs 24V DC
En+1,08... En+1,15
39 40 41 42 43 44 45 46 47 48
X6

En+1...
00 01 02 03 04 05 06 07

Fig. 4.1.1-2: Electrical connection of the digital input module 07 DI 92

Caution: The process voltage must be included in the earthing concept of the control system (e.g., earthing of the minus terminal).

Addressing must be done with the coding switch under the slide cover located on the right side of the module housing.
**Addressing**

An address must be set for each module to enable the basic unit to correctly access the inputs and outputs.

A detailed description about "Addressing" can be found in the chapter "Addressing" of the basic units and couplers.

The address setting is accomplished with the DIL switch located under the slide cover on the right side of the module housing.

When using basic units 07 KR 91, 07 KT 92 to 07 KT 94 as bus master, the following address assignments apply:

<table>
<thead>
<tr>
<th>Basic units 07 KR 91 / 07 KT 92/93/94</th>
</tr>
</thead>
<tbody>
<tr>
<td>Terminal</td>
</tr>
<tr>
<td>----------</td>
</tr>
<tr>
<td>5</td>
</tr>
<tr>
<td>6</td>
</tr>
<tr>
<td>7</td>
</tr>
<tr>
<td>8</td>
</tr>
<tr>
<td>9</td>
</tr>
<tr>
<td>10</td>
</tr>
<tr>
<td>11</td>
</tr>
<tr>
<td>12</td>
</tr>
<tr>
<td>15</td>
</tr>
<tr>
<td>16</td>
</tr>
<tr>
<td>17</td>
</tr>
<tr>
<td>18</td>
</tr>
<tr>
<td>19</td>
</tr>
<tr>
<td>20</td>
</tr>
<tr>
<td>21</td>
</tr>
<tr>
<td>22</td>
</tr>
</tbody>
</table>

n: Module address, can be set with address DIL switch with switches 2...7.

Recommended module addresses for 07 KR 91 / 07 KT 92/93/94 as bus master:

08, 10, 12,...60 (even-numbered addresses)

The module occupies two addresses on the CS31 system bus for inputs.

Switches 1 and 8 of the address DIL switch must be set to OFF.

Fig 4.1.1-3: Addresses of the channels

---

Note:

Module 07 DI 92 reads the position of the address switch only during the initialization after the power was switched on, which means, that changes of the setting during operation will remain ineffective until the next initialization.

**Input/output configuration**

No configuration data are required for the 07 DI 92.

**Normal operation**

- The module automatically initializes after the power has been switched on. During that time, all LEDs are switched on.
- When the CS31 system bus does not run, LED ③ flashes
- The LED ③ goes out again after the system bus runs correctly and the unit does not detect any error.
- The 32 green LEDs ① show the signal status of the 32 inputs.

**Displays**

By pressing the test button, an LED test is initiated. All LEDs must light up. Following that, the position of the address switch is displayed for about 3 seconds by LEDs 00 to 07 which was set by module 07 DI 92 during the initialization. In this case LED 00 shows the setting of switch 1 (LEDs 0...7 are assigned to switches 1...8 ).
Technical data 07 DI 92

In general, the technical system data listed under "System data and system configuration" in chapter 1 of volume 2 of the Advant Controller 31 system description are valid. Additional data or data which are different from the system data are listed as follows.

Technical data for the complete unit

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Permissible temperature range during operation</td>
<td>0...55 °C</td>
</tr>
<tr>
<td>Rated supply voltage</td>
<td>24 V DC</td>
</tr>
<tr>
<td>Nominal signal voltage at inputs</td>
<td>24 V DC</td>
</tr>
<tr>
<td>Max. current consumption</td>
<td>0.15 A</td>
</tr>
<tr>
<td>Max. nominal load capacity for supply terminals</td>
<td>4.0 A</td>
</tr>
<tr>
<td>Max. power dissipation inside the unit</td>
<td>10 W</td>
</tr>
<tr>
<td>Protection against incorrect polarity of supply voltage</td>
<td>yes</td>
</tr>
</tbody>
</table>

Conductor cross section

<table>
<thead>
<tr>
<th>Conductors</th>
<th>Cross section</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power supply</td>
<td>max. 2.5 mm²</td>
</tr>
<tr>
<td>CS31 system bus</td>
<td>max. 2.5 mm²</td>
</tr>
<tr>
<td>Signal terminals</td>
<td>max. 1.5 mm²</td>
</tr>
<tr>
<td>Reference potentials</td>
<td>max. 1.5 mm²</td>
</tr>
</tbody>
</table>

Number of inputs: 32

Electrical isolation: CS31 system bus from the rest of the unit, group from group, all groups from the rest of the unit

Reference potential for inputs: each group has a separate reference potential, see Fig. 4.1.1–2

Number of interfaces: 1 CS31 system bus interface

Address setting: Coding switch under the cover located on the right side of the housing

Operation and error displays: a total of 33 LEDs

Technical data for the digital inputs

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of channels per module</td>
<td>32</td>
</tr>
<tr>
<td>Division of channels into groups</td>
<td>4 groups with 8 channels each, channels En,00...En,07 and En,08...En,15, channels En+1,00...En+1,07 and En+1,08...En+1,15</td>
</tr>
<tr>
<td>Reference potentials for the inputs</td>
<td>ZP0, ZP1, ZP2 and ZP3</td>
</tr>
<tr>
<td>Electrical isolation</td>
<td>group from group, all groups from the rest of the unit</td>
</tr>
<tr>
<td>Input signal delay</td>
<td>typ. 7 ms</td>
</tr>
<tr>
<td>Signalization of the input signals</td>
<td>one green LED per channel, LEDs activated according to the input signal</td>
</tr>
<tr>
<td>Input signal voltage</td>
<td>24 V DC</td>
</tr>
<tr>
<td>0 signal</td>
<td>-30 V...+5 V</td>
</tr>
<tr>
<td>1 signal</td>
<td>+13 V...+30 V</td>
</tr>
<tr>
<td>Residual ripple for 0 signal</td>
<td>within -30 V...+5 V</td>
</tr>
<tr>
<td>For 1 signal</td>
<td>within +13 V...+30 V</td>
</tr>
</tbody>
</table>
Input current per channel
- input voltage = + 24 V, typ. 7.0 mA
- input voltage = + 5 V, ≥ 0.2 mA
- input voltage = + 13 V, ≥ 2.0 mA
- input voltage = + 30 V, ≤ 9.0 mA

Conductor cross section for the removable terminal blocks: max. 1.5 mm² (distance between terminals 3.81 mm)

Connection to the CS31 system bus
- Interface standard: EIA RS-485
- Electrical isolation: against supply voltage, inputs and outputs
- Conductor cross section for the removable terminal blocks: max. 2.5 mm² (grid space 5.08 mm)

Mechanical data
- Mounting and DIN rail according to DIN EN 50022–35, 15 mm deep. The DIN rail is centrally positioned between upper and lower edge of the module.
- Mounting with screws: 4 screws M4
- Width x height x depth: 120 x 140 x 85 mm
- Connection method: removable connectors with screw-type terminals
  - max. 2.5 mm² (grid space 5.08 mm)
  - max. 1.5 mm² (grid space 3.81 mm)
- Weight: 450 g
- Dimensions for installation: see illustration on next page

Installation instructions
- Mounting position: vertical, connectors must point upward and downward
- Cooling: The natural convection cooling must not be obstructed by cable ducts or other components in the cabinet

Ordering data
- Module 07 DI 92
- Order No. GJR5 2524 00 R0101
- Scope of delivery:
  - Digital input module 07 DI 92
  - 1 5-pin connector (grid space 5.08 mm)
  - 1 3-pin connector (grid space 5.08 mm)
  - 4 10-pin connectors (grid space 3.81 mm)
The depth of the unit is 85 mm. If a DIN rail is used for the installation, the installation depth increases by the depth of the DIN rail.

Fig. 4.1.1-4: 07 DI 92, Front panel and outline dimensions

Dimensions for the installation holes are shown in bold print
4.3.1 Digital Input/Output Module 07 DC 91

16 digital inputs, 8 digital outputs, 8 configurable inputs/outputs, 24 V DC, CS31 system bus

Contents

Intended purpose ................................................. 4.3.1-1
Display and operating elements
on the front panel .......................................... 4.3.1-1
Electrical connection ........................................... 4.3.1-1
Addressing .......................................................... 4.3.1-3
Input/output configuration .....................................4.3.1-4
Normal operation.................................................. 4.3.1-4
Diagnosis and display .......................................... 4.3.1-4
Technical data ......................................................4.3.1-5
Dimensions for installation ....................................4.3.1-8

Intended purpose

The digital input/output module 07 DC 91 is used as a remote module on the CS31 system bus. It has 32 channels with the following features:

• 16 inputs, 24 V DC, in two groups.
• 8 outputs, 24 V DC, in one group.
  The outputs
  • work with transistors,
  • have a rated load capacity of 0.5 A and
  • are protected against overload and short circuits.
• 8 inputs/outputs, each of which can be addressed
  • as input,
  • as output or
  • as re-readable output (combined input/output)

The technical data are identical with the normal inputs and outputs.

The operating voltage of the module is 24 V DC.

The system bus connection is electrically isolated from the rest of the unit.

The module offers a number of diagnosis functions (see chapter "Diagnosis and displays").

Displays and operating elements on the front panel

1 16 green LEDs to indicate the signal status at the inputs,
16 yellow LEDs to indicate the signal status at the outputs or at the configurable inputs/outputs
2 List of diagnosis information related to the LEDs, when used for diagnosis display
3 Red LED for error message
4 Test button

Electrical connection

The module is mounted on a DIN rail (15 mm high) or with 4 screws. The following illustration shows the electrical connection of the input/output module.
4.3.1-2 Advant Controller 31 / Issued: 10.98
07 DC 91

Fig. 4.3.1-2: Electrical connection of the digital input/output module 07 DC 91. The example shows 19 channels used as inputs and 13 channels used as outputs.

Caution: The process voltage must be included in the earthing concept of the control system (e.g. earthing of the minus terminal).

Addressing must be done with the coding switch under the slide cover located on the right side of the module housing.

Fig. 4.3.1-2: Electrical connection of the digital input/output module 07 DC 91. The example shows 19 channels used as inputs and 13 channels used as outputs.
Adressing

An address must be set for each module to enable the central unit to correctly access the inputs and outputs.

A detailed description about "Addressing" can be found in the chapter "Addressing" for the central processing unit and coupler.

The address must be set at the DIL switch, located under the slide cover on the right side of the module.

When using the central units 07 KR 91, 07 KT 92 to 07 KT 94 as bus master, the following operating modes (address allocations) apply, depending on the position of the address DIL switch No.1:

### Central units 07 KR 91 / 07 KT 92/93/94

When the address DIL switch No. 1 is set to **ON**, it means that 16 inputs and 16 outputs are permanently allocated. In this case all configurable channels are outputs.

Address DIL switch No. 8 is not used.

<table>
<thead>
<tr>
<th>Terminal/Input</th>
<th>Terminal/Output</th>
<th>Input</th>
</tr>
</thead>
<tbody>
<tr>
<td>5 E n,00</td>
<td>28 A n,00</td>
<td></td>
</tr>
<tr>
<td>6 E n,01</td>
<td>29 A n,01</td>
<td></td>
</tr>
<tr>
<td>7 E n,02</td>
<td>30 A n,02</td>
<td></td>
</tr>
<tr>
<td>8 E n,03</td>
<td>31 A n,03</td>
<td></td>
</tr>
<tr>
<td>9 E n,04</td>
<td>32 A n,04</td>
<td></td>
</tr>
<tr>
<td>10 E n,05</td>
<td>33 A n,05</td>
<td></td>
</tr>
<tr>
<td>11 E n,06</td>
<td>34 A n,06</td>
<td></td>
</tr>
<tr>
<td>12 E n,07</td>
<td>35 A n,07</td>
<td></td>
</tr>
<tr>
<td>13 E n,08</td>
<td>36 A n,08</td>
<td>E n+1,00</td>
</tr>
<tr>
<td>14 E n,09</td>
<td>37 A n,09</td>
<td>E n+1,01</td>
</tr>
<tr>
<td>15 E n,10</td>
<td>38 A n,10</td>
<td>E n+1,02</td>
</tr>
<tr>
<td>16 E n,11</td>
<td>39 A n,11</td>
<td>E n+1,03</td>
</tr>
<tr>
<td>17 E n,12</td>
<td>40 A n,12</td>
<td>E n+1,04</td>
</tr>
<tr>
<td>18 E n,13</td>
<td>41 A n,13</td>
<td>E n+1,05</td>
</tr>
<tr>
<td>19 E n,14</td>
<td>42 A n,14</td>
<td>E n+1,06</td>
</tr>
<tr>
<td>20 E n,15</td>
<td>43 A n,15</td>
<td>E n+1,07</td>
</tr>
</tbody>
</table>

n: The group number can be set at address DIL switch with switches 2...7.

Recommended module addresses
for 07 KR 91 / 07 KT 92/93/94 as bus master: 08, 10, 12...60 (even-numbered addresses)

With this setting, the module uses two group numbers on the CS31 system bus occupying 24 binary input channels and 16 binary output channels. 16 inputs, 8 outputs and 8 configurable inputs/outputs are available.

An +1,00...15 and En1,08...15 are not used. They can be used for other modules if needed.

### Central units 07 KR 91 / 07 KT 92/93/94

When the address DIL switch No. 1 is in the **OFF** position (factory setting), it means that 16 inputs and 8 outputs are permanently set. The 8 configurable channels can be addressed individually as inputs or outputs.

Address DIL switch No. 8 is not used.

<table>
<thead>
<tr>
<th>Terminal/Input</th>
<th>Terminal/Output</th>
<th>Input</th>
</tr>
</thead>
<tbody>
<tr>
<td>5 E n,00</td>
<td>28 A n,00</td>
<td></td>
</tr>
<tr>
<td>6 E n,01</td>
<td>29 A n,01</td>
<td></td>
</tr>
<tr>
<td>7 E n,02</td>
<td>30 A n,02</td>
<td></td>
</tr>
<tr>
<td>8 E n,03</td>
<td>31 A n,03</td>
<td></td>
</tr>
<tr>
<td>9 E n,04</td>
<td>32 A n,04</td>
<td></td>
</tr>
<tr>
<td>10 E n,05</td>
<td>33 A n,05</td>
<td></td>
</tr>
<tr>
<td>11 E n,06</td>
<td>34 A n,06</td>
<td></td>
</tr>
<tr>
<td>12 E n,07</td>
<td>35 A n,07</td>
<td></td>
</tr>
<tr>
<td>13 E n,08</td>
<td>36 A n,08</td>
<td>E n+1,00</td>
</tr>
<tr>
<td>14 E n,09</td>
<td>37 A n,09</td>
<td>E n+1,01</td>
</tr>
<tr>
<td>15 E n,10</td>
<td>38 A n,10</td>
<td>E n+1,02</td>
</tr>
<tr>
<td>16 E n,11</td>
<td>39 A n,11</td>
<td>E n+1,03</td>
</tr>
<tr>
<td>17 E n,12</td>
<td>40 A n,12</td>
<td>E n+1,04</td>
</tr>
<tr>
<td>18 E n,13</td>
<td>41 A n,13</td>
<td>E n+1,05</td>
</tr>
<tr>
<td>19 E n,14</td>
<td>42 A n,14</td>
<td>E n+1,06</td>
</tr>
<tr>
<td>20 E n,15</td>
<td>43 A n,15</td>
<td>E n+1,07</td>
</tr>
</tbody>
</table>

n: The group number can be set at address DIL switch with switches 2...7.

Recommended module addresses
for 07 KR 91 / 07 KT 92/93/94 as bus master: 08, 10, 12...60 (even-numbered addresses)

With this setting, the module uses only one group number on the CS31 system bus. In this case 16 inputs and outputs are available.

Fig. 4.3.1-4: Addresses of channels when DIL switch No. 1 is set to OFF

Note:
Module 07 DC 91 reads the setting of the address switch only during the initialization, after switching on the power supply, meaning that changes of the setting during operation remain ineffective until the next initialization process.
Input/output configuration
Module 07 DC 91 does not store any configuration data. The 8 configurable channels are defined as inputs or outputs by the user program, e.g. by reading or writing data in the user program. Every configurable input/output channel can be used as input or output (or re-readable output). When used as input, the channel must not be assigned a 1 signal (see Fig. 4.3.1-3 and 4.3.1-4 for setting of the address DIL switch and address assignment).

Normal operation
- The module initializes automatically after power is switched on. During this time all LEDs are switched on.
- If the CS31 system bus does not run, the LED 3 flashes.
- LED 3 goes out again after the bus operation runs correctly and the module does not detect an error.
- The 16 green and the 16 yellow LEDs 1 indicate the signal status of the 32 channels.

Diagnosis and display
In case of an overload or a short-circuit, the output switches off and then performs re-starting attempts. An acknowledgement of the output is therefore not necessary. However, the error message is displayed by the LED.

Diagnosis functions:
- Short-circuit / overload of outputs (I > 0.7 A)
- Reporting of a short-circuit or overload condition to the central unit
- Storing and making this information available when recalled (kind of error and error location)
- Error inside of module
- Error on CS31 system bus

If one of these errors occurs, the red LED 3 lights up. The error is transmitted to the central unit or the coupler. For additional information see instructions supplied there under "Diagnosis".

Using test button 4 and the LED displays a diagnosis interrogation can be performed directly at the unit.

Pressing the test button for the first time, En, 00 is selected: the status LED of the selected input flashes, all other status displays are switched off during this test. After releasing the test button, the diagnosis information for this channel is displayed for about 3 seconds by the green LEDs 00 to 07.

Explanation of LEDs:
00 not used
01 not used
02 not used
03 not used
04 Overload or short circuit, only for outputs
05 not used
06 not used
07 not used

The explanation for the LEDs 2 is also printed on the front panel in English.

With every successive pressing and releasing of the test button, the process is repeated for the other input and output channels (I/O channels).

After calling up the last channel and pressing the test button once again, an LED test is performed. All LEDs must light up. Then the setting of the address switch is displayed for about 3 seconds by LEDs 00 to 07 which the 07 DC 91 module has set during initialization. In this case LED 0 shows the setting of switch 1 (LEDs 0...7 are assigned to switches 1...8).

The error messages in the I/O module and central unit are deleted, as soon as the errors have been corrected, if no further errors exist and when the error correction has been acknowledged.

Acknowledgement of an error after error correction:
- by pressing the test button for about 5 seconds, or
- by the PLC program, or
- by the PC.

Note:
The short-circuit and overload message indicates which channel has got the error.
The error message refers to a group of 4 outputs. This means, a short-circuit on one single channel (channel 0, 1, 2 or 3) is indicated as an error for all 4 channels (the whole group). The diagnosis message sent to the PLC always contains the first channel of the channel group, in this case channel 0.

After conclusion of the diagnosis interrogation, the 32 green and yellow LEDs again show the signal status of the channels.
Technical Data for 07 DC 91

In general, the technical system data listed under "System data and system configuration" in chapter 1 of volume 2 of the "Advant Controller 31" system description are valid. Additional data or data which are different from the system data are listed as follows.

Technical data of the complete unit

<table>
<thead>
<tr>
<th>Requirement</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Permissible temperature range during operation</td>
<td>0...55 °C</td>
</tr>
<tr>
<td>Rated supply voltage</td>
<td>24 V DC</td>
</tr>
<tr>
<td>Rated signal voltage for inputs and outputs</td>
<td>24 V DC</td>
</tr>
<tr>
<td>Max. current consumption without load</td>
<td>0.15 A</td>
</tr>
<tr>
<td>Max. rated load for supply terminals</td>
<td>4.0 A</td>
</tr>
<tr>
<td>Max. power dissipation in module (outputs without load)</td>
<td>5 W</td>
</tr>
<tr>
<td>Max. power dissipation in module (outputs under load)</td>
<td>10 W</td>
</tr>
<tr>
<td>Protection against reversed polarity of power connection</td>
<td>yes</td>
</tr>
</tbody>
</table>

Conductor cross section

- for removable connectors
  - power input: max. 2.5 mm²
  - CS31 system bus: max. 2.5 mm²
  - signal terminals: max. 1.5 mm²

Number of binary inputs: 16
Number of binary transistor outputs: 8
Number of configurable inputs and outputs: 8

Reference potential for all inputs and outputs: Terminals 24/25 (minus pole of supply voltage, terminal M)

Number of interfaces: 1 CS31 system bus interface
Electrical isolation: CS31 system bus interface against the rest of the unit

Address setting: Coding switch under the slide cover located on the right side of the housing

Diagnosis: see chapter "Diagnosis and displays"
Operation and error messages: a total of 33 LEDs

Technical data of the digital inputs

- Number of channels per unit: 16
- Distribution of channels in groups: 2 groups of 8 channels each, channels En,00...En,07, and En,08...En,15
- Reference potential for all inputs: Terminals 24/25 (minus pole of supply voltage, terminal M)
- Electrical isolation: from CS31 system bus
- Input delay: typ. 7 ms
- Signalization of input signals: one green LED per channel, LED activated according to the input signal
- Input signal voltage:
  - 0 signal: -30 V...+ 5 V
  - 1 signal: +13 V...+ 30 V
  - Residual ripple: at 0 signal within -30 V...+ 5 V, at 1 signal within +13 V...+ 30 V
Input current per channel

- Input voltage = +24 V  typ. 7.0 mA
- Input voltage = +5 V  ≥ 1.0 mA
- Input voltage = +13 V  ≥ 2.0 mA
- Input voltage = +30 V  ≤ 9.0 mA

Conductor cross section
- for removable connectors  max. 1.5 mm² (grid space 3.81 mm)

Technical data of digital outputs

- Number of channels per unit 8 transistor outputs
- Distribution of channels in groups 1 group of 8 channels
  channels An,00…An,07
- Reference potential for all inputs Terminals 24/25 (minus pole of supply voltage, terminal M)
- Common voltage supply terminals for all outputs Terminals 22/23 (plus pole of supply voltage, terminal L+)
- Electrical isolation from CS31 system bus
- Signalization of output signals one yellow LED for each channel, LED activated according to output signal
- Output current
  - nominal value 500 mA at L+ = 24 V
  - maximum value 4 A total current per group
  - leakage current at 0 signal < 0.5 mA
- De-magnetization during inductive load via internal varistor
- Switching frequency at inductive load max. 0.5 Hz
- Switching frequency with lamps max. 11 Hz at max. 5 W
- Protection against short-circuit/overload yes
  - overload message (I ≥ 0.7 A) yes, after approx. 100 ms
  - limiting of output current yes
  - reactivation after short-circuit/overload automatically
- Resistance to feedback against 24V signals yes
- Total load current (including output current of configured inputs and outputs) max. 8 A
- Conductor cross section
  - for removable connectors  max. 1.5 mm² (grid space 3.81 mm)

Technical data of configurable inputs and outputs

The configurable channels are defined individually by the user program as either inputs or outputs. This is done by reading or writing data to/from the respective channel.

- Number of channels per unit 8 inputs / transistor outputs
- Distribution of channels in groups
  - when using channels as inputs channels En+1,00…En+1,07
  - when using channels as outputs channels An,08…An,15
- Signalization of input and output signals one yellow LED per channel, LED activated according to binary signal
- Technical data when used as outputs refer to digital outputs
Technical data when used as inputs

Input current per channel refer to digital inputs

Input signal voltage
- 0 signal
  - 6 V...+5 V
- 1 signal
  - +13 V...+30 V
  - residual ripple at 0 signal
    - within -6 V...+5 V
  - at 1 signal
    - within +13 V...+30 V

* Due to the direct connection to the output, the demagnetizing varistor is also effective at the input when disconnecting inductive loads (see figure). This is why the difference between UPx and the input signal may not exceed the clamp voltage of the varistor. The varistor limits the voltage to approx. 36 V. Following this, the input voltage must range from -12 V to +30 V when UPx = 24 V and from -6 V to +30 V when UPx = 30 V.

The following figure shows the circuit arrangement of a digital input/output.

Connection to the CS31 system bus

Interface standard EIA RS-485

Electrical isolation against voltage supply, input and output

Conductor cross section for removable 3-pole connector max. 2.5 mm²

Mechanical data

Mounting to DIN rail according to DIN EN 50022-35, 15 mm deep. The DIN rail is centrally positioned between upper and lower edges of the module.

Mounting with screws by 4 screws M4

Width x height x depth 120 x 140 x 85 mm

Connector removable connectors with screw-type terminals

 conductor cross section max. 2.5 mm² (grid space 5.08 mm)
  max. 1.5 mm² (grid space 3.81 mm)

Weight 450 g

Dimensions for installation refer to figure on next page

Installation instructions

Installation position vertical with connectors pointing up and down

Cooling The natural convection cooling must not be hindered by cable ducts or other additional components installed in the cabinet.

Ordering data

Module 07 DC 91 Order No. GJR5 2514 00 R0202

Scope of delivery Digital input/output module 07 DC 91

1 5-pin connector (grid space 5.08 mm)
1 3-pin connector (grid space 5.08 mm)
4 9-pin connectors (grid space 3.81 mm)
The depth of the module is 85 mm. If a DIN rail is used, the installation depth must be increased by the dimension of the rail.

Fig. 4.3.1-5: 07 DC 91, Front panel foil and outside dimension
Dimensions for installation holes are shown in bold print
4.3.2 Digital Input/Output Module 07 DC 92

32 configurable digital inputs/outputs, 24 V DC, electrically isolated in groups, outputs can be loaded with 500 mA, CS31 system bus

Contents

Intended purpose ................................................. 4.3.2-1
Display and operating elements on the front panel .......................................... 4.3.2-1
Electrical connection ........................................... 4.3.2-1
Addressing .......................................................... 4.3.2-3
Acknowledging outputs after a short circuit ................................................... 4.3.2-3
Input/output configuration .....................................4.3.2-3
Normal operation.................................................. 4.3.2-3
Diagnosis and display .......................................... 4.3.2-4
Technical Data .....................................................4.3.2-5
Dimensions for installation ............................ 4.3.2-8

Intended purpose

The digital input/output module 07 DC 92 is used as a remote module on the CS31 system bus. It contains 32 inputs/outputs, 24 V DC, in 4 groups with the following features:

- The inputs/outputs can be accessed individually
  - as input,
  - as output or
  - as re-readable output (combined input/output)

- The outputs
  - work with transistors,
  - have a nominal load rating of 0.5 A and
  - are protected against overload and short circuit.

- The 4 groups of inputs/outputs are electrically isolated from each other and from the rest of the unit.

- The module occupies two digital addresses for inputs and outputs on the CS31 system bus. It is possible to configure the unit solely as an output module. In this case, the addresses for the inputs are not needed.

The unit works with a supply voltage of 24 V DC.

The system bus connection is electrically isolated from the rest of the unit.

The module offers a number of diagnosis functions (see chapter “Diagnosis and displays”).

Displays and operating elements on the front panel

1 32 yellow LEDs to indicate the signal status of the configurable inputs and outputs

2 List of diagnosis information concerning the LEDs when they are used for diagnosis display

3 Red LED for error message

4 Test button

Electrical connection

The module can be mounted on a DIN rail (height 15 mm) or with 4 screws. The following figure shows the electrical connection of the input/output module.
Fig. 4.3.2-2: Electrical connection of the digital input/output module 07 DC 92.

The example shows 19 channels as inputs and 13 as outputs.

**Caution:**
The process voltage must be included in the earthing concept of the control system (e.g. earthing of the minus terminal).

Addressing must be done with the coding switch under the slide cover located on the right side of the module housing.

If all channels of one group are used as inputs, it is not necessary to apply supply voltage to UP0, UP1, etc.
Addressing

An address must be set for each module to enable the central unit to correctly access the inputs and outputs. A detailed description about "Addressing" can be found in the chapter "Addressing" for the central processing unit and coupler.

The address setting is accomplished with the DIL switch located under the slide cover on the right side of the module housing.

When using central units 07 KR 91 or 07 KT 92 to 07 KT 94 as bus master, the following possibilities (address assignments) are offered, depending on the setting of the address DIL switch No. 1:

<table>
<thead>
<tr>
<th>Terminal</th>
<th>Input</th>
<th>Output</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>E n,00</td>
<td>A n,00</td>
</tr>
<tr>
<td>6</td>
<td>E n,01</td>
<td>A n,01</td>
</tr>
<tr>
<td>7</td>
<td>E n,02</td>
<td>A n,02</td>
</tr>
<tr>
<td>8</td>
<td>E n,03</td>
<td>A n,03</td>
</tr>
<tr>
<td>9</td>
<td>E n,04</td>
<td>A n,04</td>
</tr>
<tr>
<td>10</td>
<td>E n,05</td>
<td>A n,05</td>
</tr>
<tr>
<td>11</td>
<td>E n,06</td>
<td>A n,06</td>
</tr>
<tr>
<td>12</td>
<td>E n,07</td>
<td>A n,07</td>
</tr>
<tr>
<td>13</td>
<td>E n,08</td>
<td>A n,09</td>
</tr>
<tr>
<td>14</td>
<td>E n,09</td>
<td>A n,10</td>
</tr>
<tr>
<td>15</td>
<td>E n,10</td>
<td>A n,11</td>
</tr>
<tr>
<td>16</td>
<td>E n,11</td>
<td>A n,12</td>
</tr>
<tr>
<td>17</td>
<td>E n,12</td>
<td>A n,13</td>
</tr>
<tr>
<td>18</td>
<td>E n,13</td>
<td>A n,14</td>
</tr>
<tr>
<td>19</td>
<td>E n,14</td>
<td>A n,15</td>
</tr>
<tr>
<td>20</td>
<td>E n,15</td>
<td>A n,16</td>
</tr>
<tr>
<td>21</td>
<td>E n,17</td>
<td>A n,17</td>
</tr>
<tr>
<td>22</td>
<td>E n,18</td>
<td>A n,18</td>
</tr>
<tr>
<td>23</td>
<td>E n,19</td>
<td>A n,19</td>
</tr>
<tr>
<td>24</td>
<td>E n,20</td>
<td>A n,20</td>
</tr>
<tr>
<td>25</td>
<td>E n,21</td>
<td>A n,21</td>
</tr>
<tr>
<td>26</td>
<td>E n,22</td>
<td>A n,22</td>
</tr>
<tr>
<td>27</td>
<td>E n,23</td>
<td>A n,23</td>
</tr>
<tr>
<td>28</td>
<td>E n,24</td>
<td>A n,24</td>
</tr>
<tr>
<td>29</td>
<td>E n,25</td>
<td>A n,25</td>
</tr>
<tr>
<td>30</td>
<td>E n,26</td>
<td>A n,26</td>
</tr>
<tr>
<td>31</td>
<td>E n,27</td>
<td>A n,27</td>
</tr>
<tr>
<td>32</td>
<td>E n,28</td>
<td>A n,28</td>
</tr>
<tr>
<td>33</td>
<td>E n,29</td>
<td>A n,29</td>
</tr>
<tr>
<td>34</td>
<td>E n,30</td>
<td>A n,30</td>
</tr>
<tr>
<td>35</td>
<td>E n,31</td>
<td>A n,31</td>
</tr>
<tr>
<td>36</td>
<td>E n,32</td>
<td>A n,32</td>
</tr>
<tr>
<td>37</td>
<td>E n,33</td>
<td>A n,33</td>
</tr>
<tr>
<td>38</td>
<td>E n,34</td>
<td>A n,34</td>
</tr>
<tr>
<td>39</td>
<td>E n,35</td>
<td>A n,35</td>
</tr>
<tr>
<td>40</td>
<td>E n,36</td>
<td>A n,36</td>
</tr>
<tr>
<td>41</td>
<td>E n,37</td>
<td>A n,37</td>
</tr>
<tr>
<td>42</td>
<td>E n,38</td>
<td>A n,38</td>
</tr>
<tr>
<td>43</td>
<td>E n,39</td>
<td>A n,39</td>
</tr>
<tr>
<td>44</td>
<td>E n,40</td>
<td>A n,40</td>
</tr>
<tr>
<td>45</td>
<td>E n,41</td>
<td>A n,41</td>
</tr>
<tr>
<td>46</td>
<td>E n,42</td>
<td>A n,42</td>
</tr>
<tr>
<td>47</td>
<td>E n,43</td>
<td>A n,43</td>
</tr>
</tbody>
</table>

Item n in the table designates the module address that can be set with the address DIL switch with switches 2...7.

When using 07 KR 91 / 07 KT 92/93/94 as bus master, we recommend to use even-numbered module addresses (08, 10, 12......60).

In this setting, (DIL switch 1 in OFF position = factory setting) the module occupies two addresses for inputs and outputs.

If the DIL switch 1 is set to ON, the unit is configured solely as an output module. In this case, the addresses for inputs are not needed.

Note:
Module 07 DC 92 reads the setting of the address switch only during initialization, after switching on the power supply, meaning that changes of the setting during operation remain ineffective until the next initialization process.

Acknowledging outputs after a short-circuit

When an overload or short-circuit occurs, the output limits the current and thermically switches it off. The LED of the overloaded output is flashing.

After the overload or short-circuit is corrected, the outputs are switched on again automatically. A manual acknowledgement or one initiated by the user program is not necessary. The error message has to be acknowledged.

I/O configuration

Module 07 DC 92 does not store configuration data. The 32 configurable channels are defined by the user program as inputs or outputs, which means that through reading and writing data in the user program, each configurable input/output channel can be used as input, output, or readable output.

Normal operation

- The module automatically initializes after the power has been switched on. During that time, all LEDs are switched on.
- When the CS31 system bus does not run, LED 3 flashes.
- LED 3 goes out again when the bus operation runs correctly and the module does not recognize an error.
- The 32 yellow LEDs 1 show the signal status of the 32 channels.
Diagnosis and displays

Diagnosis functions:
- Short-circuit/overload of outputs (I > 0.7 A)
- Reporting of a short-circuit or overload condition to the central unit and flashing of the corresponding LED
- Storing and making this information available for recall (kind of error and location of error)
- Error inside of module
- Error on CS31 system bus

If one of these errors occur, the red LED 3 will light up. The error message will be reported to the central unit or to the coupler. For additional information refer to chapter "Diagnosis" for these devices.

A direct diagnosis inquiry can be made with the test button 4 and the upper LED displays 1.

By pressing the test button once, the channel E/A n, 00 is selected: the status LED of the selected input flashes while all other status indicators are switched off during the test. After releasing the test button, the diagnosis information for this channel is displayed for about 3 seconds by the upper yellow LEDs 00 to 07.

Explanation of the lit LEDs:

00 not used
01 not used
02 not used
03 not used
04 Overload or short-circuit, only for outputs
05 not used
06 not used
07 not used

The explanation of the LEDs 2 is also printed on the front panel.

The procedure is repeated for the other input and output channels with each successive pressing and releasing of the test button.

After accessing the last channel, another pressing of the test button initiates an LED test. All LEDs must light up. Following that, the position of the address switch is displayed by LEDs 00 to 07 for about 3 seconds which was set by module 07 DC 92 during the initialization. In this case LED 00 shows the setting of switch 1 (LEDs 0...7 are assigned to switches 1...8).

The error messages at the I/O module and at the central control unit go out as soon as the errors have been corrected, no new errors exist and the error correction has been acknowledged.

Acknowledging an error after error correction:
- by pressing the test button for about 5 seconds or
- by the PLC program, or
- by the PC.

Notes:
The short-circuit and overload display can indicate in which group of 8 channels the error has occurred.

The error message to the PLC is as follows:

Overload in group 00...07 Channel 00 is reported
Overload in group 08...15 Channel 08 is reported
Overload in group 16...23 Channel 15 is reported
Overload in group 24...31 Channel 15 is reported

After the diagnosis interrogation has finished, the 32 yellow LEDs again show the signal status of the channels.
Technical Data for 07 DC 92

In general, the technical system data listed under "System data and system configuration" in chapter 1 of volume 2 of the "Advant Controller 31" system description are valid. Additional data or data which are different from the system data are listed as follows.

Technical data of the complete unit

- Permissible temperature range during operation: 0...55 °C
- Rated supply voltage: 24 V DC
- Rated signal voltage for inputs and outputs: 24 V DC
- Max. current consumption without load: 0.15 A
- Max. rated load for supply terminals: 4.0 A
- Max. power dissipation in module (outputs without load): 5 W
- Max. power dissipation in module (outputs under load): 10 W
- Protection against reversed polarity of power connection: yes

Conductor cross section

- for the removable connectors:
  - power supply: max. 2.5 mm²
  - CS31 system bus: max. 2.5 mm²
  - signal terminals: max. 1.5 mm²
  - supply for I/O groups: max. 1.5 mm²

Number of configurable inputs/outputs: 32 (the configurable channels are defined individually by the user program to be either inputs or outputs. This is accomplished by interrogating or assigning the appropriate channel).

Electrical isolation

- CS31 system bus from the rest of the unit
- inputs/outputs group from group, all groups from the rest of the unit
- Supply of the I/O groups: each group is supplied individually, see Fig. 4.3.2-2
- Number of interfaces: 1 CS31 system bus interface
- Address setting: Coding switch located under the slide cover at the right side of the housing
- Diagnosis: see chapter "Diagnosis and display"
- Operation and error displays: a total of 33 LEDs

Technical data of the I/O channels as binary inputs

- Number of channels per unit: 32
- Division of channels into groups: 4 groups with 8 channels each, channels En,00...En,07 and En,08...En,15 channels En+1,00...En+1,07 and En+1,08...En+1,15
- Reference potential for inputs: ZP0, ZP1, ZP2 and ZP3
- Electrical isolation: group from group, all groups from the rest of the unit
- Input delay: typ. 7 ms
- Signalization of the input signals: one yellow LED per channel, LED activated according to the input signal
Input signal voltage

<table>
<thead>
<tr>
<th>Condition</th>
<th>Voltage Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 signal (when UPx connected)</td>
<td>-6 V...+5 V</td>
</tr>
<tr>
<td>0 signal (when UPx not connected)</td>
<td>-30 V...+5 V</td>
</tr>
<tr>
<td>1 signal</td>
<td>+13 V...+30 V</td>
</tr>
<tr>
<td>Residual ripple at 0 signal</td>
<td>-6 V...+5 V</td>
</tr>
<tr>
<td>(UPx connected)</td>
<td></td>
</tr>
<tr>
<td>at 0 signal (UPx not connected)</td>
<td>-30 V...+5 V</td>
</tr>
<tr>
<td>at 1 signal</td>
<td>+13 V...+30 V</td>
</tr>
</tbody>
</table>

Due to the direct connection to the output, the demagnetizing varistor is also effective at the input when disconnecting inductive loads (see figure). This is why the difference between UPx and the input signal may not exceed the clamp voltage of the varistor. The varistor limits the voltage to approx. 36 V. Following this, the input voltage must range from -12 V to +30 V when UPx = 24 V and from -6 V to +30 V when UPx = 30 V.

When all 8 channels of the group are used as inputs and terminal UPx is not wired-up, there are no restrictions to the input signals. In this case, the input voltage can range from -30 V to +30 V. The following figure shows the circuit arrangement of a digital input/output.

Input current per channel

<table>
<thead>
<tr>
<th>Voltage Level</th>
<th>Current</th>
</tr>
</thead>
<tbody>
<tr>
<td>input voltage = +24 V</td>
<td>typ. 7.0 mA</td>
</tr>
<tr>
<td>input voltage = +5 V</td>
<td>≥ 0.2 mA</td>
</tr>
<tr>
<td>input voltage = +13 V</td>
<td>≥ 2.0 mA</td>
</tr>
<tr>
<td>input voltage = +30 V</td>
<td>≤ 9.0 mA</td>
</tr>
</tbody>
</table>

Conductor cross section for the removable connectors

max. 1.5 mm² (grid space 3.81 mm)

Technical data of I/O channels as digital outputs

<table>
<thead>
<tr>
<th>Data Point</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of channels per unit</td>
<td>32 transistor-outputs</td>
</tr>
<tr>
<td>Division of channels in groups</td>
<td>4 groups with 8 channels each, channels An,00...An,07 and An,08...An,15, channels An+1,00...An+1,07 and An+1,08...An+1,15</td>
</tr>
<tr>
<td>Reference potentials for outputs</td>
<td>ZP0, ZP1, ZP2 and ZP3</td>
</tr>
<tr>
<td>Voltage supply for outputs</td>
<td>UP0, UP1, UP2 and UP3</td>
</tr>
<tr>
<td>Electrical isolation</td>
<td>group from group, all groups from the rest of the unit</td>
</tr>
<tr>
<td>Signalization of output signals</td>
<td>one yellow LED per channel, LED activated according to the input signal</td>
</tr>
<tr>
<td>Output current</td>
<td>nominal value 500 mA at L+ = 24 V</td>
</tr>
<tr>
<td></td>
<td>max. value 4 A total current for each group</td>
</tr>
<tr>
<td></td>
<td>leakage current at 0 signal &lt; 0.5 mA</td>
</tr>
<tr>
<td>Demagnetization at inductive load</td>
<td>via internal varistor</td>
</tr>
<tr>
<td>Switching frequency for inductive load</td>
<td>max. 0.5 Hz</td>
</tr>
<tr>
<td>Switching frequency for lamp load</td>
<td>max. 11 Hz at max. 5 W</td>
</tr>
</tbody>
</table>
Protection against short-circuit/overload
  overload message (I > 0,7 A)
  limitation of output current
  reactivation after short-circuit/overload
Yes
Yes, after approx. 100 ms
Yes
Automatically

Resistance to feedback against 24V signals
Yes

Load current (total)
Max. 4 A for each group

Conductor cross section
  for the removable connectors
Max. 1.5 mm² (grid space 3.81 mm)

**Connection to the CS31 system bus**

Interface standard
EIA RS-485

Galvanic separation
From supply voltage, inputs and outputs

Conductor cross section for the
  removable 3-pole connector
Max. 2.5 mm²

**Mechanical data**

Mounting to DIN rail
According to DIN EN 50022-35, 15 mm deep.
The DIN rail is centrally positioned between upper and
lower edges of the module.

Mounting with screws
By 4 screws M4

Width x height x depth
120 x 140 x 85 mm

Connector
  conductor cross section
Removable connectors with screw-type terminals
Max. 2.5 mm² (grid space 5.08 mm)
Max. 1.5 mm² (grid space 3.81 mm)

Weight
450 g

Dimensions for installation
See figure on next page

**Installation instructions**

Installation position
Vertical with connectors pointing up and down

Cooling
The natural convection cooling must not be hindered by
cable ducts or other additional components installed in
the cabinet.

**Ordering data**

Module 07 DC 92
Order No. GJR5 2522 00 R0101

Scope of delivery
Digital Input and output module 07 DC 92
1 5-pin connector (grid space 5.08 mm)
1 3-pin connector (grid space 5.08 mm)
4 10-pin connectors (grid space 3.81 mm)
The depth of the module is **85 mm**. If a DIN rail is used, the installation depth must be increased by the dimension of the rail.

Fig. 4.3.2-5: **07 DC 92**, Front panel foil and outside dimensions.
Dimensions for installation holes are shown in bold print.
4.3.3 Keyboard Controller 07 TC 90
32 switches/keys and 32 LEDs controllable

Fig. 4.3.3-1: Keyboard controller 07 TC 90

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Timing Diagram for multiplex controlling of the LEDs and switches ........ 4.3.3-5
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Intended purpose
The module 07 TC 90 is used for coupling control panels consisting of pushbuttons or switches and LEDs to the CS31 system bus. These control panels should be connectable to the CS31 system bus without large expenditure of wiring and software in the PLC.

The pushbuttons/switches and LEDs should preferably be connected via a printed circuit board (printed circuit board of the control panel manufacturer). The device 07 TC 90 is connected to this printed circuit board via a flat cable or a direct connector. The mechanical connection can be made by means of the four mounting holes on the device.

The device 07 TC 90 can input or control up to 32 switches/pushbuttons and 32 LEDs which are arranged in 4x8 matrix. In the PLC software one digital input is assigned to every pushbutton/switch and one digital output to every LED. Multiplexing is carried out by the 07 TC 90.

The device behaves like a combined I/O module on the CS31 system bus. It presents all keys and LEDs as individual digital signals.

Displays and components on the printed circuit board
1 Interface to the CS31 system bus (basic housing for 6-pole plug-in type terminal)
2 Power supply (basic housing for 3-pole plug-in type terminal)
3 Multiplex interface (40-pole pin block) for connecting the switches/pushbuttons and LEDs
4 Red LED for displaying the CS31 system bus status
5 Address switch

Electrical connection
The 24 V DC power supply is carried out via 3 plug-in type poles on a 9-pole connector housing.
## Connector assignment of the multiplex interface

<table>
<thead>
<tr>
<th>Terminal</th>
<th>Terminal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Column input 0</td>
</tr>
<tr>
<td>2</td>
<td>Column input 1</td>
</tr>
<tr>
<td>3</td>
<td>Column input 2</td>
</tr>
<tr>
<td>4</td>
<td>Column input 3</td>
</tr>
<tr>
<td>5</td>
<td>Column input 4</td>
</tr>
<tr>
<td>6</td>
<td>Column input 5</td>
</tr>
<tr>
<td>7</td>
<td>Column input 6</td>
</tr>
<tr>
<td>8</td>
<td>Column input 7</td>
</tr>
<tr>
<td>9</td>
<td>unused</td>
</tr>
<tr>
<td>10</td>
<td>unused</td>
</tr>
<tr>
<td>11</td>
<td>unused</td>
</tr>
<tr>
<td>12</td>
<td>unused</td>
</tr>
<tr>
<td>13</td>
<td>unused</td>
</tr>
<tr>
<td>14</td>
<td>unused</td>
</tr>
<tr>
<td>15</td>
<td>unused</td>
</tr>
<tr>
<td>16</td>
<td>unused</td>
</tr>
<tr>
<td>17</td>
<td>unused</td>
</tr>
<tr>
<td>18</td>
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</tr>
<tr>
<td>19</td>
<td>unused</td>
</tr>
<tr>
<td>20</td>
<td>unused</td>
</tr>
<tr>
<td>21</td>
<td>Column driver 0</td>
</tr>
<tr>
<td>22</td>
<td>Column driver 1</td>
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<td>23</td>
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<tr>
<td>24</td>
<td>Column driver 3</td>
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<tr>
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</tr>
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<td>27</td>
<td>Column driver 6</td>
</tr>
<tr>
<td>28</td>
<td>Column driver 7</td>
</tr>
<tr>
<td>29</td>
<td>unused</td>
</tr>
<tr>
<td>30</td>
<td>used (+ 24 V, for internal use)</td>
</tr>
<tr>
<td>31</td>
<td>used (+ 24 V, for internal use)</td>
</tr>
<tr>
<td>32</td>
<td>unused</td>
</tr>
<tr>
<td>33</td>
<td>Line driver 4</td>
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<td>34</td>
<td>Line driver 4</td>
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<td>37</td>
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<td>38</td>
<td>Line driver 2</td>
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<td>39</td>
<td>Line driver 1</td>
</tr>
<tr>
<td>40</td>
<td>Line driver 1</td>
</tr>
</tbody>
</table>

Fig. 4.3.3-2: Connector assignment of the multiplex interface
### Identifier in PLC | Switch/pushbutton No. | Terminal on 40-pole connector
--- | --- | ---
E n, 00 | 0 | 1 Line driver output
E n, 01 | 1 | 2
E n, 02 | 2 | 3
E n, 03 | 3 | 4
E n, 04 | 4 | 5
E n, 05 | 5 | 6
E n, 06 | 6 | 7
E n, 07 | 7 | 8
E n, 08 | 8 | Terminal 39 and 40
E n, 09 | 9 | Terminal 37 and 38
E n, 10 | 10 | Address switch No. 1 OFF see addressing
E n, 11 | 11 | Terminal 35 and 36
E n, 12 | 12 | Terminal 33 and 34
E n, 13 | 13 | Terminal 37 and 38
E n, 14 | 14 | Terminal 39 and 40
E n, 15 | 15 | Terminal 33 and 34
E n+1, 00 | 16 | Address switch No. 1 ON
E n+1, 01 | 17 | Line driver output
E n+1, 02 | 18 | Terminal 39 and 40
E n+1, 03 | 19 | Terminal 37 and 38
E n+1, 04 | 20 | Terminal 35 and 36
E n+1, 05 | 21 | Terminal 33 and 34
E n+1, 06 | 22 | Terminal 37 and 38
E n+1, 07 | 23 | Terminal 39 and 40
E n+1, 08 | 24 | Terminal 37 and 38
E n+1, 09 | 25 | Terminal 39 and 40
E n+1, 10 | 26 | Terminal 37 and 38
E n+1, 11 | 27 | Terminal 39 and 40
E n+1, 12 | 28 | Terminal 37 and 38
E n+1, 13 | 29 | Terminal 39 and 40
E n+1, 14 | 30 | Terminal 37 and 38
E n+1, 15 | 31 | Terminal 39 and 40

Example for a diode type: 1N4148

---

**Fig. 4.3.3-3:** Connection diagram 07 TC 90, connection of the switches/pushbuttons
# Connection diagram, connection of the LEDs

<table>
<thead>
<tr>
<th>Identifier in PLC</th>
<th>LED No.</th>
<th>Terminal on 40-pole connector</th>
</tr>
</thead>
<tbody>
<tr>
<td>A n , 00</td>
<td>0</td>
<td>21</td>
</tr>
<tr>
<td>A n , 01</td>
<td>1</td>
<td>22</td>
</tr>
<tr>
<td>A n , 02</td>
<td>2</td>
<td>23</td>
</tr>
<tr>
<td>A n , 03</td>
<td>3</td>
<td>24</td>
</tr>
<tr>
<td>A n , 04</td>
<td>4</td>
<td>25</td>
</tr>
<tr>
<td>A n , 05</td>
<td>5</td>
<td>26</td>
</tr>
<tr>
<td>A n , 06</td>
<td>6</td>
<td>27</td>
</tr>
<tr>
<td>A n , 07</td>
<td>7</td>
<td>28</td>
</tr>
<tr>
<td>A n , 08</td>
<td>8</td>
<td>21</td>
</tr>
<tr>
<td>A n , 09</td>
<td>9</td>
<td>22</td>
</tr>
<tr>
<td>A n , 10</td>
<td>10</td>
<td>23</td>
</tr>
<tr>
<td>A n , 11</td>
<td>11</td>
<td>24</td>
</tr>
<tr>
<td>A n , 12</td>
<td>12</td>
<td>25</td>
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<tr>
<td>A n , 13</td>
<td>13</td>
<td>26</td>
</tr>
<tr>
<td>A n , 14</td>
<td>14</td>
<td>27</td>
</tr>
<tr>
<td>A n , 15</td>
<td>15</td>
<td>28</td>
</tr>
<tr>
<td>A n +1 , 00</td>
<td>16</td>
<td>21</td>
</tr>
<tr>
<td>A n +1 , 01</td>
<td>17</td>
<td>22</td>
</tr>
<tr>
<td>A n +1 , 02</td>
<td>18</td>
<td>23</td>
</tr>
<tr>
<td>A n +1 , 03</td>
<td>19</td>
<td>24</td>
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<td>A n +1 , 04</td>
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<td>A n +1 , 05</td>
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<td>A n +1 , 06</td>
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<td>27</td>
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<tr>
<td>A n +1 , 07</td>
<td>23</td>
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<tr>
<td>A n +1 , 08</td>
<td>24</td>
<td>21</td>
</tr>
<tr>
<td>A n +1 , 09</td>
<td>25</td>
<td>22</td>
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<tr>
<td>A n +1 , 10</td>
<td>26</td>
<td>23</td>
</tr>
<tr>
<td>A n +1 , 11</td>
<td>27</td>
<td>24</td>
</tr>
<tr>
<td>A n +1 , 12</td>
<td>28</td>
<td>25</td>
</tr>
<tr>
<td>A n +1 , 13</td>
<td>29</td>
<td>26</td>
</tr>
<tr>
<td>A n +1 , 14</td>
<td>30</td>
<td>27</td>
</tr>
<tr>
<td>A n +1 , 15</td>
<td>31</td>
<td>28</td>
</tr>
</tbody>
</table>

### Address switch No. 1
- **ON**
- **OFF**

**Example for a resistor:**
1 kΩ, 0.25 W

**Example for a diode type:**
1N4148

---

**Fig. 4.3.3-4:** Connection diagram 07 TC 90, connection of the LEDs
Timing diagram for multiplex controlling of the LEDs and switches

Fig. 4.3.3-5: Timing diagram for multiplex controlling of the LEDs and switches

Addressing

ON
OFF

ON: with key debouncing
OFF: without key debouncing

Bit signific. 1
Bit signific. 2
Bit signific. 4
Bit signific. 8
Bit signific. 16
Bit signific. 32

Address; 0...63

OFF: 32 inputs + 32 outputs
ON: 16 inputs + 16 outputs

Depending on the switch setting the device uses 32 digital inputs and outputs or 16 digital inputs and outputs on the bus.

Diagnosis

Red LED

- OFF: CS31 system bus is running
- flashes: CS31 system bus is not running, CS31 system bus not connected
- ON: Initialization phase after switching on the supply voltage

There are no other diagnosis and configuration possibilities via the CS31 system bus.
**Technical data**

- The device cannot be used as I/O module for standard applications because of the multiplex method for the I/O signals.
- Principally it should be taken into consideration that the EMC behaviour can be affected by the LED and key module connected via the flat cable.

<table>
<thead>
<tr>
<th>Power supply</th>
</tr>
</thead>
<tbody>
<tr>
<td>Device rated supply voltage</td>
</tr>
<tr>
<td>Max. current consumption</td>
</tr>
<tr>
<td>Connector</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CS31 System bus interface</th>
</tr>
</thead>
<tbody>
<tr>
<td>Interface standard</td>
</tr>
<tr>
<td>Connector</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Multiplex interface</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line driver</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td>Remark</td>
</tr>
<tr>
<td>Column driver</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td>Column inputs</td>
</tr>
<tr>
<td>Min. high-signal</td>
</tr>
<tr>
<td>Max. low-signal</td>
</tr>
<tr>
<td>Matrix</td>
</tr>
<tr>
<td>Multiplex frequency</td>
</tr>
<tr>
<td>Connector</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>General mechanical data</th>
</tr>
</thead>
<tbody>
<tr>
<td>Displays</td>
</tr>
<tr>
<td>Temperature range</td>
</tr>
<tr>
<td>Reverse-connect protection for supply voltage</td>
</tr>
<tr>
<td>Electrical isolation</td>
</tr>
</tbody>
</table>
**Mechanical dimensions, connector arrangement**

Fig. 4.3.3-7: Connector arrangement, mechanical dimensions

Dimensions: 130 mm x 100 mm x 30 mm (length x width x height)

Weight: approx. 0.1 kg

**Ordering data**

07 TC 90  
GJR5251800R101

**Note:**

A corresponding counterpart is required for the 9-pole connector basic housing (not supplied as standard accessory). Several manufacturers, e.g. Phönix, Wago, Weidmüller, offer this plug-in component in various versions (straight or bended, as screw-type terminal or cage tension spring terminal).

The Phönix name for the 9-pole connector basic housing (pin grid space 5.08 mm) used for the keyboard controller 07 TC 90 is: COMBICON basic housing MSTBV 2.5/9-G-5.08

The corresponding screw-type terminal would be e.g.: Phönix MSTB 2.5/9-ST-5.08, Part No. 1757 080.
Appendix:
Calculation of the LED resistors

- The typical current in the column input is 7 mA, maximum 10 mA.
- The line driver is loaded via the switches/pushbuttons with max. 8 column inputs simultaneously.
- The maximum load-carrying capacity of a line driver is 500 mA. If the load caused by the column inputs is subtracted (80 mA max.), max. 420 mA remain for controlling the LEDs.
- A line driver must be able to drive max. 8 LEDs at the same time. For this purpose, 420 mA/8 = 52.5 mA per LED are available.
- The voltage drops (LED + diode + saturation voltage of the drivers) are approx. 4 V. The maximum supply voltage is 30 V. The maximum voltage at the resistor is 30 V - 4 V = 26 V.
- Thus, the minimum LED resistor is
  \[ R_{LED_{\text{min}}} = \frac{26 \, \text{V}}{52.5 \, \text{mA}} = 0.495 \, \text{k}\Omega \]
- Due to the multiplex method, the effective LED current is only ¼ of the impulse current of 52.5 mA max. The maximum brightness of the LEDs corresponds with the brightness of LEDs which are continuously operated with 52.5 mA/4 = 13.125 mA.
- The maximum power dissipation at the 495 Ω resistor is calculated with the following formula
  \[ P_V = \frac{U_{max} \cdot I_{max}}{4} \]
  \[ = \frac{26 \, \text{V} \cdot 52.5 \, \text{mA}}{4} = 341 \, \text{mW} \]
- The following table shows the calculation results for different resistors:

<table>
<thead>
<tr>
<th>( R_{LED} ) (Ω)</th>
<th>510</th>
<th>680</th>
<th>1 kΩ</th>
<th>1.5 kΩ</th>
<th>2.2 kΩ</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated impulse current, ( L^+ = 24 , \text{V} )</td>
<td>39.2 mA</td>
<td>29.4 mA</td>
<td>20 mA</td>
<td>13.3 mA</td>
<td>9.1 mA</td>
</tr>
<tr>
<td>Active LED-current, ( L^+ = 24 , \text{V} )</td>
<td>9.8 mA</td>
<td>7.4 mA</td>
<td>5 mA</td>
<td>3.3 mA</td>
<td>2.3 mA</td>
</tr>
<tr>
<td>Max. impulse current, ( L^+ = 30 , \text{V} )</td>
<td>51 mA</td>
<td>38.2 mA</td>
<td>26 mA</td>
<td>17.3 mA</td>
<td>11.8 mA</td>
</tr>
<tr>
<td>Max. power dissipation, ( L^+ = 30 , \text{V} )</td>
<td>331 mW</td>
<td>249 mW</td>
<td>169 mW</td>
<td>113 mW</td>
<td>77 mW</td>
</tr>
<tr>
<td>Resistor type</td>
<td>( \geq 0.5 , \text{W} )</td>
<td>( \geq 0.33 , \text{W} )</td>
<td>( \geq 0.25 , \text{W} )</td>
<td>( \geq 0.25 , \text{W} )</td>
<td>( &gt; 0.125 , \text{W} )</td>
</tr>
</tbody>
</table>

Formulas for calculating the table values:

- Rated impulse current, \( L^+ = 24 \, \text{V} \)
  \[ I_{imp} = \frac{24 \, \text{V} - 4 \, \text{V}}{R_{LED}} \]

- Active LED current, \( L^+ = 24 \, \text{V} \)
  \[ I_{\text{wirk}} = \frac{I_{imp}}{4} \]

- Maximum impulse current, \( L^+ = 30 \, \text{V} \)
  \[ I_{imp,\text{max}} = \frac{30 \, \text{V} - 4 \, \text{V}}{R_{LED}} \]

- Maximum power dissipation
  \[ P_{\text{max}} = \frac{I_{imp,\text{max}} \cdot (30 \, \text{V} - 4 \, \text{V})}{4} \]

- Resistor type selected
4.3.4 Keyboard Controller 07 TC 91
32 switches/keys and 32 LEDs controllable

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Connector assignment of power supply and
CS31 bus, mechanical dimensions .................... 4.3.4-2
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switches/pushbuttons ....................................... 4.3.4-3
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Intended purpose
The module 07 TC 91 is used for coupling control panels
consisting of pushbuttons or switches and LEDs to the
CS31 system bus. These control panels should be con-
ectable to the CS31 system bus without large expendi-
ture of wiring and software in the PLC.

The pushbuttons-switches and LEDs should preferably
be connected via a printed circuit board (printed circuit
board of the control panel manufacturer). The device
07 TC 91 is connected to this printed circuit board via a
flat cable or a direct connector.

The mechanical connection can be made by means of
the four mounting holes on the device.

The device 07 TC 91 can input or control up to 32 switch-
es/pushbuttons and 32 LEDs which are arranged in 4x8
matrix. In the PLC software one digital input is assigned
to every pushbutton switch and one digital output to every
LED. Multiplexing is carried out by the 07 TC 91.

The device behaves like a combined I/O module on the
CS31 system bus. It presents all keys and LEDs as indi-
vidual digital signals.

Displays and components on the printed
circuit board
① Interface to the CS31 system bus (basic housing
for 9-pole plug-in type terminal)
② Power supply (basic housing for 9-pole plug-in
type terminal)
③ Multiplex interface (20-pole pin block) for connect-
ing the switches/pushbuttons and LEDs
④ Red LED for displaying the CS31 system bus
status
⑤ Address switch

Electrical connection
The 24 V DC power supply is carried out via 3 plug-in type
poles on a 9-pole connector housing.
Connector assignment of the multiplex interface

<table>
<thead>
<tr>
<th>Terminal</th>
<th>Terminal</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Column input 7</td>
</tr>
<tr>
<td>2</td>
<td>Column input 6</td>
</tr>
<tr>
<td>3</td>
<td>Column input 5</td>
</tr>
<tr>
<td>4</td>
<td>Column input 4</td>
</tr>
<tr>
<td>5</td>
<td>Column input 3</td>
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<td>6</td>
<td>Column input 2</td>
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<td>7</td>
<td>Column input 1</td>
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<td>Column input 0</td>
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<td>9</td>
<td>Column driver 7</td>
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<td>10</td>
<td>Column driver 6</td>
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<td>11</td>
<td>Column driver 5</td>
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<td>18</td>
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<tr>
<td>19</td>
<td>Line driver 1</td>
</tr>
<tr>
<td>20</td>
<td>Line driver 0</td>
</tr>
</tbody>
</table>

Fig. 4.3.4-2: Connector assignment of the multiplex interface X2

Fig. 4.3.4-3: Connector assignment of X1 (power supply and CS31 bus), mechanical dimensions
### Identifier in PLC | Switch/pushbutton No. | Terminal on 20-pole connector X2
--- | --- | ---
E₀₀ | 0 | Column inputs
E₀₁ | 1 |
E₀₂ | 2 |
E₀₃ | 3 |
E₀₄ | 4 |
E₀₅ | 5 |
E₀₆ | 6 |
E₀₇ | 7 |
E₀₈ | 8 |
E₀₉ | 9 |
E₁₀ | 10 |
E₁₁ | 11 |
E₁₂ | 12 |
E₁₃ | 13 |
E₁₄ | 14 |
E₁₅ | 15 |
E₁₆ | 16 |
E₁₇ | 17 |
E₁₈ | 18 |
E₁₉ | 19 |
E₂₀ | 20 |
E₂₁ | 21 |
E₂₂ | 22 |
E₂₃ | 23 |
E₂₄ | 24 |
E₂₅ | 25 |
E₂₆ | 26 |
E₂₇ | 27 |
E₂₈ | 28 |
E₂₉ | 29 |
E₃₀ | 30 |
E₃₁ | 31 |

**Address switch No. 1 ON**

**Address switch No. 1 OFF**

**Line driver output**

**Terminal 20**

**Terminal 19**

**Terminal 18**

**Terminal 17**

---

**Example for a diode type:**

1N4148

---

*Fig. 4.3.4-4: Connection diagram 07 TC 91, connection of the switches/pushbuttons*
### Connection diagram, connection of the LEDs

<table>
<thead>
<tr>
<th>Identifier in PLC</th>
<th>LED No.</th>
<th>Terminal on 20-pole connector X2</th>
</tr>
</thead>
<tbody>
<tr>
<td>An, 00</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>An, 01</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>An, 02</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>An, 03</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>An, 04</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>An, 05</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td>An, 06</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>An, 07</td>
<td>7</td>
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</tr>
<tr>
<td>An, 08</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>An, 09</td>
<td>9</td>
<td></td>
</tr>
<tr>
<td>An, 10</td>
<td>10</td>
<td></td>
</tr>
<tr>
<td>An, 11</td>
<td>11</td>
<td></td>
</tr>
<tr>
<td>An, 12</td>
<td>12</td>
<td></td>
</tr>
<tr>
<td>An, 13</td>
<td>13</td>
<td></td>
</tr>
<tr>
<td>An, 14</td>
<td>14</td>
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<tr>
<td>An, 15</td>
<td>15</td>
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<tr>
<td>An+1, 00</td>
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<td>An+1, 01</td>
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<td>An+1, 03</td>
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<td>An+1, 09</td>
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<tr>
<td>An+1, 10</td>
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<td></td>
</tr>
<tr>
<td>An+1, 11</td>
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</tr>
<tr>
<td>An+1, 12</td>
<td>28</td>
<td></td>
</tr>
<tr>
<td>An+1, 13</td>
<td>29</td>
<td></td>
</tr>
<tr>
<td>An+1, 14</td>
<td>30</td>
<td></td>
</tr>
<tr>
<td>An+1, 15</td>
<td>31</td>
<td></td>
</tr>
</tbody>
</table>

**Column driver**

- Terminal 17
- Terminal 18
- Terminal 19
- Terminal 20

**Line driver output**

Example for a diode type: 1N4148

Example for a resistor: 1 kΩ, 0.25 W

---

Fig. 4.3.4-5: Connection diagram 07 TC 91, connection of the LEDs
Timing diagram for multiplex controlling of the LEDs and switches

![Timing diagram](image)

Diagnosis

Red LED

- OFF: CS31 system bus is running
- flashes: CS31 system bus is not running, CS31 system bus not connected
- ON: Initialization phase after switching on the supply voltage

There are no other diagnosis and configuration possibilities via the CS31 system bus.

Addressing

![Address setting](image)

Addressing

To address the device:

- OFF: with key debouncing
- ON: without key debouncing

Address: Bit signific. 1, Bit signific. 2, Bit signific. 4, Bit signific. 8, Bit signific. 16, Bit signific. 32

Address range: 0...63

OFF: 32 inputs + 32 outputs
ON: 16 inputs + 16 outputs

Depending on the switch setting the device uses 32 digital inputs and outputs or 16 digital inputs and outputs on the bus.
Technical data
As far as possible, the AC31 system data are valid.

Exceptions:
- Open construction system,
  degree of protection IP00.
- The user is responsible for the appropriate
  installation and observance of the necessary
  degree of protection.

- The device cannot be used as I/O module for
  standard applications because of the multiplex
  method for the I/O signals.
- Principally it should be taken into consideration that
  that the EMC behaviour can be affected by the
  LED and key module connected via the flat cable.
- After each reset function, the module performs a
  lamp test for a period of ca. 250 ms.

Power supply
Device rated supply voltage
24 V DC
Max. current consumption
600 mA (including key and LED controlling)
Connector
Basic housing for 9-pole plug-in terminal
see Fig. 4.3.4-8
Protection against reversed connected supply voltage
yes

CS31 system bus interface
Interface standard
RS-485
Connector
Basic housing for 9-pole plug-in terminal
see Fig. 4.3.4-8
Electrical isolation
CS31 system bus against the rest of the unit

Multiplex interface
Line drivers
24 V DC
500 mA per line, short-circuit proof
Remark: The line current consists of the current for
the LEDs via the column drivers and the
input currents via the pushbuttons.
Column drivers
max. 50 mA, "Open Collector"
Sum <500 mA (because of the line drivers)
Column inputs
24 V DC, 7 mA (typical), no input delay, but key
debouncing by the software, can be disabled by
switch, see Fig. 4.3.4-7 "Address setting";
debounce time: ca. 40 ms,
without key debouncing the updating time is 1 multiplex
cycle (approx. 12 ms)
Min. high signal (at 24 V DC supply voltage)
$U_{H_{\text{min}}} = 16 \text{ V referred to internal 0 V, corresponds to } 3.4 \text{ mA input current}$
Max. low signal (at 24 V DC supply voltage)
$U_{L_{\text{max}}} = 6 \text{ V referred to internal 0 V, corresponds to } 1.3 \text{ mA input current}$
Matrix
4 lines with 8 columns for pushbuttons switches and
8 columns for LEDs
Multiplex frequency
approx. 83 Hz; 3 ms pulse / 9 ms interval
Connector
20-pole pin block with twisting protection, suitable for
flat cable; grid spacing 2.54 mm; according to DIN 41651
Displays
red LED for CS31 system bus status
Mechanical dimensions, connector assignment

Dimensions
87 mm x 72 mm x 30 mm (length x width x height)

Weight
c. 0.1 kg

Ordering data
07 TC 91
GJR5 2527 00 R0101

Note:
A corresponding counterpart is required for the 9-pole connector basic housing (not supplied as standard accessory). Several manufacturers, e.g. Phönix, Wago, Weidmüller, offer this plug-in component in various versions (straight or bended, as screw-type terminal or cage tension spring terminal).

The Phönix name for the 9-pole connector basic housing (pin grid space 3.81 mm) used for the keyboard controller 07 TC 91 is:
COMBICON basic housing EMCV 1,5/9-G-3,81
Part No. 1860 715

The corresponding screw-type terminal would be e.g.:
Phönix MC 1,5/9-ST-3,81GY, Part No. 1883 666.
Appendix:

Calculation of the LED resistors

- The typical current in the column input is 7 mA, maximum 10 mA.
- The line driver is loaded via the switches/pushbuttons with max. 8 column inputs simultaneously.
- The maximum load-carrying capacity of a line driver is 500 mA. If the load caused by the column inputs is subtracted (80 mA max.), max. 420 mA remain for controlling the LEDs.
- A line driver must be able to drive max. 8 LEDs at the same time. For this purpose, 420 mA/8 = 52.5 mA per LED are available. The 8 column drivers are loadable with max. 50 mA each.
- The voltage drops (LED + diode + saturation voltage of the drivers) are approx. 4 V. The maximum supply voltage is 30 V. The maximum voltage at the resistor is 30 V - 4 V = 26 V.
- Thus, the minimum LED resistor is
  \[ R_{\text{LEDmin}} = \frac{26 \text{ V}}{50 \text{ mA}} = 0.520 \text{ k}\Omega \]
- The next higher value of a standard resistor is 560 Ω. When switched on, the current flowing through this resistor is
  \[ I_{\text{LEDmax}} = \frac{26 \text{ V}}{560 \text{ Ω}} = 46.4 \text{ mA} \]
- Due to the multiplex method, the effective LED current is only ¼ of the impulse current of 46.4 mA max. The maximum brightness of the LEDs corresponds with the brightness of LEDs which are continuously operated with 46.4 mA/4 = 11.6 mA.
- The maximum power dissipation at the 560 Ω resistor is calculated with the following formula
  \[ P_v = \frac{U_{\text{max}} \cdot I_{\text{max}}}{4} = \frac{26 \text{ V} \cdot 46.4 \text{ mA}}{4} = 302 \text{ mW} \]

The following table shows the calculation results for different resistors:

<table>
<thead>
<tr>
<th>R_{\text{LED}}</th>
<th>560 Ω</th>
<th>680 Ω</th>
<th>1 kΩ</th>
<th>1.5 kΩ</th>
<th>2.2 kΩ</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated impulse current, L+ = 24 V</td>
<td>35.7 mA</td>
<td>29.4 mA</td>
<td>20 mA</td>
<td>13.3 mA</td>
<td>9.1 mA</td>
</tr>
<tr>
<td>Active LED-current, L+ = 24 V</td>
<td>8.9 mA</td>
<td>7.4 mA</td>
<td>5 mA</td>
<td>3.3 mA</td>
<td>2.3 mA</td>
</tr>
<tr>
<td>Max. impulse current, L+ = 30 V</td>
<td>46.4 mA</td>
<td>38.2 mA</td>
<td>26 mA</td>
<td>17.3 mA</td>
<td>11.8 mA</td>
</tr>
<tr>
<td>Max. power dissipation, L+ = 30 V</td>
<td>302 mW</td>
<td>249 mW</td>
<td>169 mW</td>
<td>113 mW</td>
<td>77 mW</td>
</tr>
<tr>
<td>Resistor type</td>
<td>≥0.5 W</td>
<td>&gt;0.33 W</td>
<td>&gt;0.25 W</td>
<td>&gt;0.25 W</td>
<td>&gt;0.125 W</td>
</tr>
</tbody>
</table>

Formulas for calculating the table values:

- Rated impulse current, L+ = 24 V
  \[ I_{\text{imp}} = \frac{24 \text{ V} - 4 \text{ V}}{R_{\text{LED}}} \]
- Active LED current, L+ = 24 V
  \[ I_{\text{wirk}} = \frac{I_{\text{imp}}}{4} \]
- Maximum impulse current, L+ = 30 V
  \[ I_{\text{imp,max}} = \frac{30 \text{ V} - 4 \text{ V}}{R_{\text{LED}}} \]
- Maximum power dissipation
  \[ P_{\text{max}} = \frac{I_{\text{imp,max}} \cdot (30 \text{ V} - 4 \text{ V})}{4} \]
- Resistor type selected
4.4.1 Digital Input Module 07 DI 93-I
16 input channels 24 V DC, degree of protection IP67, electrically isolated CS31 system bus connection

Contents
Intended purpose ................................................. 4.4.1-1
Displays and connections at the module housing ............ 4.4.1-1
Electrical connection ............................................ 4.4.1-1
Dimensioned drawing ........................................... 4.4.1-3
Addressing .......................................................... 4.4.1-4
I/O configuration .................................................. 4.4.1-4
Normal operation ............................................... 4.4.1-4
Diagnosis and displays ......................................... 4.4.1-5
Technical data .................................................... 4.4.1-6

Intended purpose
The digital input module 07 DI 93-I is a remote module on the CS31 system bus. It has 16 input channels with the following features:

• Housing and connection according to IP67 degree of protection.

• The CS31 bus line is looped through from module to module (one PG9 screwed conduit entry each for input and output).

• Each pair of inputs have their own connector and thus can be unplugged individually.

• The inputs
  • allow you to connect sensors with 2-pole and 3-pole technique (switching contacts, initiators etc.)
  • provide a short-circuit/overload-proof supply voltage for the sensors, with can be loaded with max. 50 mA by each sensor
  • have a rated signal current of approx. 8 mA each

The module is mounted with screws.

The CS31 system bus connection is electrically isolated from remaining module components.

The module offers diagnosis functions.

Displays and connections at the module housing

1. 16 yellow LEDs to indicate the signal status of the inputs
2. LEDs “Bus Error”, "Input Overload", "Supply Bus" and "Supply I/O" indicating operating conditions and errors
3. CS31 bus connector
4. 8 connectors for 16 inputs
5. 24 V DC power supply
6. Labelling fields
7. Bore holes for mounting

Electrical connection
The following pages show all details necessary for the electrical connection.
Fig. 4.4.1-2: Details for the electrical connection of the input module 07 DI 93-I

With this module, switch 8 must be set to OFF

- Bit signific. 1
- Bit signific. 2
- Bit signific. 4
- Bit signific. 8
- Bit signific. 16
- Bit signific. 32
- not used

Module address; bit significances

Setting of the module address

The module address is calculated by the sum of significances of those switches which are set to ON.
Example: Switch 3 and 6 ON
Module address: $16 + 2 = 18$

Insertable jumper for power supply

Power supply 24 V DC for bus logic
Power supply 24 V DC for the inputs
Reference potential for power supply.
Insertable jumper shorts 3 terminals.
It must not be removed.
Protective earth

Connection of the 24 V DC power supply:
If the bus logic and the inputs are to be supplied from different voltage sources, the insertable jumper has to be shortened, so that only the two terminals I/O are short-circuited.

CS31 module address

CS31 bus connector

Power supply 24 V DC for bus logic
Power supply 24 V DC for the inputs
Reference potential for power supply.
Insertable jumper shorts 3 terminals.
It must not be removed.
Protective earth

Front view

16 inputs DI (channel numbers 0 to 15)

Input Overload
Bus Error
Supply Bus
Supply IO

Indication of errors and operating conditions
Labelling fields
Channel numbers

Input assignment see next page

Fig. 4.4.1-2: Details for the electrical connection of the input module 07 DI 93-I
Fig. 4.4.1-3: Detailed pin assignment of the inputs of the module 07 DI 93-I

<table>
<thead>
<tr>
<th>Plug 1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
<th>7</th>
</tr>
</thead>
<tbody>
<tr>
<td>DI 0</td>
<td>PE</td>
<td>0V</td>
<td>24V</td>
<td>Input</td>
<td>Input</td>
<td>unused</td>
</tr>
<tr>
<td>DI 1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>DI 2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>DI 3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>DI 4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>DI 5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>DI 6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>DI 7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>DI 8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The height is 70.5 mm.

Front view

The height is 70.5 mm.

The dimensions for mounting with screws (assembly holes) are printed in bold.

Fig. 4.4.1-4: Outline dimensions of the input module 07 DI 93-I
Addressing

An address has to be set on each module, so that the central unit can access the inputs correctly.

A detailed description concerning the item "Addressing" is contained in the chapters "Addressing" of the central units and couplers.

The module address is set on the DIL switch located under the cover at the top side of the module.

Meaning of the address switches:

<table>
<thead>
<tr>
<th>Bit signific.</th>
<th>Module address; bit significances</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td></td>
</tr>
<tr>
<td>16</td>
<td></td>
</tr>
<tr>
<td>32</td>
<td></td>
</tr>
</tbody>
</table>

ON
OFF

With this module, switch 8 must be set to OFF

Setting of the module address:
The module address is calculated by the sum of significances of those switches which are set to ON (see the following example).

Example:

<table>
<thead>
<tr>
<th>Bit signific.</th>
<th>Module address; bit significances</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>1 = 1</td>
</tr>
<tr>
<td>2</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>2 = 2</td>
</tr>
<tr>
<td>4</td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>4 = 0</td>
</tr>
<tr>
<td>8</td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>8 = 0</td>
</tr>
<tr>
<td>16</td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>16 = 0</td>
</tr>
<tr>
<td>32</td>
<td></td>
</tr>
<tr>
<td>0</td>
<td>32 = 0</td>
</tr>
</tbody>
</table>

With this module, switch 8 must be set to OFF

The module uses 16 inputs on the CS31 system bus.

I/O configuration

With this module, an I/O configuration is not necessary.

Normal operation

- The module initializes itself after power ON. During initialization process all four LEDs 2 are ON.
- After the initialization process, the two red LEDs 2 go out again, if the bus is running correctly and the module does not detect any error. The green LEDs "Supply Bus" and "Supply I/O" light up.
- The 16 yellow LEDs 1 indicate the signal status of the inputs and outputs.
Diagnosis and displays

Diagnosis functions:
- Bus Error (on the CS31 system bus)
- Input Overload (overload or short-circuit at the power supply of the sensors)
- Supply Bus (power supply of the bus logic)
- Supply I/O (power supply for the inputs and outputs)

Error message to the master:
In case of a short-circuit or an overload, an error message is sent to the master, along with the error code No. 4. In each case, the channel No. 0 is given as faulty, independent of the really involved channel. The error message is kept up for a period of at least 5 seconds, even if the short-circuit or overload has been remedied in a shorter time.

Fur further information concerning diagnosis, see the descriptions of the central units and couplers used as bus masters.
Technical Data 07 DI 93-I

In general, the technical system data listed under "System data and system configuration" in chapter 1 of volume 2 of the "Advant Controller 31" system description are valid. Additional data or data which are different from the system data are listed as follows.

Connectors and terminals

| Power supply 24 V DC | Screw-type terminals 1.5 mm² inside the housing, PG9 gland for cable |
| CS31bus line | Screw-type terminals 1.5 mm² inside the housing, PG9 gland for cable |
| 16 inputs | 8 x 5-pole M12 connectors (female) 2 inputs per connector |

Power supply

| Rated supply voltage | 24 V DC |
| Current consumption, without output loads | max. 80 mA |
| Conductor cross section | max. 1.5 mm² (with inserted jumper) max. 2.5 mm² (without inserted jumper) |

Inputs

| Number of inputs per module | 16 |
| Signal level of the inputs with signal 1 | 11 - 30 V |
| with signal 0 | 0 - 5 V |
| Signal input current with signal 1 | approx. 8 mA |
| Input signal delay | approx. 1 ms |
| Short-circuit protection for sensors | PTC, $I_c \geq 1.6$ A |
Interfaces
Transmission standard between the central unit and input/output modules: EIA RS-485 (CS31 system bus)
Bus transmission time: 387 µs

LED indicators
Input signals: 1 yellow LED per channel
Bus Error: 1 red LED
Input Overload (short-circuit or overload): 1 red LED (lights up, when there is an overload or a short-circuit at the supply voltage for the sensors)
Supply Bus (power supply for bus logic): 1 green LED (lights up, when the supply voltage is ≥ 18 V)
Supply IO (power supply for inputs/outputs): 1 green LED (lights up, when the supply voltage is ≥ 18 V)

Mechanical data
Degree of protection according to DIN 40040, IEC 529: IP 67
Dimensions (length x width x height): 220 x 63.0 x 70.5 mm
Mounting dimension: 208 mm (+0.5 mm)
Weight: approx. 470 g

Ordering data
Input/output module: 07 DI 93-I
Order No.: GJV3 0756 13 R0202
Accessories:
- 5-pole M12 plug, male, "straight": GJV3 0756 17 R0001
- 5-pole M12 plug, male, "bended": GJV3 0756 18 R0001
- 4-pole M12 plug, male, "straight": GJV3 0756 24 R0001
- M12 filler plug (4 plugs are provided with the module): GJV3 0756 19 R0001

Note:
In order to meet the degree of protection IP67, suitable cables with certain diameters must be used at the cable glands (bus, I/O connectors, supply voltage): cable diameters for I/O 4.5 mm to 6.5 mm, for supply voltage and CS31 system bus 5 mm to 10 mm. The electrical specifications for the bus cables can be found under the Advant Controller 31 system data.
Unused cable glands have to be sealed with filler plugs.
4.4.2 Digital Output Module 07 DO 93-I
8 output channels 24 V DC/2A, degree of protection IP67, electrically isolated CS31 system bus connection

Contents
Intended purpose ................................................. 4.4.2-1
Displays and connections at the module housing .................. 4.4.2-1
Electrical connection ........................................... 4.4.2-1
Dimensioned drawing ........................................... 4.4.2-3
Addressing .......................................................... 4.4.2-4
I/O configuration ................................................... 4.4.2-4
Normal operation .................................................. 4.4.2-4
Diagnosis and displays ........................................ 4.4.2-5
Technical data ......................................................4.4.2-6

Intended purpose
The digital output module 07 DO 93-I is a remote module on the CS31 system bus. It has 8 output channels with the following features:

- Housing and connection according to IP67 degree of protection.
- The CS31 bus line is looped through from module to module (one PG9 screwed conduit entry each for input and output).
- Each output has its own connector and thus can be unplugged individually.
- The outputs
  - employ semiconductors,
  - have a rated load capability of 2 A and
  - are overload and short-circuit proof.

The module is mounted with screws.
The CS31 system bus connection is electrically isolated from remaining module components.
The module offers diagnosis functions.

Displays and connections at the module housing
1. 8 yellow LEDs to indicate the signal status of the outputs
2. LEDs "Bus Error", "Overload", "Supply Bus" and "Supply I/O" indicating the operating conditions and errors
3. CS31 bus connector
5. 8 connectors for 8 outputs
6. 24 V DC power supply
7. Labelling fields
8. Bore holes for mounting

Electrical connection
The following pages show all details necessary for the electrical connection.
Fig. 4.4.2-2: Details for the electrical connection of the output module 07 DO 93-I

OFF: Channel No. \leq 7
ON: Channel No. > 7

Bit signific. 1
Bit signific. 2
Bit signific. 4
Bit signific. 8
Bit signific. 16
Bit signific. 32

not used

Module address; bit significances

Setting of the module address

The module address is calculated by the sum of significances of those switches which are set to ON.
Example: Switches 3 and 6 ON
Module address: 16 + 2 = 18

Insertable jumper for power supply
Fuse for short-circuit or overload

Power supply 24 V DC for bus logic
Power supply 24 V DC for the outputs

Reference potential for power supply.
Insertable jumper shorts 3 terminals.
It must not be removed.

Protective Earth

Connection of the 24 V DC power supply:
If the bus logic and the outputs are to be supplied from different voltage sources, the insertable jumper has to be shortened, so that only the two terminals I/O are short-circuited.
Fig. 4.4.2-3: Detailed pin assignment of the outputs of the module 07 DO 93-I

Plug 1

Plug 2

Plug 3

Plug 4

Plug 5

Plug 6

Plug 7

Plug 8

5 PE 3 0V 2 Output 4 Output ○ unused

07 DO 93-I

The height is 70.5 mm

Front view

The dimensions for mounting with screws (assembly holes) are printed in bold. The height of the module is 70.5 mm.

Fig. 4.4.2-4: Outline dimensions of the output module 07 DO 93-I
**Addressing**

An address has to be set on each module, so that the central unit can access the outputs correctly.

**A detailed description concerning the item "Addressing" is contained in the chapters "Addressing" of the central units and couplers.**

The module address is set on the DIL switch located under the cover at the top side of the module.

**Meaning of the address switches:**

<table>
<thead>
<tr>
<th>Bit signific.</th>
<th>Module address; bit significances</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td></td>
</tr>
<tr>
<td>16</td>
<td></td>
</tr>
<tr>
<td>32</td>
<td></td>
</tr>
<tr>
<td>OFF: Channel No. ≤ 7</td>
<td></td>
</tr>
<tr>
<td>ON: Channel No. &gt; 7</td>
<td></td>
</tr>
</tbody>
</table>

Setting of the module address:

The module address is calculated by the sum of significances of those switches which are set to ON (see the following example).

**Central units 07 KR 91 / 07 KT 92 / 93**

<table>
<thead>
<tr>
<th>The DIL switch No. 8 is set to:</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chan.</td>
</tr>
<tr>
<td>------</td>
</tr>
<tr>
<td>DO 0</td>
</tr>
<tr>
<td>DO 1</td>
</tr>
<tr>
<td>DO 2</td>
</tr>
<tr>
<td>DO 3</td>
</tr>
<tr>
<td>DO 4</td>
</tr>
<tr>
<td>DO 5</td>
</tr>
<tr>
<td>DO 6</td>
</tr>
<tr>
<td>DO 7</td>
</tr>
</tbody>
</table>

xx: Group number of the address, set on the DIL switch with the switches 2...7.

The module uses 8 outputs on the CS31 system bus.

**I/O configuration**

With this module, an I/O configuration is not necessary.

**Normal operation**

- The module initializes itself after power ON. During initialization process all four LEDs (2) are ON.
- After the initialization process, the two red LEDs (2) go out again, if the bus is running correctly and the module does not detect any error. The green LEDs “Supply Bus” and “Supply I/O” light up.
- The 8 yellow LEDs (1) indicate the signal status of the outputs.
Behaviour in case of short-circuit or overload at outputs:

If a short-circuit or an overload has occurred on an output channel, the involved channel will be switched off as a reaction on a high temperature of the switching transistor. In certain intervals, the module then tries to switch on the channel again. Before every switching-on trial, the signal at all channels will be interrupted for a period of approx. 20 μs. This is also valid for those channels which are not involved in the overload or short-circuit event.

After the short-circuit or overload has been eliminated, the involved channel can operate immediately again. The red LED "Overload" goes out.

Error message to the master:

In case of a short-circuit or an overload, an error message is sent to the master, along with the error code No. 4. In each case, the channel No. 0 is given as faulty, independent of the really involved channel. The error message is kept up for a period of at least 5 seconds, even if the short-circuit or overload has been remedied in a shorter time.

For further information concerning diagnosis, see the descriptions of the central units and couplers used as bus masters.

Diagnosis and displays

Diagnosis functions:

- Bus Error (on the CS31 system bus)
- Overload (overload or short-circuit at an output)
- Supply Bus (power supply of the bus logic)
- Supply I/O (power supply for the inputs and outputs)

Diagnosis and error table:

<table>
<thead>
<tr>
<th>Bus Error</th>
<th>Overload</th>
<th>Supply Bus</th>
<th>Supply I/O</th>
</tr>
</thead>
<tbody>
<tr>
<td>☐ ☐ ☐ ☐</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LED flash. LED ON LED OFF</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Explanation

☐ ☐ ☐ ☐ The module is not connected at all, or the 24 V DC power supply is OFF.

☐ ☐ ☐ ☐ Power is ON, the bus is running, no error.

☐ ☐ ☐ ☐ Power is ON, there is a short-circuit/overload on at least one output, the bus is running.

☐ ☐ ☐ ☐ Power is ON, the bus does not run.

☐ ☐ ☐ ☐ Power in ON, there is a short-circuit/overload on at least one output, the bus does not run.

☐ ☐ ☐ ☐ Initialization phase after power ON.
Technical Data 07 DO 93-I

In general, the technical system data listed under "System data and system configuration" in chapter 1 of volume 2 of the "Advant Controller 31" system description are valid. Additional data or data which are different from the system data are listed as follows.

Connectors and terminals

<table>
<thead>
<tr>
<th>Connector/terminal</th>
<th>Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power supply 24 V DC</td>
<td>Screw-type terminals 1.5 mm² inside the housing, PG9 gland for cable</td>
</tr>
<tr>
<td>CS31 bus line</td>
<td>Screw-type terminals 1.5 mm² inside the housing, PG9 gland for cable</td>
</tr>
<tr>
<td>8 outputs</td>
<td>8 x 5-pole M12 connectors (female)</td>
</tr>
</tbody>
</table>

Power supply

<table>
<thead>
<tr>
<th>Description</th>
<th>Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated supply voltage</td>
<td>24 V DC</td>
</tr>
<tr>
<td>Current consumption, without output loads</td>
<td>max. 80 mA</td>
</tr>
<tr>
<td>Supply current for the outputs</td>
<td>max. 10 A</td>
</tr>
<tr>
<td>Conductor cross section</td>
<td>max. 1.5 mm² (with inserted jumper)</td>
</tr>
<tr>
<td></td>
<td>max. 2.5 mm² (without inserted jumper)</td>
</tr>
<tr>
<td>Internal fuse (under the cover)</td>
<td>16 A, slow-acting, 5 x 20 mm</td>
</tr>
</tbody>
</table>

Outputs

<table>
<thead>
<tr>
<th>Description</th>
<th>Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of outputs per module</td>
<td>8 (overload and short-circuit proof, electrically not isolated)</td>
</tr>
<tr>
<td>Signal level of the outputs at signal 1</td>
<td>like L+, max. internal voltage loss 0.1 V</td>
</tr>
<tr>
<td>Output load capability</td>
<td>max. current per output 2 A, 100 % ED</td>
</tr>
<tr>
<td></td>
<td>lamp load per output max. 50 W</td>
</tr>
<tr>
<td></td>
<td>total switching current (all outputs together) max. 10 A</td>
</tr>
<tr>
<td>Switching frequency with inductive load</td>
<td>max. 1 Hz</td>
</tr>
<tr>
<td></td>
<td>with resistive load max. 100 Hz</td>
</tr>
<tr>
<td>Short-circuit and overload protection</td>
<td>electronically</td>
</tr>
<tr>
<td>Short-circuit indication</td>
<td>yes, with a red LED</td>
</tr>
<tr>
<td>Limitation of output voltage, if an inductive</td>
<td>by an integrated suppressor diode</td>
</tr>
<tr>
<td>load is switched off</td>
<td></td>
</tr>
</tbody>
</table>
**Interfaces**
Transmission standard between the central unit and input/output modules: EIA RS-485 (CS31 system bus)

Bus transmission time: 323 μs

**LED indicators**
- **Output signals**: 1 yellow LED per channel
- **Bus Error**: 1 red LED
- **Overload (short-circuit or overload)**: 1 red LED (lights up when at least one output is short-circuited or overloaded)
- **Supply Bus (power supply for bus logic)**: 1 green LED (lights up when the supply voltage is ≥ 18 V)
- **Supply IO (power supply for inputs/outputs)**: 1 green LED (lights up when the supply voltage is ≥ 18 V)

**Mechanical data**
- Degree of protection according to DIN 40040, IEC 529: IP 67
- Dimensions (length x width x height): 220 x 63.0 x 70.5 mm
- Mounting dimension: 208 mm (+0.5 mm)
- Weight: approx. 470 g

**Ordering data**
- Input/output module 07 DO 93-I: GJV3 0756 11 R0202

**Accessories:**
- 5-pole M12 plug, male, "straight": GJV3 0756 17 R0001
- 5-pole M12 plug, male, "bended": GJV3 0756 18 R0001
- 4-pole M12 double plug, male, "straight": GJV3 0756 24 R0001
- M12 filler plug (4 plugs are provided with the module): GJV3 0756 19 R0001

**Note:**
In order to meet the degree of protection IP67, suitable cables with certain diameters must be used at the cable glands (bus, I/O connectors, supply voltage): cable diameters for I/O 4.5 mm to 6.5 mm, for supply voltage and CS31 system bus 5 mm to 10 mm. The electrical specifications for the bus cables can be found under the Advant Controller 31 system data.

Unused cable glands have to be sealed with filler plugs.
4.4.3 Digital Input/Output Module 07 DK 93-I

8 input channels 24 V DC, 4 output channels 24 V DC/2A, degree of protection IP67, electrically isolated CS31 system bus connection

Contents

Intended purpose ................................................. 4.4.3-1
Displays and connections ....................................4.4.3-1
Electrical connection ........................................... 4.4.3-1
Dimensioned drawing ........................................... 4.4.3-2
Addressing .......................................................... 4.4.3-3
I/O configuration ................................................... 4.4.3-4
Normal operation .................................................. 4.4.3-4
Diagnosis and displays ........................................ 4.4.3-5
Technical data ...................................................... 4.4.3-6

Intended purpose

The digital input/output module 07 DK 93-I is a remote module on the CS31 system bus. It has 8 input and 4 output channels with the following features:

• Housing and connection according to IP67 degree of protection.

• The CS31 bus line is looped through from module to module (one PG9 screwed conduit entry each for input and output).

• Each output and each pair of inputs have their own connector and thus can be unplugged individually.

• The inputs
  • allow you to connect sensors with 2-pole and 3-pole technique (switching contacts, initiators etc.)
  • provide a short-circuit/overload-proof supply voltage for the sensors, which can be loaded with max. 100 mA per channel
  • have a rated signal current of approx. 8 mA each

• The outputs
  • employ semiconductors,
  • have a rated load capability of 2 A and
  • are overload and short-circuit proof.

The module is mounted with screws.

The CS31 system bus connection is electrically isolated from remaining module components.

The module offers diagnosis functions.

Displays and connections at the module housing

1. 12 yellow LEDs to indicate the signal status of the inputs and outputs
2. LEDs "Bus Error", "Overload", "Input Overload", "Supply Bus" and "Supply I/O" indicating the operating conditions and errors
3. CS31 bus connector
4. 4 connectors for 8 inputs
5. 4 connectors for 4 outputs
6. 24 V DC power supply
7. Labelling fields
8. Bore holes for mounting

Electrical connection

The following pages show all details necessary for the electrical connection.
Fig. 4.4.3-2: Details for the connection of the input/output module 07 DK 93-I

### Setting of the module address

The module address is calculated by the sum of significances of those switches which are set to ON.

Example: Switch 3 and 6 ON
Module address: $16 + 2 = 18$

### Connection of the 24 V DC power supply:

If the bus logic, inputs and outputs are to be supplied from different voltage sources, the insertable jumper over INT, I and O must be removed.

### Indication of errors and operating conditions

- **Yellow**: Channel No. $\leq 7$
- **Red**: Channel No. $> 7$
- **Red**: Input Overload
- **Red**: Overload
- **Red**: Bus Error
- **Green**: Supply Bus
- **Green**: Supply 10

### Assignment of inputs and outputs

see next page
Fig. 4.4.3-3: Detailed pin assignment of the inputs and outputs of the module 07 DK 93-I

07 DK 93-I

Fig. 4.4.3-4: Outline dimensions of the input/output module 07 DK 93-I

The dimensions for mounting with screws (assembly holes) are printed in **bold**.
The height of the module is **70.5 mm**.
Addressing

An address has to be set on each module, so that
the central unit can access the inputs and outputs correctly.

A detailed description concerning the item “Addressing” is contained in the chapters “Addressing” of the central units and couplers.

The module address is set on the DIL switch located under the cover at the top side of the module.

Meaning of the address switches:

When using the central units 07 KR 91, 07 KT 92 or 07 KT 93, the possible module addresses range from 0...61.

In connection with the central units 07 KR 91 and 07 KT 92/KT93 as bus masters, the following address allocations are valid:

The module uses 8 inputs and 8 outputs on the CS31 system bus.

I/O configuration

With this module, an I/O configuration is not necessary.

Normal operation

• The module initializes itself after power ON. During initialization process all five LEDs are ON.

• After the initialization process, the three red LEDs go out again, if the bus is running correctly and the module does not detect any error. The green LEDs "Supply Bus" and "Supply I/O" light up.

• The 12 yellow LEDs indicate the signal status of the inputs and outputs.
Diagnosis and displays

Diagnosis functions:
- Bus Error (on the CS31 system bus)
- Overload (overload or short-circuit at an output)
- Input Overload (overload or short-circuit at the power supply of the sensors)
- Supply Bus (power supply of the bus logic)
- Supply I/O (power supply for the inputs and outputs)

Diagnosis and error table:

<table>
<thead>
<tr>
<th>Bus Error</th>
<th>Overload</th>
<th>Input Overload</th>
<th>Supply Bus</th>
<th>Supply I/O</th>
<th>Explanation</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>The module is not connected at all, or the 24 V DC power supply is OFF.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Power is ON, the bus is running, no error.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Power is ON, there is a short-circuit/overload on at least one output, the bus is running.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Power is ON, the bus does not run.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Power is ON, there is a short-circuit/overload on at least one output, the bus does not run.</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Initialization phase after power ON.</td>
</tr>
</tbody>
</table>

Behaviour in case of short-circuit or overload at sensors:
The supply connections at the sensor input connectors are connected via a temperature-dependent resistor (PTC thermistor) to the +24 V DC input of the screw-type terminals. If an overload or a short-circuit occurs with a sensor, the PTC will work as an overload protection. A voltage monitoring circuitry generates an error message in case of undervoltage (the LED "Input Overload" lights up). The following figure demonstrates the function.

After the short-circuit or overload has been eliminated, the module can operate correctly again. The red LED "Input Overload" goes out.

Behaviour in case of short-circuit or overload at outputs:
If a short-circuit or an overload has occurred on an output channel, the involved channel will be switched off as a reaction on a high temperature of the switching transistor. In certain intervals, the module then tries to switch on the channel again. Before every switching-on trial, the signal at all channels will be interrupted for a period of approx. 20 µs. This is also valid for those channels which are not involved in the overload or short-circuit event.

After the short-circuit or overload has been eliminated, the involved channel can operate immediately again. The red LED "Overload" goes out.

Error message to the master:
In case of a short-circuit or an overload, an error message is sent to the master, along with the error code No. 4. In each case, the channel No. 0 is given as faulty, independent of the really involved channel. The error message is kept up for a period of at least 5 seconds, even if the short-circuit or overload has been remedied in a shorter time.

For further information concerning diagnosis, see the descriptions of the central units and couplers used as bus masters.
Technical Data 07 DK 93-I

In general, the technical system data listed under "System data and system configuration" in chapter 1 of volume 2 of the "Advant Controller 31" system description are valid. Additional data or data which are different from the system data are listed as follows.

Connectors and terminals

Power supply 24 V DC
Screw-type terminals max. 1.5 mm² inside the housing, PG9 gland for cable

CS31 bus line
Screw-type terminals max. 1.5 mm² inside the housing, PG9 gland for cable

8 inputs
4 x 5-pole M12 connectors (female), 2 inputs per connector

4 outputs
4 x 5-pole M12 connectors (female)

Power supply
Rated supply voltage 24 V DC
Current consumption, without output loads max. 80 mA
Supply current for the outputs max. 8 A
Conductor cross section max. 1.5 mm² (with inserted jumper)
max. 2.5 mm² (without inserted jumper)
Internal fuse (under the cover) 10 A, slow-acting, 5 x 20 mm

Inputs
Number of inputs per module 8
Signal level of the inputs with signal 1 11 - 30 V
with signal 0 0 - 5 V
Signal input current with signal 1 approx. 8 mA
Input signal delay approx. 1 ms
Short-circuit protection for sensors PTC, Iₖ ≥ 1.6 A

Outputs
Number of outputs per module 4 (overload and short-circuit proof, electrically not isolated)
Signal level of the outputs at signal 1 like L+, max. internal voltage loss 0.1 V
Output load capability
max. current per output 2 A, 100 % ED
lamp load per output max. 50 W
total switching current max. 8 A
Switching frequency with inductive load
with resistive load max. 1 Hz
max. 100 Hz
Short-circuit and overload protection electronically
Short-circuit indication yes, with a red LED
Limitation of output voltage, if an inductive load is switched off by an integrated suppressor diode
Interfaces
Transmission standard between the central unit and input/output modules EIA RS-485 (CS31 system bus)
Bus transmission time 387 μs

LED indicators
Input signals 1 yellow LED per channel
Output signals 1 yellow LED per channel
Bus Error 1 red LED
Overload (short-circuit or overload) 1 red LED (lights up when at least one output is short-circuited or overloaded)
Input Overload (short-circuit or overload) 1 red LED (lights up when there is an overload or a short-circuit at the supply voltage for the sensors)
Supply Bus (power supply for bus logic) 1 green LED (lights up when the supply voltage is ≥ 18 V)
Supply IO (power supply for inputs/outputs) 1 green LED (lights up when the supply voltage is ≥ 18 V)

Mechanical data
Degree of protection according to DIN 40040, IEC 529 IP 67
Dimensions (length x width x height) 220 x 63.0 x 70.5 mm
Mounting dimension 208 mm (+0.5 mm)
Weight approx. 470 g

Ordering data
Input/output module 07 DK 93-I GJV3 0756 23 R0202
Accessories:
5-pole M12 plug, male, "straight" GJV3 0756 17 R0001
5-pole M12 plug, male, "bended" GJV3 0756 18 R0001
4-pole M12 double plug, male, "straight" GJV3 0756 24 R0001
M12 filler plug (4 plugs are provided with the module) GJV3 0756 19 R0001

Note:
In order to meet the degree of protection IP67, suitable cables with certain diameters must be used at the cable glands (bus, I/O connectors, supply voltage): cable diameters for I/O 4.5 mm to 6.5 mm, for supply voltage and CS31 system bus 5 mm to 10 mm. The electrical specifications for the bus cables can be found under the Advant Controller 31 system data.
Unused cable glands have to be sealed with filler plugs.
5 Analog modules

5.1 General information for the use of analog modules ................................................................. 5.1-1

5.2 Analog input modules
5.2.1 07 AI 91: Analog input module, 8 inputs, configurable for temperature sensors or as voltage inputs, 12 bit resolution ......................................................................................... 5.2.1-1

5.3 Analog output modules

5.4 Analog input/output modules
5.4.1 07 AC 91: Analog input/output module, 16 inputs/outputs, configurable for ±10 V, 0...10 V, 0...20 mA, 8/12 bit resolution, 2 operating modes ......................................................... 5.4.1-1
Analog modules

Feuchte
50...95 %, ohne Betauung

Luftdruck Betrieb
> 800 hPa/< 2000 m

Lagerung
> 660 hPa/< 3500 m

Kriech- und Luftstrecken
Die Kriech- und Luftstrecken entsprechen Überspannungskategorie II, Verschmutzungsgrad 2

Prüfspannungen
- 230 V Kreise (Netz, 230 V Ein/Ausgänge) gegen übrige Kreise: 2500 V
- 120 V Kreise (Netz) gegen übrige Kreise: 1500 V
- 24 V Kreise (Speisung, 24 V Ein/Ausgänge), wenn sie gegen übrige Kreise potentialgetrennt sind: 500 V
- CS31-Bus gegen übrige Kreise: 500 V

Elektromagnetische Verträglichkeit

Störfestigkeit
gegen die Entladung statischer Elektrizität (ESD) nach EN 61000-4-2
- Störspannung bei Luftentladung: 8 kV
- Störspannung bei Kontaktentladung: 6 kV

Störfestigkeit gegen die Einwirkung gestrahlter Störgrößen (CW radiated) nach ENV 50140
- Prüffeldstärke: 10 V/m
5.1 General information for the use of analog modules

Contents

5.1.1 Analog input/output modules .......................... 5.1-1
5.1.2 Position of the relevant data bits in the 16-bit word .................................... 5.1-1
5.1.3 Conversion characteristics for analog modules ...................................... 5.1-3

5.1.1 Analog input/output modules

All analog input modules perform an internal analog-digital conversion. After the conversion, the analog value is represented with 8 or 12 bits, depending on the resolution. For processing and storing this relevant bits (bits which contain the analog information), a 16-bit word is available.

All analog output modules perform an internal digital-analog conversion. The digital value before the conversion (consisting of 8 or 12 relevant bits) is stored in a 16-bit word.

5.1.2 Position of the relevant data bits in the 16-bit word

The figure on the next page shows the position of the relevant data bits of modules with a resolution of 8 or 12 bits. The 50 % value means either

- Analog value 5 V in the range of 0...10 V or -10 V...+10 V
- Analog value 10 mA in the range 0...20 mA
- Analog value 12 mA in the range 4...20 mA

(The distance between 4 and 20 mA is 16 mA. 50 % of 16 mA is 8 mA. The real analog input or output value is then 12 mA, after adding the offset of 4 mA.)

The following pages show in detail:

- the conversion characteristics of the analog inputs and outputs referring to
  - the resolution (8 bits or 12 bits)
  - the analog signal range
- tables showing the significances of the bits in the 16-bit word
### Bit No. in the 16-bit word

<table>
<thead>
<tr>
<th>Bit No.</th>
<th>15</th>
<th>14</th>
<th>13</th>
<th>12</th>
<th>11</th>
<th>10</th>
<th>9</th>
<th>8</th>
<th>7</th>
<th>6</th>
<th>5</th>
<th>4</th>
<th>3</th>
<th>2</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>-100 %</td>
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<td>50 %</td>
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<td>25 %</td>
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<td>12.5 %</td>
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<td>6.25 %</td>
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<td>3.13 %</td>
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<td>1.56 %</td>
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<td>0.78 %</td>
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<td>0.39 %</td>
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<td>0.20 %</td>
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<td>0.10 %</td>
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<td>0.05 %</td>
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<tr>
<td>0.02 %</td>
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<td></td>
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</tr>
</tbody>
</table>

**8-bit resolution, value range 0...+ 100 %,**  
in the range of -100...+100 % bit 7 is 0, the sign is assigned to bit 15

<table>
<thead>
<tr>
<th>Bit No.</th>
<th>15</th>
<th>14</th>
<th>13</th>
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</table>

**12-bit resolution, value range 0...+ 100 % and -100...+100 %,**  
the sign is assigned to bit 15

Fig.: Position of the relevant bits in the 16-bit word with a resolution of 8 bits and 12 bits
5.1.3 Conversion characteristics for analog modules

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<tr>
<th>Input modules</th>
<th>Output modules</th>
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<td><img src="image2.png" alt="Graph" /></td>
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<td><img src="image3.png" alt="Graph" /></td>
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<td><img src="image9.png" alt="Graph" /></td>
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See also table 5.1-1

See also table 5.1-2

See also table 5.1-3

See also table 5.1-4
### Table 5.1-1
Significances of the bits in the 16-bit word for analog **input** modules with a **8-bit** resolution, 07 KR 91, 07 KT 92, 07 KT 93, 07 KT 94 as master

<table>
<thead>
<tr>
<th>Meas. range</th>
<th>0 V...+10 V</th>
<th>0...5 V</th>
<th>0...20 mA</th>
<th>4...20 mA</th>
</tr>
</thead>
<tbody>
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<td>40 mV</td>
<td>20 mV</td>
<td>+ 0.08 mA</td>
<td>+ 0.062 mA</td>
</tr>
<tr>
<td>Max. value</td>
<td>+ 9.96 V</td>
<td>+ 4.98 V</td>
<td>+ 19.92 mA</td>
<td>+ 19.94 mA</td>
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<tr>
<td>Min. value</td>
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<td>0 V</td>
<td>0 mA</td>
<td>4.0 mA</td>
</tr>
<tr>
<td>Offset</td>
<td>0 V</td>
<td>0 V</td>
<td>0 mA</td>
<td>4.0 mA</td>
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</table>

<table>
<thead>
<tr>
<th>16-bit word</th>
<th>Bit 15</th>
<th>Bit 14</th>
<th>Bit 13</th>
<th>Bit 12</th>
<th>Bit 11</th>
<th>Bit 10</th>
<th>Bit 9</th>
<th>Bit 8</th>
<th>Bit 7</th>
<th>Bit 6</th>
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<th>Bit 4</th>
<th>Bit 3</th>
<th>Bit 2</th>
<th>Bit 1</th>
<th>Bit 0</th>
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<td>0 V</td>
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### Table 5.1-2
Significances of the bits in the 16-bit word for analog **input** modules with a **12-bit** resolution, 07 KR 91, 07 KT 92, 07 KT 93, 07 KT 94 as master

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<th>±5 V</th>
<th>±20 mA</th>
<th>4...20 mA</th>
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<td>+ 20 mA</td>
<td>+ 20 mA</td>
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<td>Min. value</td>
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<td>- 5 V</td>
<td>- 20 mA</td>
<td>+ 4.0 mA</td>
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<tr>
<td>Offset</td>
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<td>0 mA</td>
<td>4.0 mA</td>
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</table>

<table>
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<tr>
<th>16-bit word</th>
<th>Bit 15</th>
<th>Bit 14</th>
<th>Bit 13</th>
<th>Bit 12</th>
<th>Bit 11</th>
<th>Bit 10</th>
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<th>Bit 04</th>
<th>Bit 03</th>
<th>Bit 02</th>
<th>Bit 01</th>
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<td>- 5 V</td>
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<td>- 5 V</td>
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<tr>
<td>Bit 12</td>
<td>- 10 V</td>
<td>- 5 V</td>
<td>- 20 mA</td>
<td>0 mA</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Bit 13</td>
<td>- 10 V</td>
<td>- 5 V</td>
<td>- 20 mA</td>
<td>0 mA</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Bit 14</td>
<td>- 10 V</td>
<td>- 5 V</td>
<td>- 20 mA</td>
<td>0 mA</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*) For input modules the most significant bit is 0, if $I > 4$ mA and 1, if $I < 4$ mA.
### Table 5.1-3
Significances of the bits in the 16-bit word for analog output modules with a 8-bit resolution, 07 KR 91, 07 KT 92, 07 KT 93, 07 KT 94 as master

<table>
<thead>
<tr>
<th>Meas. range</th>
<th>±10 V</th>
<th>±12,5 V</th>
<th>0...20 mA</th>
<th>4...20 mA</th>
</tr>
</thead>
<tbody>
<tr>
<td>Resolution</td>
<td>5 mV</td>
<td>6 mV</td>
<td>0.01 mA</td>
<td>0.008 mA</td>
</tr>
<tr>
<td>Max. value</td>
<td>+ 9.995 V</td>
<td>+ 12.49 V</td>
<td>+ 19.99 mA</td>
<td>+ 19.99 mA</td>
</tr>
<tr>
<td>Min. value</td>
<td>- 10 V</td>
<td>- 12.5 V</td>
<td>0 mA</td>
<td>4.0 mA</td>
</tr>
<tr>
<td>Offset</td>
<td>0 V</td>
<td>0 V</td>
<td>0 mA</td>
<td>4.0 mA</td>
</tr>
</tbody>
</table>

16-bit word

<table>
<thead>
<tr>
<th>Bit 15</th>
<th>- 10 V</th>
<th>- 12.5 V</th>
<th>- 20 mA</th>
<th>- 16 mA</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bit 14</td>
<td>+ 5 V</td>
<td>+ 6.25 V</td>
<td>+ 10 mA</td>
<td>+ 8 mA</td>
</tr>
<tr>
<td>Bit 13</td>
<td>+ 2.5 V</td>
<td>+ 3.12 V</td>
<td>+ 5 mA</td>
<td>+ 4 mA</td>
</tr>
<tr>
<td>Bit 12</td>
<td>+ 1.25 V</td>
<td>+ 1.56 V</td>
<td>+ 2.5 mA</td>
<td>+ 2 mA</td>
</tr>
<tr>
<td>Bit 11</td>
<td>+ 0.62 V</td>
<td>+ 0.77 V</td>
<td>+ 1.25 mA</td>
<td>+ 1 mA</td>
</tr>
<tr>
<td>Bit 10</td>
<td>+ 0.31 V</td>
<td>+ 0.39 V</td>
<td>+ 0.62 mA</td>
<td>+ 0.5 mA</td>
</tr>
<tr>
<td>Bit 09</td>
<td>+ 0.15 V</td>
<td>+ 0.19 V</td>
<td>+ 0.31 mA</td>
<td>+ 0.25 mA</td>
</tr>
<tr>
<td>Bit 08</td>
<td>+ 0.08 V</td>
<td>+ 0.1 V</td>
<td>+ 0.15 mA</td>
<td>+ 0.125 mA</td>
</tr>
<tr>
<td>Bit 07</td>
<td>+ 0.04 V</td>
<td>+ 0.05 V</td>
<td>+ 0.08 mA</td>
<td>0.062 mA</td>
</tr>
<tr>
<td>Bit 06</td>
<td>+ 0.02 V</td>
<td>+ 0.025 V</td>
<td>+ 0.04 mA</td>
<td>0.031 mA</td>
</tr>
<tr>
<td>Bit 05</td>
<td>+ 0.01 V</td>
<td>+ 0.012 V</td>
<td>+ 0.02 mA</td>
<td>0.015 mA</td>
</tr>
<tr>
<td>Bit 04</td>
<td>+ 0.005 V</td>
<td>+ 0.006 V</td>
<td>+ 0.01 mA</td>
<td>0.008 mA</td>
</tr>
<tr>
<td>Bit 03</td>
<td>0 V</td>
<td>0 V</td>
<td>0 mA</td>
<td>0 mA</td>
</tr>
<tr>
<td>Bit 02</td>
<td>0 V</td>
<td>0 V</td>
<td>0 mA</td>
<td>0 mA</td>
</tr>
<tr>
<td>Bit 01</td>
<td>0 V</td>
<td>0 V</td>
<td>0 mA</td>
<td>0 mA</td>
</tr>
<tr>
<td>Bit 00</td>
<td>0 V</td>
<td>0 V</td>
<td>0 mA</td>
<td>0 mA</td>
</tr>
</tbody>
</table>

Table 5.1-4
Significances of the bits in the 16-bit word for analog output modules with a 12-bit resolution, 07 KR 91, 07 KT 92, 07 KT 93, 07 KT 94 as master

<table>
<thead>
<tr>
<th>Meas. range</th>
<th>±10 V</th>
<th>±12,5 V</th>
<th>0...20 mA</th>
<th>4...20 mA</th>
</tr>
</thead>
<tbody>
<tr>
<td>Resolution</td>
<td>80 mV</td>
<td>80 mV</td>
<td>0.08 mA</td>
<td>0.062 mA</td>
</tr>
<tr>
<td>Max. value</td>
<td>+ 9.92 V</td>
<td>+ 19.92 V</td>
<td>+ 19.94 mA</td>
<td>+ 19.94 mA</td>
</tr>
<tr>
<td>Min. value</td>
<td>- 10 V</td>
<td>0 mA</td>
<td>+ 4.0 mA</td>
<td>+ 4.0 mA</td>
</tr>
<tr>
<td>Offset</td>
<td>0 V</td>
<td>0 mA</td>
<td>0 mA</td>
<td>0 mA</td>
</tr>
</tbody>
</table>

16-bit word

<table>
<thead>
<tr>
<th>Bit 15</th>
<th>- 10 V</th>
<th>- 12.5 V</th>
<th>- 20 mA</th>
<th>- 16 mA</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bit 14</td>
<td>+ 5 V</td>
<td>+ 6.25 V</td>
<td>+ 10 mA</td>
<td>+ 8 mA</td>
</tr>
<tr>
<td>Bit 13</td>
<td>+ 2.5 V</td>
<td>+ 3.12 V</td>
<td>+ 5 mA</td>
<td>+ 4 mA</td>
</tr>
<tr>
<td>Bit 12</td>
<td>+ 1.25 V</td>
<td>+ 1.56 V</td>
<td>+ 2.5 mA</td>
<td>+ 2 mA</td>
</tr>
<tr>
<td>Bit 11</td>
<td>+ 0.62 V</td>
<td>+ 0.77 V</td>
<td>+ 1.25 mA</td>
<td>+ 1 mA</td>
</tr>
<tr>
<td>Bit 10</td>
<td>+ 0.31 V</td>
<td>+ 0.39 V</td>
<td>+ 0.62 mA</td>
<td>+ 0.5 mA</td>
</tr>
<tr>
<td>Bit 09</td>
<td>+ 0.15 V</td>
<td>+ 0.19 V</td>
<td>+ 0.31 mA</td>
<td>+ 0.25 mA</td>
</tr>
<tr>
<td>Bit 08</td>
<td>+ 0.08 V</td>
<td>+ 0.1 V</td>
<td>+ 0.15 mA</td>
<td>+ 0.125 mA</td>
</tr>
<tr>
<td>Bit 07</td>
<td>+ 0.04 V</td>
<td>+ 0.05 V</td>
<td>+ 0.08 mA</td>
<td>0.062 mA</td>
</tr>
<tr>
<td>Bit 06</td>
<td>+ 0.02 V</td>
<td>+ 0.025 V</td>
<td>+ 0.04 mA</td>
<td>0.031 mA</td>
</tr>
<tr>
<td>Bit 05</td>
<td>+ 0.01 V</td>
<td>+ 0.012 V</td>
<td>+ 0.02 mA</td>
<td>0.015 mA</td>
</tr>
<tr>
<td>Bit 04</td>
<td>+ 0.005 V</td>
<td>+ 0.006 V</td>
<td>+ 0.01 mA</td>
<td>0.008 mA</td>
</tr>
<tr>
<td>Bit 03</td>
<td>0 V</td>
<td>0 V</td>
<td>0 mA</td>
<td>0 mA</td>
</tr>
<tr>
<td>Bit 02</td>
<td>0 V</td>
<td>0 V</td>
<td>0 mA</td>
<td>0 mA</td>
</tr>
<tr>
<td>Bit 01</td>
<td>0 V</td>
<td>0 V</td>
<td>0 mA</td>
<td>0 mA</td>
</tr>
<tr>
<td>Bit 00</td>
<td>0 V</td>
<td>0 V</td>
<td>0 mA</td>
<td>0 mA</td>
</tr>
</tbody>
</table>

4) If bit 15 = 1, the output current is 0 mA.
5) If bit 15 = 1, the output current is 4 mA.
### Intended purpose

The analog input module 07 AI 91 is used as a remote module at the CS31 system bus. It has 8 analog input channels with the following features:

- The channels can be configured in pairs for the connection of the following temperature or voltage sensors:
  - $\pm 10\, \text{V} / \pm 5\, \text{V} / \pm 500\, \text{mV} / \pm 50\, \text{mV}$
  - 4...20 mA (with external 250 $\Omega$ resistor)
  - Pt100 / Pt1000 with linearization
  - Thermocouples types J, K and S with linearization
  - Only electrically isolated sensors may be used.
- The range of $\pm 5\, \text{V}$ can also be used for measuring 0..20 mA with an additional external 250 $\Omega$ resistor.
- The configuration of the input channels as well as the setting of the module address are performed with the DIL switches.
- The 07 AI 91 uses one module address (group number) in the word input range. Each of the 8 channels use 16 bits.
- The unit is powered with 24 V DC. The CS31 system bus connection is electrically isolated from the rest of the unit.
- The module offers a number of diagnosis functions (see chapter "Diagnosis and displays"). The diagnosis functions perform a self-calibration for all channels.

### Displays and operating elements on the front panel

1. 8 green LEDs for channel selection and diagnosis,
2. 8 green LEDs for analog value display of one channel
3. List of diagnosis information relating to the LEDs, when used for diagnosis display
4. Red LED for error messages
5. Test button

### Electrical connection

The module is mounted on a DIN rail (15 mm high) or with 4 screws. The following figure shows the electrical connection of the input module.
**Caution:** The process supply voltage must be included in the grounding concept of the control system (e.g., grounding of the minus terminal)

The connections of all temperature and voltage sensors must be electrically isolated from their mounting environment.

The setting of the module address as well as the configuration of the analog channels are performed with the DIL switches (see next page).

Fig. 5.2.1-2: Electrical connection of the analog input module 07 AI 91
Configuration of input channels and setting of the module address at the CS31 bus

The measuring ranges for the analog channels are set in pairs (i.e. always for two channels together) using DIL switches 1 and 2. The setting of address DIL switch determines the module address, the analog value representation and the line frequency suppression (50 Hz, 60 Hz or none).

The switches are located under the slide cover on the right side of the module housing. The following figure shows the possible settings.

<table>
<thead>
<tr>
<th>DIL switch 1</th>
<th>DIL switch 2</th>
<th>Address DIL switch</th>
</tr>
</thead>
<tbody>
<tr>
<td>ON</td>
<td>ON</td>
<td>ON</td>
</tr>
<tr>
<td>OFF</td>
<td>OFF</td>
<td>OFF</td>
</tr>
<tr>
<td>Measuring range of channel 0 and 1</td>
<td>Measuring range of channel 2 and 3</td>
<td>Module address</td>
</tr>
<tr>
<td>Measuring range of channel 4 and 5</td>
<td>Measuring range of channel 6 and 7</td>
<td>Analog value representation</td>
</tr>
<tr>
<td>Line freq. suppression</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Printed circuit board

<table>
<thead>
<tr>
<th>Printed circuit board</th>
</tr>
</thead>
<tbody>
<tr>
<td>Module address</td>
</tr>
<tr>
<td>Analog value representation</td>
</tr>
<tr>
<td>Line freq. suppression</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Module address</th>
</tr>
</thead>
<tbody>
<tr>
<td>Analog value representation</td>
</tr>
<tr>
<td>Line freq. suppression</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Module address</th>
</tr>
</thead>
<tbody>
<tr>
<td>Analog value representation</td>
</tr>
<tr>
<td>Line freq. suppression</td>
</tr>
</tbody>
</table>

Specifications for the platinum temperature sensors are:
- **Pt100** = platinum 100 Ω at 0 °C, measuring range -50...400 °C, constant current 2 mA
- **Pt1000** = platinum 1000 Ω at 0 °C, measuring range -50...400 °C, constant current 0.2 mA

Specifications for the thermocouples are:
- **Type J** = Fe-CuNi, 0...1200 °C iron / copper-nickel
- **Type K** = NiCr-NiAl, 0...1372 °C nickel-chromium / nickel-aluminium
- **Type S** = Pt10Rh-Pt, 0...1600 °C platinum-10% rhodium / platinum

Specifications for the platinum temperature sensors are:
- **Pt100** = platinum 100 Ω at 0 °C, measuring range -50...400 °C, constant current 2 mA
- **Pt1000** = platinum 1000 Ω at 0 °C, measuring range -50...400 °C, constant current 0.2 mA

Specifications for the thermocouples are:
- **Type J** = Fe-CuNi, 0...1200 °C iron / copper-nickel
- **Type K** = NiCr-NiAl, 0...1372 °C nickel-chromium / nickel-aluminium
- **Type S** = Pt10Rh-Pt, 0...1600 °C platinum-10% rhodium / platinum

Fig. 5.2.1-3: Configuration of the input channels and setting of module address at the CS31 system bus
Measuring ranges of the input channels

All input signals are evaluated as differential signals. The sensor signal is connected with two poles to the inputs U+ and U_ (example see Fig. 5.2.1-2). The relationship between input signal and the output numerical value is shown in figs. 5.2.1-7 and 5.2.1-8. All channels not used must be short-circuited (see also bridging of unused channels at Pt100/Pt1000 channels).

\[ \pm 10 \text{ V} / \pm 5 \text{ V} / \pm 500 \text{ mV} / \pm 50 \text{ mV} \]

The set measuring range resulting from the A/D conversion is displayed in the following number range:

-32760 ... 0 ... +32760

If input voltages overflow the measuring range, the overflow number of +32767 is output. If the input voltage underflows the measuring range, the underflow number of -32767 is output. In both cases, an error message is sent via the CS31 system bus.

All unused channels must be short-circuited.

**4...20 mA / 0...20 mA**

The following configurations must be set:

<table>
<thead>
<tr>
<th>Measuring range</th>
<th>Setting</th>
</tr>
</thead>
<tbody>
<tr>
<td>4...20 mA</td>
<td>4...20 mA</td>
</tr>
<tr>
<td>0...20 mA</td>
<td>±5 V</td>
</tr>
</tbody>
</table>

Both channel input terminals must be externally bridged with a shunt of 250 \( \Omega \).

Unused inputs for 0...20 mA must be short-circuited. In this case, they do not need a shunt. Unused inputs for 4...20 mA can be circuited in parallel with another 4...20 mA input channel without requiring an extra shunt. In this way, error messages (underflow) can be avoided.

**Pt100 / Pt1000**

When resistance thermometers are used, a constant current must flow through the shunt to build the necessary voltage drop for the evaluation. For this purpose the module provides two constant current sinks.

The two following figures show the connection of Pt100 and Pt1000 resistance thermometers. In these configurations the module performs a linearization of the Pt100/ Pt1000 characteristic curves. The integrated current sinks of 2 mA and 0.2 mA is also considered in these measuring circuits. This way their tolerances are compensated.

The following allocation applies to the constant current sinks:

For resistance thermometers connected to terminals 5 to 15 (channels EA_n,00 to EA_n,03), only one of the two sinks may be used on terminals 16 or 17 (2 mA or 0.2 mA).
For resistance thermometers connected to terminals 24 to 34 (channels EA n,04 to EA n,07), only one of the two sinks may be used for terminals 35 or 36 (2 mA or 0.2 mA).

Simultaneous operation of Pt100 and Pt1000 sensors is possible when one group (i.e. Pt100) is connected to the upper terminal bank and the other (i.e. Pt1000) is connected to the lower terminal bank.

Terminals 7, 10, 13, 26, 29 and 32 (marked NC) can be used as connecting points for the current loop (see also Fig. 5.2.1-2).

The measuring range of -50°C...400°C is assigned linearly to the number range of -1022...+8190 (see also Figs. 5.2.1-7 and 5.2.1-8).

If input voltages overflow the measuring range, the overflow number of +32767 is output. If the input voltage underflows the measuring range, the underflow number of -32767 is output. In both cases, an error message is sent via the CS31 system bus.

In case of open circuit (wire break in the current loop), the numeric value of -32767 is displayed. In case of wire breakage (in a sensor circuit), the numeric value +32767 is displayed. Both cases cause an error message via the CS31 system bus.

If unused Pt100/Pt1000 channels are bridged, the wire bridge simulates 0 Ω, the measuring value for very low temperature. This causes the error message “range underflow”. To avoid such error messages, bridge unused Pt100/Pt1000 channels as follows:

- Pt 100 with a resistor of 120 Ω
- Pt 1000 with a resistor of 1200 Ω

Connection of other temperature-dependent resistors

Basically all temperature-dependent resistors can be connected in place of the Pt100/Pt1000 sensors. For a configuration you can use the settings ±5 V, ±500 mV and ±50 mV. If necessary, the linearization of the resistance characteristic must be performed in the PLC user program. The integrated current sinks can be used, however, consider the following:

- The voltage drop of all resistors connected in series must not exceed
  - 7 V (when using the current sink of 0.2 mA),
  - 2.5 V (when using the current sink of 2 mA)
- The tolerance of the integrated current sinks of ±1.5 % must be considered in the tolerance calculations for the temperature measurement (no compensation as for Pt100/Pt1000).

Thermocouples types J, K, and S

Thermocouples are connected to terminals U+ and U-, either directly or via compensating wires to both poles. Be sure to observe polarity.

The reference junction temperature sensor is integrated in the module near the terminals. When evaluating the absolute temperature, the terminals are considered as the reference junction.

Fig. 5.2.1-6: Connection of two type J and two type K sensors

The thermoelectric voltage generated by the thermocouples is converted into binary values inside the module and then linearized according to the thermocouple type. In order to get the absolute temperature, the temperature of the reference junction is added.

The measuring value is allocated linearly to the numeric values as follows (see also Figs. 5.2.1-7 and 5.2.1-8):

- **Type J**: 0°C...1200°C number range 0...24576
- **Type K**: 0°C...1372°C number range 0...28096
- **Type S**: 0°C...1600°C number range 0...32760
If input voltages overflow the measuring range, the overflow number of +32767 is output. If the input voltage underflows the measuring range, the underflow number of -32767 is output. In both cases, an error message is sent via the CS31 system bus. Temperatures below 0°C are considered as "underflow".

A wire break will cause the numerical value of -32767 as well as an error message via the CS31 system bus.

All channels not used must be short-circuited.

Note:

Since L type thermocouples (iron/constantan, Fe-CuNi according to DIN 43710) are similar to J type thermocouples, they also can be used in the temperature range of 0...900 °C. The slightly greater thermo emf generated by the L type thermocouple, however, pretends a little higher temperature. The following table illustrates this fact (all temperature data is referred to a reference temperature of 0 °C):

<table>
<thead>
<tr>
<th>Temperature at the measuring point</th>
<th>Temperature evaluated by the module, if an L type thermocouple is used instead of a J type thermocouple</th>
</tr>
</thead>
<tbody>
<tr>
<td>25 °C</td>
<td>25.63 °C</td>
</tr>
<tr>
<td>50 °C</td>
<td>51.23 °C</td>
</tr>
<tr>
<td>100 °C</td>
<td>101.89 °C</td>
</tr>
<tr>
<td>200 °C</td>
<td>203.13 °C</td>
</tr>
<tr>
<td>400 °C</td>
<td>405.69 °C</td>
</tr>
<tr>
<td>600 °C</td>
<td>609.78 °C</td>
</tr>
<tr>
<td>900 °C</td>
<td>920.41 °C</td>
</tr>
</tbody>
</table>

**Configuration of unused channels**

No evaluation of the channels

Compare with configuration, Fig. 5.2.1-3

If the channels (in pairs) are not needed, we suggest to exclude them from the evaluation (acquisition of measuring values and processing within the software). The processing of the remaining channels is then faster. Unused channels must be short-circuited.
when recognizing an open circuit, the numerical values +32767 or -32767 respectively will be displayed.

The following two illustrations show the evaluation functions of the module.

Relationship between measuring values and the location of bits in a 16 bit word

The measuring ranges for analog channels are configured in pairs (always together for two channels, see Fig. 5.2.1-3). When above or below a measuring range (out of range) or

<table>
<thead>
<tr>
<th>Sign</th>
<th>±10 V</th>
<th>±5 V</th>
<th>±0.5 V</th>
<th>±50 mV</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>-10V</td>
<td>5V</td>
<td>-500mV</td>
<td>-50mV</td>
</tr>
<tr>
<td></td>
<td>2.5V</td>
<td>1.25V</td>
<td>625mV</td>
<td>12.5mV</td>
</tr>
<tr>
<td></td>
<td>625mV</td>
<td>313mV</td>
<td>156mV</td>
<td>6.3V</td>
</tr>
<tr>
<td></td>
<td>78mV</td>
<td>39mV</td>
<td>20mV</td>
<td>0.8mV</td>
</tr>
<tr>
<td></td>
<td>10mV</td>
<td>5mV</td>
<td>2mV</td>
<td>0.4mV</td>
</tr>
<tr>
<td></td>
<td>1mV</td>
<td>0.5mV</td>
<td>0.2mV</td>
<td>0.2mV</td>
</tr>
<tr>
<td></td>
<td>0.1mV</td>
<td>0.05</td>
<td>0.02</td>
<td>0.01</td>
</tr>
</tbody>
</table>

Bit values: -32768 16384 8192 4096 2048 1024 512 256 128 64 32 16 8 4 2 1

Measuring ranges ±10 V, ±5 V, ±500 mV, ±50 mV, 12 bit resolution plus sign:
The meas. range of -100...+100 % corresponds with the num. values 8008H...7FF8H (-32760...+32760), range overflow: 7FFFH (32767), range underflow: 8001H (-32767)

<table>
<thead>
<tr>
<th>Sign</th>
<th>Pt100</th>
<th>Pt1000</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>-1600°C</td>
<td>800°C</td>
</tr>
<tr>
<td></td>
<td>200°C</td>
<td>100°C</td>
</tr>
<tr>
<td></td>
<td>50°C</td>
<td>62.5°C</td>
</tr>
<tr>
<td></td>
<td>12.5°C</td>
<td>20°C</td>
</tr>
<tr>
<td></td>
<td>5°C</td>
<td>10°C</td>
</tr>
<tr>
<td></td>
<td>7.8°C</td>
<td>5°C</td>
</tr>
<tr>
<td></td>
<td>3.13°C</td>
<td>1.56</td>
</tr>
<tr>
<td></td>
<td>0.78°C</td>
<td>0.78</td>
</tr>
</tbody>
</table>
|      | 0.20%   | 0.2%
|      | 0.05%   | 0.05%
|      | 0.02%   | 0.02%
|      | 0.01%   | 0.01%
|      | 0.005%  | 0.005%

Bit values: -32768 16384 8192 4096 2048 1024 512 256 128 64 32 16 8 4 2 1

Measuring ranges for Pt100/Pt1000 with 12 bit resolution plus sign:
The meas. range of -50...+400°C corresponds with the num. values of FC02H...1FFEH (-1022...+8190), range overflow / wire break in the sensor wiring: 7FFFH (32767), range underflow / wire break in the sensor wiring: 8001H (-32767)

Measuring range for thermocouples with 12 bit resolution without sign:
Measuring range of 0...+1600 °C corresponds with the num. values of 0H...7FF8H (0...+32760), range overflow: 7FFFH (32767), range underflow / wire break: 8001H (-32767)

Fig. 5.2.1-7: Relationship between measuring values and arrangement of bits in the 16 bit word
Fig. 5.2.1-8: Relationship between measuring value and numerical value, voltage and temperature inputs

Conversion formula for temperatures:
A temperature of 1600 °C results in a numeric value of 32760, i.e. +1 K increases the numeric value by 20.48.
Numerical value = \( \theta / °C \times 20.48 \)

Conversion formula for voltage:
100 % input voltage results in a numeric value of 32760, i.e. 1 % higher input voltage increases the numeric value by 327.6.

**Specifications for platinum resistance thermometers are:**
- **Pt100**: platinum 100 Ω bei 0 °C, measuring range -50...400 °C, constant current 2 mA
- **Pt1000**: platinum 1000 Ω bei 0 °C, measuring range -50...400 °C, constant current 0.2 mA

**Specifications for thermocouples (pairs) are:**
- **Type J**: Fe-CuNi, 0...1200 °C, iron / copper-nickel
- **Type K**: NiCr-NiAl, 0...1372 °C, nickel-chromium / nickel-aluminium
- **Type S**: Pt10Rh-Pt, 0...1600 °C, platinum-10% rhodium / platinum

---

**Conversion formula for temperatures:**
A temperature of 1600 °C results in a numeric value of 32760, i.e. +1 K increases the numeric value by 20.48.
Numerical value = \( \theta / °C \times 20.48 \)

**Conversion formula for voltage:**
100 % input voltage results in a numeric value of 32760, i.e. 1 % higher input voltage increases the numeric value by 327.6.

**Specifications for platinum resistance thermometers are:**
- **Pt100**: platinum 100 Ω bei 0 °C, measuring range -50...400 °C, constant current 2 mA
- **Pt1000**: platinum 1000 Ω bei 0 °C, measuring range -50...400 °C, constant current 0.2 mA

**Specifications for thermocouples (pairs) are:**
- **Type J**: Fe-CuNi, 0...1200 °C, iron / copper-nickel
- **Type K**: NiCr-NiAl, 0...1372 °C, nickel-chromium / nickel-aluminium
- **Type S**: Pt10Rh-Pt, 0...1600 °C, platinum-10% rhodium / platinum
Addressing

Each module must have an address installed to enable the central unit to correctly access the inputs and outputs.

A detailed description about "Addressing" can be found in the chapter "Addressing" for the central processing unit and couplers.

The address setting must be performed at the DIL switch under the slide cover on the right side of the module housing (see Fig. 5.2.1-3). If central units 07 KR 91, 07 KT 92, 07 KT 93 and 07 KT 94 are used as bus master, the following address allocations apply:

<table>
<thead>
<tr>
<th>Central units</th>
<th>07 KR 91 / 07 KT 92 / 07 KT 93 / 07 KT 94...</th>
</tr>
</thead>
<tbody>
<tr>
<td>The address switch DIL No. 8 is set to <strong>OFF</strong>:</td>
<td></td>
</tr>
<tr>
<td>Channel</td>
<td>Address in PLC program</td>
</tr>
<tr>
<td>EA n,00</td>
<td>EW n,00</td>
</tr>
<tr>
<td>EA n,01</td>
<td>EW n,01</td>
</tr>
<tr>
<td>EA n,02</td>
<td>EW n,02</td>
</tr>
<tr>
<td>EA n,03</td>
<td>EW n,03</td>
</tr>
<tr>
<td>The address switch DIL No. 8 is set to <strong>ON</strong>:</td>
<td></td>
</tr>
<tr>
<td>Channel</td>
<td>Address in PLC program</td>
</tr>
<tr>
<td>EA n,00</td>
<td>EW n,08</td>
</tr>
<tr>
<td>EA n,01</td>
<td>EW n,09</td>
</tr>
<tr>
<td>EA n,02</td>
<td>EW n,10</td>
</tr>
<tr>
<td>EA n,03</td>
<td>EW n,11</td>
</tr>
</tbody>
</table>

n: Group number of address, set with address DIL switch with switches 5...8. Addresses for 07 KR 91 / 07 KT 92 / 07 KT 93 as bus master: 00...05, as of 07 KT 94: 00...05 and 08...15.

The module uses 8 analog inputs at the CS31 system bus.

Normal operation

- The module initializes automatically after the power has been switched on. During this time all LEDs are switched on.
- If the CS31 system bus does not yet run, the red error LED will flash. Should an error occur during initialization, the error LED will also light up.

Diagnosis and displays

Module 07 AI 91 offers the following diagnosis functions:

- Detection of open circuit at connection for Pt100/ Pt1000 resistance thermometers or thermocouples
- Storing and making this information available when recalled (kind of error and error location)
- Recognition of an internal module error
- Recognition of a transmission error

If one of these errors occur, the red Error LED will light up. The error message is then sent to the central unit or to the coupler.

For central units 07 KR 91 / 07 KT 92 / 07 KT 93 / 07 KT 94 the errors will be displayed as follows:

- Open circuits (cut wire)
  
  Error class 4 (FK4) M 255.14
  Error number: 09 -> MW 255.08
  Unit type: 01 -> MW 255.09
  Group number: -> MW 255.10
  Channel number: -> MW 255.11

- Range exceeded (out of range)
  
  Error class 4 (FK4) M 255.14
  Error number: 10 dec. -> MW 255.08
  Unit type: 01 -> MW 255.09
  Group number: -> MW 255.10
  Channel number: -> MW 255.11

Using the test button, all diagnosis functions can be separately selected for each channel. Pressing the test button for the first time selects channel 0 and LED 0 will flash.

![Diagram of LEDs](image)

Fig. 5.2.1-9: LEDs for displaying channel selection and diagnosis

When releasing the test button, the error information for this channel is displayed by the green LEDs 0 to 7 for about 3 seconds.
Explanations of LEDs when lighting up:

0. not used
1. not used
2. not used
3. Open circuit (cut wire)
4. Range exceeded (out of range)
5. not used
6. not used
7. not used

Explanations for the LEDs are also printed on the front panel.

The error messages at the module and at the central unit go out as soon as the error has been corrected, when no more faults have been detected and when the error correction has been acknowledged.

Acknowledgement of an error after error correction:

- by pressing the test button for about 5 seconds,
- using the PC, or
- using the PLC program of the central unit.

With each pressing and releasing of the test button, the procedure is repeated for the other input channels.

After checking the last channel by again pressing the test button, an LED test is performed. All LEDs of the module must light up. After that, the setting of the address DIL switch (module address at the CS31 system bus) is displayed for about 5 seconds. LED 0 shows the position of switch 1 (LEDs 0...7 are assigned to switches 1...8).

Display of an analog value

When the test button is not pressed, 8 LEDs display the analog value of the selected channel.

Explanation:

<table>
<thead>
<tr>
<th>all LEDs OFF</th>
<th>-&gt; minimum value</th>
</tr>
</thead>
<tbody>
<tr>
<td>all LEDs ON</td>
<td>-&gt; maximum value</td>
</tr>
</tbody>
</table>

Minimum and maximum values are:

<table>
<thead>
<tr>
<th>Configuration</th>
<th>Min. value</th>
<th>Max. value</th>
</tr>
</thead>
<tbody>
<tr>
<td>+/- 10 V</td>
<td>-10 V</td>
<td>+10 V</td>
</tr>
<tr>
<td>+/- 5 V</td>
<td>-5 V</td>
<td>+5 V</td>
</tr>
<tr>
<td>+/- 500 mV</td>
<td>-500 mV</td>
<td>+500 mV</td>
</tr>
<tr>
<td>+/- 50 mV</td>
<td>-50 mV</td>
<td>+50 mV</td>
</tr>
<tr>
<td>+/- 20 mA</td>
<td>-20 mA</td>
<td>+20 mA</td>
</tr>
<tr>
<td>0 V / 0 mA</td>
<td>4 LEDs ON</td>
<td>20 mA</td>
</tr>
<tr>
<td>4...20 mA</td>
<td>4 mA</td>
<td>20 mA</td>
</tr>
<tr>
<td>12 mA</td>
<td>4 LEDs ON</td>
<td></td>
</tr>
<tr>
<td>Pt100</td>
<td>-50 °C</td>
<td>+400 °C</td>
</tr>
<tr>
<td>Pt1000</td>
<td>-50 °C</td>
<td>+400 °C</td>
</tr>
<tr>
<td>0 °C</td>
<td>1 LED ON</td>
<td></td>
</tr>
<tr>
<td>Thermoc. type J</td>
<td>0 °C</td>
<td>+1600 °C</td>
</tr>
<tr>
<td>Thermoc. type K</td>
<td>0 °C</td>
<td>+1600 °C</td>
</tr>
<tr>
<td>Thermoc. type S</td>
<td>0 °C</td>
<td>+1600 °C</td>
</tr>
<tr>
<td>800 °C</td>
<td>4 LEDs ON</td>
<td></td>
</tr>
</tbody>
</table>

Fig. 5.2.1-11: Min. and max. values for analog display

LEDs

Fig. 5.2.1-10: Display of an analog value with LEDs
Technical Data for 07 AI 91

In general, the technical system data listed under "System data and system configuration" in chapter 1 of volume 2 of the "Advant Controller 31" system description are valid. Additional data or data which are different from the system data are listed as follows.

Technical data for the complete module

- Permissible temperature range during operation: 0...55 °C
- Rated supply voltage: 24 V DC
- Max. current consumption: max. 0.15 A
- Max. power dissipation: max. 3 W
- Protection against reversed polarity of power connection: yes
- Conductor cross section for the removable connectors: max. 2.5 mm²
- Number of analog input channels: 8
- Electrical isolation: CS31 system bus interface from the rest of the unit
- Addressing and configuration: Coding switch under right housing cover
- Diagnosis: see chapter "Diagnosis and displays"
- Operation and error displays: total of 17 LEDs, see chapter "Diagnosis and displays"

Technical data for analog inputs (applies to all settings)

- Number of channels per module: 8 (each configurable in pairs)
- Electrical isolation: from CS31 system bus
- Line frequency suppression: can be configured for 50 Hz, 60 Hz, or none
- Input delay: 0 (no RC combination)
- Line frequency hum suppression (software filter): 20.0 ms at 50-Hz suppression, 16.7 ms at 60-Hz suppression
- Permissible input overvoltage: max. +/- 30 V
- Updating period per channel incl. input delay and conversion time:
  - suppression 50/60 Hz: typ. 100 ms
  - no suppression: typ. 30 ms
  - when using thermocouples and suppression 50/60 Hz: typ. 150 ms

The total updating time is reduced when not all channels are used (configuration see Fig. 5.2.1-3).

Voltage inputs

- Input resistance: > 1 MΩ
- Measuring ranges (nominal values): +/- 10 V, +/- 5 V, +/- 500 mV, +/- 50 mV
- Resolution: 12 bit + sign
- Total error: ≤ ±0.5 % of full scale
- Channels not used: must be bridged
Current inputs 0...20 mA / 4...20 mA

By bridging the input terminals with a shunt, the voltage input can also be used for input currents. The following specifications are valid:

- **Current range**: 0...20 mA, 4...20 mA
- **Selected measuring range**: +/- 5 V, 4...20 mA
- **Required external shunt**: 250 Ω, 250 Ω
- **Destruction limits of the shunt** depends on its load capabilities
- **Total error**: ≤ ±0.5 % of full scale ± tolerance of the shunt
- **Channels not used**: must be bridged

**Pt100/Pt1000 input**

- **Evaluation range with linearization**: -50°C...+400°C
- **Resistance of the sensors within the evaluation range**: Pt100: 80.31 Ω...247.04 Ω, Pt1000: 803.1 Ω...2470.4 Ω
- **Resolution**: 12 bit + sign (1 LSB = 0.1°C)
- **Permissible total line resistance of both current-carrying lines**: max. 50 Ω per sensor (in 4-wire configuration)
- **Evaluation error within the range of -50...+400°C**: Pt100: +/- 0.5 % of full scale, Pt1000: +/- 1.0 % of full scale (linearity, linearization, temperature range, resolution, adjustment)
- **Constant current sinks for the sensors**: Pt100: 2 mA, Pt1000: 0.2 mA
- **Power dissipation in the sensor**: Pt100, meas. value = 0°C: 0.4 mW, meas. value = 400°C: 1.0 mW, Pt1000, meas. value = 0°C: 0.04 mW, meas. value = 400°C: 0.1 mW
- **No-load voltage of the current output**: < +15 V
- **Permissible total voltage drop at the sensors and lines circuited in series**: max. 7.0 V (current sink 0.2 mA is used), max. 2.5 V (current sink 2.0 mA is used)
- **Cable length, if cables have been laid in parallel**: shielded: max. 50 m, two-core shielded and cross section ≥ 0.5 mm²: max. 200 m

**Unused input channels:**

If unused Pt100/Pt1000 channels are bridged, the wire bridge with 0 Ω simulates the measuring value of a very low temperature. This will cause the error message "range underflow". To avoid such error messages, bridge unused Pt100/Pt1000 channels as follows:
- Pt100 with a resistor of 120 Ω
- Pt1000 with a resistor of 1200 Ω

**Connection of other temperature-dependent resistors:**

Basically all temperature-dependent resistors (PTC, NTC) can be connected instead of Pt100/Pt1000 sensors. For the configuration use the settings ± 5 V, ± 500 mV and ± 50 mV. If necessary, the linearization of the resistor curves must be performed in the PLC user program. The installed current sinks can be used, however, note the following:
- The voltage drop of all series connected resistors must not exceed
  
  **7 V** (when using current sinks of 0.2 mA),

  **2.5 V** (when using current sinks of 2 mA).

- The tolerance of the installed current sinks (± 1.5 %) must be added in the tolerance calculation for the temperature measurements (no compensation as for Pt100/Pt1000).

### Input by thermocouples

Possible thermocouples types J, K and S

**Evaluation range with linearization**

- **Type J** Fe-CuNi 0°C...+1200°C at 0....57.942 mV
- **Type K** NiCr-NiAl 0°C...+1372°C at 0....41.269 mV
- **Type S** Pt10Rh-Pt 0°C...+1600°C at 0.... 9.585 mV

The module has an internal reference junction. The temperature value of this junction is added to the temperature measured by the thermocouple.

- **Resolution** 12 bit + sign (1 LSB = 0.4°C)

- **Evaluation error within the range of 0...+1600°C** +/- 0.5 % of full scale
  (linearity, linearization, temperature range, resolution, adjustment)

- **Cable length, if cables have been laid in parallel**
  shielded max. 50 m
  two-core shielded and cross section > 0.5 mm² max. 200 m

- **Unused channels** have to be short-circuited

### Connection to the CS31 system bus

- **Interface standard** EIA RS-485
- **Electrical isolation** versus supply voltage and inputs

### Mechanical data

- **Mounting on DIN rail** according to DIN EN 50022-35, 15 mm deep.
  The DIN rail is positioned centrally between the upper and the lower edges of the module.

- **Mounting with screws** by 4 screws M4

- **Width x height x depth** 120 x 140 x 85 mm

- **Wiring method** removable terminal blocks with screw-type terminals
  max. 2.5 mm²

- **Conductor cross section**

- **Weight** 450 g

- **Installation dimensions** see Fig. 5.2.1-13

### Installation instructions

- **Installation position** vertical, connector terminals must point upward and downward

- **Cooling** The natural convection cooling must not be blocked by cable ducts or other components installed in the cabinet.
Ordering data
Module 07 AI 91
Scope of delivery:

Order No. GJR5 2516 00 R0202
Analog input module 07 AI 91
1 3-pole terminal block
3 5-pole terminal blocks
2 9-pole terminal blocks
The depth of the device is 85 mm. If a DIN rail is used for the installation, the depth must be increased by the depth of the rail.

Fig. 5.2.1-13: 07 Al 91, Front panel foil, dimensions for installation holes are in bold print.
5.4.1 Analog Input/Output Module 07 AC 91
16 inputs/outputs, configurable for ±10 V, 0...10 V, 0...20 mA, 8/12 bit resolution, 2 operating modes, CS31 system bus

Fig. 5.4.1-1: Analog input/output module 07 AC 91

Contents
Intended purpose ........................................... 5.4.1-1
Display and operating elements on the front panel .................. 5.4.1-1
Electrical connection ...................................... 5.4.1-1
Configuration .............................................. 5.4.1-3
Measuring ranges of analog channels .......................... 5.4.1-4
Addressing .................................................... 5.4.1-6
Normal operation .......................................... 5.4.1-6
Diagnosis and displays ..................................... 5.4.1-6
Technical data ............................................... 5.4.1-8
   Front panel foil and outside dimensions .......... 5.4.1-11

Intended purpose
The analog input/output module 07 AC 91 is used as a remote module on the CS31 system bus. It contains 16 analog input/output channels that can be configured in two operating modes:

- Operating mode "12 bits":
  8 input channels, individually configurable ±10 V or 0...20 mA, 12 bit resolution plus
  8 output channels, individually configurable ±10 V or 0...20 mA, 12 bit resolution

- Operating mode "8 bits":
  16 channels, configurable in pairs as inputs or outputs, 0...10 V or 0...20 mA, 8 bit resolution

- The configuration is set with DIL switches.

The PLC offers an interconnection element ANAI4_20 for measuring signals of 4...20 mA (refer to 907 PC 331, connection element library).

The module 07 AC 91 uses up to eight input words on the CS31 system bus plus up to eight output words. In the operating mode "8 bits", 2 analog values are packed into one word.

The operating voltage of the unit is 24 V DC. The CS31 system bus connection is electrically isolated from the rest of the module.

The module offers a number of diagnosis functions (see chapter "Diagnosis and displays").

Displays and operating elements on the front panel
1. 8 green LEDs for channel selection and diagnosis
2. 8 green LEDs for analog value display of a channel
3. List of diagnosis information relating to the LEDs, when they are used for diagnosis display
4. Red LED for error messages
5. Test button

Electrical connection
The module can be installed on a DIN rail (15 mm high) or with 4 screws. The figure on the next page shows the electrical connection for the input/output module.
Operating mode "12 bits": 8 analog inputs (terminals 4 to 19) and 8 analog outputs (terminals 27 to 42), configurable for ±10 V or 0...20 mA, resolution 12 bits
Operating mode "8 bits": 16 analog channels configurable in pairs as inputs or outputs for 0...10 V or 0...20 mA, resolution 8 bits

The terminals SHIELD of the CS31 system bus and PE of the supply voltage are not electrically connected.

Caution: The process voltage must be included in the grounding concept of the control system (e.g. grounding of the minus terminal).

The connections of all analog value sensors and receivers must be electrically isolated from their installation environment.

The cable shields of the analog wiring must be connected to cabinet ground where they enter the cabinet.

Setting of the module address and configuration of the analog channels is done with the DIL switches (see next page).

Bild 5.4.1-2: Electrical connection of the analog input/output module 07 AC 91
Configuration of analog channels and settings of the module address on the CS31 bus

The 16 analog channels can be configured in two operating modes with DIL switches (located under the slide cover on the right side of the module housing (see following figure):

**Operating mode "12 bits":** 8 analog inputs (terminals 4 to 19) plus 8 analog outputs (term. 27 to 42), configurable for ±10 V or 0...20 mA, solution 12 bits

**Operating mode "8 bits":** 16 analog channels configurable in pairs as inputs or outputs for 0...10 V or 0...20 mA, solution 8 bits

---

**DIL switch 1**

<table>
<thead>
<tr>
<th>ON</th>
<th>OFF</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**DIL switch 2**

<table>
<thead>
<tr>
<th>ON</th>
<th>OFF</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Address DIL switch**

<table>
<thead>
<tr>
<th>ON</th>
<th>OFF</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
</tr>
</tbody>
</table>

---

**Terminals**

<table>
<thead>
<tr>
<th>4</th>
<th>6</th>
<th>8</th>
<th>10</th>
<th>12</th>
<th>14</th>
<th>16</th>
<th>18</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>7</td>
<td>9</td>
<td>11</td>
<td>13</td>
<td>15</td>
<td>17</td>
<td>19</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>27</th>
<th>29</th>
<th>31</th>
<th>33</th>
<th>35</th>
<th>37</th>
<th>39</th>
<th>41</th>
</tr>
</thead>
<tbody>
<tr>
<td>28</td>
<td>30</td>
<td>32</td>
<td>34</td>
<td>36</td>
<td>38</td>
<td>40</td>
<td>42</td>
</tr>
</tbody>
</table>

---

**Operating mode "12 bits"**

- **Analog input**
  - 0 1 2 3 4 5 6 7
- **Analog output**
  - 0 1 2 3 4 5 6 7

Each switch of the DIL switches 1 and 2 configures one channel: OFF = ±10 V, ON = 20 mA

Example: DIL switch 1, position 1 = OFF: analog channel 0 is configured for ±10 V.

---

**Operating mode "8 bits"**

- **Analog input**
  - 0 1 2 3 4 5 6 7
- **Analog output**
  - 8 9 10 11 12 13 14 15

**Setting**

- **Input or output / 0...10 V or 0...20 mA**
  - in pairs, e.g. together for two channels with two switches per pair

- **Left switch OFF = Both channels analog inputs**
- **Left switch ON = Both channels analog outputs**

- **Right switch OFF = 0...10 V**
- **Right switch ON = 0...20 mA**

Example: DIL-switch 1, positions 1 and 2: Analog channels 0 and 1 are configured as inputs for 0...20 mA.

---

**Module address**

**Operating mode**

**ON**

- OFF: Channel-No. ≤ 7
- ON: Channel-No. > 7
- Bit significance 1
- Bit significance 2
- Bit significance 4
- Bit significance 8
- Monitoring of measuring range limits, OFF=yes, ON=no
- reserved (set to OFF)

**OFF: oper. mode "12 bits"**

- ON: oper. mode "8 bits"

The module address is calculated from the sum of the significances of the switches that are in position ON.

Example: Switches 6 and 7 ON

Module address: 2 + 1 = 3

---

**Important!**

- The analog outputs must be enabled with a binary 1 signal (24 V) at terminals 25 (+) and 26 (-).
- Both of these terminals are electrically isolated, which means that the reference signal and the control signal must be connected.
- Without connecting the enabled signal, the analog voltage outputs are set to 0 V and the current outputs to 0 mA.

---

Fig. 5.4.1-3: Configuration of the analog channels and setting of the module address on the CS31 system bus
Operating mode "12 bits":

For configuration see preceding page. If input values overflow or underflow the measuring range, the values 32767 or -32767 are output.

Resolution in the control system:
All measured values will be converted with a resolution of 12 bits which are either 11 bits + sign or 12 bits without sign.

Examples:

<table>
<thead>
<tr>
<th>Measuring range</th>
<th>Range of numerical display</th>
</tr>
</thead>
<tbody>
<tr>
<td>-10 V...0...10 V</td>
<td>-32760_d...0...32760_d</td>
</tr>
<tr>
<td></td>
<td>8008_h...0000_h...7FF8_h</td>
</tr>
<tr>
<td>0...20 mA</td>
<td>0...32760_d</td>
</tr>
<tr>
<td></td>
<td>0000...7FF8_h</td>
</tr>
</tbody>
</table>

The relationship between analog signal and converted numerical value is shown in the following figure.

![Diagram showing the relationship between analog value and numerical value and position of bits in the word.]

Conversion formula for voltages:
100 % of input voltage produces a number of 32760, i.e. 1 % more input voltage increases the output number by 327.6
Operating mode "8 bits":
For configuration please see second preceding page.

Resolution in the control system:

The converted analog values of two analog channels are packed into a word with 8 bit each (low byte and high byte).

The smallest difference that can be detected on the analog side (e.g. 40 mV in the range of 0...10 V) results in a change of the numeric value by 1 in the PLC program.

Examples:
0...10 V  0...255d  00h...FFh
0...20 mA 0...255d  00h...FFh

The relationship between analog signal and converted numerical value is shown in the following figure.

Operating mode "8 bits":
For configuration please see second preceding page.

Resolution in the control system:

The converted analog values of two analog channels are packed into a word with 8 bit each (low byte and high byte).

The smallest difference that can be detected on the analog side (e.g. 40 mV in the range of 0...10 V) results in a change of the numeric value by 1 in the PLC program.

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The relationship between analog signal and converted numerical value is shown in the following figure.

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The smallest difference that can be detected on the analog side (e.g. 40 mV in the range of 0...10 V) results in a change of the numeric value by 1 in the PLC program.

Examples:
0...10 V  0...255d  00h...FFh
0...20 mA 0...255d  00h...FFh

The relationship between analog signal and converted numerical value is shown in the following figure.

Operating mode "8 bits":
For configuration please see second preceding page.

Resolution in the control system:

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The smallest difference that can be detected on the analog side (e.g. 40 mV in the range of 0...10 V) results in a change of the numeric value by 1 in the PLC program.

Examples:
0...10 V  0...255d  00h...FFh
0...20 mA 0...255d  00h...FFh

The relationship between analog signal and converted numerical value is shown in the following figure.

Operating mode "8 bits":
For configuration please see second preceding page.

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The smallest difference that can be detected on the analog side (e.g. 40 mV in the range of 0...10 V) results in a change of the numeric value by 1 in the PLC program.

Examples:
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0...20 mA 0...255d  00h...FFh

The relationship between analog signal and converted numerical value is shown in the following figure.

Operating mode "8 bits":
For configuration please see second preceding page.

Resolution in the control system:

The converted analog values of two analog channels are packed into a word with 8 bit each (low byte and high byte).

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Examples:
0...10 V  0...255d  00h...FFh
0...20 mA 0...255d  00h...FFh

The relationship between analog signal and converted numerical value is shown in the following figure.

Operating mode "8 bits":
For configuration please see second preceding page.

Resolution in the control system:

The converted analog values of two analog channels are packed into a word with 8 bit each (low byte and high byte).

The smallest difference that can be detected on the analog side (e.g. 40 mV in the range of 0...10 V) results in a change of the numeric value by 1 in the PLC program.

Examples:
0...10 V  0...255d  00h...FFh
0...20 mA 0...255d  00h...FFh

The relationship between analog signal and converted numerical value is shown in the following figure.
Addressing

Each module must have an address installed to enable the central unit to correctly access the inputs and outputs.

A detailed description about "Addressing" can be found in the chapter "Addressing" for the central processing unit and couplers.

The setting of the address must be done with the DIL switch located under the slide cover on the right side of the module housing (see Fig. 5.4.1-3). When using central units 07 KR 91, 07 KT 92, 07 KT 93 and 07 KT 94 as bus master, the following address allocations result:

<table>
<thead>
<tr>
<th>Central units</th>
<th>07 KR 91 / 07 KT 92 / 07 KT 93 / 07 KT 94...</th>
</tr>
</thead>
</table>

Operating mode "12 bits",
Address DIL switch No. 1 in OFF position

<table>
<thead>
<tr>
<th>Channel</th>
<th>Address in Channel PLC program</th>
<th>Channel</th>
<th>Address in Channel PLC program</th>
</tr>
</thead>
<tbody>
<tr>
<td>E0</td>
<td>EW n,00</td>
<td>A0</td>
<td>AW n,00</td>
</tr>
<tr>
<td>E1</td>
<td>EW n,01</td>
<td>A1</td>
<td>AW n,01</td>
</tr>
<tr>
<td>E2</td>
<td>EW n,02</td>
<td>A2</td>
<td>AW n,02</td>
</tr>
<tr>
<td>E3</td>
<td>EW n,03</td>
<td>A3</td>
<td>AW n,03</td>
</tr>
<tr>
<td>E4</td>
<td>EW n,04</td>
<td>A4</td>
<td>AW n,04</td>
</tr>
<tr>
<td>E5</td>
<td>EW n,05</td>
<td>A5</td>
<td>AW n,05</td>
</tr>
<tr>
<td>E6</td>
<td>EW n,06</td>
<td>A6</td>
<td>AW n,06</td>
</tr>
<tr>
<td>E7</td>
<td>EW n,07</td>
<td>A7</td>
<td>AW n,07</td>
</tr>
</tbody>
</table>

Operating mode "8 bits",
Address DIL switch No. 1 in ON position

<table>
<thead>
<tr>
<th>Channel</th>
<th>Address in Channel PLC program</th>
<th>Channel</th>
<th>Address in Channel PLC program</th>
</tr>
</thead>
<tbody>
<tr>
<td>E00</td>
<td>EW n,00 Lo</td>
<td>A00</td>
<td>AW n,00 Lo</td>
</tr>
<tr>
<td>E01</td>
<td>EW n,00 Hi</td>
<td>A01</td>
<td>AW n,00 Hi</td>
</tr>
<tr>
<td>E02</td>
<td>EW n,01 Lo</td>
<td>A02</td>
<td>AW n,01 Lo</td>
</tr>
<tr>
<td>E03</td>
<td>EW n,01 Hi</td>
<td>A03</td>
<td>AW n,01 Hi</td>
</tr>
<tr>
<td>E04</td>
<td>EW n,02 Lo</td>
<td>A04</td>
<td>AW n,02 Lo</td>
</tr>
<tr>
<td>E05</td>
<td>EW n,02 Hi</td>
<td>A05</td>
<td>AW n,02 Hi</td>
</tr>
<tr>
<td>E06</td>
<td>EW n,03 Lo</td>
<td>A06</td>
<td>AW n,03 Lo</td>
</tr>
<tr>
<td>E07</td>
<td>EW n,03 Hi</td>
<td>A07</td>
<td>AW n,03 Hi</td>
</tr>
<tr>
<td>E08</td>
<td>EW n,04 Lo</td>
<td>A08</td>
<td>AW n,04 Lo</td>
</tr>
<tr>
<td>E09</td>
<td>EW n,04 Hi</td>
<td>A09</td>
<td>AW n,04 Hi</td>
</tr>
<tr>
<td>E10</td>
<td>EW n,05 Lo</td>
<td>A10</td>
<td>AW n,05 Lo</td>
</tr>
<tr>
<td>E11</td>
<td>EW n,05 Hi</td>
<td>A11</td>
<td>AW n,05 Hi</td>
</tr>
<tr>
<td>E12</td>
<td>EW n,06 Lo</td>
<td>A12</td>
<td>AW n,06 Lo</td>
</tr>
<tr>
<td>E13</td>
<td>EW n,06 Hi</td>
<td>A13</td>
<td>AW n,06 Hi</td>
</tr>
<tr>
<td>E14</td>
<td>EW n,07 Lo</td>
<td>A14</td>
<td>AW n,07 Lo</td>
</tr>
<tr>
<td>E15</td>
<td>EW n,07 Hi</td>
<td>A15</td>
<td>AW n,07 Hi</td>
</tr>
</tbody>
</table>

| n: Group number of the address, set at address DIL switch with switches 5...8. Addresses for 07 KR 91 / 07 KT 92 / 07 KT 93 as bus master: 00...05, as of 07 KT 94 also 08...15. Lo = low byte, Hi = high byte |

As shown in the table, the module occupies 8 analog inputs and 8 analog outputs on the CS31 system bus.

If the module is configured in operating mode "8 bits" only for inputs or only for outputs, only 8 analog inputs or 8 analog outputs are used on the CS31 system bus. In this case, not occupied input or output addresses can be used by other modules.

If the address DIL switch No. 8 is switched to ON, all channel numbers change by 08, i.e. address AW n,00 changes to AW n,08, etc. This applies for the address assignments for inputs and outputs in both operation modes.

Normal operation

- After the supply voltage was switched on, the module initializes automatically. During initialization process all LEDs are switched on.
- If the CS31 system bus does not (yet) run, the red error LED will light up. If an error occurs during the initialization process, the red error LED will also light up.

Diagnosis and displays

The module 07 AC 91 offers the following diagnosis functions:

- Analog value is out of measuring range
- Storing this information and possibility for recall (kind of error and location of error)

If an error occurs, the red LED lights up. The error message will be transmitted to the central unit or the coupler.

In the central units 07 KR 91 / 07 KT 92 / 07 KT 93 / 07 KT 94, the errors are displayed as follows:

- Out of range
  
<table>
<thead>
<tr>
<th>Error classification</th>
<th>Error recognition</th>
<th>Module type</th>
<th>Group number</th>
<th>Channel number</th>
</tr>
</thead>
<tbody>
<tr>
<td>4 (FK4)</td>
<td>10 dec. -&gt; MW 255.08</td>
<td>* 01/03/05 -&gt; MW 255.09</td>
<td>-&gt; MW 255.10</td>
<td>-&gt; MW 255.11</td>
</tr>
</tbody>
</table>

In the initial state after initialization, channel 0 is selected and the corresponding analog value is displayed (see also figures 5.4.1-6 and 5.4.1-7).

_______________________________________

* 01 if only inputs are configured
  03 if only outputs are configured
  05 if inputs and outputs are configured
Diagnosis functions can be selected individually for each channel with the test button. The initial actuation of the test button selects channel 0. The diagnosis LEDs 0 to 3 display the channel number in hexadecimal code.

After releasing the test button, the diagnosis information of this channel is displayed for about 3 seconds by the green LEDs 0 to 7.

**Explanation of lit LEDs:**
- 0 not used
- 1 not used
- 2 not used
- 3 not used
- 4 Out of range
- 5 not used
- 6 not used
- 7 not used

Explanations for the LEDs are also printed in English on the front panel.

The error messages on the module and on the central unit go out again as soon as the error has been corrected, no new errors have been recognized and the error correction was acknowledged.

**Acknowledging an error after error correction:**
- by pressing the test button for about 5 seconds, or
- with the PC, or
- with the PLC program in the central unit

The current input has a self-protecting feature for the measuring range 0...20 mA. If the current gets too high, the current input shunt is switched off and the value for "overflow" is output. Re-activation is attempted again in increments of approx. 1 second to facilitate the correct measurement as soon as the current regains acceptable limits.

With each successive pressing and releasing of the test button, the process is repeated for the other channels.

After interrogating the last channel and pressing the test button once more, an LED test is initiated. All LEDs of the module must light up. Following this, the position of the DIL address switch is displayed for about 3 seconds (module address on the CS31 system bus). In this case, LED 0 shows the position of switch 1 (LEDs 0...7 are assigned to switches 1...8).

**Display of an analog value**

When the test button is not pressed, the analog value of the selected channel is displayed with 8 LEDs.

**Explanation:**
- all LEDs OFF -> minimum value
- all LEDs ON -> maximum value

**Fig. 5.4.1-6:** Display LEDs for channel selection and diagnosis

- Configuration ±10 V and 0 V at E0
- Display: [ ] [ ] [ ] [ ] [ ] [ ] [ ]
In general, the technical system data listed under "System data and system configuration" in chapter 1 of volume 2 of the “Advant Controller 31” system description are valid. Additional data or data which are different from the system data are listed as follows.

### Technical data for the complete unit

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Permissible temperature range during operation</td>
<td>0...55 °C</td>
</tr>
<tr>
<td>Rated supply voltage</td>
<td>24 V DC</td>
</tr>
<tr>
<td>Max. current consumption</td>
<td>0.2 A</td>
</tr>
<tr>
<td>Max. power dissipation</td>
<td>5 W</td>
</tr>
<tr>
<td>Protection against reversed polarity of power connection</td>
<td>yes</td>
</tr>
<tr>
<td>Number of binary inputs</td>
<td>1 as enabling input for the analog outputs</td>
</tr>
<tr>
<td>Number of analog input channels</td>
<td>8 or 16, depending on the operating mode</td>
</tr>
<tr>
<td>Number of analog output channels</td>
<td>8 or 16, depending on the operating mode</td>
</tr>
<tr>
<td>Electrical isolation</td>
<td>CS31 system bus interface from the rest of the unit, 1 binary input from the rest of the unit</td>
</tr>
<tr>
<td>Address setting and configuration</td>
<td>Coding switch under the cover located on the right side of the housing</td>
</tr>
<tr>
<td>Diagnosis</td>
<td>see chapter &quot;Diagnosis and displays&quot;</td>
</tr>
<tr>
<td>Operation and error displays</td>
<td>a total of 17 LEDs, see chapter &quot;Diagnosis and displays&quot;</td>
</tr>
<tr>
<td>Method of connections</td>
<td>removable screw-type terminal blocks</td>
</tr>
<tr>
<td>supply terminals, CS31 system bus</td>
<td>max. 1 x 2.5 mm² or max. 2 x 1.5 mm²</td>
</tr>
<tr>
<td>all other terminals</td>
<td>max. 1 x 1.5 mm²</td>
</tr>
<tr>
<td>Max. length of the analog cables,</td>
<td>100 m</td>
</tr>
<tr>
<td>two-core shielded and cross section ≥ 0.5 mm²</td>
<td></td>
</tr>
<tr>
<td>Conversion error of the analog values (non-linearity, factory calibration and resolution)</td>
<td>typ. 0.5 %, max. 1 %</td>
</tr>
<tr>
<td>Max. permissible potential difference between</td>
<td>± 1 V</td>
</tr>
<tr>
<td>terminal M (minus of the supply voltage) and</td>
<td></td>
</tr>
<tr>
<td>terminals AGND (minus of analog inputs and outputs)</td>
<td></td>
</tr>
<tr>
<td>Common reference potential for all analog signals</td>
<td>AGND (minus terminal of analog inputs and outputs)</td>
</tr>
<tr>
<td>Electrical isolation of analog signals</td>
<td>none (see also Fig. 5.4.1-2).</td>
</tr>
</tbody>
</table>

### Technical data of the binary input (enabling input for analog outputs)

The analog outputs must be enabled by a binary 1 signal (24V) at terminals 25 (+) and 26 (-).

<table>
<thead>
<tr>
<th>Signal level</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 signal (-30...+5 V)</td>
<td>voltage outputs are at 0 V, current outputs are at 0 mA</td>
</tr>
<tr>
<td>1 signal (+13...+30 V)</td>
<td>analog outputs are active</td>
</tr>
</tbody>
</table>

Electrical isolation

yes, i.e. the reference potential and the control signal must be connected.
Technical data of analog inputs

Number of channels per module, oper. mode "12 bits" 8
Number of channels per module, oper. mode "8 bits" up to 16
Configurability oper. mode "12 bits" ±10 V, 0...20 mA (each channel can be configured individually)
Configurability oper. mode "8 bits" 0...10 V, 0...20 mA (channels can be configured in pairs)
Signalization of input signals see diagnosis
Input resistance per channel voltage input > 100 kΩ
current input approx. 330 Ω
The current input has a self-protecting feature. If the current gets too high, the current input shunt is switched off and the value for "overflow" is output. Re-activation is attempted again in increments of approx. 1 second to facilitate the correct measurement as soon as the current regains acceptable limits.
Time constant of the input filter 470 µs for voltage,
100 µs for current
Conversion cycle (over 8 inputs + 8 outputs) 8 ms
Resolution range ±10 V oper. mode "12 bit" 5 mV (11 bit plus sign)
range 0...20 mA oper. mode "12 bit" 5 µA (12 bit without sign)
Resolution range 0...10 V oper. mode "8 bit" 40 mV (8 bit without sign)
range 0...20 mA oper. mode "8 bit" 80 µA (8 bit without sign)
Relationship between input signal and hexcode operating mode "12 bits" -100 %...0...100 % = 8008H...0000H...7FF8H (-32760...0...32760 decimal)
Relationship between input signal and hexcode operating mode "8 bits" 0...100 % = 00H...FFH (0...255 decimal)
Voltage inputs not used can be bridged to increase noise immunity
Current inputs not used are low in ohms, can remain open

Technical data of analog outputs

Number of channels per unit, oper. mode "12 bits" 8
Number of channels per unit, oper. mode "8 bits" up to 16
Configurability oper. mode "12 bits" ±10 V, 0...20 mA (each channel can be configured individually)
Configurability oper. mode "8 bits" 0...10 V, 0...20 mA (channels can be configured in pairs)
Signalization of output channels see diagnosis
Output loadability as voltage output max. +20 mA (source, current flows out of the output)
max. -10 mA (sink, current flows into the output)
Output load resistance (burden), if current output 0...500 Ω
Resolution see "analog inputs"
Relationship between output signal and hexcode see "analog inputs"
Outputs not used remain open
Connection to the CS31 system bus

Interface standard
EIA RS-485

Electrical isolation
from the rest of the unit

Mechanical data

Mounting on DIN rail
according to DIN EN 50022-35, 15 mm deep. The DIN rail is positioned centrally between the upper and the lower edges of the module.

Mounting with screws
by 4 screws M4

Width x height x depth
120 x 140 x 85 mm

Wiring method
removable terminal blocks with screw-type terminals

  supply terminals, CS31 system bus
  max. 1 x 2.5 mm² or max. 2 x 1.5 mm²

  all other terminals
  max. 1 x 1.5 mm²

Weight
450 g

Installation dimensions
see Fig. 5.4.1-9

Installation instructions

Installation position
vertical, connector terminals must point upward and downward

Cooling
The natural convection cooling must not be blocked by cable ducts or other components installed in the cabinet.

Ordering data

Module 07 AC 91
Order No. GJR5 2523 00 R0101

Scope of delivery
Analog input and output module 07 AC 91
1 2-pole terminal block (grid space 3.81 mm)
1 3-pole terminal block (grid space 5.08 mm)
1 5-pole terminal block (grid space 5.08 mm)
4 8-pole terminal blocks (grid space 3.81 mm)
The depth of the device is 85 mm. If a DIN rail is used for the installation, the depth must be increased by the depth of the rail.

Fig. 5.4.1-9: 07 AC 91, Front panel foil and outside dimensions, dimensions for mounting holes are shown in bold print
6 Special Modules

6.1 NCB: CS31 system bus amplifier ................................................................. 6.1-1
6.2 NCBR: CS31 system bus amplifier with redundancy .............................. 6.2-1
6.1 CS31 System Bus Module NCB

CS31 system bus amplifier, system bus electrically isolated

Contents

Intended purpose ................................................... 6.1-1
Display and operating elements
  on the front panel ............................................ 6.1-1
Electrical connection .............................................. 6.1-2
Installation ............................................................. 6.1-2
Diagnosis and displays .......................................... 6.1-3
Error detection ....................................................... 6.1-4
Technical data ....................................................... 6.1-4

Intended purpose

The NCB is an amplifier for the CS31 system bus. Using up to 3 NCBs, a total length of 3 km can be obtained for the CS31 system bus.

The use of NCBs is entirely transparent. A diagnosis is possible for every part of the bus system on both the basic unit and the NCB.

Display and operating elements on the front panel

1) Removable connector for the 24 V DC power supply
2) Removable connector for the CS31 system bus input
3) Removable connector for the CS31 system bus output
4) A green LED indicates that the power supply is ON
5) TXI: communication is running from the CS31 bus master to the NCB
   TXO: communication is running from the CS31 remote module to the NCB
6) 2 red LEDs "OUT" and "IN" indicate the CS31 system bus status

Fig. 6.1-1: CS31 system bus amplifier NCB
**Electrical connection**

![Diagram of electrical connection]

Fig. 6.1-2: Electrical connection of the CS31 system bus amplifier NCB

**Installation**

All EMC standards have to be met (see also AC31 system data)

![Diagram of installation]

Fig. 6.1-3: Installation of a CS31 bus line with 3 NCB bus amplifiers

The bus terminating resistor of 120 Ω 1/4 W is already integrated in the NCB.

In order to guarantee a correct diagnosis function, the terminals "IN" have to be connected to the CS31 master's side.

For a safe and error-free data transmission over long distances, the selection of an appropriate bus cable is very important.
The following figure shows a typical configuration.

![Typical configuration diagram](image)

Fig 6.1-4: Typical configuration

The cable can be selected according to the following rules:

**L1 + L2 + L3 + L4 < 1200 m**

A standard cable (twisted pair) can be used.

**1200 m < L1 + L2 + L3 + L4 < 1500 m**

Cable specifications:
- Twisted pair, twisting rate > 10/m
- Cable capacitance < 100 nF/km (100 pF/m)
- Cable impedance 80...120 Ω (= \sqrt{L/C})
- Transmission speed
  
(55 % of light velocity)

**1500 m < L1 + L2 + L3 + L4 < 2000 m**

Cable specifications:
- Twisted pair, twisting rate > 10/m
- Cable capacitance < 100 nF/km (100 pF/m)
- Cable impedance 80...120 Ω (= \sqrt{L/C})
- Transmission speed
  
(66 % of light velocity)

Diagnosis and displays

Both an open-circuit and a short-circuit on the CS31 system bus is detected by the NCB. The defective part of the bus line and all parts behind it are out of order. The other bus segments work properly.

In case of an error, the red LEDs "OUT" or "IN" light up.

In the basic unit, an error is indicated as "Remote Unit Error" (FK3).

The access to the error is different with the different types of basic units.

07 KR 31 / 07 KT 31

- Access to the error with the function block COPY to the memory address
  SEG: 0h and OFFSET: 8660h

  If the value of 2Ah is found, one of the NCBs is in the error state. (The red LED on the basic unit remains OFF.)

  Using the function block COPY, the error is reset in the basic unit by writing the value 0h to the memory address 0:8660h.

- Access to the error as FK3 error number 17d with versions produced after April 1996 (07 KR 31 index J and 07 KT 31 index B).

07 KR 91 / 07 KT 92 / 07 KT 93

- Access to the error with the function block COPY to the memory address
  SEG: C000h and OFFSET: 0100h

  If the value of 2Ah is found, one of the NCBs is in the error state. (The red LED on the basic unit lights up.)

  Using the function block COPY, the error is reset in the basic unit by writing the value 0h to the memory address 0C00:0100h.

07 CS 61 (for T200)

- Version index c R202

  The error is seen as a bus error with no remote modules connected.

- Version index d R202 and the following:

  The error is set in bit 4 of the word
  MW 4104,03 for the first CS31 line,
  MW 4105,11 for the second CS31 line,
  MW 4107,03 for the third CS31 line and
  MW 4108,11 for the fourth CS31 line.

07 CS 91 (for T300 and UCZA/UCZB)

- A diagnosis is not available.
**Error detection**

- Check all CS31 system bus connections. The bus connectors BUS1 and BUS2 must not be reversed.
- Check the CS31 system bus.
  
  In case of a short-circuit, all remote modules in the concerned bus segment do not work.

The NCB only can detect an open-circuit if it is between the NCB and the first remote module. In this case, the basic unit will detect a disconnected remote module.

After the error has been remedied, the NCB acknowledges it automatically. The red LED goes out again.

Under normal conditions, a remote unit error is always detected by the basic unit.

---

**Technical data**

**General data**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated supply voltage</td>
<td>24 V DC</td>
</tr>
<tr>
<td>Max. power dissipation inside the module</td>
<td>4 W</td>
</tr>
<tr>
<td>Max. delay between input and output signals</td>
<td>&lt; 2 µs</td>
</tr>
</tbody>
</table>

**Technical data of the system bus terminals "IN"**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Interface standard</td>
<td>EIA RS-485</td>
</tr>
<tr>
<td>Terminating resistor 120 Ω, 1/4 W</td>
<td>integrated</td>
</tr>
<tr>
<td>Electrical isolation</td>
<td>yes</td>
</tr>
<tr>
<td>Isolation IN/OUT and IN/power supply</td>
<td>1000 V AC</td>
</tr>
</tbody>
</table>

**Technical data of the system bus terminals "OUT"**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Interface standard</td>
<td>EIA RS-485</td>
</tr>
<tr>
<td>Terminating resistor 120 Ω, 1/4 W</td>
<td>integrated</td>
</tr>
<tr>
<td>Electrical isolation</td>
<td>yes</td>
</tr>
<tr>
<td>Isolation OUT/IN and OUT/power supply</td>
<td>1000 V AC</td>
</tr>
</tbody>
</table>

**LED displays**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Indication of the power supply voltage</td>
<td>1 green LED</td>
</tr>
<tr>
<td>Indication of communication TXI</td>
<td>1 yellow LED</td>
</tr>
<tr>
<td>&quot;Communication is running from the CS31 bus master to the NCB&quot;</td>
<td></td>
</tr>
<tr>
<td>TXO</td>
<td>1 yellow LED</td>
</tr>
<tr>
<td>&quot;Communication is running from the CS31 remote module to the NCB&quot;</td>
<td></td>
</tr>
<tr>
<td>Indications CS31 system bus</td>
<td>2 red LEDs &quot;OUT&quot; and &quot;IN&quot;</td>
</tr>
</tbody>
</table>

**Mechanical data**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dimensions, width x height x depth</td>
<td>120 x 80 x 85 mm</td>
</tr>
<tr>
<td>Weight</td>
<td>340 g</td>
</tr>
<tr>
<td>Mounting on DIN rail</td>
<td>yes</td>
</tr>
</tbody>
</table>

**Ordering data**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Order No.</td>
<td>NCB</td>
</tr>
<tr>
<td>FPR3471200R1002</td>
<td></td>
</tr>
</tbody>
</table>
## Intended purpose

The NCBR is an amplifier for the CS31 system bus with integrated redundancy functions.

The NCBR allows two types of configuration:

- **Reliable installation**
  - In this case, NCBRs are used to build up redundant data transmission systems.

- **Extended installation**
  - In this case, NCBRs are used to build up star-type configurations which cover wide areas of application.

The redundancy is performed with two parallel buses between two NCBRs or with one bus using one NCBR in ring configuration.

Due to the integrated amplifier function, a maximum bus length of up to 1800 m is possible with the installation of 3 NCBRs.

The use of NCBRs is entirely transparent. A diagnosis is possible for every part of the bus system on both the basic unit and the NCBR.

## Display and operating elements on the front panel

1. Removable connector for the 24 V DC power supply
2. Removable connector for the CS31 system bus input 1
3. Removable connector for the CS31 system bus input 2
4. Power supply
5. Out configuration
6. ERRORS OUT1
7. OUT2
8. IN1
9. IN2

---

Fig. 6.2-1: CS31 system bus amplifier NCBR with redundancy
(4) Removable connector for the CS31 system bus output 1

(5) Removable connector for the CS31 system bus output 2

(6) A green LED indicates that the power supply is ON

(7) 2 yellow LEDs “TXI” and “TXO” to display the communication between the basic unit and the NCBR

(8) 4 red LEDs “OUT1”, “OUT2”, “IN1” and “IN2” to display of errors on the different CS31 bus segments

(9) Selection of parallel or ring configuration

**Electrical connection**

Fig. 6.2-2: Electrical connection of the CS31 system bus amplifier NCBR

b1: BUS 1
b2: BUS 2
IN1 and OUT1: Segment 1
IN2 and OUT2: Segment 2
**Installation**

All EMC standards have to be met (see also AC31 system data).

**Parallel configuration**

The bus terminating resistor of 120 Ω 1/4 W is already integrated in the NCBR.

The maximum length of each CS31 bus segment is 500 m.

The maximum length in one direction (e.g. L1 + L2 + L4) depends on the cable specifications (see also chapter “Cable selection”).

In case of an error on L2 (resp. L3) the remote modules connected on L2 (resp. L3) between the failure location and the preceding NCBR lose their communication with the basic unit.
The maximum length of each CS31 bus segment is 500 m.

The maximum length in one direction (e.g. L1 + L2) depends on the cable specifications (see also chapter "Cable selection").
Star configuration

In this configuration, up to 7 NCBRs can be used.

The maximum length of each CS31 bus segment is 500 m.

For correct function with the maximum length in one direction (e.g. L1 + L3 + L6 = 1500 m) the use of a suitable cable is very important (see also chapter “Cable selection” on the following pages).

Fig. 6.2-5: Star configuration with several NCBR bus amplifiers
The 3 possible configurations can be combined with each other.

In one direction, a maximum of 3 NCBRs can be used. The entire configuration must not contain more than 31 remote modules. NCBRs do not use a bus address here.

Examples for other configurations:

Cable selection

For a safe and error-free data transmission over long distances, the selection of an appropriate bus cable is very important.

- The maximum length of each CS31 bus segment is 500 m.
- If the total length is less than 1200 m, a standard cable (twisted pair) can be used.
If the cable length is between 1200 m and 1500 m, a cable with the following specifications can be used:

Cable specifications:
- Twisted pair, twisting rate > 10/m
- Cable capacitance < 100 nF/km (100 pF/m)
- Cable impedance 80...120 0 (= \sqrt{L/C})
- Transmission speed
  (= 1/(L/C)) > 55 % of light velocity
  (55 % of 300 000 km/s)

If the cable length is between 1500 m and 1800 m, a cable with the following specifications can be used:

Cable specifications:
- Twisted pair, twisting rate > 10/m
- Cable capacitance < 100 nF/km (100 pF/m)
- Cable impedance 80...120 0 (= \sqrt{L/C})
- Transmission speed
  (= 1/(L/C)) > 66 % of light velocity
  (66 % of 300 000 km/s)

Function
The NCBR is waiting for a character at the inputs IN1 and IN2.

When the first character of a frame is received on one of the two buses, the corresponding line becomes valid and the characters on the other bus line are ignored then.

After the complete frame has been received (delay > 20 μs) the NCBR waits for a new frame on both buses.

Redundancy and star configuration
The switch "OUT CONFIGURATION" has to be in the position PARALLEL.
Frames are sent from both bus outputs now.

Ring configuration
The switch "OUT CONFIGURATION" has to be in the position RING.
After initialization, the electrical signal on OUT1 is changed, and the NCBR checks whether the signal on OUT2 follows this change.
- If yes, the ring is closed. The data frame is sent only from OUT1 now.
- If no, the ring is open. The data frame is sent from both outputs (OUT1 and OUT2) now.

The red LEDs are alternatively ON.
The NCBR checks for each character if it arrives at OUT2.
If the ring is closed again, the two frames are overlapping each other.
If a transmission error occurs, the telegram currently sent is ignored and will be sent again.

Diagnosis and displays
Both an open-circuit and a short-circuit on the CS31 system bus is detected by the NCBR.
These errors are displayed on the basic unit.
In case of a bus error, the red LEDs "OUT1", "OUT2", "IN1" or "IN2" light up.
The access to the errors is different with the different types of basic units:

07 KR 31 / 07 KT 31
- Access to the error with the function block COPY to the memory address SEG: 0h and OFFSET: 8660h
  If the value of 2Ah is found, one of the NCBRs is in the error state. (The red LED on the basic unit remains OFF.)
  Using the function block COPY, the error is reset in the basic unit by writing the value 0h to the memory address 0:8660h.
- Access to the error as FK3 error number 17d with versions produced after April 1996 (07 KR 31 index J and 07 KT 31 index B).

07 KR 91 / 07 KT 92 / 07 KT 93 / 07 KT 94
- Access to the error with the function block COPY to the memory address SEG: C000h and OFFSET: 0100h
  If the value of 2Ah is found, one of the NCBRs is in the error state. (The red LED on the basic unit lights up.)
  Using the function block COPY, the error is reset in the basic unit by writing the value 0h to the memory address 0C00:0100h.

07 CS 61 (for T200)
- Version index c R202
  The error is seen as a bus error with no remote modules connected.
- Version index d R202 and the following:
  The error is set in bit 4 of the word MW 4104,03 for the first CS31 line, MW 4105,11 for the second CS31 line, MW 4107,03 for the third CS31 line and MW 4108,11 for the fourth CS31 line.

07 CS 91 (for T300 and UCZA/UCZB)
- A diagnosis is not available.
Error detection

- Check all CS31 system bus connections. The bus connectors BUS1 and BUS2 must not be reversed.
- Check the CS31 system bus.
  
In case of a short-circuit, all remote modules in the concerned bus segment do not work.

The NCBR only can detect an open-circuit if it is between the NCBR and the first remote module of the last segment between two NCBRs (in parallel or ring configuration).

After the error has been remedied, the NCBR acknowledges it automatically. The red LED goes out again.

Under normal conditions, a remote unit error is always detected by the basic unit.

---

Technical data

General data

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated supply voltage</td>
<td>24 V DC</td>
</tr>
<tr>
<td>Max. power dissipation inside the module</td>
<td>6 W</td>
</tr>
<tr>
<td>Max. delay between input and output signals</td>
<td>&lt; 2 µs</td>
</tr>
<tr>
<td>Isolation IN1 / IN2 / OUT1 / OUT2 and power supply (see Electrical Connection)</td>
<td>1000 V AC</td>
</tr>
</tbody>
</table>

Technical data of the system bus terminals "IN1/IN2"

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Interface standard</td>
<td>EIA RS-485</td>
</tr>
<tr>
<td>Terminating resistor</td>
<td>120 Ω, 1/4 W integrated</td>
</tr>
<tr>
<td>Electrical isolation</td>
<td>yes</td>
</tr>
</tbody>
</table>

Technical data of the system bus terminals "OUT1/OUT2"

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Interface standard</td>
<td>EIA RS-485</td>
</tr>
<tr>
<td>Terminating resistor</td>
<td>120 Ω, 1/4 W integrated</td>
</tr>
<tr>
<td>Electrical isolation</td>
<td>yes</td>
</tr>
</tbody>
</table>

LED displays

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Indication of the power supply voltage</td>
<td>1 green LED</td>
</tr>
<tr>
<td>Indication of communication</td>
<td>TXI</td>
</tr>
<tr>
<td></td>
<td>TXO</td>
</tr>
<tr>
<td>Indications CS31 system bus</td>
<td>4 red LEDs &quot;OUT1&quot;, &quot;OUT2&quot;, &quot;IN1&quot; and &quot;IN2&quot;</td>
</tr>
</tbody>
</table>

Mechanical data

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dimensions, width x height x depth</td>
<td>120 x 80 x 85 mm</td>
</tr>
<tr>
<td>Weight</td>
<td>340 g</td>
</tr>
<tr>
<td>Mounting on DIN rail</td>
<td>yes</td>
</tr>
</tbody>
</table>

Ordering data

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Order No.</td>
<td>NCBR</td>
</tr>
<tr>
<td></td>
<td>FPR3471300R1002</td>
</tr>
</tbody>
</table>

---
### Interface Cables

Interface cables for connection between peripheral units and the 9-pole serial interfaces of the basic units 07 KR 91, 07 KT 92, 07 KT 93 and 07 KT 94 (AC31) and the communication processors 07 KT 62 and 07 KP 64 (ABB Procontic T200):

<table>
<thead>
<tr>
<th>Type</th>
<th>Order number</th>
</tr>
</thead>
<tbody>
<tr>
<td>07 SK 90 R1</td>
<td>GJR5 2502 00 R0001</td>
</tr>
<tr>
<td>07 SK 91 R1</td>
<td>GJR5 2503 00 R0001</td>
</tr>
<tr>
<td>07 SK 92 R1</td>
<td>GJR5 2504 00 R0001</td>
</tr>
</tbody>
</table>
### 10.1 Interface Cables 07 SK 90 R1, 07 SK 91 R1 and 07 SK 92 R1

for connection of peripheral units to the 9-pole serial interfaces of the basic units 07 KR 91, 07 KT 92, 07 KT 93 and 07 KT 94 (Advant Controller 31) and the communication processors 07 KP 62 and 07 KP 64 (ABB Procontic T200) and 07 KP 90 (Advant Controller 31)

### 10.1.1 Survey table

The following table shows, which interface cables can be used for connections between peripheral units and the 9-pole interfaces of the AC31 basic units and the T200 communication processors.

<table>
<thead>
<tr>
<th>Connection from the processor unit interface</th>
<th>via the system cable (interface cable)</th>
<th>to the peripheral unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>07 KR 91 COM1</td>
<td>07 SK 90</td>
<td>PC programming unit with 907 PC 331</td>
</tr>
<tr>
<td>07 KT 9x COM1</td>
<td>07 SK 90</td>
<td>Operating station in active mode</td>
</tr>
<tr>
<td>07 KP 62 COM1</td>
<td>07 SK 90</td>
<td>Terminal</td>
</tr>
<tr>
<td>07 KR 91 COM1, 07 KT 9x COM1, 07 KP 62 COM1</td>
<td>07 SK 90</td>
<td>Operating station in passive mode</td>
</tr>
<tr>
<td>07 KP 64 CONSOLE, 07 KP 90 CONSOLE</td>
<td>07 SK 91</td>
<td></td>
</tr>
<tr>
<td>07 KR 91 COM1, 07 KT 9x COM1, 07 KT 9x COM2, 07 KP 62 COM1, 07 KP 62 COM2</td>
<td>07 SK 92</td>
<td>Modem with a standard interface, for signal names and pin assignment see chapter 10.4</td>
</tr>
</tbody>
</table>
### Intended purpose

The cable 07 SK 90 is used to connect a 9-pole interface connector of AC31 basic units or T200 communication processors with a peripheral unit in order to operate in programming or active mode (see 10.1.1 Survey table). If the peripheral has a 9-pole interface, the adaptor provided with (25-pole to 9-pole) can be employed for adaption.

### Mechanical design

**Plug 1**

SUB-D plug, 9-pole male on the side of 07 KP 6x, 07 KR 91, 07 KT 9x. The housing is metal-plated, the shield is connected to the metal plate.

**Plug 2**

SUB-D plug, 25-pole female, on the side of the peripheral unit. The plugs are mounted to both interfaces by means of screws.

### Cable type

LICYCY 5 x 0.14/15

### Adaptor provided

25-pole male/9-pole female for connection of peripheral units with 9-pole interfaces (male).

### Technical data

- **Length**: 5 m
- **Weight**: 220 g
- **Order number**: GJR5 2502 00 R1

### Terminal assignment of the 07 SK 90 interface cable and the adaptor provided with

**Fig. 10.2-2:** Terminal assignment of the 07 SK 90 interface cable and the adaptor provided with

---

**Fig. 10.2-1:** Interface cable 07 SK 90 R1 with adaptor
10.3 Interface cable 07 SK 91 R1 with adaptor

**Intended Purpose**

The cable 07 SK 91 is used to connect a 9-pole interface connector of AC31 basic units or T200 communication processors with a peripheral unit in order to operate in MMC mode or passive mode (see 10.1.1 Survey table). If the peripheral unit has a 9-pole connector, a commercially available adaptor (25-pole to 9-pole) has to be employed for adaption.

**Mechanical design**

**Plug 1**

SUB-D plug, 9-pole male on the side of 07 KP 6x, 07 KR 91, 07 KT 9x. The housing is metal-plated, the shield is connected to the metal plate.

**Plug 2**

SUB-D plug, 25-pole female on the side of the peripheral unit. The plugs are mounted to both interfaces by means of screws.

**Cable type**

LICYCY 5 x 0.14/15

**Adaptor provided**

25-pole male/25-pole male for connection of peripheral units with 25-pole interfaces (female).

**Technical data**

<table>
<thead>
<tr>
<th>Length</th>
<th>5 m</th>
</tr>
</thead>
<tbody>
<tr>
<td>Weight</td>
<td>220 g</td>
</tr>
<tr>
<td>Order number</td>
<td>GJR5 2503 00 R1</td>
</tr>
</tbody>
</table>

**Terminal assignment of the 07 SK 91 interface cable and the adaptor provided with**

---

**Advant Controller 31 / Issued: 10.98**

10-5 07 SK 90...07 SK 92
10.4 Interface cable 07 SK 92 R1

**Intended purpose**

The cable 07 SK 92 is used to connect a 9-pole interface connector of AC31 basic units or T200 communication processors to a modem with a standard interface (see 10.1.1 Survey table). If another modem has to be connected, the cable has to be modified possibly.

**Mechanical design**

**Plug 1**

SUB-D plug, 9-pole male on the side of 07 KP 6x, 07 KR 91, 07 KT 9x. The housing is metal-plated, the shield is connected to the metal plate.

**Plug 2**

SUB-D plug, 25-pole male on the side of the modem. The plugs are mounted to both interfaces by means of screws.

**Cable type**

LICYCY 5 x 0.14/15

**Technical data**

Length 5 m  
Weight 220 g  
Order number GJR5 2504 00 R1

---

Fig. 10.4-1: Interface cable 07 SK 92 R1

---

Fig. 10.4-4: Terminal assignment 07 SK 92

---

Fig. 10.2-4: Interface cable 07 SK 92 R1
## Accessories

<table>
<thead>
<tr>
<th>Type</th>
<th>Order number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power Supply Unit 07 NG 32, 24 V DC, 2.5 A</td>
<td>GJV3 0756 01 R1</td>
</tr>
<tr>
<td>Power Supply Unit 07 NG 34, 24 V DC, 5 A</td>
<td>GJV3 0756 02 R1</td>
</tr>
<tr>
<td>Power Supply Unit 07 NG 35, 24 V DC, 10 A</td>
<td>GJV3 0756 03 R1</td>
</tr>
<tr>
<td>Power Supply Unit 07 NG 36, 24 V DC, 20 A</td>
<td>GJV3 0756 04 R1</td>
</tr>
<tr>
<td>Lithium Battery Module 07 LE 90 R1 for use in processor units</td>
<td>GJR5250700R1</td>
</tr>
</tbody>
</table>
11.1 Power Supply Unit 07 NG 32 R1
primary voltage: 115/230 V AC, secondary voltage: 24 V DC, 2.5 A

The 07 NG 32 R1 power supply unit generates a 24 V DC voltage from a single-phase mains voltage of 115 V AC or 230 V AC. For applications in electronic control systems, the output voltage is smoothed by electrolytic capacitors. The power supply unit has a load capability of 2.5 A. A green LED indicates that the output voltage is present.

The primary and the secondary voltage are protected by built-in miniature fuses (5 x 20 mm). The electrical connections are made via screw-type terminals (see Fig. 11.1-1, Top view with circuit diagram imprint and terminal assignment). Cable grips fasten the cables. There is an electrical isolation between the primary and the secondary voltage according to VDE 0551 (safety electrical isolation).

The power supply unit has a mounting base which allows the user to snap the unit onto a DIN rail (EN 50022). If this mounting base is removed, the unit can be fastened by 4 screws M4 (see Fig. 11.1-2, drilling pattern).

The power supply unit must be mounted so that the convection air current is not disturbed.

---

![Diagram of the power supply unit](image-url)

The power supply unit has a height (depth if mounted on rear panel) of approx. 145 mm

Fig. 11.1-1: Top view with circuit diagram imprint and terminal assignment

Fig. 11.1-2: Drilling pattern and outline dimensions of the power supply unit
Technical data

Electrical data, input specifications

Primary voltage 115 V AC
- Rated voltage 115 V AC
- Limiting values 103.5...126.5 V AC
- Mains frequency 50 or 60 Hz
- Current consumption
  - with no load approx. 180 mA
  - with rated load approx. 900 mA
- Miniature fuse primary 2.5 A slow-acting, sand-filled

Primary voltage 230 V AC
- Rated voltage 230 V AC
- Limiting values 207...253 V AC
- Mains frequency 50 or 60 Hz
- Current consumption
  - Leerlauf approx. 90 mA
  - Nennlast approx. 450 mA
- Miniature fuse primary 1.6 A slow-acting, sand-filled (inserted by factory)

Max. conductor cross section of the terminals 2.5 mm²

Electrical data, output specifications

Secondary voltage (output voltage)
- Rated voltage 24 V DC
- Limiting values 19.2...30 V DC
- Max. ripple content ≤ 5 %
- Indication "voltage present" by green LED

Output load capability
- Rated current (permitted continuous load) 2.5 A
- Miniature fuse, secondary 4.0 A medium time-lag, sand-filled

Max. conductor cross section of the terminals 2.5 mm²; plus and minus poles are assigned to two terminals each (in parallel)

Mechanical data

Mounting onto a DIN rail or with 4 screws M4

Mechanical dimensions
- Mounting base 85 x 75 mm (120 mm), see Fig. 11.1-2, Drilling pattern
- Height (depth if mounted on rear panel) 145 mm

Weight 2.2 kg

Cooling The power supply unit must be mounted so that the convection air current is not disturbed.

Ambient temperature max. 55 °C (at 100 % load)

Standards, regulations VDE 0160, transformer according to VDE 0551

Ordering data

Order number 07 NG 32 R1 GJV3 0756 01 R1
11.2 Power Supply Unit 07 NG 34 R1
primary voltage: 115/230 V AC, secondary voltage: 24 V DC, 5 A

The 07 NG 34 R1 power supply unit generates a 24 V DC voltage from a single-phase mains voltage of 115 V AC or 230 V AC. For applications in electronic control systems, the output voltage is smoothed by electrolytic capacitors. The power supply unit has a load capability of 5 A. A green LED indicates that the output voltage is present.

The primary and the secondary voltage are protected by built-in miniature fuses (5 x 20 mm). The electrical connections are made via screw-type terminals (see Fig. 11.2-1, Top view with circuit diagram imprint and terminal assignment). Cable grips fasten the cables. There is an electrical isolation between the primary and the secondary voltage according to VDE 0551 (safety electrical isolation).

The power supply is fastened by 4 screws M5 (see Fig. 11.2-2, drilling pattern).

The power supply unit must be mounted so that the convection air current is not disturbed.

The power supply unit has a height (depth if mounted on rear panel) of approx. 145 mm

Fig. 11.2-1: Top view with circuit diagram imprint and terminal assignment
The power supply unit has a height (depth if mounted on rear panel) of approx. 145 mm

Fig. 11.2-2: Drilling pattern and outline dimensions of the power supply unit
Technical data

Electrical data, input specifications

Primary voltage **115 V AC**
- Rated voltage 115 V AC
- Limiting values 103.5...126.5 V AC
- Mains frequency 50 or 60 Hz
  - Current consumption with no load approx. 0.35 A
  - with rated load approx. 1.60 A
- Miniature fuse primary 4 A slow-acting, sand-filled

Primary voltage **230 V AC**
- Rated voltage 230 V AC
- Limiting values 207...253 V AC
- Mains frequency 50 or 60 Hz
  - Current consumption with no load approx. 0.17 A
  - with rated load approx. 0.85 A
- Miniature fuse, primary 2 A slow-acting, sand-filled (inserted by factory)
- Max. conductor cross section of the terminals 2.5 mm²

Electrical data, output specifications

Secondary voltage (output voltage)
- Rated voltage 24 V DC
- Limiting values 19.2...30 V DC
- Max. ripple content ≤ 5 %
- Indication "voltage present" by green LED

Output load capability
- Rated current (permitted continuous load) 5 A

Miniature fuse, secondary 8 A medium time-lag, sand-filled

Max. conductor cross section of the terminals 2.5 mm², plus and minus poles are assigned to two terminals each (in parallel)

Mechanical data

Mounting by 4 screws M5

Mechanical dimensions
- Mounting base 110 x 110 mm (135 mm), see Fig. 11.2-2, Drilling pattern
- Height (depth if mounted on rear panel) 145 mm

Weight 4 kg

Cooling The power supply unit must be mounted so that the convection air current is not disturbed.

Ambient temperature max. 55 °C (at 100 % load)

Standards, regulations VDE 0160, transformer according to VDE 0551

Ordering data

Order number 07 NG 34 R1 GJV3 0756 02 R1
The 07 NG 35 R1 power supply unit generates a 24 V DC voltage from a three-phase mains voltage of 230 V AC or 400 V AC. The output voltage is gained by using a 3-phase bridge-connected rectifier. Together with a filter capacitor, this guarantees a small ripple content of the voltage. The power supply unit has a load capability of 10 A. It is suitable for applications in electronic control systems. A green LED indicates that the output voltage is present.

The secondary voltage is protected by a built-in automatic circuit-breaker. The primary voltage (mains) has to be protected by external fuses.

The electrical connections are made via screw-type terminals. With 3-phase mains voltage of 230 V, the primary windings of the transformer are delta-connected, with 3-phase mains voltage of 400 V, the windings are star-connected (see Fig. 11.3-1). Cable grips fasten the cables. There is an electrical isolation between the primary and the secondary voltage according to VDE 0551 (safety electrical isolation).

The power supply is fastened by 4 screws M5 (see Fig. 11.3-2, Drilling pattern).

The power supply unit must be mounted so that the convection air current is not disturbed.

**Fig. 11.3-1:** Imprinted circuit diagram and terminal diagrams for 230 V und 400 V 3-phase AC
The power supply unit has a height (depth if mounted on rear panel) of approx. 145 mm.

Fig. 11.3-2: Drilling pattern and outline dimensions of the power supply unit
Technical data

Electrical data, input specifications

Primary voltage **230 V 3-phase AC**

- **Rated voltage**: 230 V 3-phase AC
- **Limiting values**: 207...253 V AC
- **Mains frequency**: 50 or 60 Hz
- **Current consumption**
  - with no load: approx. 0.22 A
  - with rated load: approx. 0.85 A
- **Fusing, primary**
  - external

Primary voltage **400 V 3-phase AC**

- **Rated voltage**: 400 V 3-phase AC
- **Limiting values**: 360...440 V AC
- **Mains frequency**: 50 or 60 Hz
- **Current consumption**
  - with no load: approx. 0.15 A
  - with rated load: approx. 0.50 A
- **Fusing, primary**
  - external

Max. conductor cross section of the terminals: 2 x 1.5 mm²

Electrical data, output specifications

Secondary voltage (output voltage)

- **Rated voltage**: 24 V DC
- **Limiting values**: 19.2...30 V DC
- **Max. ripple content**: ≤ 2 %
- **Indication "voltage present"**
  - by green LED

**Output load capability**

- **Rated current (permitted continuous load)**: 10 A

- **Fusing, secondary**: automatic circuit-breaker B 10 A

- **Max. conductor cross section of the terminals**: 2 x 4 mm², plus and minus poles are assigned to two terminals each (in parallel)

Mechanical data

- **Mounting**: by 4 screws M5
- **Mechanical dimensions**
  - Mounting base: 232 x 175 mm, see Fig. 11.3-2, Drilling pattern
  - Height (depth if mounted on rear panel): 125 mm
- **Weight**: 6 kg
- **Cooling**: The power supply unit must be mounted so that the convection air current is not disturbed.
- **Ambient temperature**: max. 55 °C (at 100 % load)
- **Standards, regulations**: VDE 0160, transformer according to VDE 0551

Ordering data

**Order number**

- 07 NG 35 R1
- GJV3 0756 03 R1
11.4  Power Supply Unit 07 NG 36 R1

primary voltage: 230/400 V 3-phase AC, secondary voltage: 24 V DC, 20 A

The 07 NG 36 power supply unit generates a 24 V DC voltage from a three-phase mains voltage of 230 V AC or 400 V AC. The output voltage is gained by using a 3-phase bridge-connected rectifier. Together with a filter capacitor, this guarantees a small ripple content of the voltage. The power supply unit has a load capability of 20 A. It is suitable for applications in electronic control systems. A green LED indicates that the output voltage is present.

The secondary voltage is protected by a built-in B-type automatic circuit-breaker. The primary voltage (mains) has to be protected by external fuses.

The electrical connections are made via screw-type terminals. With 3-phase mains voltage of 230 V, the primary windings of the transformer are delta-connected, with 3-phase mains voltage of 400 V, the windings are star-connected (see Fig. 11.4-1). Cable grips fasten the cables. There is an electrical isolation between the primary and the secondary voltage according to VDE 0551 (safety electrical isolation).

The power supply is fastened by 4 screws M6 (see Fig. 11.4-2, Drilling pattern).

The power supply unit must be mounted so that the convection air current is not disturbed.

Fig. 11.4-1:  Imprinted circuit diagram and terminal diagrams for 230 V and 400 V 3-phase AC
The power supply unit has a height (depth if mounted on rear panel) of approx. 136 mm

Fig. 11.4-2: Drilling pattern and outline dimensions of the power supply unit
Technical data

Electrical data, input specifications

Primary voltage 230 V 3-phase AC
- Rated voltage 230 V 3-phase AC
- Limiting values 207...253 V AC
- Mains frequency 50 or 60 Hz
- Current consumption with no load approx. 0.35 A
- Current consumption with rated load approx. 1.70 A
- Fusing, primary external

Primary voltage 400 V 3-phase AC
- Rated voltage 400 V 3-phase AC
- Limiting values 360...440 V AC
- Mains frequency 50 or 60 Hz
- Current consumption with no load approx. 0.25 A
- Current consumption with rated load approx. 1.00 A
- Fusing, primary external

Max. conductor cross section of the terminals 2 x 1.5 mm²

Electrical data, output specifications

Secondary voltage (output voltage)
- Rated voltage 24 V DC
- Limiting values 19.2...30 V DC
- Max. ripple content ≤ 2 %
- Indication "voltage present" by green LED

Output load capability
- Rated current (permitted continuous load) 20 A

Max. conductor cross section of the terminals 2 x 4 mm², plus and minus poles are assigned to two terminals each (in parallel)

Mechanical data

Mounting by 4 screws M6

Mechanical dimensions
- Mounting base 268 x 200 mm, see Fig. 11.4-2, Drilling pattern
- Height (depth if mounted on rear panel) 136 mm

Weight 15 kg

Cooling The power supply unit must be mounted so that the convection air current is not disturbed.

Ambient temperature max. 55 °C (at 100 % load)

Standards, regulations VDE 0160, transformer according to VDE 0551

Ordering data

Order number 07 NG 36 R1 GJV3 0756 04 R1
The 07 LE 90 R1 lithium battery module is used for RAM data back-up in several processor units of programmable control systems. It is equipped with a 2-pole plug and two soldered wires.

The following handling advice has to be taken into consideration:

- Use only genuine ABB lithium battery modules.
- At the end of lifetime, replace the battery module by a new one.
- **Do not short-circuit battery!** It may cause overheating or explosion. Prevent accidental short-circuit. Therefore, do not put battery into metallic boxes or on metallic surfaces.
- **Do not try to charge battery!** It may cause overheating or explosion!
- Replace battery only during the power is on. Otherwise you can loose data.
- Dispose of the battery environmentally acceptable!
- Pay attention to the battery monitoring facilities on the devices, e.g., LED indications, whether a battery is exhausted or missing. The battery lifetime depends on the unit where it is installed.

### Battery Lifetime

The value of the battery lifetime says how long the battery is able to back-up the stored data while the unit is not supplied by the internal voltages.

<table>
<thead>
<tr>
<th>Type of unit, where the battery is installed</th>
<th>Battery lifetime t (guaranteed values at 25°C)</th>
</tr>
</thead>
<tbody>
<tr>
<td>07 KP 62 R101 (ABB Procontic T200)</td>
<td>min. 5 000 h</td>
</tr>
<tr>
<td>07 KP 63 R101 (ABB Procontic T200)</td>
<td>min. 5 000 h</td>
</tr>
<tr>
<td>07 KR 91 (Advant Controller 31)</td>
<td>min. 4 200 h</td>
</tr>
<tr>
<td>07 KT 9x (Advant Controller 31)</td>
<td>min. 4 200 h</td>
</tr>
</tbody>
</table>

### Technical Data

- **Capacity**: 1000 mAh
- **No-load voltage**: 3.6 V
- **Rated voltage**: 3.5 V
- **Temperature coefficient of rated voltage**: approx. – 1 mV/K
- **Temperature coefficient of capacity**: < – 1.5 % at 0...70 °C
- **Self discharge**: < 3.0 % per year at 25 °C
  < 6.0 % per year at 40 °C
  < 25.0 % per year at 70 °C
- **Weight**: 20 g
- **Dimensions**: 18 mm x 53 mm
- **Order number**: GJR5250700R1