Relion® 670 SERIES

Transformer protection RET670
Version 2.2 ANSI
Commissioning manual
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This product complies with the directive of the Council of the European Communities on the approximation of the laws of the Member States relating to electromagnetic compatibility (EMC Directive 2004/108/EC) and concerning electrical equipment for use within specified voltage limits (Low-voltage directive 2006/95/EC). This conformity is the result of tests conducted by ABB in accordance with the product standard EN 60255-26 for the EMC directive, and with the product standards EN 60255-1 and EN 60255-27 for the low voltage directive. The product is designed in accordance with the international standards of the IEC 60255 series and ANSI C37.90. The DNP protocol implementation in the IED conforms to "DNP3 Intelligent Electronic Device (IED) Certification Procedure Subset Level 2", available at www.dnp.org.
# Table of contents

<table>
<thead>
<tr>
<th>Section 1</th>
<th>Introduction ............................................................................................................................................</th>
<th>13</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.1</td>
<td>This manual .......................................................................................................................................</td>
<td>13</td>
</tr>
<tr>
<td>1.2</td>
<td>Intended audience ..........................................................................................................................</td>
<td>13</td>
</tr>
<tr>
<td>1.3</td>
<td>Product documentation ...................................................................................................................</td>
<td>14</td>
</tr>
<tr>
<td>1.3.1</td>
<td>Product documentation set ...........................................................................................................</td>
<td>14</td>
</tr>
<tr>
<td>1.3.2</td>
<td>Document revision history ............................................................................................................</td>
<td>15</td>
</tr>
<tr>
<td>1.3.3</td>
<td>Related documents .........................................................................................................................</td>
<td>15</td>
</tr>
<tr>
<td>1.4</td>
<td>Document symbols and conventions ...............................................................................................</td>
<td>16</td>
</tr>
<tr>
<td>1.4.1</td>
<td>Symbols ..............................................................................................................................................</td>
<td>16</td>
</tr>
<tr>
<td>1.4.2</td>
<td>Document conventions .......................................................................................................................</td>
<td>17</td>
</tr>
<tr>
<td>1.5</td>
<td>IEC 61850 edition 1 / edition 2 mapping .....................................................................................</td>
<td>17</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Section 2</th>
<th>Safety information ..........................................................................................................................</th>
<th>27</th>
</tr>
</thead>
<tbody>
<tr>
<td>2.1</td>
<td>Symbols on the product .................................................................................................................</td>
<td>27</td>
</tr>
<tr>
<td>2.2</td>
<td>Warnings ...........................................................................................................................................</td>
<td>27</td>
</tr>
<tr>
<td>2.3</td>
<td>Caution signs ...................................................................................................................................</td>
<td>28</td>
</tr>
<tr>
<td>2.4</td>
<td>Note signs .........................................................................................................................................</td>
<td>29</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Section 3</th>
<th>Available functions .........................................................................................................................</th>
<th>31</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.1</td>
<td>Main protection functions .............................................................................................................</td>
<td>31</td>
</tr>
<tr>
<td>3.2</td>
<td>Back-up protection functions .......................................................................................................</td>
<td>32</td>
</tr>
<tr>
<td>3.3</td>
<td>Control and monitoring functions ..............................................................................................</td>
<td>34</td>
</tr>
<tr>
<td>3.4</td>
<td>Communication ...............................................................................................................................</td>
<td>41</td>
</tr>
<tr>
<td>3.5</td>
<td>Basic IED functions .....................................................................................................................</td>
<td>43</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Section 4</th>
<th>Starting up .......................................................................................................................................</th>
<th>45</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.1</td>
<td>Factory and site acceptance testing ...........................................................................................</td>
<td>45</td>
</tr>
<tr>
<td>4.2</td>
<td>Commissioning checklist .............................................................................................................</td>
<td>45</td>
</tr>
<tr>
<td>4.3</td>
<td>Checking the power supply .........................................................................................................</td>
<td>46</td>
</tr>
<tr>
<td>4.4</td>
<td>Energizing the IED .......................................................................................................................</td>
<td>46</td>
</tr>
<tr>
<td>4.4.1</td>
<td>Checking the IED operation .........................................................................................................</td>
<td>46</td>
</tr>
<tr>
<td>4.4.2</td>
<td>IED start-up sequence ..................................................................................................................</td>
<td>46</td>
</tr>
<tr>
<td>4.5</td>
<td>Setting up communication between PCM600 and the IED ...........................................................</td>
<td>47</td>
</tr>
<tr>
<td>4.6</td>
<td>Writing an application configuration to the IED .........................................................................</td>
<td>52</td>
</tr>
<tr>
<td>4.7</td>
<td>Checking CT circuits .....................................................................................................................</td>
<td>52</td>
</tr>
<tr>
<td>4.8</td>
<td>Checking VT circuits .....................................................................................................................</td>
<td>53</td>
</tr>
<tr>
<td>4.9</td>
<td>Using the RTXP test switch ..........................................................................................................</td>
<td>53</td>
</tr>
<tr>
<td>4.10</td>
<td>Checking the binary input/output circuits ..................................................................................</td>
<td>54</td>
</tr>
<tr>
<td>4.10.1</td>
<td>Binary input circuits .....................................................................................................................</td>
<td>54</td>
</tr>
</tbody>
</table>
Section 11  Testing functionality by secondary injection

11.1  Testing disturbance report
11.1.1  Introduction ................................................................. 97
11.1.2  Disturbance report settings ........................................... 97
11.1.3  Disturbance recorder (DR) ............................................. 97
11.1.4  Event recorder (ER) and Event list (EL) ......................... 98

11.2  Identifying the function to test in the technical reference manual ........................................ 98
11.3  Differential protection .................................................... 99
11.3.1  Transformer differential protection T2WPDIF and T3WPDIF(87T) ........................................ 99
11.3.1.1  Verifying the settings .................................................. 99
11.3.1.2  Completing the test .................................................... 100
11.3.2  High impedance differential protection HZPDIF (87) ........ 100
11.3.2.1  Verifying the settings .................................................. 100
11.3.2.2  Completing the test .................................................... 100
11.3.3  Restricted earth fault protection, low impedance REFPDIF (87N) ........................................ 101
11.3.3.1  Verifying the settings .................................................. 101
11.3.3.2  Completing the test .................................................... 101
11.3.4  Additional security logic for differential protection LDRGFC (11) ........................................ 101
11.3.4.1  Verifying the settings .................................................. 101
11.3.4.2  Completing the test .................................................... 103
11.3.5  Self-adaptive differential protection for two-winding power transformers PSTPDIF (87T) .... 103
11.3.5.1  Preparing the function for testing .................................. 103
11.3.5.2  Verifying the zero-sequence current reduction from the winding one side ..................... 104
<table>
<thead>
<tr>
<th>Section</th>
<th>Description</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>11.3.5.3</td>
<td>Verifying the three-phase pickup from winding one side</td>
<td>105</td>
</tr>
<tr>
<td>11.3.5.4</td>
<td>Verifying the single-phase pickup from winding one side</td>
<td>105</td>
</tr>
<tr>
<td>11.3.5.5</td>
<td>Verifying the zero-sequence current reduction from winding two side</td>
<td>106</td>
</tr>
<tr>
<td>11.3.5.6</td>
<td>Verifying the three-phase pickup from winding two side</td>
<td>106</td>
</tr>
<tr>
<td>11.3.5.7</td>
<td>Verifying the single-phase pickup from winding two side</td>
<td>107</td>
</tr>
<tr>
<td>11.3.5.8</td>
<td>Verifying the operate characteristic</td>
<td>108</td>
</tr>
<tr>
<td>11.3.5.9</td>
<td>Completing the test</td>
<td>109</td>
</tr>
<tr>
<td>11.4</td>
<td>Impedance protection</td>
<td>109</td>
</tr>
<tr>
<td>11.4.1</td>
<td>Distance protection zones, quadrilateral characteristic ZMQPDIS (21)</td>
<td>109</td>
</tr>
<tr>
<td>11.4.1.1</td>
<td>Measuring the operating limit of set values</td>
<td>112</td>
</tr>
<tr>
<td>11.4.1.2</td>
<td>Measuring the operating time of distance protection zones</td>
<td>113</td>
</tr>
<tr>
<td>11.4.1.3</td>
<td>Completing the test</td>
<td>113</td>
</tr>
<tr>
<td>11.4.2</td>
<td>Phase selection, quad, fixed angle, load encroachment FDPSPDIS (21)</td>
<td>113</td>
</tr>
<tr>
<td>11.4.2.1</td>
<td>Measuring the operating limit of set values</td>
<td>116</td>
</tr>
<tr>
<td>11.4.2.2</td>
<td>Completing the test</td>
<td>116</td>
</tr>
<tr>
<td>11.4.3</td>
<td>Full scheme distance protection, mho characteristic ZMHPDIS (21)</td>
<td>116</td>
</tr>
<tr>
<td>11.4.3.1</td>
<td>Phase-to-phase faults</td>
<td>117</td>
</tr>
<tr>
<td>11.4.3.2</td>
<td>Phase-to-ground faults</td>
<td>117</td>
</tr>
<tr>
<td>11.4.4</td>
<td>Faulty phase identification with load encroachment FMPSPDIS (21)</td>
<td>118</td>
</tr>
<tr>
<td>11.4.5</td>
<td>Distance protection zones, quadrilateral characteristic, separate settings ZMRPDIS (21)</td>
<td>118</td>
</tr>
<tr>
<td>11.4.5.1</td>
<td>Measuring the operating limit of set values</td>
<td>122</td>
</tr>
<tr>
<td>11.4.5.2</td>
<td>Measuring the operating time of distance protection zones</td>
<td>122</td>
</tr>
<tr>
<td>11.4.6</td>
<td>Phase selection, quadrilateral characteristic with settable angle FRPSPDIS (21)</td>
<td>123</td>
</tr>
<tr>
<td>11.4.6.1</td>
<td>Measuring the operating limit of set values</td>
<td>125</td>
</tr>
<tr>
<td>11.4.6.2</td>
<td>Completing the test</td>
<td>125</td>
</tr>
<tr>
<td>11.4.7</td>
<td>High speed distance protection zones, quadrilateral and mho characteristic ZMFPDIS (21)</td>
<td>126</td>
</tr>
<tr>
<td>11.4.7.1</td>
<td>Measuring the operating limit of set values</td>
<td>129</td>
</tr>
<tr>
<td>11.4.7.2</td>
<td>Measuring the operating time of distance protection zones</td>
<td>130</td>
</tr>
<tr>
<td>11.4.7.3</td>
<td>Completing the test</td>
<td>130</td>
</tr>
<tr>
<td>11.4.8</td>
<td>High speed distance for series compensated line zones, quadrilateral and mho characteristic ZMFCPDIS (21)</td>
<td>130</td>
</tr>
<tr>
<td>11.4.8.1</td>
<td>Measuring the operating limit of set values</td>
<td>133</td>
</tr>
<tr>
<td>11.4.8.2</td>
<td>Measuring the operating time of distance protection zones</td>
<td>134</td>
</tr>
<tr>
<td>11.4.8.3</td>
<td>Completing the test</td>
<td>134</td>
</tr>
<tr>
<td>11.4.9</td>
<td>Power swing detection ZMRPSB (68)</td>
<td>134</td>
</tr>
<tr>
<td>11.4.9.1</td>
<td>Verifying the signal and settings</td>
<td>135</td>
</tr>
<tr>
<td>11.4.9.2</td>
<td>Completing the test</td>
<td>141</td>
</tr>
<tr>
<td>11.4.10</td>
<td>Power swing logic PSLPSCH</td>
<td>141</td>
</tr>
<tr>
<td>11.4.10.1</td>
<td>Testing the carrier send and trip signals</td>
<td>141</td>
</tr>
<tr>
<td>11.4.10.2</td>
<td>Testing the influence of the residual overcurrent protection</td>
<td>142</td>
</tr>
<tr>
<td>11.4.10.3</td>
<td>Checking the underreaching zone</td>
<td>142</td>
</tr>
</tbody>
</table>
11.4.10.4 Completing the test..................................................................................................................... 142
11.4.11 Pole slip protection PSPPPAM (78).................................................................................................. 143
11.4.11.1 Verifying the settings.................................................................................................................. 143
11.4.11.2 Completing the test.................................................................................................................. 145
11.4.12 Out-of-step protection OOSPPAM.................................................................................................. 145
11.4.12.1 Verifying the settings.................................................................................................................. 146
11.4.12.2 Test of point RE ($R_{FwdR}, X_{FwdX}$)........................................................................................ 150
11.4.12.3 Test of the boundary between zone 1 and zone 2, which is defined by the parameter $ReachZ1$.................................................................................................................. 154
11.4.12.4 Test of the point SE ($R_{RvsR}, X_{RvsX}$)..................................................................................... 158
11.4.13 Automatic switch onto fault logic ZCVP Sof .............................................................................. 162
11.4.13.1 Activating ZCVP Sof externally................................................................................................. 162
11.4.13.2 Initiating ZCVP Sof automatically and setting mode to impedance........................................ 163
11.4.13.3 Initiating ZCVP Sof automatically and setting mode to VILevel.............................................. 163
11.4.13.4 Completing the test.................................................................................................................. 163
11.4.14 Phase preference logic PPLPHIZ .................................................................................................. 163
11.4.14.1 Completing the test.................................................................................................................. 164
11.4.15 Phase preference logic PPL2PHIZ................................................................................................. 164
11.4.16 Completing the test....................................................................................................................... 165
11.4.17 Under impedance protection for Generator ZGVPDIS............................................................... 165
11.4.17.1 Verifying the settings................................................................................................................ 165
11.4.17.2 Completing the test.................................................................................................................. 168
11.5 Current protection............................................................................................................................... 168
11.5.1 Instantaneous phase overcurrent protection 3-phase output PHPIOC (50)................................. 168
11.5.1.1 Measuring the trip limit of set values........................................................................................... 169
11.5.1.2 Completing the test.................................................................................................................. 169
11.5.2 Directional phase overcurrent protection, four steps OC4PTOC (51-67).................................. 170
11.5.2.1 Verifying the settings................................................................................................................ 169
11.5.2.2 Completing the test.................................................................................................................. 170
11.5.3 Instantaneous residual overcurrent protection EFPIOC (50N)................................................... 170
11.5.3.1 Measuring the trip limit of set values......................................................................................... 171
11.5.3.2 Completing the test.................................................................................................................. 171
11.5.4 Four step residual overcurrent protection, (Zero sequence or negative sequence directionality) EF4PTOC (51N/67N)........................................................................................................ 171
11.5.4.1 Four step directional ground fault protection............................................................................. 172
11.5.4.2 Four step non-directional ground fault protection........................................................................ 172
11.5.4.3 Completing the test.................................................................................................................. 172
11.5.5 Four step negative sequence overcurrent protection NS4PTOC (46I2)...................................... 172
11.5.5.1 Completing the test.................................................................................................................. 173
11.5.6 Sensitive directional residual overcurrent and power protection SDEPSDE (67N).................... 173
11.5.6.1 Measuring the trip and time limit for set values......................................................................... 174
11.5.6.2 Completing the test.................................................................................................................. 178
<p>| 11.5.7 | Thermal overload protection, one time constant, Fahrenheit/Celsius LFPTTR/LCPTTR (26) | 179 |
| 11.5.7.1 | Measuring the trip and time limit of set values | 179 |
| 11.5.7.2 | Completing the test | 179 |
| 11.5.8 | Thermal overload protection, two time constants TRPTTR (49) | 180 |
| 11.5.8.1 | Checking trip and reset values | 180 |
| 11.5.8.2 | Completing the test | 180 |
| 11.5.9 | Breaker failure protection, phase segregated activation and output CCRBRF (50BF) | 181 |
| 11.5.9.1 | Checking the phase current trip value, Pickup_PH | 181 |
| 11.5.9.2 | Checking the residual (ground fault) current trip value Pickup_N set below Pickup_PH | 181 |
| 11.5.9.3 | Checking the re-trip and back-up times | 182 |
| 11.5.9.4 | Verifying the re-trip mode | 182 |
| 11.5.9.5 | Verifying the back-up trip mode | 183 |
| 11.5.9.6 | Verifying instantaneous back-up trip at CB faulty condition | 183 |
| 11.5.9.7 | Verifying the case FunctionMode = CB Pos | 184 |
| 11.5.9.8 | Verifying the case FunctionMode = Current or CB Pos | 184 |
| 11.5.9.9 | Verifying the external start signal has timed out | 185 |
| 11.5.9.10 | Verifying that backup signal is released when STALARM is reset | 185 |
| 11.5.9.11 | Test of FollowStart&amp;Mode behaviour | 185 |
| 11.5.9.12 | Test of FollowStart behaviour | 185 |
| 11.5.9.13 | Completing the test | 186 |
| 11.5.10 | Stub protection STBPTOC (50STB) | 186 |
| 11.5.10.1 | Measuring the trip limit of set values | 186 |
| 11.5.10.2 | Completing the test | 186 |
| 11.5.11 | Pole discrepancy protection CCPDSC (52PD) | 187 |
| 11.5.11.1 | Verifying the settings | 187 |
| 11.5.11.2 | Completing the test | 187 |
| 11.5.12 | Directional underpower protection GUPPDUP (37) | 188 |
| 11.5.12.1 | Verifying the settings | 188 |
| 11.5.12.2 | Completing the test | 189 |
| 11.5.13 | Directional overpower protection GOPPDOP (32) | 189 |
| 11.5.13.1 | Verifying the settings | 189 |
| 11.5.13.2 | Completing the test | 190 |
| 11.5.14 | Broken conductor check BRCTOC (46) | 190 |
| 11.5.14.1 | Measuring the trip and time limit of set values | 190 |
| 11.5.14.2 | Completing the test | 191 |
| 11.5.15 | Capacitor bank protection CBPGAP | 191 |
| 11.5.15.1 | Verifying the settings and operation of the function | 191 |
| 11.5.15.2 | Completing the test | 195 |
| 11.5.16 | Negative-sequence time overcurrent protection for machines NS2PTOC (46I2) | 195 |
| 11.5.16.1 | Verifying settings by secondary injection | 195 |
| 11.5.16.2 | Completing the test | 196 |</p>
<table>
<thead>
<tr>
<th>Section</th>
<th>Description</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>11.8.1.6</td>
<td>Completing the test</td>
<td>214</td>
</tr>
<tr>
<td>11.9</td>
<td>Secondary system supervision</td>
<td>214</td>
</tr>
<tr>
<td>11.9.1</td>
<td>Current circuit supervision CCSSPVC (87)</td>
<td>214</td>
</tr>
<tr>
<td>11.9.1.1</td>
<td>Verifying the settings</td>
<td>215</td>
</tr>
<tr>
<td>11.9.1.2</td>
<td>Completing the test</td>
<td>215</td>
</tr>
<tr>
<td>11.9.2</td>
<td>Fuse failure supervision FUFSVPVC</td>
<td>215</td>
</tr>
<tr>
<td>11.9.2.1</td>
<td>Checking that the binary inputs and outputs trip as expected</td>
<td>215</td>
</tr>
<tr>
<td>11.9.2.2</td>
<td>Measuring the trip value for the negative sequence function</td>
<td>216</td>
</tr>
<tr>
<td>11.9.2.3</td>
<td>Measuring the trip value for the zero-sequence function</td>
<td>217</td>
</tr>
<tr>
<td>11.9.2.4</td>
<td>Measuring the trip value for the dead line detection function</td>
<td>217</td>
</tr>
<tr>
<td>11.9.2.5</td>
<td>Checking the operation of the dv/dt and di/dt based function</td>
<td>218</td>
</tr>
<tr>
<td>11.9.2.6</td>
<td>Completing the test</td>
<td>218</td>
</tr>
<tr>
<td>11.9.3</td>
<td>Fuse failure supervision</td>
<td>218</td>
</tr>
<tr>
<td>11.9.3.1</td>
<td>Completing the test</td>
<td>219</td>
</tr>
<tr>
<td>11.9.4</td>
<td>Voltage based delta supervision DELVSPVC</td>
<td>219</td>
</tr>
<tr>
<td>11.9.4.1</td>
<td>Verifying the signals and settings</td>
<td>219</td>
</tr>
<tr>
<td>11.9.4.2</td>
<td>Completing the test</td>
<td>220</td>
</tr>
<tr>
<td>11.9.5</td>
<td>Current based delta supervision DELISPVC(?I)</td>
<td>220</td>
</tr>
<tr>
<td>11.9.5.1</td>
<td>Verifying the signals and settings</td>
<td>220</td>
</tr>
<tr>
<td>11.9.5.2</td>
<td>Completing the test</td>
<td>221</td>
</tr>
<tr>
<td>11.9.6</td>
<td>Delta supervision of real input DELSPVC</td>
<td>221</td>
</tr>
<tr>
<td>11.9.6.1</td>
<td>Verifying the signals and settings</td>
<td>221</td>
</tr>
<tr>
<td>11.9.6.2</td>
<td>Completing the test</td>
<td>222</td>
</tr>
<tr>
<td>11.10</td>
<td>Control</td>
<td>222</td>
</tr>
<tr>
<td>11.10.1</td>
<td>Synchrocheck, energizing check, and synchronizing SESRSYN (25)</td>
<td>222</td>
</tr>
<tr>
<td>11.10.1.1</td>
<td>Testing the synchronizing function</td>
<td>224</td>
</tr>
<tr>
<td>11.10.1.2</td>
<td>Testing the synchrocheck functionality</td>
<td>224</td>
</tr>
<tr>
<td>11.10.1.3</td>
<td>Testing the energizing check</td>
<td>226</td>
</tr>
<tr>
<td>11.10.1.4</td>
<td>Testing the voltage selection</td>
<td>227</td>
</tr>
<tr>
<td>11.10.1.5</td>
<td>Completing the test</td>
<td>229</td>
</tr>
<tr>
<td>11.10.2</td>
<td>Apparatus control APC</td>
<td>230</td>
</tr>
<tr>
<td>11.10.3</td>
<td>Voltage control TR1ATCC, TR8ATCC, TCMLTTC, TCMQLTTC</td>
<td>230</td>
</tr>
<tr>
<td>11.10.3.1</td>
<td>Secondary test</td>
<td>231</td>
</tr>
<tr>
<td>11.10.3.2</td>
<td>Check the activation of the voltage control operation</td>
<td>232</td>
</tr>
<tr>
<td>11.10.3.3</td>
<td>Check the normal voltage regulation function</td>
<td>232</td>
</tr>
<tr>
<td>11.10.3.4</td>
<td>Check the undervoltage block function</td>
<td>233</td>
</tr>
<tr>
<td>11.10.3.5</td>
<td>Check the upper and lower busbar voltage limit</td>
<td>233</td>
</tr>
<tr>
<td>11.10.3.6</td>
<td>Check the overcurrent block function</td>
<td>233</td>
</tr>
<tr>
<td>11.10.3.7</td>
<td>Single transformer</td>
<td>234</td>
</tr>
<tr>
<td>11.10.3.8</td>
<td>Parallel voltage regulation</td>
<td>235</td>
</tr>
<tr>
<td>11.10.3.9</td>
<td>Completing the test</td>
<td>238</td>
</tr>
<tr>
<td>11.10.4</td>
<td>Single command, 16 signals SINGLECMD</td>
<td>238</td>
</tr>
<tr>
<td>11.10.5</td>
<td>Interlocking</td>
<td>239</td>
</tr>
</tbody>
</table>
11.11 Scheme communication.................................................................................................................. 239
11.11.1 Scheme communication logic for distance or overcurrent protection ZCPSCH (85)............ 239
11.11.1.1 Testing permissive underreaching............................................................................................. 239
11.11.1.2 Testing permissive overreaching............................................................................................... 240
11.11.1.3 Testing blocking scheme........................................................................................................... 240
11.11.1.4 Testing delta blocking scheme.................................................................................................. 240
11.11.1.5 Checking of unblocking logic.................................................................................................... 241
11.11.1.6 Completing the test.................................................................................................................. 241
11.11.2 Phase segregated scheme communication logic for distance or overcurrent protection ZC1PPSCH (85).................................................................................................................. 241
11.11.2.1 Testing permissive underreaching............................................................................................. 241
11.11.2.2 Testing permissive overreaching............................................................................................... 242
11.11.2.3 Testing blocking scheme........................................................................................................... 242
11.11.2.4 Completing the test.................................................................................................................. 242
11.11.3 Current reversal and Weak-end infeed logic for distance protection 3-phase ZCRWPSCH (85)................................................................................................................................. 243
11.11.3.1 Current reversal logic................................................................................................................ 243
11.11.3.2 Weak end infeed logic.............................................................................................................. 243
11.11.3.3 Completing the test.................................................................................................................. 244
11.11.4 Scheme communication logic for residual overcurrent protection ECPSPCH (85)................. 244
11.11.4.1 Testing the directional comparison logic function............................................................... 245
11.11.4.2 Completing the test.................................................................................................................. 246
11.11.5 Current reversal and weak-end infeed logic for residual overcurrent protection ECRWPSCH (85)................................................................................................................................. 246
11.11.5.1 Testing the current reversal logic............................................................................................. 246
11.11.5.2 Testing the weak-end infeed logic............................................................................................ 247
11.11.5.3 Completing the test.................................................................................................................. 248
11.12 Logic.................................................................................................................................................. 248
11.12.1 Tripping logic, common 3-phase output SMPPTRC (94)....................................................... 248
11.12.1.1 3 phase operating mode........................................................................................................... 248
11.12.1.2 1p/3p operating mode............................................................................................................... 249
11.12.1.3 Circuit breaker lockout............................................................................................................. 249
11.12.1.4 Completing the test.................................................................................................................. 250
11.13 Monitoring..................................................................................................................................... 250
11.13.1 Gas medium supervision SSIMG............................................................................................. 250
11.13.1.1 Testing the gas medium supervision for pressure alarm and pressure lockout conditions........ 250
11.13.1.2 Testing the gas medium supervision for temperature alarm and temperature lockout conditions.......................... 251
11.13.1.3 Completing the test................................................................................................................ 251
11.13.2 Liquid medium supervision SSIML............................................................................................ 251
11.13.2.1 Testing the liquid medium supervision for level alarm and level lockout conditions............. 251
11.13.2.2 Testing the liquid medium supervision for temperature alarm and temperature lockout conditions...... 252
# Table of contents

<table>
<thead>
<tr>
<th>Section</th>
<th>Description</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>11.13.2.3</td>
<td>Completing the test</td>
<td>252</td>
</tr>
<tr>
<td>11.13.3</td>
<td>Breaker monitoring SSCBR</td>
<td>252</td>
</tr>
<tr>
<td>11.13.3.1</td>
<td>Verifying the settings</td>
<td>252</td>
</tr>
<tr>
<td>11.13.3.2</td>
<td>Completing the test</td>
<td>254</td>
</tr>
<tr>
<td>11.13.4</td>
<td>Event function EVENT</td>
<td>254</td>
</tr>
<tr>
<td>11.13.5</td>
<td>Fault locator LMBRFLO</td>
<td>254</td>
</tr>
<tr>
<td>11.13.5.1</td>
<td>Measuring the operate limit</td>
<td>255</td>
</tr>
<tr>
<td>11.13.5.2</td>
<td>Completing the test</td>
<td>256</td>
</tr>
<tr>
<td>11.13.6</td>
<td>Limit counter L4UFCNT</td>
<td>256</td>
</tr>
<tr>
<td>11.13.6.1</td>
<td>Completing the test</td>
<td>256</td>
</tr>
<tr>
<td>11.13.7</td>
<td>Estimation of transformer insulation life LOLSPT (26/49HS)</td>
<td>256</td>
</tr>
<tr>
<td>11.13.7.1</td>
<td>Verifying the signals and settings</td>
<td>256</td>
</tr>
<tr>
<td>11.13.7.2</td>
<td>Completing the test</td>
<td>257</td>
</tr>
<tr>
<td>11.13.8</td>
<td>Through fault monitoring PTRSTHR (51TF)</td>
<td>258</td>
</tr>
<tr>
<td>11.13.8.1</td>
<td>Verifying the signals and settings</td>
<td>258</td>
</tr>
<tr>
<td>11.13.8.2</td>
<td>Completing the test</td>
<td>259</td>
</tr>
<tr>
<td>11.13.9</td>
<td>Current harmonic monitoring CHMMHAI (ITHD)</td>
<td>259</td>
</tr>
<tr>
<td>11.13.9.1</td>
<td>Verifying the signals and settings</td>
<td>259</td>
</tr>
<tr>
<td>11.13.9.2</td>
<td>Completing the test</td>
<td>260</td>
</tr>
<tr>
<td>11.13.10</td>
<td>Voltage harmonic monitoring VHMMHAI (VTHD)</td>
<td>260</td>
</tr>
<tr>
<td>11.13.10.1</td>
<td>Verifying the signals and settings</td>
<td>260</td>
</tr>
<tr>
<td>11.13.10.2</td>
<td>Completing the test</td>
<td>260</td>
</tr>
<tr>
<td>11.14</td>
<td>Metering</td>
<td>261</td>
</tr>
<tr>
<td>11.14.1</td>
<td>Pulse-counter logic PCFCNT</td>
<td>261</td>
</tr>
<tr>
<td>11.14.2</td>
<td>Function for energy calculation and demand handling ETPMTR</td>
<td>261</td>
</tr>
<tr>
<td>11.14.2.1</td>
<td>Verifying the settings</td>
<td>261</td>
</tr>
<tr>
<td>11.14.2.2</td>
<td>Completing the test</td>
<td>262</td>
</tr>
<tr>
<td>11.15</td>
<td>Station communication</td>
<td>262</td>
</tr>
<tr>
<td>11.15.1</td>
<td>Multiple command and transmit MULTICMDRCV / MULTICMDSND</td>
<td>262</td>
</tr>
<tr>
<td>11.16</td>
<td>Remote communication</td>
<td>262</td>
</tr>
<tr>
<td>11.16.1</td>
<td>Binary signal transfer</td>
<td>262</td>
</tr>
<tr>
<td>11.17</td>
<td>Basic IED functions</td>
<td>264</td>
</tr>
<tr>
<td>11.17.1</td>
<td>Parameter setting group handling SETGRPS</td>
<td>264</td>
</tr>
<tr>
<td>11.17.1.1</td>
<td>Verifying the settings</td>
<td>264</td>
</tr>
<tr>
<td>11.17.1.2</td>
<td>Completing the test</td>
<td>265</td>
</tr>
<tr>
<td>11.18</td>
<td>Exit test mode</td>
<td>265</td>
</tr>
</tbody>
</table>

## Section 12  Primary injection testing

12.1 Transformer Voltage Control ATCC | 267 |
12.1.1 Load drop compensation function, LDC | 267 |
12.1.2 Voltage control of Parallel Transformers | 268 |
12.1.3 Minimum Circulating Current (MCC) method | 269 |
12.1.4 Master Follower (MF) method | 270
Section 1 Introduction

1.1 This manual

The commissioning manual contains instructions on how to commission the IED. The manual can also be used by system engineers and maintenance personnel for assistance during the testing phase. The manual provides procedures for the checking of external circuitry and energizing the IED, parameter setting and configuration as well as verifying settings by secondary injection. The manual describes the process of testing an IED in a substation which is not in service. The chapters are organized in the chronological order in which the IED should be commissioned. The relevant procedures may be followed also during the service and maintenance activities.

1.2 Intended audience

This manual addresses the personnel responsible for commissioning, maintenance and taking the IED in and out of normal service.

The commissioning personnel must have a basic knowledge of handling electronic equipment. The commissioning and maintenance personnel must be well experienced in using protection equipment, test equipment, protection functions and the configured functional logics in the IED.
1.3 Product documentation

1.3.1 Product documentation set

The engineering manual contains instructions on how to engineer the IEDs using the various tools available within the PCM600 software. The manual provides instructions on how to set up a PCM600 project and insert IEDs to the project structure. The manual also recommends a sequence for the engineering of protection and control functions, as well as communication engineering for IEC 61850.

The installation manual contains instructions on how to install the IED. The manual provides procedures for mechanical and electrical installation. The chapters are organized in the chronological order in which the IED should be installed.

The commissioning manual contains instructions on how to commission the IED. The manual can also be used by system engineers and maintenance personnel for assistance during the testing phase. The manual provides procedures for the checking of external circuitry and energizing the IED, parameter setting and configuration as well as verifying settings by secondary injection. The manual describes the process of testing an IED in a substation which is not in service. The chapters are organized in the chronological order in which the IED should be commissioned. The relevant procedures may be followed also during the service and maintenance activities.

Figure 1: The intended use of manuals throughout the product lifecycle

IEC07000220-4-en.vsd
The operation manual contains instructions on how to operate the IED once it has been commissioned. The manual provides instructions for the monitoring, controlling and setting of the IED. The manual also describes how to identify disturbances and how to view calculated and measured power grid data to determine the cause of a fault.

The application manual contains application descriptions and setting guidelines sorted per function. The manual can be used to find out when and for what purpose a typical protection function can be used. The manual can also provide assistance for calculating settings.

The technical manual contains operation principle descriptions, and lists function blocks, logic diagrams, input and output signals, setting parameters and technical data, sorted per function. The manual can be used as a technical reference during the engineering phase, installation and commissioning phase, and during normal service.

The communication protocol manual describes the communication protocols supported by the IED. The manual concentrates on the vendor-specific implementations.

The point list manual describes the outlook and properties of the data points specific to the IED. The manual should be used in conjunction with the corresponding communication protocol manual.

The cyber security deployment guideline describes the process for handling cyber security when communicating with the IED. Certification, Authorization with role based access control, and product engineering for cyber security related events are described and sorted by function. The guideline can be used as a technical reference during the engineering phase, installation and commissioning phase, and during normal service.

### 1.3.2 Document revision history

<table>
<thead>
<tr>
<th>Document revision/date</th>
<th>History</th>
</tr>
</thead>
<tbody>
<tr>
<td>–/May 2017</td>
<td>First release</td>
</tr>
<tr>
<td>A/October 2017</td>
<td>Information updated</td>
</tr>
<tr>
<td>B/March 2018</td>
<td>2.2 Maintenance release 1</td>
</tr>
<tr>
<td>C/June 2018</td>
<td>Added new functions and resolved bugs</td>
</tr>
<tr>
<td>D/November 2018</td>
<td>Added harmonic, delta, and PSTPDIF functions</td>
</tr>
</tbody>
</table>

### 1.3.3 Related documents

<table>
<thead>
<tr>
<th>Documents related to RET670</th>
<th>Document numbers</th>
</tr>
</thead>
<tbody>
<tr>
<td>Application manual</td>
<td>1MRK 504 163-UUS</td>
</tr>
<tr>
<td>Commissioning manual</td>
<td>1MRK 504 165-UUS</td>
</tr>
<tr>
<td>Product guide</td>
<td>1MRK 504 166-BEN</td>
</tr>
<tr>
<td>Technical manual</td>
<td>1MRK 504 164-UUS</td>
</tr>
<tr>
<td>Type test certificate</td>
<td>1MRK 504 166-TUS</td>
</tr>
</tbody>
</table>
### 1.4 Document symbols and conventions

#### 1.4.1 Symbols

The electrical warning icon indicates the presence of a hazard which could result in electrical shock.

![Electrical Warning Icon]

The warning icon indicates the presence of a hazard which could result in personal injury.

![Warning Icon]

The caution hot surface icon indicates important information or warning about the temperature of product surfaces.

![Caution Hot Surface Icon]

Class 1 Laser product. Take adequate measures to protect the eyes and do not view directly with optical instruments.

![Class 1 Laser Product Icon]

The caution icon indicates important information or warning related to the concept discussed in the text. It might indicate the presence of a hazard which could result in corruption of software or damage to equipment or property.

![Caution Icon]

The information icon alerts the reader of important facts and conditions.
The tip icon indicates advice on, for example, how to design your project or how to use a certain function.

Although warning hazards are related to personal injury, it is necessary to understand that under certain operational conditions, operation of damaged equipment may result in degraded process performance leading to personal injury or death. It is important that the user fully complies with all warning and cautionary notices.

1.4.2 Document conventions

- Abbreviations and acronyms in this manual are spelled out in the glossary. The glossary also contains definitions of important terms.
- Push button navigation in the LHMI menu structure is presented by using the push button icons.
  For example, to navigate between the options, use ▲ and ▼.
- HMI menu paths are presented in bold.
  For example, select Main menu/Settings.
- LHMI messages are shown in Courier font.
  For example, to save the changes in non-volatile memory, select Yes and press.
- Parameter names are shown in italics.
  For example, the function can be enabled and disabled with the Operation setting.
- Each function block symbol shows the available input/output signal.
  - the character ^ in front of an input/output signal name indicates that the signal name may be customized using the PCM600 software.
  - the character * after an input signal name indicates that the signal must be connected to another function block in the application configuration to achieve a valid application configuration.
- Dimensions are provided both in inches and millimeters. If it is not specifically mentioned then the dimension is in millimeters.

1.5 IEC 61850 edition 1 / edition 2 mapping

Function block names are used in ACT and PST to identify functions. Respective function block names of Edition 1 logical nodes and Edition 2 logical nodes are shown in the table below.

<table>
<thead>
<tr>
<th>Function block name</th>
<th>Edition 1 logical nodes</th>
<th>Edition 2 logical nodes</th>
</tr>
</thead>
<tbody>
<tr>
<td>AEGPVOC</td>
<td>AEGGAPC</td>
<td>AEGPVOC</td>
</tr>
<tr>
<td>AGSAL</td>
<td>AGSAL</td>
<td>AGSAL</td>
</tr>
<tr>
<td>SECLLN0</td>
<td>ALMCALH</td>
<td>ALMCALH</td>
</tr>
<tr>
<td>ALMCALH</td>
<td>ALMCALH</td>
<td>ALMCALH</td>
</tr>
<tr>
<td>ALTIM</td>
<td>ALTIM</td>
<td>ALTIM</td>
</tr>
<tr>
<td>ALTMS</td>
<td>ALTMS</td>
<td>ALTMS</td>
</tr>
<tr>
<td>ALTRK</td>
<td>ALTRK</td>
<td>ALTRK</td>
</tr>
<tr>
<td>BCZPDIF</td>
<td>BCZPDIF</td>
<td>BCZPDIF</td>
</tr>
<tr>
<td>BCZSPDIF</td>
<td>BCZSPDIF</td>
<td>BCZSPDIF</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Function block name</th>
<th>Edition 1 logical nodes</th>
<th>Edition 2 logical nodes</th>
</tr>
</thead>
<tbody>
<tr>
<td>BCZTPDIF</td>
<td>BCZTPDIF</td>
<td>BCZTPDIF</td>
</tr>
<tr>
<td>BDCGAPC</td>
<td>SWSGGIO</td>
<td>BBCSWI</td>
</tr>
<tr>
<td>BDZSGAPC</td>
<td>BBS6LLN0</td>
<td>LLN0</td>
</tr>
<tr>
<td>BFPTRC_F01</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F02</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F03</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F04</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F05</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F06</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F07</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F08</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F09</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F10</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F11</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F12</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F13</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F14</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F15</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F16</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F17</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F18</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F19</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F20</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F21</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F22</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F23</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BFPTRC_F24</td>
<td>BFPTRC</td>
<td>BFPTRC</td>
</tr>
<tr>
<td>BICPTRC_01</td>
<td>BICPTRC</td>
<td>BICPTRC</td>
</tr>
<tr>
<td>BICPTRC_02</td>
<td>BICPTRC</td>
<td>BICPTRC</td>
</tr>
<tr>
<td>BICPTRC_03</td>
<td>BICPTRC</td>
<td>BICPTRC</td>
</tr>
<tr>
<td>BICPTRC_04</td>
<td>BICPTRC</td>
<td>BICPTRC</td>
</tr>
<tr>
<td>BICPTRC_05</td>
<td>BICPTRC</td>
<td>BICPTRC</td>
</tr>
<tr>
<td>BRCPTOC</td>
<td>BRCPTOC</td>
<td>BRCPTOC</td>
</tr>
<tr>
<td>BRTPTOC</td>
<td>BRTPTOC</td>
<td>BRTPTOC</td>
</tr>
<tr>
<td>BTIGAPC</td>
<td>B16IFCVI</td>
<td>BTIGAPC</td>
</tr>
<tr>
<td>BUSPTRC_B1</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B2</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Function block name</th>
<th>Edition 1 logical nodes</th>
<th>Edition 2 logical nodes</th>
</tr>
</thead>
<tbody>
<tr>
<td>BUSPTRC_B3</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B4</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B5</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B6</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B7</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B8</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B9</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B10</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B11</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B12</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B13</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B14</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B15</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B16</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B17</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B18</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B19</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B20</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B21</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B22</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B23</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUSPTRC_B24</td>
<td>BUSPTRC</td>
<td>BUSPTRC</td>
</tr>
<tr>
<td>BUTPTRC_B1</td>
<td>BUTPTRC</td>
<td>BUTPTRC</td>
</tr>
<tr>
<td>BUTPTRC_B2</td>
<td>BUTPTRC</td>
<td>BUTPTRC</td>
</tr>
<tr>
<td>BUTPTRC_B3</td>
<td>BUTPTRC</td>
<td>BUTPTRC</td>
</tr>
<tr>
<td>BUTPTRC_B4</td>
<td>BUTPTRC</td>
<td>BUTPTRC</td>
</tr>
<tr>
<td>BUTPTRC_B5</td>
<td>BUTPTRC</td>
<td>BUTPTRC</td>
</tr>
<tr>
<td>BUTPTRC_B6</td>
<td>BUTPTRC</td>
<td>BUTPTRC</td>
</tr>
<tr>
<td>BUTPTRC_B7</td>
<td>BUTPTRC</td>
<td>BUTPTRC</td>
</tr>
<tr>
<td>BUTPTRC_B8</td>
<td>BUTPTRC</td>
<td>BUTPTRC</td>
</tr>
<tr>
<td>BZISGGIO</td>
<td>BZISGGIO</td>
<td>BZISGGPC</td>
</tr>
<tr>
<td>BZITGGIO</td>
<td>BZITGGIO</td>
<td>BZITGAPC</td>
</tr>
<tr>
<td>BZNPDIF_Z1</td>
<td>BZNPDIF</td>
<td>BZNPDIF</td>
</tr>
<tr>
<td>BZNPDIF_Z2</td>
<td>BZNPDIF</td>
<td>BZNPDIF</td>
</tr>
<tr>
<td>BZNPDIF_Z3</td>
<td>BZNPDIF</td>
<td>BZNPDIF</td>
</tr>
<tr>
<td>BZNPDIF_Z4</td>
<td>BZNPDIF</td>
<td>BZNPDIF</td>
</tr>
<tr>
<td>BZNPDIF_Z5</td>
<td>BZNPDIF</td>
<td>BZNPDIF</td>
</tr>
<tr>
<td>BZNPDIF_Z6</td>
<td>BZNPDIF</td>
<td>BZNPDIF</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Function block name</th>
<th>Edition 1 logical nodes</th>
<th>Edition 2 logical nodes</th>
</tr>
</thead>
<tbody>
<tr>
<td>BZNSPDIFF_A</td>
<td>BZNSPDIFF</td>
<td>BZASGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZASPDIF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZNSGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZNPDIF</td>
</tr>
<tr>
<td>BZNPDIF_B</td>
<td>BZNPDIF</td>
<td>BZBSPGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZBSPDIFF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZNSGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZNSPDIF</td>
</tr>
<tr>
<td>BZNTNPDIF_A</td>
<td>BZNTNPDIF</td>
<td>BZATGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZATPDIF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZNTGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZNTPDIF</td>
</tr>
<tr>
<td>BZNTNPDIF_B</td>
<td>BZNTNPDIF</td>
<td>BZBTGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZBTPDIF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZNTGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>BZNTPDIF</td>
</tr>
<tr>
<td>CBPGAPC</td>
<td>CBPLLNO</td>
<td>CBPMMXU</td>
</tr>
<tr>
<td></td>
<td>CBPMMXU</td>
<td>CBPMMXU</td>
</tr>
<tr>
<td></td>
<td>CBPMMXU</td>
<td>CBPMMXU</td>
</tr>
<tr>
<td></td>
<td>CBPMMXU</td>
<td>CBPMMXU</td>
</tr>
<tr>
<td></td>
<td>CBPMMXU</td>
<td>CBPMMXU</td>
</tr>
<tr>
<td></td>
<td>CBPMMXU</td>
<td>CBPMMXU</td>
</tr>
<tr>
<td>CCEPSC</td>
<td>CCRPTLD</td>
<td>CCEPSC</td>
</tr>
<tr>
<td>CCRBRF</td>
<td>CCRBRF</td>
<td>CCRBRF</td>
</tr>
<tr>
<td>CCRWPRBF</td>
<td>CCRWPRBF</td>
<td>CCRWPRBF</td>
</tr>
<tr>
<td>CCSRBF</td>
<td>CCSRBF</td>
<td>CCSRBF</td>
</tr>
<tr>
<td>CCSSPVC</td>
<td>CCSPDIF</td>
<td>CCSPDIF</td>
</tr>
<tr>
<td>CMGAPC</td>
<td>CMXU</td>
<td>CMXU</td>
</tr>
<tr>
<td>CMSQI</td>
<td>CMXU</td>
<td>CMXU</td>
</tr>
<tr>
<td>COUVGAPC</td>
<td>COUVTUV</td>
<td>COUVTUV</td>
</tr>
<tr>
<td></td>
<td>COUVTUV</td>
<td>COUVTUV</td>
</tr>
<tr>
<td>CVGAPC</td>
<td>GF2LLNO</td>
<td>GF2MMXN</td>
</tr>
<tr>
<td></td>
<td>GF2MMXN</td>
<td>GF2MMXN</td>
</tr>
<tr>
<td></td>
<td>GF2MMXN</td>
<td>GF2MMXN</td>
</tr>
<tr>
<td></td>
<td>GF2MMXN</td>
<td>GF2MMXN</td>
</tr>
<tr>
<td></td>
<td>GF2MMXN</td>
<td>GF2MMXN</td>
</tr>
<tr>
<td>CVMMXN</td>
<td>CVMMXN</td>
<td>CVMMXN</td>
</tr>
<tr>
<td>D2PTOC</td>
<td>D2PLLNO</td>
<td>D2PTOC</td>
</tr>
<tr>
<td></td>
<td>D2PLLNO</td>
<td>D2PTOC</td>
</tr>
<tr>
<td></td>
<td>D2PLLNO</td>
<td>D2PTOC</td>
</tr>
<tr>
<td></td>
<td>D2PLLNO</td>
<td>D2PTOC</td>
</tr>
<tr>
<td></td>
<td>D2PLLNO</td>
<td>D2PTOC</td>
</tr>
<tr>
<td></td>
<td>D2PLLNO</td>
<td>D2PTOC</td>
</tr>
<tr>
<td>DPGAPC</td>
<td>DPGGIO</td>
<td>DPGAPC</td>
</tr>
<tr>
<td>DRPRDRE</td>
<td>DRPRDRE</td>
<td>DRPRDRE</td>
</tr>
<tr>
<td>ECPSC</td>
<td>ECPSC</td>
<td>ECPSC</td>
</tr>
<tr>
<td>ECRWPSCH</td>
<td>ECRWPSCH</td>
<td>ECRWPSCH</td>
</tr>
<tr>
<td></td>
<td>ECRWPSCH</td>
<td>ECRWPSCH</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Function block name</th>
<th>Edition 1 logical nodes</th>
<th>Edition 2 logical nodes</th>
</tr>
</thead>
<tbody>
<tr>
<td>EF2PTOC</td>
<td>EF2LLN0 EF2PTRC EF2RDIR GEN2PHAR PH1PTOC</td>
<td>EF2PTRC EF2RDIR GEN2PHAR PH1PTOC</td>
</tr>
<tr>
<td>EF4PTOC</td>
<td>EF4LLN0 EF4PTRC EF4RDIR GEN4PHAR PH1PTOC</td>
<td>EF4PTRC EF4RDIR GEN4PHAR PH1PTOC</td>
</tr>
<tr>
<td>EFPIOC</td>
<td>EFPIOC</td>
<td>EFPIOC</td>
</tr>
<tr>
<td>EFRWPIOC</td>
<td>EFRWPIOC</td>
<td>EFRWPIOC</td>
</tr>
<tr>
<td>ETPMMTR</td>
<td>ETPMMTR</td>
<td>ETPMMTR</td>
</tr>
<tr>
<td>FDPSPDIS</td>
<td>FDPSPDIS</td>
<td>FDPSPDIS</td>
</tr>
<tr>
<td>FMPSPDIS</td>
<td>FMPSPDIS</td>
<td>FMPSPDIS</td>
</tr>
<tr>
<td>FRPSPDIS</td>
<td>FSRPDIS</td>
<td>FSRPDIS</td>
</tr>
<tr>
<td>FTAQFVR</td>
<td>FTAQFVR</td>
<td>FTAQFVR</td>
</tr>
<tr>
<td>FUFSFVC</td>
<td>SDDRFUF</td>
<td>FUFSFVC SDDSFVC</td>
</tr>
<tr>
<td>GENPDIF</td>
<td>GENPDIF</td>
<td>GENPDIF GENPDIF GENPHAR GENPTRC</td>
</tr>
<tr>
<td>GOPPDOP</td>
<td>GOPPDOP</td>
<td>GOPPDOP PH1PTRC</td>
</tr>
<tr>
<td>GRPTTTR</td>
<td>GRPTTTR</td>
<td>GRPTTTR</td>
</tr>
<tr>
<td>GSPTRR</td>
<td>GSPTRR</td>
<td>GSPTRR</td>
</tr>
<tr>
<td>GUPPDUP</td>
<td>GUPPDUP</td>
<td>GUPPDUP PH1PTRC</td>
</tr>
<tr>
<td>HZPDIF</td>
<td>HZPDIF</td>
<td>HZPDIF</td>
</tr>
<tr>
<td>INDCALCH</td>
<td>INDCALH</td>
<td>INDCALH</td>
</tr>
<tr>
<td>ITBGAPC</td>
<td>IB16FCVB</td>
<td>ITBGAPC</td>
</tr>
<tr>
<td>L3CPDIF</td>
<td>L3CPDIF</td>
<td>L3CGAPC L3CPDIF L3CPHAR L3CPTRC</td>
</tr>
<tr>
<td>L4CPDIF</td>
<td>L4CLLN0 L4CPDIF L4CPTRC</td>
<td>LLNO L4CGAPC L4CPDIF L4CPSCH L4CPTRC</td>
</tr>
<tr>
<td>L4UFCNT</td>
<td>L4UFCNT</td>
<td>L4UFCNT</td>
</tr>
<tr>
<td>L6CPDIF</td>
<td>L6CPDIF</td>
<td>L6CGAPC L6CPDIF L6CPHAR L6CPTRC</td>
</tr>
<tr>
<td>LAPPAGAPC</td>
<td>LAPPLLLN0 LAPPPDUP LAPPUPPF</td>
<td>LAPPPDUP LAPPUPPF</td>
</tr>
<tr>
<td>Function block name</td>
<td>Edition 1 logical nodes</td>
<td>Edition 2 logical nodes</td>
</tr>
<tr>
<td>---------------------</td>
<td>-------------------------</td>
<td>-------------------------</td>
</tr>
<tr>
<td>LCCRPTRC</td>
<td>LCCRPTRC</td>
<td>LCCRPTRC</td>
</tr>
<tr>
<td>LCNSPTOC</td>
<td>LCNSPTOC</td>
<td>LCNSPTOC</td>
</tr>
<tr>
<td>LCNSPTOV</td>
<td>LCNSPTOV</td>
<td>LCNSPTOV</td>
</tr>
<tr>
<td>LCP3PTOC</td>
<td>LCP3PTOC</td>
<td>LCP3PTOC</td>
</tr>
<tr>
<td>LCP3PTUC</td>
<td>LCP3PTUC</td>
<td>LCP3PTUC</td>
</tr>
<tr>
<td>LCPTTR</td>
<td>LCPTTR</td>
<td>LCPTTR</td>
</tr>
<tr>
<td>LCZSPTOC</td>
<td>LCZSPTOC</td>
<td>LCZSPTOC</td>
</tr>
<tr>
<td>LCZSPTOV</td>
<td>LCZSPTOV</td>
<td>LCZSPTOV</td>
</tr>
<tr>
<td>LD0LLN0</td>
<td>LLN0</td>
<td>-</td>
</tr>
<tr>
<td>LDLPSCH</td>
<td>LDLPSCH</td>
<td>LDLPSCH</td>
</tr>
<tr>
<td>LDRGFC</td>
<td>STSGGIO</td>
<td>LDRGFC</td>
</tr>
<tr>
<td>LEXPDIS</td>
<td>LEXPDIS</td>
<td>LEXPDIS LEXPTRC</td>
</tr>
<tr>
<td>LFPTTR</td>
<td>LFPTTR</td>
<td>LFPTTR</td>
</tr>
<tr>
<td>LMBRFLO</td>
<td>LMBRFLO</td>
<td>LMBRFLO</td>
</tr>
<tr>
<td>LOLSPTTR</td>
<td>LOLSPTTR</td>
<td>LOLSPTTR</td>
</tr>
<tr>
<td>LOVPTUV</td>
<td>LOVPTUV</td>
<td>LOVPTUV</td>
</tr>
<tr>
<td>LPHD</td>
<td>LPHD</td>
<td></td>
</tr>
<tr>
<td>LPTTR</td>
<td>LPTTR</td>
<td>LPTTR</td>
</tr>
<tr>
<td>LT3CPDIF</td>
<td>LT3CPDIF</td>
<td>LT3CGAPC LT3CPDIF LT3CPHAR LT3CPTRC</td>
</tr>
<tr>
<td>LT6CPDIF</td>
<td>LT6CPDIF</td>
<td>LT6CGAPC LT6CPDIF LT6CPHAR LT6CPTRC</td>
</tr>
<tr>
<td>MVGAPC</td>
<td>MVGGIO</td>
<td>MVGAPC</td>
</tr>
<tr>
<td>NS2PTOC</td>
<td>NS2LLN0 NS2PTOC NS2PTRC</td>
<td>NS2PTOC NS2PTRC</td>
</tr>
<tr>
<td>NS4PTOC</td>
<td>EF4LLN0 EF4PTRC EF4DIR GEN4PHAR PH1PTOC</td>
<td>EF4PTRC EF4DIR PH1PTOC</td>
</tr>
<tr>
<td>O2RWPTOV</td>
<td>GEN2LLN0 O2RWPTOV PH1PTRC</td>
<td>O2RWPTOV PH1PTRC</td>
</tr>
<tr>
<td>OC4PTOC</td>
<td>OC4LLN0 GEN4PHAR PH3PTOC PH3PTRC</td>
<td>GEN4PHAR PH3PTOC PH3PTRC</td>
</tr>
<tr>
<td>OEXPVPH</td>
<td>OEXPVPH</td>
<td>OEXPVPH</td>
</tr>
<tr>
<td>OOSPPAM</td>
<td>OOSPPAM</td>
<td>OOSPPAM OOSPTRC</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Function block name</th>
<th>Edition 1 logical nodes</th>
<th>Edition 2 logical nodes</th>
</tr>
</thead>
<tbody>
<tr>
<td>OV2PTOV</td>
<td>GEN2LLN0 OV2PTOV PH1PTRC</td>
<td>OV2PTOV PH1PTRC</td>
</tr>
<tr>
<td>PAPGAPC</td>
<td>PAPGAPC</td>
<td>PAPGAPC</td>
</tr>
<tr>
<td>PCFCNT</td>
<td>PCGGIO</td>
<td>PCFCNT</td>
</tr>
<tr>
<td>PH4SPTOC</td>
<td>GEN4PHAR OCNDLNN0 PH1BPTOC PH1PTRC</td>
<td>GEN4PHAR PH1BPTOC PH1PTRC</td>
</tr>
<tr>
<td>PHPIOC</td>
<td>PHPIOC</td>
<td>PHPIOC</td>
</tr>
<tr>
<td>PSLPSCH</td>
<td>ZMRRPSL</td>
<td>PSLPSCH</td>
</tr>
<tr>
<td>PSPPPAM</td>
<td>PSPPPAM</td>
<td>PSPPPAM PSPPTRC</td>
</tr>
<tr>
<td>QCBAY</td>
<td>QCBAY</td>
<td>BAY/LLN0</td>
</tr>
<tr>
<td>QCR5V</td>
<td>QCR5V</td>
<td>QCR5V</td>
</tr>
<tr>
<td>RCHLCCH</td>
<td>RCHLCCH</td>
<td>RCHLCCH</td>
</tr>
<tr>
<td>REFPDIF</td>
<td>REFPDIF</td>
<td>REFPDIF</td>
</tr>
<tr>
<td>ROTIPHIZ</td>
<td>ROTIPHIZ</td>
<td>ROTIPHIZ ROTIPTRC</td>
</tr>
<tr>
<td>ROV2PTOV</td>
<td>GEN2LLN0 PH1PTRC ROV2PTOV</td>
<td>PH1PTRC ROV2PTOV</td>
</tr>
<tr>
<td>SAPFRC</td>
<td>SAPFRC</td>
<td>SAPFRC</td>
</tr>
<tr>
<td>SAPTOF</td>
<td>SAPTOF</td>
<td>SAPTOF</td>
</tr>
<tr>
<td>SAPTUF</td>
<td>SAPTUF</td>
<td>SAPTUF</td>
</tr>
<tr>
<td>SCCVPTOC</td>
<td>SCCVPTOC</td>
<td>SCCVPTOC</td>
</tr>
<tr>
<td>SCHLCCH</td>
<td>SCHLCCH</td>
<td>SCHLCCH</td>
</tr>
<tr>
<td>SCILO</td>
<td>SCILO</td>
<td>SCILO</td>
</tr>
<tr>
<td>SCSWI</td>
<td>SCSWI</td>
<td>SCSWI</td>
</tr>
<tr>
<td>SDEPSDE</td>
<td>SDEPSDE</td>
<td>SDEPSDE SDEPTOC SDEPTOV SDEPTRC</td>
</tr>
<tr>
<td>SESRSYN</td>
<td>RSY1LLN0 AUT1RSYN MAN1RSYN SYNRSYN</td>
<td>AUT1RSYN MAN1RSYN SYNRSYN</td>
</tr>
<tr>
<td>SLGAPC</td>
<td>SLGGIO</td>
<td>SLGAPC</td>
</tr>
<tr>
<td>SMBRREC</td>
<td>SMBRREC</td>
<td>SMBRREC</td>
</tr>
<tr>
<td>SMPPTRC</td>
<td>SMPPTRC</td>
<td>SMPPTRC</td>
</tr>
<tr>
<td>SP16GAPC</td>
<td>SP16GGIO</td>
<td>SP16GAPC</td>
</tr>
<tr>
<td>SPC8GAPC</td>
<td>SPC8GGIO</td>
<td>SPC8GAPC</td>
</tr>
<tr>
<td>SPGAPC</td>
<td>SPGGIO</td>
<td>SPGAPC</td>
</tr>
<tr>
<td>SSCBR</td>
<td>SSCBR</td>
<td>SSCBR</td>
</tr>
<tr>
<td>SSIMG</td>
<td>SSIMG</td>
<td>SSIMG</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Function block name</th>
<th>Edition 1 logical nodes</th>
<th>Edition 2 logical nodes</th>
</tr>
</thead>
<tbody>
<tr>
<td>SSIML</td>
<td>SSIML</td>
<td>SSIML</td>
</tr>
<tr>
<td>PTRSTHR</td>
<td>PTRSTHR</td>
<td>PTRSTHR</td>
</tr>
<tr>
<td>STBPTOC</td>
<td>STBPTOC</td>
<td>BBPMSS</td>
</tr>
<tr>
<td></td>
<td></td>
<td>STBPTOC</td>
</tr>
<tr>
<td>STEFPHIZ</td>
<td>STEFPHIZ</td>
<td>STEFPHIZ</td>
</tr>
<tr>
<td>STTIPHIZ</td>
<td>STTIPHIZ</td>
<td>STTIPHIZ</td>
</tr>
<tr>
<td>SXCBR</td>
<td>SXCBR</td>
<td>SXCBR</td>
</tr>
<tr>
<td>SXSWI</td>
<td>SXSWI</td>
<td>SXSWI</td>
</tr>
<tr>
<td>T2WPDIFF</td>
<td>T2WPDIFF</td>
<td>T2WGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T2WPDIFF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T2WPHAR</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T2WPTRC</td>
</tr>
<tr>
<td>T3WPDIFF</td>
<td>T3WPDIFF</td>
<td>T3WGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T3WPDIFF</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T3WPHAR</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T3WPTRC</td>
</tr>
<tr>
<td>TCLYLTC</td>
<td>TCLYLTC</td>
<td>TCLYLTC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>TCLYLTC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>TCLSLTC</td>
</tr>
<tr>
<td>TCMYLTC</td>
<td>TCMYLTC</td>
<td>TCMYLTC</td>
</tr>
<tr>
<td>TEIGAPC</td>
<td>TEIGGIO</td>
<td>TEIGAPC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>TEIGGIO</td>
</tr>
<tr>
<td>TEILGAPC</td>
<td>TEILGIO</td>
<td>TEILGAPC</td>
</tr>
<tr>
<td>TMAGAPC</td>
<td>TMAGGIO</td>
<td>TMAGAPC</td>
</tr>
<tr>
<td>TPIIOC</td>
<td>TPIIOC</td>
<td>TPIIOC</td>
</tr>
<tr>
<td>TR1ATCC</td>
<td>TR1ATCC</td>
<td>TR1ATCC</td>
</tr>
<tr>
<td>TR8ATCC</td>
<td>TR8ATCC</td>
<td>TR8ATCC</td>
</tr>
<tr>
<td>TRPTTR</td>
<td>TRPTTR</td>
<td>TRPTTR</td>
</tr>
<tr>
<td>U2RWPTUV</td>
<td>GEN2LLN0</td>
<td>PH1PTRC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>U2RWPTUV</td>
</tr>
<tr>
<td>UV2PTUV</td>
<td>GEN2LLN0</td>
<td>PH1PTRC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>U2RWPTUV</td>
</tr>
<tr>
<td>VDCPTOV</td>
<td>VDCPTOV</td>
<td>VDCPTOV</td>
</tr>
<tr>
<td>VDSPVC</td>
<td>VDRFU</td>
<td>VDSPVC</td>
</tr>
<tr>
<td>VMMXU</td>
<td>VMMXU</td>
<td>VMMXU</td>
</tr>
<tr>
<td>VMSQI</td>
<td>VMSQI</td>
<td>VMSQI</td>
</tr>
<tr>
<td>VNMMXU</td>
<td>VNMMXU</td>
<td>VNMMXU</td>
</tr>
<tr>
<td>VRPVOC</td>
<td>VRLLN0</td>
<td>PH1PTRC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>PH1PTRC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>PH1PTUV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>VRPVOC</td>
</tr>
<tr>
<td>VSGAPC</td>
<td>VSGGIO</td>
<td>VSGAPC</td>
</tr>
<tr>
<td>WRNCALH</td>
<td>WRNCALH</td>
<td>WRNCALH</td>
</tr>
<tr>
<td>ZC1PPSCH</td>
<td>ZPCPSCH</td>
<td>ZPCPSCH</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Function block name</th>
<th>Edition 1 logical nodes</th>
<th>Edition 2 logical nodes</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZC1WPSCH</td>
<td>ZPCWPSCH</td>
<td>ZPCWPSCH</td>
</tr>
<tr>
<td>ZCLCPSPCH</td>
<td>ZCLCPSPCH</td>
<td>ZCLCPSPCH</td>
</tr>
<tr>
<td>ZCPSCH</td>
<td>ZCPSCH</td>
<td>ZCPSCH</td>
</tr>
<tr>
<td>ZCRWPSCH</td>
<td>ZCRWPSCH</td>
<td>ZCRWPSCH</td>
</tr>
<tr>
<td>ZCVPSPCH</td>
<td>ZCVPSPCH</td>
<td>ZCVPSPCH</td>
</tr>
<tr>
<td>ZGVPDIS</td>
<td>ZGVLLNO</td>
<td>PH1PTRC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ZGVPDIS</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ZGVPTUV</td>
</tr>
<tr>
<td>ZMCAPDIS</td>
<td>ZMCAPDIS</td>
<td>ZMCAPDIS</td>
</tr>
<tr>
<td>ZMCPDIS</td>
<td>ZMCPDIS</td>
<td>ZMCPDIS</td>
</tr>
<tr>
<td>ZMFCPDIS</td>
<td>ZMFCLLN0</td>
<td>PSFPDIS</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ZMFPDIS</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ZMFPTRC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ZMMMXU</td>
</tr>
<tr>
<td>ZMFPDIS</td>
<td>ZMFCLLN0</td>
<td>PSFPDIS</td>
</tr>
<tr>
<td></td>
<td></td>
<td>PSFPDIS</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ZMFPDIS</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ZMFPTRC</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ZMMMXU</td>
</tr>
<tr>
<td>ZMHDIS</td>
<td>ZMHDIS</td>
<td>ZMHDIS</td>
</tr>
<tr>
<td>ZMMAPDIS</td>
<td>ZMMAPDIS</td>
<td>ZMMAPDIS</td>
</tr>
<tr>
<td>ZMPDIS</td>
<td>ZMPDIS</td>
<td>ZMPDIS</td>
</tr>
<tr>
<td>ZMQAPDIS</td>
<td>ZMQAPDIS</td>
<td>ZMQAPDIS</td>
</tr>
<tr>
<td>ZMQPDIS</td>
<td>ZMQPDIS</td>
<td>ZMQPDIS</td>
</tr>
<tr>
<td>ZMRAPDIS</td>
<td>ZMRAPDIS</td>
<td>ZMRAPDIS</td>
</tr>
<tr>
<td>ZMRPDIS</td>
<td>ZMRPDIS</td>
<td>ZMRPDIS</td>
</tr>
<tr>
<td>ZMRPSB</td>
<td>ZMRPSB</td>
<td>ZMRPSB</td>
</tr>
<tr>
<td>ZSMGAPC</td>
<td>ZSMGAPC</td>
<td>ZSMGAPC</td>
</tr>
</tbody>
</table>
Section 2  Safety information

2.1 Symbols on the product

All warnings must be observed.

Read the entire manual before doing installation or any maintenance work on the product.

Class 1 Laser product. Take adequate measures to protect your eyes and do not view directly with optical instruments.

Do not touch the unit in operation. The installation shall take into account the worst case temperature.

2.2 Warnings

Observe the warnings during all types of work related to the product.

Only electrically skilled persons with the proper authorization and knowledge of any safety hazards are allowed to carry out the electrical installation.

National and local electrical safety regulations must always be followed. Working in a high voltage environment requires serious approach to avoid human injuries and damage to equipment.

Do not touch circuitry during operation. Potentially lethal voltages and currents are present.
Always use suitable isolated test pins when measuring signals in open circuitry. Potentially lethal voltages and currents are present.

Never connect or disconnect a wire and/or a connector to or from an IED during normal operation. Hazardous voltages and currents are present that may be lethal. Operation may be disrupted and IED and measuring circuitry may be damaged.

Dangerous voltages can occur on the connectors, even though the auxiliary voltage has been disconnected.

Always connect the IED to protective ground, regardless of the operating conditions. This also applies to special occasions such as bench testing, demonstrations and off-site configuration. This is class 1 equipment that shall be grounded.

Never disconnect the secondary connection of current transformer circuit without short-circuiting the transformer’s secondary winding. Operating a current transformer with the secondary winding open will cause a massive potential build-up that may damage the transformer and may cause injuries to humans.

Never remove any screw from a powered IED or from an IED connected to powered circuitry. Potentially lethal voltages and currents are present.

Take adequate measures to protect the eyes. Never look into the laser beam.

The IED with accessories should be mounted in a cubicle in a restricted access area within a power station, substation or industrial or retail environment.

2.3 Caution signs

Whenever changes are made in the IED, measures should be taken to avoid inadvertent tripping.
The IED contains components which are sensitive to electrostatic discharge. ESD precautions shall always be observed prior to touching components.

Always transport PCBs (modules) using certified conductive bags.

Do not connect live wires to the IED. Internal circuitry may be damaged.

Always use a conductive wrist strap connected to protective ground when replacing modules. Electrostatic discharge (ESD) may damage the module and IED circuitry.

Take care to avoid electrical shock during installation and commissioning.

Changing the active setting group will inevitably change the IEDs operation. Be careful and check regulations before making the change.

2.4 Note signs

Observe the maximum allowed continuous current for the different current transformer inputs of the IED. See technical data.
Section 3 Available functions

The following tables list all the functions available in the IED. Those functions that are not exposed to the user or do not need to be configured are not described in this manual.

3.1 Main protection functions

Table 2: Example of quantities

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>= number of basic instances</td>
</tr>
<tr>
<td>0-3</td>
<td>= option quantities</td>
</tr>
<tr>
<td>3-A03</td>
<td>= optional function included in packages A03 (refer to ordering details)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Function description</th>
<th>Transformer</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>RET670 (Customized)</td>
</tr>
<tr>
<td><strong>Differential protection</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>T2WPDIF</td>
<td>87T</td>
<td>Transformer differential protection, two winding</td>
<td>1-3</td>
</tr>
<tr>
<td>T3WPDIF</td>
<td>87T</td>
<td>Transformer differential protection, three winding</td>
<td>1-3</td>
</tr>
<tr>
<td>HZPDIF</td>
<td>87</td>
<td>High impedance differential protection, single phase</td>
<td>0-6</td>
</tr>
<tr>
<td>REFPDIF</td>
<td>87N</td>
<td>Restricted earth fault protection, low impedance</td>
<td>0-3</td>
</tr>
<tr>
<td>LDRGFC</td>
<td>11REL</td>
<td>Additional security logic for differential protection</td>
<td>0-1</td>
</tr>
<tr>
<td>PSTPDIF</td>
<td>87T</td>
<td>Self-adaptive differential protection for two-winding power transformers</td>
<td>1</td>
</tr>
<tr>
<td><strong>Impedance protection</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ZMQPDIS, ZMQAPDIS</td>
<td>21</td>
<td>Distance protection zone, quadrilateral characteristic</td>
<td>0-5</td>
</tr>
<tr>
<td>ZDRDIR</td>
<td>21D</td>
<td>Directional impedance quadrilateral</td>
<td>0-2</td>
</tr>
<tr>
<td>ZMCPDIS, ZMCPDIS</td>
<td>21</td>
<td>Distance measuring zone, quadrilateral characteristic for series compensated lines</td>
<td>0-5</td>
</tr>
<tr>
<td>ZDSRDIR</td>
<td>21D</td>
<td>Directional impedance quadrilateral, including series compensation</td>
<td>0-2</td>
</tr>
<tr>
<td>FDPSPDIS</td>
<td>21</td>
<td>Phase selection, quadrilateral characteristic with fixed angle</td>
<td>0-2</td>
</tr>
<tr>
<td>ZMHPDIS</td>
<td>21</td>
<td>Full-scheme distance protection, mho characteristic</td>
<td>0-5</td>
</tr>
<tr>
<td>ZMMPDIS, ZMMAPDIS</td>
<td>21</td>
<td>Full-scheme distance protection, quadrilateral for ground faults</td>
<td>0-5</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Function description</th>
<th>Transformer</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZDMRD</td>
<td>21D</td>
<td>Directional impedance element for mho characteristic</td>
<td>0-2</td>
</tr>
<tr>
<td>ZDARD</td>
<td></td>
<td>Additional distance protection directional function for ground faults</td>
<td>0-1</td>
</tr>
<tr>
<td>ZSMGAP</td>
<td></td>
<td>Mho impedance supervision logic</td>
<td>0-1</td>
</tr>
<tr>
<td>FMPSPDIS</td>
<td>21</td>
<td>Faulty phase identification with load enchroachment</td>
<td>0-2</td>
</tr>
<tr>
<td>ZMRPDIS, ZMRAPDIS</td>
<td>21</td>
<td>Distance measuring zone, quad characteristic separate Ph-Ph and Ph-E settings</td>
<td>0-5</td>
</tr>
<tr>
<td>FRPSPDIS</td>
<td>21</td>
<td>Phase selection, quadrilateral characteristic with settable angle</td>
<td>0-2</td>
</tr>
<tr>
<td>ZMFPDIS</td>
<td>21</td>
<td>High speed distance protection, quad and mho characteristic</td>
<td>0-2</td>
</tr>
<tr>
<td>ZMFCPDIS</td>
<td>21</td>
<td>High speed distance protection for series comp. lines, quad and mho characteristic</td>
<td>0-2</td>
</tr>
<tr>
<td>PPLPHIZ</td>
<td></td>
<td>Phase preference logic</td>
<td>0-1</td>
</tr>
<tr>
<td>PPL2PHIZ</td>
<td></td>
<td>Phase preference logic</td>
<td>0-1</td>
</tr>
<tr>
<td>ZMRPSB</td>
<td>68</td>
<td>Power swing detection</td>
<td>0-1</td>
</tr>
<tr>
<td>PLSLC</td>
<td></td>
<td>Power swing logic</td>
<td>0-1</td>
</tr>
<tr>
<td>PSPPPAM</td>
<td>78</td>
<td>Poleslip/out-of-step protection</td>
<td>0-1</td>
</tr>
<tr>
<td>OOSPPAM</td>
<td>78</td>
<td>Out-of-step protection</td>
<td>0-1</td>
</tr>
<tr>
<td>ZCVPSOF</td>
<td></td>
<td>Automatic switch onto fault logic, voltage and current based</td>
<td>0-1</td>
</tr>
<tr>
<td>ZGVPDIS</td>
<td>21</td>
<td>Underimpedance protection for generators and transformers</td>
<td>0-2</td>
</tr>
</tbody>
</table>

### 3.2 Back-up protection functions

<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Function description</th>
<th>Transformer</th>
</tr>
</thead>
<tbody>
<tr>
<td>PHPIOC</td>
<td>50</td>
<td>Instantaneous phase overcurrent protection</td>
<td>0-8</td>
</tr>
<tr>
<td>OC4PTOC</td>
<td>51_67</td>
<td>Directional phase overcurrent protection, four steps</td>
<td>0-8</td>
</tr>
<tr>
<td>EFPIOC</td>
<td>50N</td>
<td>Instantaneous residual overcurrent protection</td>
<td>0-8</td>
</tr>
<tr>
<td>EF4PTOC</td>
<td>51N</td>
<td>Directional residual overcurrent protection, four steps</td>
<td>0-8</td>
</tr>
<tr>
<td>NS4PTOC</td>
<td>46</td>
<td>Four step directional negative phase sequence overcurrent protection</td>
<td>0-8</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Function description</th>
<th>RET670 (Customized)</th>
</tr>
</thead>
<tbody>
<tr>
<td>SDEPSDE</td>
<td>67N</td>
<td>Sensitive directional residual overcurrent and power protection</td>
<td>0-3</td>
</tr>
<tr>
<td>LCPTTR</td>
<td>26</td>
<td>Thermal overload protection, one time constant, Celsius</td>
<td>0-2</td>
</tr>
<tr>
<td>LFPTTR</td>
<td>26</td>
<td>Thermal overload protection, one time constant, Fahrenheit</td>
<td>0-2</td>
</tr>
<tr>
<td>TRPTTR</td>
<td>49</td>
<td>Thermal overload protection, two time constants</td>
<td>0-6</td>
</tr>
<tr>
<td>CCBRRF</td>
<td>50BF</td>
<td>Breaker failure protection</td>
<td>0-6</td>
</tr>
<tr>
<td>STBPTOC</td>
<td>50STB</td>
<td>Stub protection</td>
<td>0-3</td>
</tr>
<tr>
<td>CCPDSC</td>
<td>52PD</td>
<td>Pole discordance protection</td>
<td>0-6</td>
</tr>
<tr>
<td>GUPPDUP</td>
<td>37</td>
<td>Directional underpower protection</td>
<td>0-2</td>
</tr>
<tr>
<td>GOPPDOP</td>
<td>32</td>
<td>Directional overpower protection</td>
<td>0-2</td>
</tr>
<tr>
<td>BRCPTOC</td>
<td>46</td>
<td>Broken conductor check</td>
<td>1</td>
</tr>
<tr>
<td>CBPGAPC</td>
<td></td>
<td>Capacitor bank protection</td>
<td>0-6</td>
</tr>
<tr>
<td>NS2PTOC</td>
<td>46I2</td>
<td>Negative sequence time overcurrent protection for machines</td>
<td>0-2</td>
</tr>
<tr>
<td>VRPVOC</td>
<td>51V</td>
<td>Voltage restrained overcurrent protection</td>
<td>0-3</td>
</tr>
</tbody>
</table>

**Voltage protection**

<table>
<thead>
<tr>
<th>Function name</th>
<th>Value</th>
<th>Description</th>
<th>RET670 (Customized)</th>
</tr>
</thead>
<tbody>
<tr>
<td>UV2PTUV</td>
<td>27</td>
<td>Two step undervoltage protection</td>
<td>0-3</td>
</tr>
<tr>
<td>OV2PTOV</td>
<td>59</td>
<td>Two step overvoltage protection</td>
<td>0-3</td>
</tr>
<tr>
<td>ROV2PTOV</td>
<td>59N</td>
<td>Two step residual overvoltage protection</td>
<td>0-3</td>
</tr>
<tr>
<td>OEXPVPH</td>
<td>24</td>
<td>Overexcitation protection</td>
<td>0-2</td>
</tr>
<tr>
<td>VDCPTOV</td>
<td>60</td>
<td>Voltage differential protection</td>
<td>0-2</td>
</tr>
<tr>
<td>LOVPTUV</td>
<td>27</td>
<td>Loss of voltage check</td>
<td>1</td>
</tr>
</tbody>
</table>

**Frequency protection**

<table>
<thead>
<tr>
<th>Function name</th>
<th>Value</th>
<th>Description</th>
<th>RET670 (Customized)</th>
</tr>
</thead>
<tbody>
<tr>
<td>SAPTUUF</td>
<td>81</td>
<td>Underfrequency protection</td>
<td>0-6</td>
</tr>
<tr>
<td>SAPTOF</td>
<td>81</td>
<td>Overfrequency protection</td>
<td>0-6</td>
</tr>
<tr>
<td>SAPFRC</td>
<td>81</td>
<td>Rate-of-change of frequency protection</td>
<td>0-6</td>
</tr>
</tbody>
</table>

**Multipurpose protection**

<table>
<thead>
<tr>
<th>Function name</th>
<th>Description</th>
<th>RET670 (Customized)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CVGAPC</td>
<td>General current and voltage protection</td>
<td>0-9</td>
</tr>
</tbody>
</table>

**General calculation**

<table>
<thead>
<tr>
<th>Function name</th>
<th>Description</th>
<th>RET670 (Customized)</th>
</tr>
</thead>
<tbody>
<tr>
<td>SMAIHPAC</td>
<td>Multipurpose filter</td>
<td>0-6</td>
</tr>
</tbody>
</table>

1) 67 requires voltage
2) 67N requires voltage
### 3.3 Control and monitoring functions

<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Function description</th>
<th>Transformer</th>
</tr>
</thead>
<tbody>
<tr>
<td>SESRSYN</td>
<td>25</td>
<td>Synchrocheck, energizing check and synchronizing</td>
<td>RET670 (Customized) - 0-6</td>
</tr>
<tr>
<td>APC15</td>
<td>3</td>
<td>Control functionality for a single bay, max 15 objects (2CB), including interlocking (see Table 4)</td>
<td></td>
</tr>
<tr>
<td>APC30</td>
<td>3</td>
<td>Control functionality for up to 6 bays, max 30 objects (6CBs), including interlocking (see Table 5)</td>
<td>0-1</td>
</tr>
<tr>
<td>QCBAY</td>
<td></td>
<td>Bay control</td>
<td>1+5/APC30</td>
</tr>
<tr>
<td>LOCREM</td>
<td></td>
<td>Handling of LR-switch positions</td>
<td>1+5/APC30</td>
</tr>
<tr>
<td>LOCREMCTRL</td>
<td></td>
<td>LHMI control of PSTO</td>
<td>1</td>
</tr>
<tr>
<td>SXCBR</td>
<td></td>
<td>Circuit breaker</td>
<td>18</td>
</tr>
<tr>
<td>TRIATCC</td>
<td>90</td>
<td>Automatic voltage control for tap changer, single control</td>
<td>0-4</td>
</tr>
<tr>
<td>TR8ATCC</td>
<td>90</td>
<td>Automatic voltage control for tap changer, parallel control</td>
<td>0-4</td>
</tr>
<tr>
<td>TCMYLTCC</td>
<td>84</td>
<td>Tap changer control and supervision, 6 binary inputs</td>
<td>0-4</td>
</tr>
<tr>
<td>TCLYLTCC</td>
<td>84</td>
<td>Tap changer control and supervision, 32 binary inputs</td>
<td>0-4</td>
</tr>
<tr>
<td>SLGAPC</td>
<td></td>
<td>Logic rotating switch for function selection and LHMI presentation</td>
<td>15</td>
</tr>
<tr>
<td>VSGAPC</td>
<td></td>
<td>Selector mini switch</td>
<td>30</td>
</tr>
<tr>
<td>DPGAPC</td>
<td></td>
<td>Generic communication function for Double Point indication</td>
<td>32</td>
</tr>
<tr>
<td>SPCBGAPC</td>
<td></td>
<td>Single point generic control function 8 signals</td>
<td>5</td>
</tr>
<tr>
<td>AUTOBITS</td>
<td></td>
<td>Automation bits, command function for DNP3.0</td>
<td>3</td>
</tr>
<tr>
<td>SINGLECMD</td>
<td></td>
<td>Single command, 16 signals</td>
<td>8</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Function description</th>
<th>Transformer</th>
</tr>
</thead>
<tbody>
<tr>
<td>I103CMD</td>
<td></td>
<td>Function commands for IEC 60870-5-103</td>
<td>1</td>
</tr>
<tr>
<td>I103GENCMD</td>
<td></td>
<td>Function commands generic for IEC 60870-5-103</td>
<td>50</td>
</tr>
<tr>
<td>I103POSCMD</td>
<td></td>
<td>IED commands with position and select for IEC 60870-5-103</td>
<td>50</td>
</tr>
<tr>
<td>I103POSCMDV</td>
<td></td>
<td>IED direct commands with position for IEC 60870-5-103</td>
<td>50</td>
</tr>
<tr>
<td>I103IEDCMD</td>
<td></td>
<td>IED commands for IEC 60870-5-103</td>
<td>1</td>
</tr>
<tr>
<td>I103USRCMD</td>
<td></td>
<td>Function commands user defined for IEC 60870-5-103</td>
<td>4</td>
</tr>
</tbody>
</table>

**Secondary system supervision**

| CCSSPVC                  | 87   | Current circuit supervision | 0-6          |
| FUFSPVC                  |      | Fuse failure supervision | 0-4          |
| VDSPVC                   | 60   | Fuse failure supervision based on voltage difference | 0-2          |
| DELVSPVC                 | 7V_78 | Voltage delta supervision, 2 phase | 4           |
| DELISPVC                 | 71   | Current delta supervision, 2 phase | 4           |
| DELSPVC                  | 78   | Real delta supervision, real | 4           |

**Logic**

| SMPPTRC                  | 94   | Tripping logic | 12           |
| SMAGAPC                  |      | General start matrix block | 12           |
| STARTCOMB                |      | Start combinator | 32           |
| TMAGAPC                  |      | Trip matrix logic | 12           |
| ALMCALH                  |      | Logic for group alarm | 5           |
| WRNCALH                  |      | Logic for group warning | 5           |
| INDCALH                  |      | Logic for group indication | 5           |
| AND, GATE, INV, LLD, OR, PULSETIMER, RSMEMORY, SRMEMORY, TIMERSET, XOR | Basic configurable logic blocks (see Table 3) | 40-420       |

Table continues on next page
### Available functions

<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI Function description</th>
<th>Transformer</th>
</tr>
</thead>
<tbody>
<tr>
<td>AND, GATE, INV, LLD, OR, PULSETIMER, RSMEMORY, SLGAPC, SRMEMORY, TIMERSET, VSGAPC, XOR</td>
<td>Extension logic package (see Table 7)</td>
<td>0-1</td>
</tr>
<tr>
<td>FXDSIGN</td>
<td>Fixed signal function block</td>
<td>1</td>
</tr>
<tr>
<td>B16i</td>
<td>Boolean to integer conversion, 16 bit</td>
<td>18</td>
</tr>
<tr>
<td>BTIGAPC</td>
<td>Boolean to integer conversion with logical node representation, 16 bit</td>
<td>16</td>
</tr>
<tr>
<td>IB16</td>
<td>Integer to Boolean 16 conversion</td>
<td>18</td>
</tr>
<tr>
<td>ITBGAPC</td>
<td>Integer to Boolean 16 conversion with Logic Node representation</td>
<td>16</td>
</tr>
<tr>
<td>TEIGAPC</td>
<td>Elapsed time integrator with limit transgression and overflow supervision</td>
<td>12</td>
</tr>
<tr>
<td>INTCOMP</td>
<td>Comparator for integer inputs</td>
<td>30</td>
</tr>
<tr>
<td>REALCOMP</td>
<td>Comparator for real inputs</td>
<td>30</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Table 3: Total number of instances for basic configurable logic blocks</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Basic configurable logic block</strong></td>
</tr>
<tr>
<td>AND</td>
</tr>
<tr>
<td>GATE</td>
</tr>
<tr>
<td>INV</td>
</tr>
<tr>
<td>LLD</td>
</tr>
<tr>
<td>OR</td>
</tr>
<tr>
<td>PULSETIMER</td>
</tr>
<tr>
<td>RSMEMORY</td>
</tr>
<tr>
<td>SRMEMORY</td>
</tr>
<tr>
<td>TIMERSET</td>
</tr>
<tr>
<td>XOR</td>
</tr>
</tbody>
</table>
### Table 4: Number of function instances in APC15

<table>
<thead>
<tr>
<th>Function name</th>
<th>Function description</th>
<th>Total number of instances</th>
</tr>
</thead>
<tbody>
<tr>
<td>SCILO</td>
<td>Interlocking</td>
<td>15</td>
</tr>
<tr>
<td>BB_ES</td>
<td></td>
<td>3</td>
</tr>
<tr>
<td>A1A2_BS</td>
<td></td>
<td>2</td>
</tr>
<tr>
<td>A1A2_DC</td>
<td></td>
<td>3</td>
</tr>
<tr>
<td>ABC_BC</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>BH_CONN</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>BH_LINE_A</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>BH_LINE_B</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>DB_BUS_A</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>DB_BUS_B</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>DB_LINE</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>ABC_LINE</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>AB_TRAFO</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>SCSWI</td>
<td>Switch controller</td>
<td>15</td>
</tr>
<tr>
<td>SXSWI</td>
<td>Circuit switch</td>
<td>14</td>
</tr>
<tr>
<td>QCRSV</td>
<td>Apparatus control</td>
<td>2</td>
</tr>
<tr>
<td>RESIN1</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>RESIN2</td>
<td></td>
<td>59</td>
</tr>
<tr>
<td>POS_EVAL</td>
<td>Evaluation of position indication</td>
<td>15</td>
</tr>
<tr>
<td>XLNPROXY</td>
<td>Proxy for signals from switching device via GOOSE</td>
<td>20</td>
</tr>
<tr>
<td>GOOSEXLNRCV</td>
<td>GOOSE function block to receive a switching device</td>
<td>20</td>
</tr>
</tbody>
</table>
Table 5: Number of function instances in APC30

<table>
<thead>
<tr>
<th>Function name</th>
<th>Function description</th>
<th>Total number of instances</th>
</tr>
</thead>
<tbody>
<tr>
<td>SCILO</td>
<td>Interlocking</td>
<td>30</td>
</tr>
<tr>
<td>BB_ES</td>
<td></td>
<td>6</td>
</tr>
<tr>
<td>A1A2_BS</td>
<td></td>
<td>4</td>
</tr>
<tr>
<td>A1A2_DC</td>
<td></td>
<td>6</td>
</tr>
<tr>
<td>ABC_BC</td>
<td></td>
<td>2</td>
</tr>
<tr>
<td>BH_CONN</td>
<td></td>
<td>2</td>
</tr>
<tr>
<td>BH_LINE_A</td>
<td></td>
<td>2</td>
</tr>
<tr>
<td>BH_LINE_B</td>
<td></td>
<td>2</td>
</tr>
<tr>
<td>DB_BUS_A</td>
<td></td>
<td>3</td>
</tr>
<tr>
<td>DB_BUS_B</td>
<td></td>
<td>3</td>
</tr>
<tr>
<td>DB_LINE</td>
<td></td>
<td>3</td>
</tr>
<tr>
<td>ABC_LINE</td>
<td></td>
<td>6</td>
</tr>
<tr>
<td>AB_TRAFO</td>
<td></td>
<td>4</td>
</tr>
<tr>
<td>SCSWI</td>
<td>Switch controller</td>
<td>30</td>
</tr>
<tr>
<td>SXSWI</td>
<td>Circuit switch</td>
<td>24</td>
</tr>
<tr>
<td>QCR5V</td>
<td>Apparatus control</td>
<td>6</td>
</tr>
<tr>
<td>RESIN1</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>RESIN2</td>
<td></td>
<td>59</td>
</tr>
<tr>
<td>POS_EVAL</td>
<td>Evaluation of position indication</td>
<td>30</td>
</tr>
<tr>
<td>QCBAY</td>
<td>Bay control</td>
<td>5</td>
</tr>
<tr>
<td>LOCREM</td>
<td>Handling of LR-switch positions</td>
<td>5</td>
</tr>
<tr>
<td>XLNPROXY</td>
<td>Proxy for signals from switching device via GOOSE</td>
<td>42</td>
</tr>
<tr>
<td>GOOSEXLNRCV</td>
<td>GOOSE function block to receive a switching device</td>
<td>42</td>
</tr>
</tbody>
</table>

Table 6: Total number of instances for configurable logic blocks Q/T

<table>
<thead>
<tr>
<th>Configurable logic blocks Q/T</th>
<th>Total number of instances</th>
</tr>
</thead>
<tbody>
<tr>
<td>ANDQT</td>
<td>120</td>
</tr>
<tr>
<td>INDCOMBSPQQT</td>
<td>20</td>
</tr>
<tr>
<td>INDEXTSPQQT</td>
<td>20</td>
</tr>
<tr>
<td>INVALIDQT</td>
<td>22</td>
</tr>
<tr>
<td>INVERTERQT</td>
<td>120</td>
</tr>
<tr>
<td>ORQT</td>
<td>120</td>
</tr>
<tr>
<td>PULSETIMERQT</td>
<td>40</td>
</tr>
<tr>
<td>RSMEMORYQT</td>
<td>40</td>
</tr>
<tr>
<td>SRMEMORYQT</td>
<td>40</td>
</tr>
<tr>
<td>TIMERSETQT</td>
<td>40</td>
</tr>
<tr>
<td>XORQT</td>
<td>40</td>
</tr>
<tr>
<td>Extended configurable logic block</td>
<td>Total number of instances</td>
</tr>
<tr>
<td>----------------------------------</td>
<td>---------------------------</td>
</tr>
<tr>
<td>AND</td>
<td>220</td>
</tr>
<tr>
<td>GATE</td>
<td>49</td>
</tr>
<tr>
<td>INV</td>
<td>220</td>
</tr>
<tr>
<td>LLD</td>
<td>49</td>
</tr>
<tr>
<td>OR</td>
<td>220</td>
</tr>
<tr>
<td>PULSETIMER</td>
<td>89</td>
</tr>
<tr>
<td>RSMEMORY</td>
<td>40</td>
</tr>
<tr>
<td>SLGAPC</td>
<td>74</td>
</tr>
<tr>
<td>SRMEMORY</td>
<td>130</td>
</tr>
<tr>
<td>TIMERSET</td>
<td>113</td>
</tr>
<tr>
<td>VSGAPC</td>
<td>120</td>
</tr>
<tr>
<td>XOR</td>
<td>89</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Function description</th>
<th>Transformer</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>RET670</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>(Customized)</td>
</tr>
<tr>
<td>Monitoring</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>AISVBAS</td>
<td></td>
<td>General service value presentation of analog inputs</td>
<td>1</td>
</tr>
<tr>
<td>EVENT</td>
<td></td>
<td>Event function</td>
<td>20</td>
</tr>
<tr>
<td>DRPRDRE, A4RADR</td>
<td></td>
<td>Disturbance report</td>
<td>1</td>
</tr>
<tr>
<td>SPGAPC</td>
<td></td>
<td>Generic communication function for Single Point indication</td>
<td>96</td>
</tr>
<tr>
<td>SP16GAPC</td>
<td></td>
<td>Generic communication function for Single Point indication 16 inputs</td>
<td>16</td>
</tr>
<tr>
<td>MVGAPC</td>
<td></td>
<td>Generic communication function for measured values</td>
<td>24</td>
</tr>
<tr>
<td>BINSTATREP</td>
<td></td>
<td>Logical signal status report</td>
<td>3</td>
</tr>
<tr>
<td>RANGE_XP</td>
<td></td>
<td>Measured value expander block</td>
<td>66</td>
</tr>
<tr>
<td>SSIMG</td>
<td>63</td>
<td>Insulation supervision for gas medium</td>
<td>21</td>
</tr>
<tr>
<td>SSIML</td>
<td>71</td>
<td>Insulation supervision for liquid medium</td>
<td>4</td>
</tr>
<tr>
<td>SSCBR</td>
<td></td>
<td>Circuit breaker condition monitoring</td>
<td>0-18</td>
</tr>
<tr>
<td>LMBRFLO</td>
<td></td>
<td>Fault locator</td>
<td>0-1</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Function description</th>
<th>Transformer</th>
</tr>
</thead>
<tbody>
<tr>
<td>RET670 (Customized)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LOLSPTR 26/49 HS</td>
<td></td>
<td>Transformer insulation loss of life monitoring</td>
<td>0-4</td>
</tr>
<tr>
<td>I103MEAS</td>
<td></td>
<td>Measurands for IEC 60870-5-103</td>
<td>1</td>
</tr>
<tr>
<td>I103MEASUSR</td>
<td></td>
<td>Measurands user defined signals for IEC 60870-5-103</td>
<td>3</td>
</tr>
<tr>
<td>I103AR</td>
<td></td>
<td>Function status auto-recloser for IEC 60870-5-103</td>
<td>1</td>
</tr>
<tr>
<td>I103EF</td>
<td></td>
<td>Function status earth-fault for IEC 60870-5-103</td>
<td>1</td>
</tr>
<tr>
<td>I103FLTPROT</td>
<td></td>
<td>Function status fault protection for IEC 60870-5-103</td>
<td>1</td>
</tr>
<tr>
<td>I103IED</td>
<td></td>
<td>IED status for IEC 60870-5-103</td>
<td>1</td>
</tr>
<tr>
<td>I103SUPERV</td>
<td></td>
<td>Supervision status for IEC 60870-5-103</td>
<td>1</td>
</tr>
<tr>
<td>I103USRDEF</td>
<td></td>
<td>Status for user defined signals for IEC 60870-5-103</td>
<td>20</td>
</tr>
<tr>
<td>L4UFCNT</td>
<td></td>
<td>Event counter with limit supervision</td>
<td>30</td>
</tr>
<tr>
<td>PTRSTHR 51TF</td>
<td></td>
<td>Through fault monitoring</td>
<td>0-2</td>
</tr>
<tr>
<td>CHMMHAI ITHD</td>
<td></td>
<td>Current harmonic monitoring, 3 phase</td>
<td>0-3</td>
</tr>
<tr>
<td>VHMMHAI VTHD</td>
<td></td>
<td>Voltage harmonic monitoring, 3 phase</td>
<td>0-3</td>
</tr>
<tr>
<td>Metering</td>
<td></td>
<td>Pulse-counter logic</td>
<td>16</td>
</tr>
<tr>
<td>PCFCNT</td>
<td></td>
<td>Function for energy calculation and demand handling</td>
<td>16</td>
</tr>
<tr>
<td>ETPMMTR</td>
<td></td>
<td></td>
<td>6</td>
</tr>
</tbody>
</table>
## 3.4 Communication

<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Function description</th>
<th>Transformer</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>RET670 (Customized)</td>
</tr>
<tr>
<td><strong>Station communication</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LONSPA, SPA</td>
<td></td>
<td>SPA communication protocol</td>
<td>1</td>
</tr>
<tr>
<td>HORZCOMM</td>
<td></td>
<td>Network variables via LON</td>
<td>1</td>
</tr>
<tr>
<td>IEC 61850-8-1</td>
<td>IEC 61850</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>GOOSEINTLKRCV</td>
<td></td>
<td>Horizontal communication via GOOSE for interlocking</td>
<td>59</td>
</tr>
<tr>
<td>GOOSEBINRCV</td>
<td></td>
<td>GOOSE binary receive</td>
<td>16</td>
</tr>
<tr>
<td>GOOSEDPRVC</td>
<td></td>
<td>GOOSE function block to receive a double point value</td>
<td>64</td>
</tr>
<tr>
<td>GOOSEINTRCV</td>
<td></td>
<td>GOOSE function block to receive an integer value</td>
<td>32</td>
</tr>
<tr>
<td>GOOSEMVRCV</td>
<td></td>
<td>GOOSE function block to receive a measurand value</td>
<td>60</td>
</tr>
<tr>
<td>GOOSESPRVC</td>
<td></td>
<td>GOOSE function block to receive a single point value</td>
<td>64</td>
</tr>
<tr>
<td>VCTRSND</td>
<td></td>
<td>Horizontal communication via GOOSE for VCTR</td>
<td>1</td>
</tr>
<tr>
<td>GOOSEVCTRRCV</td>
<td></td>
<td>Horizontal communication via GOOSE for VCTR</td>
<td>7</td>
</tr>
<tr>
<td>GOOSEVCTRCONF</td>
<td></td>
<td>GOOSE VCTR configuration for send and receive</td>
<td>1</td>
</tr>
<tr>
<td>MULTICMDRCV, MULTICMDSND</td>
<td></td>
<td>Multiple command and transmit</td>
<td>60/10</td>
</tr>
<tr>
<td>AGSAL</td>
<td></td>
<td>Generic security application component</td>
<td>1</td>
</tr>
<tr>
<td>LDOLLN0</td>
<td>IEC 61850 LD0 LLN0</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>SYSLLN0</td>
<td>IEC 61850 SYS LLN0</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>LPHD</td>
<td></td>
<td>Physical device information</td>
<td>1</td>
</tr>
<tr>
<td>PCMACCS</td>
<td></td>
<td>IED configuration protocol</td>
<td>1</td>
</tr>
<tr>
<td>FSTACCS</td>
<td></td>
<td>Field service tool access</td>
<td>1</td>
</tr>
<tr>
<td></td>
<td></td>
<td>IEC 61850-9-2 Process bus communication, 8 merging units</td>
<td>0-1</td>
</tr>
<tr>
<td>ACTIVLOG</td>
<td></td>
<td>Activity logging</td>
<td>1</td>
</tr>
<tr>
<td>ALTRK</td>
<td></td>
<td>Service tracking</td>
<td>1</td>
</tr>
<tr>
<td>PRP</td>
<td>IEC 62439-3 Parallel redundancy protocol</td>
<td></td>
<td>0-1</td>
</tr>
<tr>
<td>HSR</td>
<td>IEC 62439-3 High-availability seamless redundancy</td>
<td></td>
<td>0-1</td>
</tr>
<tr>
<td>PMUCONF</td>
<td></td>
<td>Synchrophasor report, 24 phasors (see Table 8)</td>
<td>0-1</td>
</tr>
<tr>
<td>PMUREPORT</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PHASORREPORT1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PHASORREPORT2</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PHASORREPORT3</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ANALOGRREPORT1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>BINARYREPORT1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SMAI1 - SMAI2</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3PHSUM</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PMUSTATUS</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PTP</td>
<td></td>
<td>Precision time protocol</td>
<td>1</td>
</tr>
</tbody>
</table>

Table continues on next page
## Available functions

### Transformer protection RET670

**Remote communication**

<table>
<thead>
<tr>
<th>Function name</th>
<th>Function description</th>
<th>Number of instances</th>
</tr>
</thead>
<tbody>
<tr>
<td>SCHLCCH</td>
<td>Access point diagnostic for non-redundant Ethernet port</td>
<td>6</td>
</tr>
<tr>
<td>RCHLCCH</td>
<td>Access point diagnostic for redundant Ethernet ports</td>
<td>3</td>
</tr>
</tbody>
</table>

### Remote communication

<table>
<thead>
<tr>
<th>Function name</th>
<th>Function description</th>
<th>Number of instances</th>
</tr>
</thead>
<tbody>
<tr>
<td>BinSignRec1_1</td>
<td>Binary signal transfer receive</td>
<td>3/3/6</td>
</tr>
<tr>
<td>BinSignRec1_2</td>
<td>Binary signal transfer receive</td>
<td>3/3/6</td>
</tr>
<tr>
<td>BinSignReceive2</td>
<td>Binary signal transfer receive</td>
<td>3/3/6</td>
</tr>
<tr>
<td>BinSignTrans1_1</td>
<td>Binary signal transfer transmit</td>
<td>3/3/6</td>
</tr>
<tr>
<td>BinSignTrans1_2</td>
<td>Binary signal transfer transmit</td>
<td>3/3/6</td>
</tr>
<tr>
<td>BinSignTransm2</td>
<td>Binary signal transfer transmit</td>
<td>3/3/6</td>
</tr>
</tbody>
</table>

### Scheme communication

<table>
<thead>
<tr>
<th>Function name</th>
<th>Function description</th>
<th>Number of instances</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZCPSCH</td>
<td>Scheme communication logic with delta based blocking scheme</td>
<td>0-2</td>
</tr>
<tr>
<td>ZC1PPSCH</td>
<td>Scheme communication logic for distance protection</td>
<td>0-2</td>
</tr>
<tr>
<td>ZCRWPSCH</td>
<td>Current reversal and weak-end infeed logic for distance</td>
<td>0-2</td>
</tr>
<tr>
<td>ZCIWPSCH</td>
<td>Current reversal and weak-end infeed logic for phase</td>
<td>0-2</td>
</tr>
<tr>
<td>ZCLCPSPCH</td>
<td>Local acceleration logic</td>
<td>0-1</td>
</tr>
<tr>
<td>ECPSCH</td>
<td>Scheme communication logic for residual overcurrent protection</td>
<td>0-1</td>
</tr>
<tr>
<td>ECRWPSCH</td>
<td>Current reversal and weak-end infeed logic for residual</td>
<td>0-1</td>
</tr>
<tr>
<td></td>
<td>overcurrent protection</td>
<td></td>
</tr>
</tbody>
</table>

**Table 8: Number of function instances in Synchrophasor report, 24 phasors**

<table>
<thead>
<tr>
<th>Function name</th>
<th>Function description</th>
<th>Number of instances</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMUCONF</td>
<td>Configuration parameters for C37.118 2011 and IEEE1344 protocol</td>
<td>1</td>
</tr>
<tr>
<td>PMUREPORT</td>
<td>Protocol reporting via IEEE 1344 and C37.118</td>
<td>1</td>
</tr>
<tr>
<td>PHASORREPORT1</td>
<td>Protocol reporting of phasor data via IEEE 1344 and C37.118, phasors 1-8</td>
<td>1</td>
</tr>
<tr>
<td>PHASORREPORT2</td>
<td>Protocol reporting of phasor data via IEEE 1344 and C37.118, phasors 9-16</td>
<td>1</td>
</tr>
<tr>
<td>PHASORREPORT3</td>
<td>Protocol reporting of phasor data via IEEE 1344 and C37.118, phasors 17-24</td>
<td>1</td>
</tr>
<tr>
<td>ANALOGERPORT1</td>
<td>Protocol reporting of analog data via IEEE 1344 and C37.118, analogs 1-8</td>
<td>1</td>
</tr>
<tr>
<td>BINARYREPORT1</td>
<td>Protocol reporting of binary data via IEEE 1344 and C37.118, binary 1-8</td>
<td>1</td>
</tr>
<tr>
<td>SMAI1–SMAI12</td>
<td>Signal Matrix for analog inputs</td>
<td>1</td>
</tr>
<tr>
<td>3PHSUM</td>
<td>Summation block 3 phase</td>
<td>6</td>
</tr>
<tr>
<td>PMUSTATUS</td>
<td>Diagnostics for C37.118 2011 and IEEE1344 protocol</td>
<td>1</td>
</tr>
</tbody>
</table>
### 3.5 Basic IED functions

#### Table 9: Basic IED functions

<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INERRSIG</td>
<td>Self supervision with internal event list</td>
</tr>
<tr>
<td>TIMESYNCHGEN</td>
<td>Time synchronization module</td>
</tr>
<tr>
<td>BININPUT, SYNCHCAN, SYNCHGPS, SYNCHMPPS, SYNCHLON, SYNCHPPH, SYNCHPPS, SNTP, SYNCHSPA</td>
<td>Time synchronization</td>
</tr>
<tr>
<td>TIMEZONE</td>
<td>Time synchronization</td>
</tr>
<tr>
<td>IRIG-B</td>
<td>Time synchronization</td>
</tr>
<tr>
<td>SETGRPS</td>
<td>Number of setting groups</td>
</tr>
<tr>
<td>ACTVGRP</td>
<td>Parameter setting groups</td>
</tr>
<tr>
<td>TESTMODE</td>
<td>Test mode functionality</td>
</tr>
<tr>
<td>CHNGLCK</td>
<td>Change lock function</td>
</tr>
<tr>
<td>SMBI</td>
<td>Signal matrix for binary inputs</td>
</tr>
<tr>
<td>SMBO</td>
<td>Signal matrix for binary outputs</td>
</tr>
<tr>
<td>SMMI</td>
<td>Signal matrix for mA inputs</td>
</tr>
<tr>
<td>SMAI1 - SMAI12</td>
<td>Signal matrix for analog inputs</td>
</tr>
<tr>
<td>3PHSUM</td>
<td>Summation block 3 phase</td>
</tr>
<tr>
<td>ATHSTAT</td>
<td>Authority status</td>
</tr>
<tr>
<td>ATHCHCK</td>
<td>Authority check</td>
</tr>
<tr>
<td>AUTHMAN</td>
<td>Authority management</td>
</tr>
<tr>
<td>FTPACCS</td>
<td>FTP access with password</td>
</tr>
<tr>
<td>ALTMS</td>
<td>Time master supervision</td>
</tr>
<tr>
<td>ALTIM</td>
<td>Time management</td>
</tr>
<tr>
<td>COMSTATUS</td>
<td>Protocol diagnostic</td>
</tr>
</tbody>
</table>

#### Table 10: Local HMI functions

<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LHMICTRL</td>
<td>ANSI</td>
<td>Local HMI signals</td>
</tr>
<tr>
<td>LANGUAGE</td>
<td>ANSI</td>
<td>Local human machine language</td>
</tr>
<tr>
<td>SCREEN</td>
<td>ANSI</td>
<td>Local HMI Local human machine screen behavior</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>ANSI</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FNKEYTY1–FNKEYTY5</td>
<td></td>
<td>Parameter setting function for HMI in PCM600</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>FNKEYMD1–FNKEYMD5</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LEDGEN</td>
<td></td>
<td>General LED indication part for LHMI</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>OPENCLOSE_LED</td>
<td></td>
<td>LHMI LEDs for open and close keys</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>GRP1_LED1–GRP1_LED15</td>
<td></td>
<td>Basic part for CP HW LED indication module</td>
</tr>
<tr>
<td>GRP2_LED1–GRP2_LED15</td>
<td></td>
<td></td>
</tr>
<tr>
<td>GRP3_LED1–GRP3_LED15</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Section 4  Starting up

4.1 Factory and site acceptance testing

Testing the proper IED operation is carried out at different occasions, for example:

- Acceptance testing
- Commissioning testing
- Maintenance testing

This manual describes the workflow and the steps to carry out the commissioning testing.

Factory acceptance testing (FAT) is typically done to verify that the IED and its corresponding configuration meet the requirements of the utility or industry. This test is the most complex and in depth, as it is done to familiarize the user with a new product or to verify a new configuration. The complexity of this testing depends on several factors, such as:

- New IED type
- New configuration
- Modified configuration

Site acceptance testing (SAT or commissioning testing) is typically done to verify that the installed IED is correctly set and connected to the power system. SAT requires that the acceptance testing has been performed and that the application configuration is verified.

Maintenance testing is a periodic verification that the IED is healthy and has correct settings, depending on changes in the power system. There are also other types of maintenance testing.

4.2 Commissioning checklist

Before starting up commissioning at site, check that the following items are available.

- Single line diagram
- Protection block diagram
- Circuit diagram
- Setting list and configuration
- RJ-45 Ethernet cable (CAT 5)
- Three-phase test kit or other test equipment depending on the complexity of the configuration and functions to be tested.
- PC with PCM600 installed along with the connectivity packages corresponding to the IEDs to be tested.
- Administration rights on the PC, to set up IP addresses
4.3 Checking the power supply

Do not insert anything else to the female connector but the corresponding male connector. Inserting anything else (such as a measurement probe) may damage the female connector and prevent a proper electrical contact between the printed circuit board and the external wiring connected to the screw terminal block.

Check that the auxiliary supply voltage remains within the permissible input voltage range under all operating conditions. Check that the polarity is correct before energizing the IED.

4.4 Energizing the IED

4.4.1 Checking the IED operation

Check all connections to external circuitry to ensure correct installation, before energizing the IED and carrying out the commissioning procedures.

Energize the power supply of the IED to pickup. Keep the DC power supply on until the Root menu or the selected default screen is shown on the HMI before interrupting the DC power supply again. The energization could be done in a number of ways, from energizing a whole cubicle with many IEDs to energizing each single IED one by one.

If HW (i.e. I/O and/or communication boards etc.) have been changed (i.e. removed, replaced, or added), the user should re-configure the IED by navigating in the local HMI menu to: Main menu/Configuration/Reconfigure HW modules to activate the changed hardware modules in order to enable the self-supervision function to detect possible hardware errors.

Check also the self-supervision function in Main menu/Diagnostics/IED status/General menu in local HMI to verify that the IED operates properly.

Set the IED time if no time synchronization source is configured.

To ensure that the IED is according to the delivery and ordering specifications documents delivered together with each IED, the user should also after start-up use the built in HMI to check the IED's:

- Software version, Main menu/Diagnostics/IED status/Product identifiers.
- Serial number, Main menu/Diagnostics/IED status/Product identifiers.
- Installed modules and their ordering number, Main menu/Diagnostics/IED status/Installed HW.

4.4.2 IED start-up sequence

When the IED is energized, the green LED starts flashing instantly. After approximately 55 seconds the window lights up and the window displays 'IED Startup'. The main menu is displayed and the upper row should indicate 'Available' after about 90 seconds. A steady green light indicates a successful startup.
Figure 2: Typical IED start-up sequence

1. IED energized. Green LED instantly starts flashing
2. LCD lights up and “IED startup” is displayed
3. The main menu is displayed. A steady green light indicates a successful startup.

If the upper row in the window indicates ‘Fail’ instead of ‘Available’ and the green LED flashes, an internal failure in the IED has been detected.

4.5 Setting up communication between PCM600 and the IED

The communication between the IED and PCM600 is independent of the communication protocol used within the substation or to the NCC.

The communication media is always Ethernet and the used transport layer is TCP/IP.

Each IED has an RJ-45 Ethernet interface connector on the front. The front Ethernet connector is recommended to be used for communication with PCM600.

When an Ethernet-based station protocol is used, PCM600 communication can use the same Ethernet port and IP address.

To connect PCM600 to the IED, two basic variants must be considered.

- Direct point-to-point link between PCM600 and the IED front port. The front port can be seen as a service port.
- A link via a station LAN or from remote via a network.

The physical connection and the IP address must be configured in both cases to enable communication.

The communication procedures are the same in both cases.

1. If needed, set the IP address for the IEDs.
2. Set up the PC or workstation for a direct link (point-to-point), or
3. Connect the PC or workstation to the LAN/WAN network.
4. Configure the IED IP addresses in the PCM600 project for each IED to match the IP addresses of the physical IEDs.

Setting up IP addresses

Communication between the IED and PCM600 is enabled from the LHMI. The IP address and the corresponding communication subnetwork mask must be set via the Ethernet configuration tool (ECT) for each available Ethernet interface in the IED. Each Ethernet interface has a default factory
IP address when the IED is delivered. The IP address and the subnetwork mask might have to be reset when an additional Ethernet interface is installed or an interface is replaced.

DHCP is available for the front port, and a device connected to it can thereby obtain an automatically assigned IP address via the local HMI path `Main menu/Configuration/Communication/Ethernet configuration/Front port/DHCP`.

Alternatively the default IP address for the IED front port is 10.1.150.3 and the corresponding subnetwork mask is 255.255.255.0, which can be set via the local HMI path `Main menu/Configuration/Communication/TCP-IP configuration/ETHFRNT:1`Main menu/Configuration/Communication/Ethernet configuration/AP_FRONT`.

Setting up the PC or workstation for point-to-point access to IEDs front port

An ethernet cable (max 2 m length) with RJ-45 connectors is needed to connect two physical Ethernet interfaces together without a hub, router, bridge or switch in between.

If an IED is equipped with optical LC interface, a converter between RJ-45 and LC is needed.

1. Select **Search programs and files** in the **Start menu** in Windows.
Figure 3: Select: Search programs and files

2. Type **View network connections** and click on the **View network connections** icon.
3. Right-click and select **Properties**.

4. Select the TCP/IPv4 protocol from the list of configured components using this connection and click **Properties**.
5. Select **Use the following IP address** and define **IP address** and **Subnet mask** if the front port is used and if the **IP address** is not set to be obtained automatically by the IED, see Figure 7. The IP address must be different from the IP address chosen for the IED.

6. Use the **ping** command to verify connectivity with the IED.

7. Close all open windows and start PCM600.
The PC and IED must belong to the same subnetwork for this set-up to work.

**Setting up the PC to access the IED via a network**

The same method is used as for connecting to the front port.

The PC and IED must belong to the same subnetwork for this set-up to work.

### 4.6 Writing an application configuration to the IED

When writing a configuration to the IED with PCM600, the IED is automatically set in configuration mode. When the IED is set in configuration mode, all functions are blocked. The red LED on the IED flashes, and the green LED is lit while the IED is in the configuration mode.

When the configuration is written and completed, the IED is automatically set into normal mode. For further instructions please refer to the users manuals for PCM600.

### 4.7 Checking CT circuits

Check that the wiring is in strict accordance with the supplied connection diagram.

The CTs must be connected in accordance with the circuit diagram provided with the IED, both with regards to phases and polarity. The following tests shall be performed on every primary CT connected to the IED:

- Primary injection test to verify the current ratio of the CT, the correct wiring up to the protection IED and correct phase sequence connection (that is A, B, C.)
- Polarity check to prove that the predicted direction of secondary current flow is correct for a given direction of primary current flow. This is an essential test for the proper operation of the differential function and directional protection functions.
- CT secondary loop resistance measurement to confirm that the current transformer secondary loop DC resistance is within the specification for the connected protection functions. When the measured loop resistance is near the calculated value for maximum DC resistance, perform a complete burden test.
- CT excitation test in order to confirm that the current transformer is of the correct accuracy rating and that there are no shorted turns in the current transformer windings. Manufacturer’s design curves must be available for the current transformer to compare the actual results.
- Grounding check of the individual CT secondary circuits to verify that each three-phase set of main CTs is properly connected to the station ground and only at one electrical point.
- Insulation resistance check.
While the CT primary is energized, the secondary circuit shall never be open circuited because extremely dangerous high voltages may arise.

Both the primary and the secondary sides must be disconnected from the line and the IED when plotting the excitation characteristics.

If the CT secondary circuit ground connection is removed without the current transformer primary being de-energized, dangerous voltages may result in the secondary CT circuits.

4.8 Checking VT circuits

Check that the wiring is in strict accordance with the supplied connection diagram.

Correct possible errors before continuing to test the circuitry.

Test the circuitry.

- Polarity check when applicable; this test is often omitted for CVTs
- VT circuit voltage measurement (primary injection test)
- Grounding check
- Phase relationship
- Insulation resistance check

The primary injection test verifies the VT ratio and the wiring all the way from the primary system to the IED. Injection must be performed for each phase-to-neutral circuit.

While testing VT secondary circuit and associated secondary equipment, care shall be exercised to isolate the VT from the circuit being tested to avoid backcharging the VT from the secondary side.

4.9 Using the RTXP test switch

The RTXP test switch is designed to provide the means of safe testing of the IED. This is achieved by the electromechanical design of the test switch and test plug handle. When the test plug handle is inserted, it first blocks the trip and alarm circuits then it short circuits the CT secondary circuit and opens the VT secondary circuits making the IED available for secondary injection.

When pulled out, the test handle is mechanically stopped in half withdrawn position. In this position, the current and voltage enter the protection, but the alarm and trip circuits are still
isolated and the IED is in test mode. Before removing the test handle, check the measured values in the IED.

Not until the test handle is completely removed, the trip and alarm circuits are restored for operation.

- Verify that the contact sockets have been crimped correctly and that they are fully inserted by tugging on the wires. Never do this with current circuits in service.

Current circuit
1. Verify that the contacts are of current circuit type.
2. Verify that the short circuit jumpers are located in the correct slots.

Voltage circuit
1. Verify that the contacts are of voltage circuit type.
2. Check that no short circuit jumpers are located in the slots dedicated for voltage.

Trip and alarm circuits
1. Check that the correct types of contacts are used.

### 4.10 Checking the binary input/output circuits

- **Do not insert anything else to the female connector but the corresponding male connector.** Inserting anything else (such as a measurement probe) may damage the female connector and prevent a proper electrical contact between the printed circuit board and the external wiring connected to the screw terminal block.

#### 4.10.1 Binary input circuits

- Preferably, disconnect the binary input connector from the binary input cards. Check all connected signals so that both input level and polarity are in accordance with the IED specifications.

#### 4.10.2 Binary output circuits

- Preferably, disconnect the binary output connector from the binary output cards. Check all connected signals so that both load and polarity are in accordance with IED specifications.

### 4.11 Checking optical connections

- Check that the Tx and Rx optical connections are correct.
An IED equipped with optical connections has a minimum space requirement of 180 mm (7.2 inches) for plastic fiber cables and 275 mm (10.9 inches) for glass fiber cables. Check the allowed minimum bending radius from the optical cable manufacturer.
Section 5 Configuring the IED and changing settings

5.1 Overview

The customer specific values for each setting parameter and a configuration file have to be available before the IED can be set and configured, if the IED is not delivered with a configuration.

Use the configuration tools in PCM600 to verify that the IED has the expected configuration.

Each function included in the IED has several setting parameters, which have to be set in order to make the IED behave as intended. A factory default value is provided for each parameter. The Parameter Setting Tool in PCM600 is used when changing setting parameters.

All settings can be

- Entered manually through the local HMI.
- Written from a PC, either locally or remotely using PCM600. Front or rear port communication has to be established before the settings can be written to the IED.

Make sure that the DC supply is not turned off when the IED saves the written configuration.

The IED uses a FLASH disk for storing configuration data and process data like counters, object states, Local/Remote switch position etc. Since FLASH memory is used, measures have been taken in software to make sure that the FLASH disk is not worn out by too intensive storing of data.

This means, that to be absolutely sure that all data have been saved to FLASH, it is necessary to leave the IED with auxiliary power connected after all the commissioning is done (including setting the Local/Remote switch to the desired position) for at least one hour after the last commissioning action performed on the IED.

After that time has elapsed, it will be safe to turn the IED off, no data will be lost.

5.2 Configuring analog CT inputs

The analog input channels must be configured to get correct measurement results as well as correct protection functionality. Because all protection algorithms in the IED utilize the primary system quantities, it is extremely important to make sure that connected current transformer settings are done properly. These data are calculated by the system engineer and normally set by the commissioning personnel from the local HMI or from PCM600.

The analog inputs on the transformer input module are dimensioned for either 1A or 5A. Each transformer input module has a unique combination of current and voltage inputs. Make sure the input current rating is correct and that it matches the order documentation.
The primary CT data are entered via the HMI menu under **Main menu/Configurations/Analog modules**

The following parameter shall be set for every current transformer connected to the IED:

**Table 11: CT configuration**

<table>
<thead>
<tr>
<th>Parameter description</th>
<th>Parameter name</th>
<th>Range</th>
<th>Default</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated CT primary current in A</td>
<td>CTPRIMn</td>
<td>n = channel number</td>
<td>from 0 to 99999</td>
</tr>
</tbody>
</table>

This parameter defines the primary rated current of the CT. For two sets of CTs with ratio 1000/1 and 1000/5 this parameter is set to the same value of 1000 for both CT inputs. The parameter **CTStarPoint** can be used in order to reverse the direction of the CT. This might be necessary if two sets of CTs have different neutral (WYE) point locations in relation to the protected busbar.

For main CTs with 2A rated secondary current, it is recommended to connect the secondary wiring to the 1A input.

> Take the rated permissive overload values for the current inputs into consideration.

### 5.3 Supervision of input/output modules

I/O modules configured with PCM600 (BIM, BOM or IOM) are supervised.

I/O modules that are not configured are not supervised.

Each logical I/O module has an error flag that indicates signal or module failure. The error flag is also set when the physical I/O module of the correct type is not detected in the connected slot.
Section 6 Establishing connection and verifying the SPA/IEC communication

6.1 Entering settings

If the IED is connected to a monitoring or control system via the rear SPA/IEC103 port, the SPA/IEC103 port has to be set either for SPA or IEC103 use.

6.1.1 Entering SPA settings

The SPA/IEC port is located on the rear side of the IED. Two types of interfaces can be used:

- plastic fibers with connector type HFBR
- glass fibers with connector type ST

When using the SPA protocol, the rear SPA/IEC port must be set for SPA use.

Procedure:

1. Set the port for SPA use on the local HMI under Main menu / Configuration / Communication / Station communication / Port configuration / SLM optical serial port / PROTOCOL:1. When the communication protocol is selected, the IED is automatically restarted, and the port then operates as a SPA port.
2. Set the SlaveAddress and BaudRate for the rear SPA port on the local HMI under Main menu / Configuration / Communication / Station communication / SPA / SPA:1. Use the same settings for these as is set in the SMS system for the IED.

6.1.2 Entering IEC settings

The SPA/IEC port is located on the rear side of the IED. Two types of interfaces can be used:

- plastic fibers with connector type HFBR
- glass fibers with connector type ST

When using the IEC protocol, the rear SPA/IEC port must be set for IEC use.

Procedure:

1. Set the port for IEC use on the local HMI under Main menu / Configuration / Communication / Station communication / Port configuration / SLM optical serial port / PROTOCOL:1. When the communication protocol is selected, the IED is automatically restarted, and the port then operates as an IEC port.
2. Set the SlaveAddress and BaudRate for the rear IEC port on the local HMI under Main menu / Configuration / Communication / Station communication / IEC60870-5-103 / OPTICAL103:1. Use the same settings for these as is set in the SMS system for the IED.
6.2 Verifying the communication

To verify that the rear communication with the SMS/SCS system is working, there are some different methods. Choose one of the following.

6.2.1 Verifying SPA communication

Procedure

1. Use a SPA-emulator and send “RF” to the IED. The answer from the IED should be the type and version of it, for example, “REL670 2.1...”.
2. Generate one binary event by activating a function, which is configured to an EVENT block where the used input is set to generate events on SPA. The configuration must be made with the PCM600 software. Verify that the event is presented in the SMS/SCS system.

During the following tests of the different functions in the IED, verify that the events and indications in the SMS/SCS system are as expected.

6.2.2 Verifying IEC communication

To verify that the IEC communication with the IEC master system is working, there are some different methods. Choose one of the following.

Procedure

1. Check that the master system time-out for response from the IED, for example after a setting change, is > 40 seconds.
2. Use a protocol analyzer and record the communication between the IED and the IEC master. Check in the protocol analyzer’s log that the IED answers the master messages.
3. Generate one binary event by activating a function that is configured to an event block where the used input is set to generate events on IEC. The configuration must be made with the PCM600 software. Verify that the event is presented in the IEC master system.

During the following tests of the different functions in the IED, verify that the events and indications in the IEC master system are as expected.

6.3 Fiber optic loop

The SPA communication is mainly used for SMS. It can include different numerical IEDs with remote communication possibilities. The fiber optic loop can contain < 20-30 IEDs depending on requirements on response time. Connection to a personal computer (PC) can be made directly (if the PC is located in the substation) or by telephone modem through a telephone network with ITU (CCITT) characteristics.

Table 12: Max distances between IEDs/nodes

<table>
<thead>
<tr>
<th>Material</th>
<th>Distance</th>
</tr>
</thead>
<tbody>
<tr>
<td>glass</td>
<td>&lt; 1000 m according to optical budget</td>
</tr>
<tr>
<td>plastic</td>
<td>&lt; 25 m (inside cubicle) according to optical budget</td>
</tr>
</tbody>
</table>
6.4 Optical budget calculation for serial communication with SPA/IEC

Table 13: Example

<table>
<thead>
<tr>
<th></th>
<th>Distance 1 km Glass</th>
<th>Distance 25 m Plastic</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum attenuation</td>
<td>-11 dB</td>
<td>-7 dB</td>
</tr>
<tr>
<td>4 dB/km multi mode: 820 nm - 62.5/125 um</td>
<td>4 dB</td>
<td>-</td>
</tr>
<tr>
<td>0.16 dB/m plastic: 620 nm - 1mm</td>
<td>-</td>
<td>4 dB</td>
</tr>
<tr>
<td>Margins for installation, aging, and so on</td>
<td>5 dB</td>
<td>1 dB</td>
</tr>
<tr>
<td>Losses in connection box, two contacts (0.5 dB/contact)</td>
<td>1 dB</td>
<td>-</td>
</tr>
<tr>
<td>Losses in connection box, two contacts (1 dB/contact)</td>
<td>-</td>
<td>2 dB</td>
</tr>
<tr>
<td>Margin for 2 repair splices (0.5 dB/splice)</td>
<td>1 dB</td>
<td>-</td>
</tr>
<tr>
<td>Maximum total attenuation</td>
<td>11 dB</td>
<td>7 dB</td>
</tr>
</tbody>
</table>
Section 7 Establishing connection and verifying the LON communication

7.1 Communication via the rear ports

7.1.1 LON communication

LON communication is normally used in substation automation systems. Optical fiber is used within the substation as the physical communication link.

The test can only be carried out when the whole communication system is installed. Thus, the test is a system test and is not dealt with here.

An optical network can be used within the substation automation system. This enables communication with the IEDs through the LON bus from the operator’s workplace, from the control center and also from other IEDs via bay-to-bay horizontal communication. For LON communication an SLM card should be ordered for the IEDs.

The fiber optic LON bus is implemented using either glass core or plastic core fiber optic cables.

Figure 8: Example of LON communication structure for a substation automation system
### Table 14: Specification of the fiber optic connectors

<table>
<thead>
<tr>
<th></th>
<th>Glass fiber</th>
<th>Plastic fiber</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cable connector</td>
<td>ST-connector</td>
<td>snap-in connector</td>
</tr>
<tr>
<td>Cable diameter</td>
<td>62.5/125 m</td>
<td>1 mm</td>
</tr>
<tr>
<td>Max. cable length</td>
<td>1000 m</td>
<td>10 m</td>
</tr>
<tr>
<td>Wavelength</td>
<td>820-900 nm</td>
<td>660 nm</td>
</tr>
<tr>
<td>Transmitted power</td>
<td>-13 dBm (HFBR-1414)</td>
<td>-13 dBm (HFBR-1521)</td>
</tr>
<tr>
<td>Receiver sensitivity</td>
<td>-24 dBm (HFBR-2412)</td>
<td>-20 dBm (HFBR-2521)</td>
</tr>
</tbody>
</table>

### 7.1.2 The LON Protocol

The LON protocol is specified in the LonTalkProtocol Specification Version 3 from Echelon Corporation. This protocol is designed for communication in control networks and is a peer-to-peer protocol where all the devices connected to the network can communicate with each other directly. For more information of the bay-to-bay communication, refer to the section Multiple command function.

### 7.1.3 Hardware and software modules

The hardware needed for applying LON communication depends on the application, but one very central unit needed is the LON Star Coupler and optical fibers connecting the star coupler to the IEDs. To interface the IEDs from the MicroSCADA with Classic Monitor, application library LIB520 is required.

The HV Control 670 software module is included in the LIB520 high-voltage process package, which is a part of the Application Software Library in MicroSCADA applications.

The HV Control 670 software module is used for control functions in the IEDs. The module contains a process picture, dialogues and a tool to generate a process database for the control application in MicroSCADA.

When using MicroSCADA Monitor Pro instead of the Classic Monitor, SA LIB is used together with 670 series Object Type files.

The HV Control 670 software module and 670 series Object Type files are used with both 650 and 670 series IEDs.

Use the LON Network Tool (LNT) to set the LON communication. This is a software tool applied as one node on the LON bus. To communicate via LON, the IEDs need to know

- The node addresses of the other connected IEDs.
- The network variable selectors to be used.

This is organized by LNT.

The node address is transferred to LNT via the local HMI by setting the parameter **ServicePinMsg = Yes.** The node address is sent to LNT via the LON bus, or LNT can scan the network for new nodes.
The communication speed of the LON bus is set to the default of 1.25 Mbit/s. This can be changed by LNT.

The LON communication setting parameters are set via the local HMI. Refer to the Technical manual for more detailed specifications.

If LON communication from the IED stops because of illegal communication parameter settings (outside the setting range) or due to other kind of disturbance, it is possible to reset the IED's LON port.

LON communication setting parameters (Table 15) and LON node information parameters (Table 16) can only be set via the LON Network Tool (LNT).

Some of these parameters can be viewed on the local HMI under Main menu/Configuration/Communication/Station communication/Port configuration/SLM optical LON port/LONGEN:1.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Range</th>
<th>Default</th>
<th>Unit</th>
<th>Parameter description</th>
</tr>
</thead>
<tbody>
<tr>
<td>DomainID</td>
<td>0</td>
<td>0</td>
<td>-</td>
<td>Domain identification number</td>
</tr>
<tr>
<td>SubnNetAddr*</td>
<td>0 - 255</td>
<td>0</td>
<td>-</td>
<td>Subnet address</td>
</tr>
<tr>
<td>NodeAddr*</td>
<td>0 - 127</td>
<td>0</td>
<td>-</td>
<td>Node address</td>
</tr>
<tr>
<td>NeuronID*</td>
<td>0 - 12</td>
<td>Not loaded</td>
<td>-</td>
<td>Neuron hardware identification number in hexadecimal code</td>
</tr>
<tr>
<td>Location</td>
<td>0 - 6</td>
<td>No value</td>
<td>-</td>
<td>Location of the node</td>
</tr>
</tbody>
</table>

*Can be viewed on the local HMI

ADE settings are available on the local HMI under Main menu/Configuration/Communication/Station communication/LON/ADE:1

<table>
<thead>
<tr>
<th>Name</th>
<th>Values (Range)</th>
<th>Unit</th>
<th>Step</th>
<th>Default</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operation</td>
<td>Disabled</td>
<td>-</td>
<td>-</td>
<td>Disabled</td>
<td>Operation</td>
</tr>
<tr>
<td>TimerClass</td>
<td>Slow Normal Fast</td>
<td>-</td>
<td>-</td>
<td>Slow</td>
<td>Timer class</td>
</tr>
</tbody>
</table>

LON commands are available on the local HMI under Main menu/Configuration/Communication/Station communication/Port configuration/SLM optical LON port/Service Pin Message/Generate service pin message
Table 18: LON commands

<table>
<thead>
<tr>
<th>Command</th>
<th>Command description</th>
</tr>
</thead>
<tbody>
<tr>
<td>ServPinMess</td>
<td>Command with confirmation. Transfers the node address to the LON Network Tool.</td>
</tr>
</tbody>
</table>

7.2 Optical budget calculation for serial communication with LON

Table 19: Example

<table>
<thead>
<tr>
<th></th>
<th>Distance 1 km Glass</th>
<th>Distance 10 m Plastic</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum attenuation</td>
<td>-11 dB</td>
<td>-7 dB</td>
</tr>
<tr>
<td>4 dB/km multi mode: 820 nm - 62.5/125 um</td>
<td>4 dB</td>
<td>-</td>
</tr>
<tr>
<td>0.3 dB/m plastic: 620 nm - 1mm</td>
<td>-</td>
<td>3 dB</td>
</tr>
<tr>
<td>Margins for installation, aging, and so on</td>
<td>5 dB</td>
<td>2 dB</td>
</tr>
<tr>
<td>Losses in connection box, two contacts (0.75 dB/contact)</td>
<td>1.5 dB</td>
<td>-</td>
</tr>
<tr>
<td>Losses in connection box, two contacts (1dB/contact)</td>
<td>-</td>
<td>2 dB</td>
</tr>
<tr>
<td>Margin for repair splices (0.5 dB/splice)</td>
<td>0.5 dB</td>
<td>-</td>
</tr>
<tr>
<td>Maximum total attenuation</td>
<td>11 dB</td>
<td>7 dB</td>
</tr>
</tbody>
</table>
Section 8  Establishing connection and verifying the IEC 61850 communication

8.1  Overview

The rear optical Ethernet ports are used for:

- process bus (IEC/UCA 61850-9-2LE) communication
- IEEE C37.118/1344 communication
- substation bus (IEC 61850-8-1) communication

8.2  Setting the station communication

To enable IEC 61850 communication, the corresponding ports must be activated. The rear access points can be used for IEC 61850-8-1 and IEC/UCA 61850-9-2LE communication. IEC 61850-8-1 redundant communication requires the use of two Ethernet ports, when it is activated for Ethernet port the next immediate port is selected as the second Ethernet port and will become hidden.

To enable IEC 61850 station communication:

In this example access points 1, 2 and 3 are used.

1. Activate IEC 61850-8-1 (substation bus) communication.
   Navigate to: Main menu/Configuration/Communication/Station communication/IEC61850–8–1/IEC61850–8–1:1
2. Enable operation for Access Point 1 and 3 in the Ethernet configuration tool (ECT) in PCM600.
3. Enable redundant communication for Access point 1 using ECT.
4. Enable IEC/UCA 61850-9-2LE (process bus) communication for Access point 3 by connecting it to a Merging unit in the Merging units tab in ECT.

There are no settings needed for the IEC/UCA 61850-9-2LE communication in the local HMI branch Station communication. Make sure that the optical fibers are connected correctly. Communication is enabled whenever the merging unit starts sending data.

8.3  Verifying the communication

To verify that the communication is working a test/analyzing tool, for example ITT600, can be used.
Verifying redundant IEC 61850-8-1 communication

Ensure that the IED receives IEC 61850-8-1 data on the selected Ethernet ports. Browse in the local HMI to Main menu/Diagnostics/Communication/Ethernet status/Access point/APStatusRedundant/RCHLCCCH:1 and check that ChARedStatus and ChBRedStatus are shown as Ok. Remove the optical connection to one of the Ethernet ports. Verify that either signal status (depending on which connection that was removed) is shown as Error and the other signal is shown as Ok. Be sure to re-connect the removed connection after completed verification.
Section 9 Establishing connection and verifying the IEEE C37.118/1344 communication

9.1 Overview

The IED can support synchrophasor data communication via IEEE C37.118 and/or IEEE1344 with maximum 8 TCP clients and 6 UDP client groups, simultaneously.

The rear OEM ports are used for IEEE C37.118/1344 communication. The same ports can also be used for substation bus (IEC 61850-8-1) communication and process bus (IEC/UCA 61850-9-2LE) communication where applicable.

In order to establish a connection between PMU and a TCP/UDP client (PDC client), an Ethernet communication link (e.g. optical fiber) shall be available and the required parameters on both PMU and the TCP/UDP client shall be set. For the purpose of this section, an Open PDC tool (PMU Connection Tester) installed on a PC is used as the TCP/UDP client.

9.2 Setting the PMU station communication (PMU Report)

1. Check the settings for the PMU Report parameters by navigating to: Main menu/Configuration/Communication/Station communication/phasor measurement/PMU Report/PMUREPORT:1
   1.1. Make sure that the operation of at least one PMU instance (e.g. PMU1) is Enabled.
   1.2. Enable sending the frequency data by setting the parameter SendFreqInfo to Enabled.
2. Check the operation of the required phasor channels (PHASORREPORT) by navigating to: Main menu/Configuration/Communication/Station communication/phasor measurement/PMU Report/PHASORREPORT:x.

9.3 Setting the PMU station communication (PMU configuration)

To enable IEEE C37.118/1344 communication, the corresponding OEM ports must be activated. The galvanic Ethernet front port and the rear optical ports can be used for IEEE C37.118/1344 communication.

To enable IEEE C37.118/1344 synchrophasor communication:

1. Enable communication for by navigating to: Main menu/Configuration/Communication/Ethernet configuration.
1. Select the port.
2. Set values for Mode, IPAddress and IPMask. Mode must be set to Normal.
3. Check that the correct IP address is assigned to the port.

2. Set the TCP communication parameters by navigating to: Main menu/Configuration/Communication/Station communication/phasor measurement/PMU Configuration/PMUCONF. The first three parameters are related to TCP communication. The default TCP parameters setting can be used. More information regarding TCP communication is available in the Application manual under section Short guidance for use of TCP.

3. Set the UDP communication parameters by navigating to: Main menu/Configuration/Communication/Station communication/phasor measurement/PMU Configuration/PMUCONF. The first three parameters are related to TCP communication and the rest of parameters are related to setting of six independent UDP client groups. Each UDP group has eight parameters. More information regarding UDP communication is available in the Application manual under section Short guidance for use of UDP. The following steps describes setting parameters for the first UDP group:

   3.1. Enable the first UDP group by setting the parameter SendDataUDP1 to Enabled.
   3.2. Set the UDP client IP address by setting the parameter UDPDestAddress1 to 192.168.1.99. For the purpose of the communication set-up in this section the LAN IP address of the PC is used, where the PDC client tool is installed.
   3.3. The default parameter settings can be used for the rest of parameters for the first UDP group.

9.4 Setting the TCP/UDP client communication

As an example of a TCP/UDP client, the openPDC tool (PMU Connection Tester Ver. 4.2.12) from Grid Protection Alliance is used in this section. Install PMU Connection Tester tool on a PC with Ethernet network adaptor available. The same PC used for PCM600 can be used to install the PMU Connection Tester tool.
Figure 9: PMU Connection Tester tool

The following steps explain how to set the PMU Connection Tester parameters in order to establish an IEEE C37.118 connection with the PMU:

1. Set the IP stack on PMU Connection Tester to IPv4. Note that the default IP stack on PMU Connection Tester tool is IPv6.
1. Navigate to the **Settings** tab.
2. Force the IP stack to IPv4 by setting the parameter `ForceIPv4` to **True**.

Set the Connection Parameters on PMU Connection Tester for TCP communication according to the PMU configuration.
2.1. Set *Host IP* to the PMU IP address configured for the port in use. Here the LANAB:1 IPAddress (192.168.1.10) is set.

2.2. Set *Port* to the IED's TCP port set in the PMU under parameter C37.118TCPport (4712 is default). Alternatively, in order to make an IEEE1344 communication, the 1344TCPport parameter setting can be used (4713 is default).

2.3. Set the *Protocol* as IEEE C37.118.2-2011.

2.4. Set the *Device ID Code* in PMU Connection Tester per PMU Data Stream ID Number (IDCODE). The PMU Data Stream ID Number is a user assigned ID number (1-65534) for each data stream sent out from the PMU and it is defined under parameter PMUdataStreamIDCODE. (Main menu/Configuration/Communication/Station communication/phaser measurement/PMU Report/PMUREPORT:1)

2.5. Make sure that the *Configure Alternate Command Channel* is set as *Not Defined*. The *Configure Alternate Command Channel* is used to configure a TCP channel to control the UDP data communication and transfer the header, configuration and command frames.

2.6. Continue to the section [Verifying the IEEE C37.118/1344 TCP communication](#).

3. Set the Connection Parameters on PMU Connection Tester for UDP communication according to the PMU configuration.

![PMU Connection Tester](image)

3.1. Set the *Local Port* to UDP destination port defined in the PMU under the parameter UDPDestPort1 (Default value: 8910).

3.2. Click the *Configure Alternate Command Channel* to configure the TCP channel. A new window will pop up. A TCP channel needs to be configured in order to control the UDP data communication and transfer the header, configuration and command frames.
3.3. Set the Host IP as the PMU IP address configured for the port in use. Here the LANAB1 IPAddress (192.168.1.10) is set.
3.4. Set the Port as the TCP port defined in the PMU for control of data sent over UDP client group 1 (Default value: 4713). This can be found under the parameter TCPportUDPdataCtrl1 as one of the UDP communication parameters.
3.5. Save the Alternate Command Channel Configuration settings.
3.6. Continue to the section Verifying the IEEE C37.118/1344 UDP communication

9.5 Verifying the communication

Connect your PC to the substation network and ping the connected IED and the Substation Master PC, to verify that the communication is working (up to the transport layer). Make sure that the optical fibers are connected correctly.

After checking the communication, it is time to verify the IEEE C37.118/1344 communication over both TCP and UDP protocols.

One way to verify the communication up to the application layer is to use a protocol analyzer connected to the substation bus, and monitor the communication. Alternatively, the PMU Connection Tester tool can be used to verify the IEEE C37.118/1344 communication. The section Setting the TCP/UDP client communication explains how to set the PMU Connection Tester parameters in order to establish an IEEE C37.118 connection with the PMU.

9.5.1 Verifying the IEEE C37.118/1344 TCP communication

After setting both PMU configuration and the TCP client configuration (As explained in sections Setting the PMU station communication (PMU Report), Setting the PMU station communication (PMU configuration) and Setting the TCP/UDP client communication) and making sure that the Ethernet communication is set up, click Connect on the PMU Connection Tester tool.
Figure 10: Verifying the TCP communication using PMU Connection Tester

- Now it should be possible to see the streaming synchrophasor data. In the Graph tab, observe the Frequency, data Reporting Rate, Phasor names, and Phase angles of the reported synchrophasors. Observe the real-time frame details of the Data Frame in the bottom of the window.

Figure 11: Graphic view over streaming synchrophasor data

- Open the drop-down menu in the Command field. There is a list of commands that can be sent from the client (PMU Connection Tester) to the PMU. Try different commands and make sure that the PMU is receiving and responding to them.
Figure 12: Drop-down menu with commands for testing the PMU

- Switch to the Protocol Specific tab. Here, all the IEEE C37.118 message types can be seen. If the HeaderFrame is not included, ask the PMU to send the header frame via the Send Header Frame command (Previous stage). Open each message type and observe the content of each message.
Figure 13: *All the IEEE C37.118 message types*

- It is also possible to capture the IEEE C37.118 synchrophasor data in an Excel file. This is done by navigating to **File/Capture/Start Stream Debug Capture...** The tool will ask to **Set Stream Debug Capture File Name** and path to save the capture file.
Figure 14: Start capturing the IEEE C37.118 synchrophasor data

- The synchrophasor data capturing process can be stopped at any point of time by navigating to File/Capture/Stop Stream Debug Capture...
Open the capture file and observe the captured synchrophasor data. In order to get the Phasor names on top of each column (See figure 16), the capture process should start before connecting the PMU Connection Tester to the PMU, i.e. first start the capturing and then click Connect.
Figure 16: Captured synchrophasor data

9.5.2 Verifying the IEEE C37.118/1344 UDP communication

After setting both PMU configuration and the UDP client configuration (As explained in sections Setting the PMU station communication (PMU Report), Setting the PMU station communication (PMU configuration) and Setting the TCP/UDP client communication) and making sure that the Ethernet communication is set up, click Connect on the PMU Connection Tester tool.

Now it should be possible to see the streaming synchrophasor data.

Verify the communication by following the same steps as in section Verifying the IEEE C37.118/1344 TCP communication.
Most of the times, the PMU IEDs are located in the substations. A local PDC might be located in the substation. For communications within the substation or between the IED and the WAN/LAN access point, it is important to know what is the optical budget available. The graph in Figure 18 shows the dynamic range available for a PMU – PDC configuration using typical OEMs.

As shown in the graph, if one uses a 62.5/125 µm fiber, the value under the 62.5/125 µm curve represents the remaining optical budget at any link length, which is available for overcoming non-fiber cable related losses.

Losses in the connectors and splices are typically 0.3dB/connection. The user must reserve 3dB spare for the uncertainty of the measurements.
Section 10  
Testing IED operation

10.1 Preparing for test

10.1.1 Requirements

IED test requirements:

- Calculated settings
- Application configuration diagram
- Signal matrix (SMT) configuration
- Terminal connection diagram
- Technical manual
- Three-phase test equipment
- Process bus, IEC/UCA 61850-9-2LE, MU test simulator, if IEC/UCA 61850-9-2LE process bus communication is used.
- PCM600

The setting and configuration of the IED must be completed before the testing can start.

The terminal diagram, available in the technical reference manual, is a general diagram of the IED.

Note that the same diagram is not always applicable to each specific delivery (especially for the configuration of all the binary inputs and outputs).

Therefore, before testing, check that the available terminal diagram corresponds to the IED.

The technical manual contains application and functionality summaries, function blocks, logic diagrams, input and output signals, setting parameters and technical data sorted per function.

The test equipment should be able to provide a three-phase supply of voltages and currents. The magnitude of voltage and current as well as the phase angle between voltage and current must be variable. The voltages and currents from the test equipment must be obtained from the same source and they must have minimal harmonic content. If the test equipment cannot indicate the phase angle, a separate phase-angle measuring instrument is necessary.

Prepare the IED for test before testing a particular function. Consider the logic diagram of the tested protection function when performing the test. All included functions in the IED are tested according to the corresponding test instructions in this chapter. The functions can be tested in any order according to user preferences. Only the functions that are used (Operation is set to Enabled) should be tested.

The response from a test can be viewed in different ways:

- Binary outputs signals
- Service values on the local HMI (logical signals or phasors)
- A PC with PCM600 application configuration software in work online mode
All setting groups that are used should be tested.

This IED is designed for a maximum continuous current of four times the rated current.

All references to CT and VT must be interpreted as analog values received from merging units (MU) via IEC/UCA 61850-9-2LE communication protocol, analog values received from the transformer input module, or analog values received from the LDCM.

When using a MU test simulator, make sure it is set to the correct SVID and that the system frequency is set to the same as in the IED.

Please observe the measuring accuracy of the IED, the test equipment and the angular accuracy for both of them.

Consider the configured logic from the function block to the output contacts when measuring the trip time.

After intense testing, it is important that the IED is not immediately restarted, which might cause a faulty trip due to flash memory restrictions. Some time must pass before the IED is restarted. For more information about the flash memory, refer to section “Configuring the IED and changing settings”.

**10.1.2 Preparing the IED to verify settings**

If a test switch is included, start preparation by making the necessary connections to the test switch. This means connecting the test equipment according to a specific and designated IED terminal diagram.

Put the IED into the test mode to facilitate the test of individual functions and prevent unwanted operation caused by other functions. The busbar differential protection is not included in the test mode and is not prevented to trip during the test operations. The test switch should then be connected to the IED.

Verify that analog input signals from the analog input module are measured and recorded correctly by injecting currents and voltages required by the specific IED.

To make testing even more effective, use PCM600. PCM600 includes the Signal monitoring tool, which is useful in reading the individual currents and voltages, their amplitudes and phase angles. In addition, PCM600 contains the Disturbance handling tool. The content of reports generated by the Disturbance handling tool can be configured which makes the work more efficient. For
example, the tool may be configured to only show time tagged events and to exclude analog information and so on.

Check the disturbance report settings to ensure that the indications are correct.

For information about the functions to test, for example signal or parameter names, see the technical manual. The correct initiation of the disturbance recorder is made on pickup and/or release or trip from a function. Also check that the wanted recordings of analog (real and calculated) and binary signals are achieved.

Parameters can be entered into different setting groups. Make sure to test functions for the same parameter setting group. If needed, repeat the tests for all different setting groups used. The difference between testing the first parameter setting group and the remaining is that there is no need for testing the connections.

During testing, observe that the right testing method, that corresponds to the actual parameters set in the activated parameter setting group, is used.

Set and configure the function(s) before testing. Most functions are highly flexible and permit a choice of functional and tripping modes. The various modes are checked at the factory as part of the design verification. In certain cases, only modes with a high probability of coming into operation need to be checked when commissioned to verify the configuration and settings.

### 10.2 Activating the test mode

Put the IED into the test mode before testing. The test mode blocks all protection functions and some of the control functions in the IED, and the individual functions to be tested can be unblocked to prevent unwanted operation caused by other functions. In this way, it is possible to test slower back-up measuring functions without the interference from faster measuring functions. The test switch should then be connected to the IED. Test mode is indicated when the yellow PickupLED flashes.

It is important that the IED function to be tested is put into test mode, even if the MU is sending data marked as "test". The IED will interpret these data as valid if it is not in test mode.

1. Browse to the **TESTMODE** menu and press *E*.
2. Use the up and down arrows to choose *Enabled* and press *E*.
3. Press the left arrow to exit the menu.
4. The dialog box *Save changes* appears.
5. Choose *Yes*, press *E* and exit the menu.
6. The yellow pickupLED above the LCD will start flashing when the IED is in test mode.

### 10.3 Preparing the connection to the test equipment

The IED can be equipped with a test switch of type RTXP8, RTXP18 or RTXP24 or FT. The test switch and its associated test plug handles are a part of the COMBITEST or FT system of ABB, which provides secure and convenient testing of the IED.
When using the COMBITEST, preparations for testing are automatically carried out in the proper sequence, that is, for example, blocking of tripping circuits, short circuiting of CTs, opening of voltage circuits, making IED terminals available for secondary injection. Terminals 1 and 8, 1 and 18 as well as 1 and 12 of the test switches RTXP8, RTXP18 and RTXP24 respectively are not disconnected as they supply DC power to the protection IED. When FT switch is used for testing, care shall be exercised to open the tripping circuit, ahead of manipulating the CT fingers.

The RTXH test-plug handle leads may be connected to any type of test equipment or instrument. When a number of protection IEDs of the same type are tested, the test-plug handle only needs to be moved from the test switch of one protection IED to the test switch of the other, without altering the previous connections.

Use COMBITEST test system to prevent unwanted tripping when the handle is withdrawn, since latches on the handle secure it in the half withdrawn position. In this position, all voltages and currents are restored and any re-energizing transients are given a chance to decay before the trip circuits are restored. When the latches are released, the handle can be completely withdrawn from the test switch, restoring the trip circuits to the protection IED.

If a test switch is not used, perform measurement according to the provided circuit diagrams.

Never disconnect the secondary connection of a current transformer circuit without first short-circuiting the transformer's secondary winding. Operating a current transformer with the secondary winding open will cause a massive potential build up that may damage the transformer and cause personal injury.

### 10.4 Connecting the test equipment to the IED

Connect the test equipment according to the IED specific connection diagram and the needed input and output signals for the function under test. An example of a connection is shown in figure 19.

Connect the current and voltage terminals. Pay attention to the current polarity. Make sure that the connection of input and output current terminals and the connection of the residual current conductor is correct. Check that the input and output logical signals in the logic diagram for the function under test are connected to the corresponding binary inputs and outputs of the IED under test.

To ensure correct results, make sure that the IED as well as the test equipment are properly grounded before testing.
10.5 Releasing the function to be tested

Release or unblock the function to be tested. This is done to ensure that only the function or the chain of functions to be tested are in operation and that other functions are prevented from operating. Release the tested function(s) by setting the corresponding Blocked parameter under Function test modes to No in the local HMI.

When testing a function in this blocking feature, remember that not only the actual function must be activated, but the whole sequence of interconnected functions (from measuring inputs to binary output contacts), including logic must be activated. Before starting a new test mode session, scroll through every function to ensure that only the function to be tested (and the interconnected ones) have the parameters Blocked and eventually EvDisable set to No and Yes respectively. Remember that a function is also blocked if the BLOCK input signal on the corresponding function block is active, which depends on the configuration. Ensure that the logical status of the BLOCK input signal is equal to 0 for the function to be tested. Event function blocks can also be individually blocked to ensure that no events are reported to a remote station during the test. This is done by setting the parameter EvDisable to Yes.

Any function is blocked if the corresponding setting in the local HMI under Main menu/Test/Function test modes menu remains Enabled, that is, the parameter Blocked is set to Yes and the parameter TESTMODE under Main menu/Test/IED test mode remains active. All functions that were blocked or released in a previous test mode session, that is, the parameter Test mode is set to Enabled, are reset when a new test mode session is started.

Procedure
1. Click the Function test modes menu. The Function test modes menu is located in the local HMI under Main menu/Test/Function test modes.
2. Browse to the function instance that needs to be released.
3. Set parameter Blocked for the selected function to No.

### 10.6 Verifying analog primary and secondary measurement

Verify that the connections are correct and that measuring and scaling is done correctly. This is done by injecting current and voltage to the IED.

Besides verifying analog input values from the merging unit via the IEC/UCA 61850-9-2LE process bus, analog values from the transformer input module can be verified as follows.

- Apply input signals as needed according to the actual hardware and the application configuration.

1. Inject a symmetrical three-phase voltage and current at rated value.
2. Compare the injected value with the measured values. The voltage and current phasor menu in the local HMI is located under Main menu/Measurements/Analog primary values and Main menu/Measurements/Analog secondary values.
3. Compare the frequency reading with the set frequency and the direction of the power. The frequency and active power are located under Main menu/Measurements/Monitoring/ServiceValues(MMXN)/CVMMXN:x. Then navigate to the bottom of the list to find the frequency.
4. Inject an unsymmetrical three-phase voltage and current, to verify that phases are correctly connected.

If some setting deviates, check the analog input settings under Main menu/Configuration/Analog modules

If the IEC/UCA 61850-9-2LE communication is interrupted during current injection, the report tool in PCM600 will display the current that was injected before the interruption.
10.7 Testing the protection functionality

Each protection function must be tested individually by secondary injection.

- Verify operating levels (trip) and timers.
- Verify alarm and blocking signals.
- Use the disturbance handling tool in PCM600 to evaluate that the protection function has received the correct data and responded correctly (signaling and timing).
- Use the event viewer tool in PCM600 to check that only expected events have occurred.

10.8 Forcing of binary input/output signals for testing

10.8.1 Forcing concept

Forcing of binary inputs and outputs is a convenient way to test wiring in substations as well as testing configuration logic in the IEDs. Basically it means that all binary inputs and outputs on the IED I/O modules (BOM, BIM, IOM & SOM) can be set to a value (i.e active or not-active), selected by the user, while the IED is in test mode. For inputs, this is true regardless of the actual signal voltage present on the input. For outputs, any output relay can be forced to be active or not, regardless of the current requested state of the output in the IED logic configuration.
Be observant that forcing of binary inputs and outputs on an IED, with inappropriate setup, can result in potential danger.

10.8.2 How to enable forcing

To enable forcing, the IED must first be put into IED test mode. While the IED is not in test mode, the LHMI/PCM600 menus that relate to forcing will not have any effect on the input/output status due to safety reasons.

10.8.2.1 Enable forcing by using LHMI

1. Enable IED TESTMODE by setting IEDTestMode to On under Main menu/Test/IED test mode/TESTMODE:1.

2. Exit back to the root menu.
3. Select Yes in the save dialogue box. Once the IED is in test mode the yellow Start LED starts to blink.

10.8.2.2 Enable forcing using TESTMODE function block

- Use the TESTMODE function block, appropriately configured in PCM600/ACT.

It may be convenient to control the input on mentioned component from, for example, an LHMI function key or similar during commissioning to quickly and easily enter IED test mode.

10.8.3 How to change binary input/output signals using forcing

Once the IED is in IED test mode, the LHMI/PCM600 menus can be used to control input/output signals freely.
10.8.3.1 Forcing by using LHMI

Editing a signal value directly

- Edit the input/output value directly to select the desired logical level, by doing following:

1. Select the value line of the desired signal, see figure 21.
2. Press the Enter key to edit the value.

![Figure 21: Value line of the desired signal](image)

3. Use the up/down arrows on the LHMI to change the signal value or the appropriate menu in PCM600.
   The status of the signal changes automatically to Forced (i.e. there is no need to set the status to Forced manually).

On the LHMI, these edit changes have immediate effect. This means that the value changes directly when the up/down arrow is pressed (i.e. there is no need to press the Enter key to effectuate the change).

When navigating away from a LHMI forcing menu for an I/O board, the user is prompted to either leave the signals forced, or to revert all of them back to the unforced state.
It is possible to power-cycle the IED in this state without losing the forcing states and values. This means that once a signal is forced, and the IED remains in IED test mode, the input or output will appear “frozen” at the value selected by the user, even if the IED is switched off and back on again.

### Freezing a signal value without changing it

- Set the status of a signal to Forced, in the forcing menu that corresponds to the I/O card in question. See example of LHMI menu in figure 22

![Example of LHMI menu using BIM3](image)

**Figure 22:** Example of LHMI menu using BIM3

The signal “freezes” and will not change value even if, for example, a binary input signal voltage changes level, or if a binary output is activated as the result of a protection function block activating.

### 10.8.3.2 Forcing by using PCM600

In PCM600 the concept is a bit different compared to LHMI. The forcing is accomplished by entering a forcing session. Within such a session, multiple signals on multiple I/O boards may be edited and changed at the same time and has no effect on the IED. Once the user is satisfied with the forcing setup, then all the changes can be effectuated simultaneously towards the IED, potentially changing inputs and outputs on multiple I/O boards at the same time. It is also possible to abort this operation (described in step 6 below) and to undo all forcing.

1. Right click on the IED in the plant structure and select **Signal Monitoring**.
2. Click on the **List View** tab.
3. Click **Forcing Session** in the menu **IED/Start Forcing**.

![Forcing Session menu](image)

4. Click **Start editing signal value for forcing** on the tool bar.
The Signal Monitoring menu changes and indicates the forcing values that can be edited.

<table>
<thead>
<tr>
<th>Index</th>
<th>Module Name</th>
<th>Module Type</th>
<th>Channel Name</th>
<th>Channel Type</th>
<th>Signal Name</th>
<th>Signal Value</th>
<th>Signal Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>91</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B04</td>
<td>Binary Input</td>
<td>B04</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>92</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B05</td>
<td>Binary Input</td>
<td>B05</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>93</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B06</td>
<td>Binary Input</td>
<td>B06</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>94</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B07</td>
<td>Binary Input</td>
<td>B07</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>95</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B08</td>
<td>Binary Input</td>
<td>B08</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>96</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B09</td>
<td>Binary Input</td>
<td>B09</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>97</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B10</td>
<td>Binary Input</td>
<td>B10</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>98</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B11</td>
<td>Binary Input</td>
<td>B11</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>99</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B12</td>
<td>Binary Input</td>
<td>B12</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>100</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B13</td>
<td>Binary Input</td>
<td>B13</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>101</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B14</td>
<td>Binary Input</td>
<td>B14</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>102</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B15</td>
<td>Binary Input</td>
<td>B15</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>103</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B16</td>
<td>Binary Input</td>
<td>B16</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>104</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B17</td>
<td>Binary Input</td>
<td>B17</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>105</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B18</td>
<td>Binary Input</td>
<td>B18</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>106</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B19</td>
<td>Binary Input</td>
<td>B19</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>107</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B20</td>
<td>Binary Input</td>
<td>B20</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>108</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B21</td>
<td>Binary Input</td>
<td>B21</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>109</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B22</td>
<td>Binary Input</td>
<td>B22</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>110</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B23</td>
<td>Binary Input</td>
<td>B23</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>111</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B24</td>
<td>Binary Input</td>
<td>B24</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>112</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B25</td>
<td>Binary Input</td>
<td>B25</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>113</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B26</td>
<td>Binary Input</td>
<td>B26</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>114</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B27</td>
<td>Binary Input</td>
<td>B27</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>115</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B28</td>
<td>Binary Input</td>
<td>B28</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>116</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B29</td>
<td>Binary Input</td>
<td>B29</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>117</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B30</td>
<td>Binary Input</td>
<td>B30</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>118</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B31</td>
<td>Binary Input</td>
<td>B31</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>119</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B32</td>
<td>Binary Input</td>
<td>B32</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>120</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B33</td>
<td>Binary Input</td>
<td>B33</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>121</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B34</td>
<td>Binary Input</td>
<td>B34</td>
<td>false</td>
<td></td>
</tr>
<tr>
<td>122</td>
<td>0M_S</td>
<td>Hardware Module (Hardware HDO)</td>
<td>B35</td>
<td>Binary Input</td>
<td>B35</td>
<td>false</td>
<td></td>
</tr>
</tbody>
</table>

5. Select and edit the values.
6. Click **Acknowledge and send**.

This commits the values to the IED and exits the editing session.

7. Click **Cancel** to abort the changes and revert back to actual IED values.

Regardless if the forcing changes are committed or canceled, the forcing is still active.

To force more signals, click the button *Start editing signal value for forcing* again.
10.8.4 How to undo forcing changes and return the IED to normal operation

Regardless of which input/output signals have been forced, all forced signals will return to their normal states immediately when the IED is taken out of test mode.

When the forcing is removed by exiting from IED test mode, both input and output signals may change values. This means that logic input signals may activate functions in the IED and that output relays may change state, which can be potentially dangerous.

10.8.4.1 Undo forcing by using TESTMODE component

- If the IED test mode was entered through the test mode function block:
  1. Deactivate the control input on that block.

  This immediately undoes all forcing, regardless of how it was accomplished and disabled all the way to force signals.

10.8.4.2 Undo forcing by using LHMI

- If the IED test mode was enabled to using the LHMI:
  1. Set IEDTestMode to Off in the LHMI menu.
  2. Exit from the menu and click Yes in the Save dialogue box.

  This immediately undoes all forcing, regardless of how it was accomplished and disabled.

10.8.4.3 Undo forcing by using PCM600

1. Uncheck Forcing Session under the menu IED.

   ![Forcing Session](image)

   IEC15000031-1-en

2. Click Yes in the confirmation dialogue box.
   PCM600 will revert all forced signals back to unforced and the real signal values will immediately take effect again.
This may change both binary input values and output relay states and will undo any forcing done by using the LHMI.
If the IED is left in test mode, then it is still possible to perform new forcing operations, both from LHMI and from PCM600
Section 11 Testing functionality by secondary injection

11.1 Testing disturbance report

11.1.1 Introduction

The following sub-functions are included in the disturbance report function:

- Disturbance recorder
- Event list
- Event recorder
- Trip value recorder
- Indications

If the disturbance report is enabled, then its sub-functions are also set up and so it is not possible to only disable these sub-functions. The disturbance report function is disabled (parameter Operation = Disabled) in PCM600 or the local HMI under Main menu/Settings/IED Settings/Monitoring/Disturbance report/DRPRDRE:1.

11.1.2 Disturbance report settings

When the IED is in test mode, the disturbance report can be made active or inactive. If the disturbance recorder is turned on during test mode, recordings will be made. When test mode is switched off all recordings made during the test session are cleared.

Setting OpModeTest for the control of the disturbance recorder during test mode are located on the local HMI under Main menu/Settings/IED Settings/Monitoring/Disturbance report/DRPRDRE:1.

11.1.3 Disturbance recorder (DR)

A Manual Trig can be started at any time. This results in a recording of the actual values from all recorded channels.

The Manual Trig can be started in two ways:

1. From the local HMI under Main menu/Disturbance records.
   1.1. Enter on the row at the bottom of the HMI called Manual trig.
       A new recording begins. The view is updated if you leave the menu and return.
   1.2. Navigate to General information or to Trip values to obtain more detailed information.
2. Open the Disturbance handling tool for the IED in the plant structure in PCM600.
2.1. Right-click and select *Execute manual Trig* in the window *Available recordings in IED*.
2.2. Read the required recordings from the IED.
2.3. Refresh the window *Recordings* and select a recording.
2.4. Right-click and select *Create Report or Open With* to export the recordings to any disturbance analyzing tool that can handle Comtrade formatted files.

Evaluation of the results from the disturbance recording function requires access to a PC either permanently connected to the IED or temporarily connected to the Ethernet port (RJ-45) on the front. The PCM600 software package must be installed in the PC.

Disturbance upload can be performed by the use of PCM600 or by any third party tool with IEC 61850 protocol. Reports can automatically be generated from PCM600. Disturbance files can be analyzed by any tool reading Comtrade formatted disturbance files.

It could be useful to have a printer for hard copies. The correct start criteria and behavior of the disturbance recording function can be checked when IED protective functions are tested.

When the IED is brought into normal service it is recommended to delete all recordings, made during commissioning to avoid confusion in future fault analysis.

All recordings in the IED can be deleted in two ways:

1. in the local HMI under *Main menu/Clear/Reset disturbances*, or
2. in the Disturbance handling tool in PCM600 by selecting *Delete all recordings in the IED...* in the window *Available Recordings in IED*.

### 11.1.4 Event recorder (ER) and Event list (EL)

The result from the event recorder and event list can be viewed on the local HMI or, after upload, in PCM600 as follows:

1. on the local HMI under *Main menu/Events*, or in more details via
2. the *Event Viewer* in PCM600.
   
   The internal FIFO register of all events will appear when the event viewer is launched.

When the IED is brought into normal service it is recommended to delete all events resulting from commissioning tests to avoid confusion in future fault analysis. All events in the IED can be cleared in the local HMI under *Main Menu/Clear/Clear internal event list* or *Main menu/Clear/Clear process event list*. It is not possible to clear the event lists from PCM600.

When testing binary inputs, the event list (EL) might be used instead. No uploading or analyzing of registrations is then needed since the event list keeps running, independent of start of disturbance registration.

### 11.2 Identifying the function to test in the technical reference manual

Use the technical manual to identify function blocks, logic diagrams, input and output signals, setting parameters and technical data.
11.3 Differential protection

11.3.1 Transformer differential protection T2WPDIF and T3WPDIF (87T)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.3.1.1 Verifying the settings

1. Go to Main menu/Test/Function test modes/Differential protection and make sure that the restricted earth fault protection, low impedance function REPFDIF (87N) is set to Disabled and that the four step residual overcurrent function EF4PTOC (S1N/67N) under Main menu/Test/Function test modes/Current protection is set to Disabled, since they are configured to the same current transformer inputs as the transformer differential protection. Make sure that the transformer differential functions T2WPDIF (87T) or T3WPDIF (87T) are unblocked.
2. Connect the test set for injection of three-phase currents to the current terminals of the IED, which are connected to the CTs on the HV side of the power transformer.
3. Increase the current in phase A until the protection function operates and note the operating current.
4. Check that the trip and alarm contacts operate according to the configuration logic.
5. Decrease the current slowly from operate value and note the reset value. Depending on the power transformer phase shift/vector group (Yd (Wye/Delta) and so on), the single-phase injection current may appear as differential current in one or two phases and the operating value of the injected single-phase current will be different.
6. Check in the same way the function by injecting current in phases B and C respectively.
7. Inject a symmetrical three-phase current and note the operate value.
8. Connect the timer and set the current to twice the operate value.
9. Switch on the current and note the operate time.
10. Check in the same way the measuring circuits connected to the CTs on the LV side and other current inputs to the transformer differential protection.
11. Finally check that trip information is stored in the event menu.

Information on how to use the event menu is found in the operator’s manual.

12. If available on the test set, a second harmonic current of about 20% (assumes 15% setting on I1/I2 ratio parameter) can be added to the fundamental frequency in phase A. Increase the current in phase A above the pickup value measured in step 6. Repeat test with current injection in phases B and C respectively. The balancing of currents flowing into and out of the differential zone is checked by primary injection testing. Fifth harmonic blocking can be tested in a similar way.

For more detailed formulas, please refer to the Application manual.
11.3.1.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.3.2 High impedance differential protection HZPDIF (87)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.3.2.1 Verifying the settings

1. Connect single-phase or three-phase test set to inject the operating voltage. The injection is done across the measuring branch.

   The required trip and alarm voltage, as well as the used stabilizing resistance value must be set in the function. Note as well that used CT input in the IED must have ratio set as 1:1. This is essential for the measurement of the expected value. Normally a slightly higher operating value is no problem as the sensitivity is not influenced much.

2. Increase the voltage and make note of the trip value Pickup. This is done by manual testing and without trip of the test set.
3. Connect the trip contact to the test set to stop the test set for measurement of trip times below.
4. Reduce the voltage slowly and make note of the reset value. The reset value must be high for this function.
5. Check the operating time by injecting a voltage corresponding to 1.2 x Pickup level. Make note of the measured trip time.
6. If required, verify the trip time at another voltage. Normally 2 x Pickup is selected.
7. If used, measure the alarm level operating value. Increase the voltage and make note of the trip value AlarmPickup. This is done with manual test and without trip of the test set.
8. Measure the operating time on the alarm output by connecting the stop of the test set to an output from tAlarm. Inject a voltage 1.2 x AlarmPickup and measure the alarm time.
9. Check that trip and alarm outputs trip according to the configuration logic.
10. Finally check that pickup and alarm information is stored in the event menu and if a serial connection to the SA is available verify that the correct and only the required signals are presented on the local HMI and on the SCADA system.

   Information on how to use the event menu is found in the operator’s manual.

11.3.2.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.
11.3.3 Restricted earth fault protection, low impedance REFPDIF (87N)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.3.3.1 Verifying the settings

1. Connect the test set for single-phase current injection to the protection terminals connected to the CT in the power transformer neutral-to-ground circuit.
2. Increase the injection current and note the operating value of the protection function.
3. Check that all trip and pickup signals appear according to the configuration logic.
4. Decrease the current slowly from operate value and note the reset value.
5. Connect the timer and set the current to ten times the value of the \textit{IdMin} setting.
6. Switch on the current and note the operate time.
7. Connect the test set to terminal A and neutral of the three-phase current input configured to REFPDIF (87N). Also inject a current higher than half the \textit{IdMin} setting in the neutral-to-ground circuit with the same phase angle and with polarity corresponding to an internal fault.
8. Increase the current injected in A, and note the operate value. Decrease the current slowly and note the reset value.
9. Inject current into terminals B and C in the same way as in step 7 above and note the operate and reset values.
10. Inject a current equal to 10\% of rated current into terminal A.
11. Inject a current in the neutral-to-ground circuit with the same phase angle and with polarity corresponding to an external fault.
12. Increase the current to five times the operating value and check that the protection does not operate.
13. Finally check that trip information is stored in the event and disturbance recorder.

11.3.3.2 Completing the test

continue to test another function or end the test by changing the \textit{TESTMODE} setting to \textit{Disabled}. Restore connections and settings to their original values, if they were changed for testing purposes.

11.3.4 Additional security logic for differential protection LDRGFC (11)

11.3.4.1 Verifying the settings

Prepare the IED for verification of settings outlined in section 1 “Overview” and section 2 “Preparing for test” in this chapter.

Current variation local criteria

Procedure

1. Set \textit{Operation} to \textit{Enabled}.
2. Connect the test set for three phase current injection to the appropriate IED terminals.
3. Inject symmetrical phase currents equal to half the rated current
4. Increase the injected current in phase A slowly by more than the set *Pick Up ICV*. No signal shall be activated.
5. Decrease the injected current in phase A to half the rated current.
6. Increase the injected current in phase A in a step by more than the set *Pick Up ICV*. Pickup shall be activated.

**Undervoltage criteria**

Procedure

1. Connect the test set for three phase current injection and three phase voltage to the appropriate IED terminals.
2. Inject symmetrical phase voltages equal to the rated voltage.
3. Decrease the injected voltage in phase A and note the operated value (pickup value) of the function.
4. Increase the voltage slowly and note the reset value.
5. Connect a trip output contact to a timer.
6. Reduce the injected voltage stepwise to 90 % of the trip level, and check the time delay.
7. Repeat points 3–6 for phases B and C.
8. Inject symmetrical phase voltages equal to the rated voltage.
9. Decrease the injected phase-phase voltage A-B and note the operated value (start value) of the function.
10. Increase the voltage slowly and note the reset value.
11. Connect a trip output contact to a timer.
12. Reduce the injected voltage stepwise to 90 % of the trip level, and check the time delay.
13. Repeat points 9–12 for phases B-C and C-A.

**Zero sequence overcurrent criteria**

Procedure

1. Connect the test set for three phase voltage injection (A, B, C) or residual voltage injection (N) to the appropriate IED terminals. This is dependent on how the IED is fed from the CT.
2. Increase the injected zero sequence current and note the trip value (pickup value) of the studied step of the function.
3. Decrease the current slowly and note the reset value.
4. Connect a trip output contact to a timer.
5. Set the injected current to 200 % of the trip level of the tested stage, switch on the current and check the time delay.

**Low current criteria**

Procedure

1. Connect the test set for three phase current injection to the appropriate IED terminals.
2. Inject a symmetrical three phase current larger than the set value *PU_37*
3. Decrease the injected current in phase A and note the trip value (pickup value) of the studied step of the function.
4. Increase the current slowly and note the reset value.
5. Connect a trip output contact to a timer.
6. Decrease the injected current stepwise to 50 % of the trip level and check the time delay.
7. Repeat steps 3 – 6 for phases B and C.

11.3.4.2 Completing the test

Continue to test another function or end the test by changing the test mode setting to disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.3.5 Self-adaptive differential protection for two-winding power transformers PSTPDIF (87T)

11.3.5.1 Preparing the function for testing

Prepare the IED in general for verification of settings outlined in Section Preparing the IED to verify settings.

Navigate to Main menu/Test/Function test mode and make sure that only the PSTPDIF function and associated trip function blocks are enabled. All other protection functions, which may interfere with testing or tripping shall have the setting Blocked set to Yes.

The PSTPDIF function is self-adaptive. Thus, special measures and procedures shall be used during testing. Off-the-shelf available test sets with associated software and their testing routines for standard transformer differential protection will not work, in general, for this function. They may work for special case, when the phase angle shift is zero degree (i.e. as for Yy0 standard power transformer). However, for specific phase angle shift (e.g. tap position) of the protected PST, which is different from zero degree, this standard routines cannot be used.

To test this function, it is strongly recommended to use a test set with six current sources and then follow the sequence-wise testing procedure as explained for the traditional transformer differential protection in the paper Easy and Intuitive Method for Testing Transformer Differential Relays published by ABB. This paper is available on the ABB website.

To prepare the function to be tested for specific phase angle shift (e.g. specific tap position) of the protected PST, proceed as follows:

1. Make sure that the binary input STOPCOMP into PSTPDIF function has the logical value zero.
2. Wait for five seconds and inject the rated load using the positive sequence current systems only on both sides of the PST, which correspond to the set default transformation ratio and phase angle shift (typically, zero degree). Instructions on magnitude calculation and phase angles for the six currents to be injected are available in the above mentioned paper. When currents are injected correctly, check that all three differential currents are approximately 0% and the bias current is 100%. These values are available as outputs and service values from the PSTPDIF function.
3. Slowly (for example, one degree per second) rotate the positive sequence currents (i.e. all three phases together) for winding two side until the required phase angle shift is achieved. Note that the rotation in anti-clock-wise direction of winding two positive sequence current will result in corresponding but negative value of the estimated phase angle shift displayed on the IED. When PST is of an asymmetrical design, the magnitudes of the currents on the...
winding two side shall also be adjusted accordingly. Once the desired tap is reached, make sure that all three differential currents are still approximately 0% and the bias current is still 100%. Also, check that DIFRATIO and DIFANGLE outputs have value that corresponds to the PST tap position at which the function needs to be tested.

4. Apply the logical value one to the binary input STOPCOMP into PSTPDIF function. The function will now remember the transformation ratio and phase angle shift, which corresponds to this particular PST tap. These two values will be stored and used as long as the input STOPCOMP has a logical value one. To perform this, the IED shall be configured accordingly beforehand. Typically, one front LHMI key or one binary input can be configured in the configuration tool to perform this task. If additional security is needed, this action can be only allowed when the IED is in Testmode.

5. Interrupt all six currents from the test set and verify that DIFRATIO and DIFANGLE outputs still have value that corresponds to the PST tap position, at which the function needs to be tested.
   The function is now prepared to be tested at this particular tap position in a similar way as the standard transformer differential protection.

The following sections explain how the PSTPDIF shall be tested for this particular tap position. Note that, the above procedure and the following tests shall be repeated for every PST tap position, at which the function needs to be tested.

Note that the following tests can be used for any dedicated tap-position available in the protected PST. In actual installation, perform these tests for two extrema tap positions, mid-tap position and one or two more freely selected tap-position(s).

### 11.3.5.2 Verifying the zero-sequence current reduction from the winding one side

1. Connect the test set for three-phase currents injection to the current terminals of the IED, which are connected to the CTs on the winding one side of the power transformer.
2. Inject current in all three phases with equal magnitudes corresponding to the rated load of the PST (i.e. 100%) in secondary amperes as given below:

   \[ I_{\text{Injected \_ W1}} = \frac{C{T}_{\text{secW1}}}{C{T}_{\text{primW1}}} \times I_{\text{Base \_ W1}} \]

   (Equation 1)

   The phase angle for all three injected currents on the test set shall have the same value of zero degree. As a consequence, the PSTPDIF (87T) function will work in either of the following two ways:

   - Trip, if the parameter \( ZSCurrSubtrWI = \text{Off} \) and all three differential currents and the bias current shall be 100%. These values are available as outputs and service values from the PSTPDIF (87T) function.
   - Not trip, if the parameter \( ZSCurrSubtrWI = \text{On} \) and all three differential currents and the bias current shall be 0%. These values are available as outputs and service values from the PSTPDIF (87T) function.
11.3.5.3 Verifying the three-phase pickup from winding one side

1. Connect the test set for three-phase currents injection to the current terminals of the IED, which are connected to the CTs on the winding one side of the power transformer.
2. Inject symmetrical three-phase currents (for example, positive sequence current only). Increase the current magnitude symmetrically in all three phases until the protection function operates and note the operating current. The operating secondary current should have the following value:

\[ I_{\text{pickup,}W1} = \frac{CT_{\text{sec,}W1}}{CT_{\text{prim,}W1}} \times IdMin \times I_{\text{base,}W1} \]  

(Equation 2)

The pickup current is independent of the actual phase angle shift across the protected PST.

3. Check that the trip and alarm contacts operate according to the configuration logic.
4. Decrease the current slowly from the operate value and note the reset value.
5. Switch off the currents and connect an external timer to measure the operate time of the function. Set the current in all three phases to twice the above measured pickup value.
6. Switch on the currents and note the operate time of the PSTPDIF function.
7. Check that the trip information is stored in the IED internal Event List and Disturbance Recorder.
8. Check that the trip information is properly indicated on the LHMI LEDs.

11.3.5.4 Verifying the single-phase pickup from winding one side

1. Connect the test set for three-phase currents injection to the current terminals of the IED, which are connected to the CTs on the winding one side of the power transformer.
2. Increase the current magnitude in phase L1 only until the protection function operates and note the secondary current at the moment of pickup. The operating secondary current should have one of the following two values:
   • Equal to the tested three-phase pickup value from winding one side if the parameter ZSCurrSubtrW1 = Off.
   • Greater for factor 1.5 from the tested three-phase pickup value from winding one side if the parameter ZSCurrSubtrW1 = On.

The pickup current is independent of the actual phase angle shift across the protected PST.

3. Check that the trip and alarm contacts operate according to the configuration logic.
4. Decrease the current slowly from the operate value and note the reset value.
5. If available on the test set, a second harmonic current of about 20% (assumes 15% default setting value for I2/I1Ratio parameter) shall be added to the fundamental frequency current in phase L1. Increase the fundamental frequency current magnitude in phase L1 above the pickup value measured in step 2. Make sure that trip signal from the PSTPDIF function is not given. Check that the second harmonic blocking is active in the corresponding phase.
6. If available on the test set, a fifth harmonic current of about 30% (assumes 25% default setting value for I5/I1Ratio parameter) shall be added to the fundamental frequency current...
in phase L1. Increase the fundamental frequency current magnitude in phase L1 more than the pickup value measured in step 2. Make sure that trip signal from the PSTPDIF function is not given. Check that the fifth harmonic blocking is active in the corresponding phase.

7. Repeat the procedure by injecting current in phases L2 and L3 respectively.

11.3.5.5 Verifying the zero-sequence current reduction from winding two side

1. Connect the test set for three-phase currents injection to the current terminals of the IED, which are connected to the CTs on the winding two side of the power transformer.
2.Inject current in all three phases with equal magnitudes corresponding to the rated load of the PST (i.e. 100%) in secondary amperes as given below:

\[ I_{Injected,W2} = \frac{C{Tsec}_{W2}}{C{Tprim}_{W2}} \times I_{Base,W2} \]  

(Equation 3)

The phase angle for all three injected current on the test set shall have the same value of zero degree. As a consequence the, PSTPDIF function will work in either of the following two ways:

- Trip, if parameter \( ZSCurrSubtrW2 = \text{Off} \) and all three differential currents and the bias current shall be 100%. These values are available as outputs and service values from the PSTPDIF function.
- Not trip, if parameter \( ZSCurrSubtrW2 = \text{On} \) and all three differential currents and the bias current shall be 0%. These values are available as outputs and service values from the PSTPDIF function.

11.3.5.6 Verifying the three-phase pickup from winding two side

1. Connect the test set for three-phase currents injection to the current terminals of the IED, which are connected to the CTs on the winding two side of the power transformer.
2. Inject symmetrical three-phase currents (for example, positive sequence current only). Increase the current magnitude symmetrically in all three phases until the protection function operates and note the operating current. The operating secondary current should have the following value:

\[ I_{Pickup,P2,W2} = \frac{C{Tsec}_{W2}}{C{Tprim}_{W2}} \times IdMin \times I_{Base,W2} \]  

(Equation 4)

The pickup current is independent of the actual phase angle shift across the protected PST.

3. Check that the trip and alarm contacts operate according to the configuration logic.
4. Decrease the current slowly from the operate value and note the reset value.
5. Switch off the currents and connect an external timer to measure the operate time of the function. Set the current in all three phases to twice the above measured pickup value.
6. Switch on the currents and note the operate time of the PSTPDIF function.
7. Check that the trip information is stored in the IED internal Event List and Disturbance Recorder.
8. Check that the trip information is properly indicated on the LHMI LEDs.

11.3.5.7 Verifying the single-phase pickup from winding two side

1. Connect the test set for three-phase currents injection to the current terminals of the IED, which are connected to the CTs on the winding two side of the power transformer.
2. Increase the current magnitude in phase L1 only until the protection function operates and note the operating current. The secondary current at the moment of pickup will be greater for a certain factor from the measured three-phase pickup value depending on the set value for the parameter ZSCurrSubtrW2 and the actual phase angle shift across the protected power transformer for which the test is conducted, as explained below:
   • When ZSCurrSubtrW2 = Off, this factor will have a value between 1.00 and 1.50 depending on the actual phase angle shift as shown in Figure 23:

\[ \text{1-Ph Pickup Factor Value from W2 Side} \]

![Figure 23: Single-phase pickup factor when ZSCurrSubtrW2 = Off](image)

For phase angle shifts greater than ±60 degrees, actual operation will happen in one of other two phases where current has not been injected.

• When ZSCurrSubtrW2 = On, this factor will have a value between 1.50 and 1.732 depending on the actual phase angle shift as shown in Figure 24:
For phase angle shifts greater than ±30 degrees, actual operation will happen in one of the other two phases where the current has not been injected.

The pickup current is dependent on the actual phase angle shift across the protected PST.

3. Check that the trip and alarm contacts operate according to the configuration logic.
4. Decrease the current slowly from operate value and note the reset value.
5. Repeat the procedure by injecting current in phases L2 and L3 respectively.

### 11.3.5.8 Verifying the operate characteristic

1. Connect the test set for injection of two three-phase current sets to the current terminals of the IED, which are connected to the CTs on the winding one and winding two sides respectively of the power transformer.
2. Inject the x-times (where, x = any value between 1.25*IdMin and 0.95*IdUnre parameters) the rated load current using the positive sequence current systems only on both sides of the PST. For actual secondary current magnitudes of the rated PST load, see tests for zero-sequence current reduction given above. For more details on how to calculate magnitudes and the phase angles for the six currents to be injected, refer to the paper *Easy and Intuitive Method for Testing Transformer Differential Relays* published by ABB. When correct currents are injected, check that all three differential currents are approximately 0% and the bias current is equal to x*100%. These values are available as outputs and service values from the PSTPDIF function. At this point, a stable condition for bias current of x*100% is being injected.
3. If the factor x has a value greater than 5, make sure that the testing is done relatively quickly to avoid overheating of the input current transformers inside IED. Their continuous rating is 4 times the rated current (i.e. 1 A or 5 A secondary).
4. Slowly decrease the positive sequence currents (i.e. all three phases together) for winding two side until the PSTPDIF function gives a trip signal. At that moment, pause the injection and note down the values of all three differential currents in percent. The bias current still
should have the same value (i.e. x*100%). Similarly, note the secondary current magnitude for winding one and two sides injected at that point of time.

5. Stop the injection and repeat above points 1-4 for different x-values.

6. From the recorded differential and bias values in percent, draw the operating characteristic from the measurements. Compare the values as described in the Technical Manual.

11.3.5.9 Completing the test

Continue to test another function or end the test by navigating to Main menu/Test/IED test mode/TESTMODE:1 and change the IEDTestMode setting to Off. Restore connections and settings to their original values, if they were changed for testing purposes.

11.4 Impedance protection

11.4.1 Distance protection zones, quadrilateral characteristic ZMQPDIS (21)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Measure operating characteristics during constant current conditions. Keep the measured current as close as possible to its rated value or lower. But make sure it is higher than the set minimum operating current.

Ensure that the maximum continuous current to the IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

The test procedure has to take into consideration that the shaped load encroachment characteristic is active. It is therefore necessary to check the setting. To verify the settings with the shaped load encroachment characteristic the test should be carried out according to figures 25 and 26 and tables 20 and 21. In cases where the load encroachment characteristic is activated tests according to the adjusted figures should be carried out.

To verify the settings for the operating points according to the following fault types should be tested:

- One phase-to-phase fault
- One phase-to-ground fault

The shape of the operating characteristic depends on the values of the setting parameters.

The figures illustrating the characteristic for the distance protection function can be used for settings with and without load encroachment. The solid lines designate the diagram applicable when the load current compensation is active. The solid line and all test points except 13 are valid for this case. When the load current compensation is inactive, the dotted lines and test point 13 are valid. Test points 5, 6, and 7 are not valid for this measurement.
Figure 25: Distance protection characteristic with test points for phase-to-phase measurements

Table 20: Test points for phase-to-phase loops L1-L2 (Ohm/Loop)

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Set value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>X1_set</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>X1_set</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>R1_set</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>0.8 x X1_set</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.8 x R1_set + RFPP/2</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>X</td>
<td>0.5 x X1_set</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5 x R1_set + RFPP/2</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>0.85 x RFPP x tan (ArgRLd)</td>
<td>ArgRLd = angle for the maximal load transfer</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.85 x RFPP</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>X</td>
<td>RLdFw x tan (ArgLd)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFw</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFw</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>X</td>
<td>-0.2143 x RFPP/2</td>
<td>Exact: 0.8 x RFPP/2 (ArgDir=20°)</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.8 x RFPP/2</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>X</td>
<td>-0.4 x RLdFw x tan(ArgDir=20°)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.4 x RLdFw</td>
<td></td>
</tr>
</tbody>
</table>

Table continues on next page
Table 20 is used in conjunction with figure 25.

![Distance protection characteristic with test points for phase-to-ground measurements](ANSI05000369-2-en.vsd)

**Figure 26:** Distance protection characteristic with test points for phase-to-ground measurements

Table 21 is used in conjunction with figure 26.

### Table 21: Test points for phase-to-earth L3-E (Ohm/Loop)

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>$(2 \times X_{1 \text{set}} + X_0)/3$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>$(2 \times X_{1 \text{set}} + X_0)/3$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>$2 \times R_{1 \text{set}} + R_{0 \text{set}})/3$</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>$0.8 \times (2 \times X_{1 \text{set}} + X_0)/3$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>$0.8 \times (2 \times R_{1 \text{set}} + R_{0 \text{set}})/3 + RFPE_{\text{set}}$</td>
<td></td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>4</td>
<td>X</td>
<td>0.5 x (2 x X1&lt;sub&gt;set&lt;/sub&gt; + R0&lt;sub&gt;set&lt;/sub&gt;) / 3</td>
<td></td>
</tr>
<tr>
<td>R</td>
<td></td>
<td>0.5 x (2 x R1&lt;sub&gt;set&lt;/sub&gt; + R0&lt;sub&gt;set&lt;/sub&gt;) / 3 + RFPE&lt;sub&gt;set&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>0.85 x RFPE&lt;sub&gt;set&lt;/sub&gt; x tan(ArgLd&lt;sub&gt;set&lt;/sub&gt;)</td>
<td>ArgLd = angle for the maximal load transfer</td>
</tr>
<tr>
<td>R</td>
<td></td>
<td>0.85 x RFPE</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>X</td>
<td>RLdFw&lt;sub&gt;set&lt;/sub&gt; x tan(ArgLd&lt;sub&gt;set&lt;/sub&gt;)</td>
<td></td>
</tr>
<tr>
<td>R</td>
<td></td>
<td>RLdFw&lt;sub&gt;set&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>R</td>
<td></td>
<td>RLdFw&lt;sub&gt;set&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>X</td>
<td>-0.1072 x RLdFw&lt;sub&gt;set&lt;/sub&gt;</td>
<td>Exact: 0.4 x RFPE x tan (ArgDir=20°)</td>
</tr>
<tr>
<td>R</td>
<td></td>
<td>0.4 x RLdFw&lt;sub&gt;set&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>X</td>
<td>-0.8 x RLdFw&lt;sub&gt;set&lt;/sub&gt; x tan(ArgDir=20°)</td>
<td></td>
</tr>
<tr>
<td>R</td>
<td></td>
<td>0.8 x RLdFw&lt;sub&gt;set&lt;/sub&gt;</td>
<td></td>
</tr>
<tr>
<td>10</td>
<td>X</td>
<td>0.17 x (2 x X1&lt;sub&gt;set&lt;/sub&gt; + X0&lt;sub&gt;set&lt;/sub&gt;)</td>
<td>Exact: 0.5 x (2 x X1&lt;sub&gt;set&lt;/sub&gt; X0&lt;sub&gt;set&lt;/sub&gt;) / 3</td>
</tr>
<tr>
<td>R</td>
<td></td>
<td>-0.36 x (2 x X1&lt;sub&gt;set&lt;/sub&gt; + X0&lt;sub&gt;set&lt;/sub&gt;)</td>
<td>Exact: 0.5 x (2X1&lt;sub&gt;set&lt;/sub&gt; + X0&lt;sub&gt;set&lt;/sub&gt;) / (3 x tan(AgNegDir=30°))</td>
</tr>
<tr>
<td>11</td>
<td>X</td>
<td>0.27 x (2 x X1&lt;sub&gt;set&lt;/sub&gt; + X0&lt;sub&gt;set&lt;/sub&gt;)</td>
<td>Exact: 0.8 x (2 x X1&lt;sub&gt;set&lt;/sub&gt; + X0&lt;sub&gt;set&lt;/sub&gt;) / 3</td>
</tr>
<tr>
<td>R</td>
<td></td>
<td>-0.57 x (2 x X1&lt;sub&gt;set&lt;/sub&gt; + X0&lt;sub&gt;set&lt;/sub&gt;)</td>
<td>Exact: 0.8 x (2X1&lt;sub&gt;set&lt;/sub&gt; + X0&lt;sub&gt;set&lt;/sub&gt;) / (3 x tan(AgNegDir=30°))</td>
</tr>
<tr>
<td>12</td>
<td>X</td>
<td>0.5 x (2 x X1&lt;sub&gt;set&lt;/sub&gt; + X0&lt;sub&gt;set&lt;/sub&gt;) / 3</td>
<td></td>
</tr>
<tr>
<td>R</td>
<td></td>
<td>0.5 x (2 x R1&lt;sub&gt;set&lt;/sub&gt; + R0&lt;sub&gt;set&lt;/sub&gt;) / 3</td>
<td></td>
</tr>
<tr>
<td>13</td>
<td>X</td>
<td>0</td>
<td>Only used when RLdFw &gt; RFPE</td>
</tr>
<tr>
<td>R</td>
<td></td>
<td>RFPE</td>
<td></td>
</tr>
</tbody>
</table>

### 11.4.1 Measuring the operating limit of set values

**Procedure:**

1. Subject the IED to healthy normal load conditions for at least two seconds.
2. Apply the fault condition and slowly decrease the measured impedance to find the operating value of the phase-to-phase fault for zone 1 according to test point 1 in figure 25 and table 20. Compare the result of the measurement with the set value.
3. Repeat steps 1 to 2 to find the operating value for test points 2, 3 in table 20. Observe that the zones that are not tested have to be blocked and the zone that is tested has to be released.
4. Repeat steps 1 to 3 to find the operating value for the phase-to-ground fault C-E according to figure 26 and table 21.
Test points 8, 9, 10 and 11 are intended to test the directional lines of impedance protection. Since directionality is a common function for all 5 measuring zones, it is only necessary to test points 8, 9, 10 and 11 once in the forward direction in order to test the accuracy of directionality (directional angles). Directional functionality testing (trip inside, no-trip outside) should always be made for all impedance zones set with directionality (forward or reverse).

11.4.1.2 Measuring the operating time of distance protection zones

Procedure:

1. Subject the IED to healthy normal load conditions for at least two seconds.
2. Apply the fault condition to find the operating time for the phase-to-phase fault according to test point 12 in figure 25 and table 20 for zone 1. Compare the result of the measurement with the setting tIPP.
3. Repeat steps 1 to 2 to find the operating time for the phase-to-ground fault according to test point 12 in figure 26 and table 21. Compare the result of the measurement with the setting t1PE.
4. Repeat steps 1 to 3 to find the operating time for all other used measuring zones. Observe that the zones that are not tested have to be blocked and the zone that is tested has to be released.

11.4.1.3 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.4.2 Phase selection, quad, fixed angle, load encroachment FDPSPDIS (21)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

The phase selectors operate on the same measuring principles as the impedance measuring zones. So it is necessary to follow the same principles as for distance protection, when performing the secondary injection tests.

Measure operating characteristics during constant current conditions. Keep the measured current as close as possible to the rated value of its associated input transformer, or lower. But ensure that it is higher than the set minimum operating current.

Ensure that the maximum continuous current to the IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

To verify the settings the operating points according to figures 27 and 28 should be tested. See also tables 22 and 23 for information.
Table 22: Test points for phase-to-ground loop CG (Ohm/loop)

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>[X1+XN]</td>
<td>XN=(X₀-X₁)/3</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFwd</td>
<td>When RLdFwd &lt; RFltFwPG</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RFltFwPG</td>
<td>When RLdFwdPG &gt; RFltFwPG</td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>0.85·[X1+XN]</td>
<td>R≈0.491·(X₁+XN)+RFltFwPG</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td></td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>X</td>
<td>0.85·[X1+XN]</td>
<td>Can be limited by RFltFwPG</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>-0.85·[X1+XN]·tan(ArgNegRes-90°)</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>RFltFwPG·tan(LdAngle)</td>
<td>Only when RLdFwd &lt; RFltFwPG</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RFltFwPG</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>X</td>
<td>-0.5·RLdFwd·tan(ArgDir)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5·RLdFwd</td>
<td></td>
</tr>
</tbody>
</table>

Table 22 is used together with figure 27.
Table 23: Test points for phase-to-phase loops A-B (Ohm/phase)

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>X1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFwd</td>
<td>When RLdFwd &lt; 0,5·RFLdFwPP</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5·RFLdFwPP</td>
<td>When RLdFwd &gt; 0,5·RFLdFwPP</td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>0.85·X1</td>
<td>R=0.491·X1+0.5·RFLdFwPP</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.85·X1·1/tan(60°)+0.5·RFLdFwPP</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>X</td>
<td>0.85·X1</td>
<td>Can be limited by RFLdFwPP</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>-0.85·X1·tan(ArgNegRes-90°)</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>0.5·RFLdFwPP·tan(ArgLd)</td>
<td>Only when RLdFwd &lt; RFLdFwPP</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5·RFLdFwPP</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>X</td>
<td>-0.5·RLdFwd·tan(ArgDir)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5·RLdFwd</td>
<td></td>
</tr>
</tbody>
</table>

Table 23 is used together with figure 28.
11.4.2.1 Measuring the operating limit of set values

Procedure:

1. Supply the IED with healthy conditions for at least two seconds.
2. Apply the fault condition and slowly decrease the measured impedance to find the operate value for of the phase-to-ground loop ECG, test point 1, according to figure 27. Compare the result of the measurement with the expected value according to table 22. The corresponding binary signals that inform about the operation of the phase selection measuring elements are available in the local HMI under Main menu/Test/Function status/Impedance Protection/PhaseSelection(PDIS, 21)/FDPSPDIS:x.
3. Repeat steps 1 to 2 to find the operate values for the remaining test points according to figure 27 and table 22.

When the load encroachment characteristic is deliberately set very high in order not to have an influence, then the test points 2 and 5 can be replaced by test point 7.

4. Repeat steps 1 to 3 to find the operate value for the phase-to-phase fault in A - C according to figure 28 and table 23.

11.4.2.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.4.3 Full scheme distance protection, mho characteristic ZMHPDIS (21)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Consider releasing of the zone to be tested by setting the phase selector FMSPSPDIS to Enabled.

Keep the current constant when measuring operating characteristics. Keep the current as close as possible to its rated value or lower. But make sure it is higher than the set minimum operating current.

Ensure that the maximum continuous current in an IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

To verify the mho characteristic, at least two points should be tested.

In the following, three test points are proposed. The mho characteristic always goes through the origin, which automatically gives a fourth point for the characteristic.
11.4.3.1 Phase-to-phase faults

![Diagram of phase-to-phase fault test points](IEC07000009-4-en.vsd)

**Figure 29:** Proposed test points for phase-to-phase fault

<table>
<thead>
<tr>
<th>Test points</th>
<th>reach</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>ZPP - sin(ZAngPP)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>ZPP - sin(ZAngPP)</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>0,5· (ZPP· sin(ZAngPP)</td>
<td>0,5·ZPP + ΔR = 0,5·ZPP·(1+cos(ZAngPP))</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0,5·ZPP·sin(ZAngPP)</td>
<td>0,5·ZPP - ΔR = 0,5·ZPP(1-cos(ZAngPP))</td>
</tr>
</tbody>
</table>

Change the magnitude and angle of phase-to-phase voltage to achieve impedances at test points 1, 2 and 3. For each test point, observe that the output signals, PICKUP, PU_\_x and PHPH\_FLT are activated where x refers to the actual phase to be tested. After the timer tPP for the actual zone has elapsed, also the signals TRIP, TRPP and TR_\_x shall be activated.

11.4.3.2 Phase-to-ground faults

For simplicity, the same test points as for phase-to-phase faults are proposed, but considering new impedance values.
Figure 30: Proposed test points for phase-to-ground faults

Table 25: Test points for phase-to-ground loops A-B (Ohm/Loop)

<table>
<thead>
<tr>
<th>Test points</th>
<th>Reach</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>ZPG \cdot \sin(ZAngPG)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>ZPG \cdot \cos(ZAngPG)</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>0,5 \cdot ZPG \cdot \sin(ZAngPG)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0,5 \cdot ZPG + \Delta R = 0,5 \cdot ZPG \cdot (1 - \cos(ZAngPG))</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>0,5 \cdot ZPG \cdot \sin(ZAngPG)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0,5 \cdot ZPG - \Delta R = 0,5 \cdot ZPG \cdot (1 - \cos(ZAngPG))</td>
<td></td>
</tr>
</tbody>
</table>

Check also in the same way as for phase-to-ground fault for each test point that the output signals PHG_FLT, are activated where x refers to the actual phase to be tested. After the timer $t_{PG}$ for the zone has elapsed, also the signals TRIP, TRPG and TR_x shall be activated.

11.4.4 Faulty phase identification with load encroachment FMPSPDIS (21)

There is no specific test routine for this function. The function is tested in conjunction with other impedance (mho) functions.

11.4.5 Distance protection zones, quadrilateral characteristic, separate settings ZMRPDIS (21)

Prepare the IED for verification of settings as outlined in section "Preparing for test" in this chapter.

Consider releasing Zone 1 with the Phase selection with load encroachment, quadrilateral characteristic (FRPSDPIS). If the autorecloser is not released and in service, trip will always be three phase.
Measure operating characteristics during constant current conditions. Keep the measured current as close as possible to its rated value or lower. But make sure it is higher than 30% of the rated current.

Ensure that the maximum continuous current in an IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

The test procedure has to take into consideration that the shaped load encroachment characteristic is active. It is therefore necessary to check the setting. To verify the settings with the shaped load encroachment characteristic the test should be carried out according to figures 31 and 32 and tables 26 and 27. In cases where the load encroachment characteristic is activated tests according to the adjusted figures should be carried out.

To verify the settings for the operating points according to the following fault types should be tested:

- One phase-to-phase fault
- One phase-to-ground fault

The shape of the operating characteristic depends on the values of the setting parameters.

The figures illustrating the characteristic for the distance protection function can be used for settings with and without load encroachment. The solid lines designate the diagram applicable when the load current compensation is active. The solid line and all test points except 13 are valid for this case. Test points 5, 6, and 7 are not valid for this measurement.

![Distance protection characteristic with test points for phase-to-phase measurements](ANSI05000368-1-en.vsd)

**Figure 31:** Distance protection characteristic with test points for phase-to-phase measurements

Table 26 is used in conjunction with figure 31.
Figure 32: Distance protection characteristic with test points for phase-to-ground measurements

Table 27 is used in conjunction with figure 32.

Table 26: Test points for phase-to-phase loops L1-L2 (Ohm/Loop)

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Set value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>$X_{1\text{set}}$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>$X_{1\text{set}}$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>$R_{1\text{set}}$</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>0.8 $X_{1\text{set}}$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.8 $R_{1\text{set}}$ + RFPP/2</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>X</td>
<td>0.5 $X_{1\text{set}}$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5 $R_{1\text{set}}$ + RFPP/2</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>0.85 RFPP $\times$ tan (LdAngle)</td>
<td>$LdAngle$ = angle for the maximal load transfer</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.85 RFPP</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>X</td>
<td>RLdFwd $\times$ tan (LdAngle)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFwd</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>X</td>
<td>RLdFwd $\times$ tan (LdAngle)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>X</td>
<td>$-0.2143 \times RFPP/2$</td>
<td>Exact: 0.8 $\times$ RFPP/2 (ArgDir)</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.8 $\times$ RFPP/2</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>X</td>
<td>$-0.4 \times RLdFwd \times$ tan(ArgDir)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.4 $\times$ RLdFwd</td>
<td></td>
</tr>
</tbody>
</table>

Table continues on next page
## Table 27: Test points for phase-to-earth L3-G (Ohm/Loop)

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Set value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>((2 \times X_{\text{set}} + X_{0\text{set}})/3)</td>
<td>Exact (-0.5 \times X_{\text{set}} \times \tan(\text{ArgNegRes-90}))</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>((2 \times X_{\text{set}} + X_{0\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(2 \times X_{\text{set}} + R_{\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>(0.8 \times (2 \times X_{\text{set}} + X_{0\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.8 \times (2 \times X_{\text{set}} + X_{0\text{set}})/3 + \text{RFPG}_{\text{set}})</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>X</td>
<td>(0.5 \times (2 \times X_{\text{set}} + X_{0\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.5 \times (2 \times X_{\text{set}} + X_{0\text{set}})/3 + \text{RFPG}_{\text{set}})</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>(0.85 \times \text{RFPG}_{\text{set}} \times \tan(LdAngle))</td>
<td>(LdAngle = \text{angle for the maximal load transfer.})</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.85 \times \text{RFPG})</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>X</td>
<td>(RLdFwd_{\text{set}} \times \tan(LdAngle))</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(RLdFwd_{\text{set}})</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(RLdFwd_{\text{set}})</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>X</td>
<td>(-0.2143 \times RLdFwd_{\text{set}})</td>
<td>\text{Exact: } 0.8 \times \text{RFPG} \times \tan (\text{ArgDir})</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.8 \times RLdFwd_{\text{set}})</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>X</td>
<td>(-0.8 \times RLdFwd_{\text{set}} \times \tan(\text{ArgDir}))</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.8 \times RLdFwd_{\text{set}})</td>
<td></td>
</tr>
<tr>
<td>10</td>
<td>X</td>
<td>(0.17 \times (2 \times X_{\text{set}} + X_{0\text{set}}))</td>
<td>\text{Exact: } 0.5 \times (2 \times X_{\text{set}} + X_{0\text{set}})/3</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(-0.36 \times (2 \times X_{\text{set}} + X_{0\text{set}}))</td>
<td>\text{Exact: } 0.5 \times (2 \times X_{\text{set}} + X_{0\text{set}})/(3 \times \tan (\text{ArgNegDir90}))</td>
</tr>
<tr>
<td>11</td>
<td>X</td>
<td>(0.27 \times (2 \times X_{\text{set}} + X_{0\text{set}}))</td>
<td>\text{Exact: } 0.8 \times (2 \times X_{\text{set}} + X_{0\text{set}})/3</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(-0.57 \times (2 \times X_{\text{set}} + X_{0\text{set}}))</td>
<td>\text{Exact: } 0.8 \times (2 \times X_{\text{set}} + X_{0\text{set}})/(3 \times \tan (\text{ArgNegDir90}))</td>
</tr>
</tbody>
</table>

Table continues on next page
### 11.4.5.1 Measuring the operating limit of set values

**Procedure:**

1. Subject the IED to healthy normal load conditions for at least two seconds.
2. Apply the fault condition and slowly decrease the measured impedance to find the operating value of the phase-to-phase fault for zone 1 according to test point 1 in figure 31 and table 26. Compare the result of the measurement with the set value.
3. Repeat steps 1 to 2 to find the operating value for test points 2, 3 in table 26. Observe that the zones that are not tested have to be blocked and the zone that is tested has to be released.
4. Repeat steps 1 to 3 above to find the operating value for the phase-to-ground fault L3-G according to figure 32 and table 27.

**Note:** Test points 8, 9, 10 and 11 are intended to test the directional lines of impedance protection. Since directionality is a common function for all 5 measuring zones, it is only necessary to test points 8, 9, 10 and 11 once, in the forward direction in order to test the accuracy of directionality (directional angles). Directional functionality testing (trip inside, no-trip outside) should always be made for all impedance zones set with directionality (forward or reverse).

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>12</td>
<td>X</td>
<td>0.5 x (2 x X1_set + X0_set)/3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5 x (2 x R1_set + R0_set)/3</td>
<td></td>
</tr>
<tr>
<td>13</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RFPG</td>
<td></td>
</tr>
</tbody>
</table>

### 11.4.5.2 Measuring the operating time of distance protection zones

**Procedure:**

1. Subject the IED to healthy normal load conditions for at least two seconds.
2. Apply the fault condition to find the operating time for the phase-to-phase fault according to test point 10 in figure 31 and table 26 for zone 1. Compare the result of the measurement with the setting tPP.
3. Repeat steps 1 to 2 to find the operating time for the phase-to-ground fault according to test point 10 in figure 32 and table 27. Compare the result of the measurement with the setting tPG.
4. Repeat steps 1 to 2 to find the operating time for all other used measuring zones. Observe that the zones that are not tested have to be blocked and the zone that is tested has to be released.
11.4.6 Phase selection, quadrilateral characteristic with settable angle FRPSPDIS (21)

Prepare the IED for verification of settings as outlined in section "Preparing for test" in this chapter.

The phase selectors operate on the same measuring principles as the impedance measuring zones. So it is necessary to follow the same principles as for distance protection, when performing the secondary injection tests.

Measure operating characteristics during constant current conditions. Keep the measured current as close as possible to the rated value of its associated input transformer, or lower. But ensure that it is higher than 30% of the rated current.

Ensure that the maximum continuous current of an IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

To verify the settings the operating points according to figures 33 and 34 should be tested. See also tables 28 and 29 for information.

![Operating characteristic for phase selection function, forward direction single-phase faults](image)

**Figure 33:** Operating characteristic for phase selection function, forward direction single-phase faults
Figure 34: Operating characteristic for phase selection function, forward direction phase-to-phase faults

Table 28: Test points for phase-to-ground loop CG (Ohm/loop)

<table>
<thead>
<tr>
<th>Test point</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X [X1+XN] R 0</td>
<td>XN=(X_0-X_1)/3</td>
</tr>
<tr>
<td>2</td>
<td>X 0 R RLdFwd</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>X 0.85 [X1+XN] R 0.85 [X1+XN]/1/tan(60°)+RFFwPE</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>X 0.85 [X1+XN] R -0.85 [X1+XN]/tan (AngNegRes-90°)</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>X RFltFwdPG-tan (LdAngle) R RFltFwdPG</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>X -0.5·RLdFwd-tan (ArgDir) R 0.5·RLdFwd</td>
<td></td>
</tr>
</tbody>
</table>

The table showing test points for phase-to-ground loops is used together with figure 33.
Table 29: Test points for phase-to-phase loops A-B

<table>
<thead>
<tr>
<th>Test point</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>X1</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFwd</td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>0.85·X1</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.85·X1·1/tan(60°)+0.5 RFLdFwdPP</td>
</tr>
<tr>
<td>4</td>
<td>X</td>
<td>0.85·X1</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>-0.85·X1·tan (AngNegRes-90°)</td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>0.5·RFLdFwdPP·tan (ArgLd)</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5·RFLdFwdPP</td>
</tr>
<tr>
<td>6</td>
<td>X</td>
<td>-0.5·RFLdFwd - tan (ArgDir)</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5·RLdFwd</td>
</tr>
</tbody>
</table>

The table showing test points for phase-to-phase loops is used together with figure 34.

11.4.6.1 Measuring the operating limit of set values

Procedure:

1. Supply the IED with healthy conditions for at least two seconds.
2. Apply the fault condition and slowly decrease the measured impedance to find the operate value for of the phase-to-ground loop ECG, test point 1, according to figure 33. Compare the result of the measurement with the expected value according to table 28. The corresponding binary signals that inform about the operation of the phase selection measuring elements are available in the local HMI under Main menu/Test/Function status/Impedance Protection/Phase Selection(PDIS, 21)/FRPS:PDIS:x.
3. Repeat steps 1 to 2 to find the operate values for the remaining test points according to figure 33 and table 28.

When the load encroachment characteristic is deliberately set very high in order not to have an influence, then the test points 2 and 5 can be replaced by test point 7.

4. Repeat steps 1 to 3 to find the operate value for the phase-to-phase fault in A - C according to figure 34 and table 29.

11.4.6.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.
11.4.7 High speed distance protection zones, quadrilateral and mho characteristic ZMFPDIS (21)

Prepare the IED for verification of settings outlined in section "Requirements" and section "Preparing for test" in this chapter.

Measure operating characteristics during constant current conditions. Keep the measured current as close as possible to its rated value or lower. But make sure it is higher than the set minimum operating current.

Ensure that the maximum continuous current to the IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

The test procedure has to take into consideration that the shaped load encroachment characteristic is active. It is therefore necessary to check the setting. To verify the settings with the shaped load encroachment characteristic the test should be carried out according to figures 35 and 36 and tables 30 and 31. In cases where the load encroachment characteristic is activated tests according to the adjusted figures should be carried out.

To verify the settings for the operating points according to the following fault types should be tested:

- One phase-to-phase fault
- One phase-to-ground fault

The shape of the operating characteristic depends on the values of the setting parameters.

The figures illustrating the characteristic for the distance protection function can be used for settings with and without load encroachment. The solid lines designate the diagram applicable when the load current compensation is active. The solid line and all test points except 13 are valid for this case. When the load current compensation is inactive, the dotted lines and test point 13 are valid. Test points 5, 6, and 7 are not valid for this measurement.
Figure 35: Distance protection characteristic with test points for phase-to-phase measurements

Table 30: Test points for phase-to-phase loops A-B (Ohm/Loop)

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Set value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>X₁₅₀</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>X₁₅₀</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>R₁₅₀</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>0.8 x X₁₅₀</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.8 x R₁₅₀+ RFPPZx/2</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>X</td>
<td>0.5 x X₁₅₀</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5 x R₁₅₀+ RFPPZx/2</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>0.85 x RFPPZx x tan (LdAngle)</td>
<td>LdAngle = angle for the maximal load transfer</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.85 x RFPPZx</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>X</td>
<td>RLdFwd x tan (LdAngle)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFwd</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFwd</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>X</td>
<td>-0.2143 x RFPPZx/2</td>
<td>Exact: 0.8 x RFPPZx/2 (ArgDir=20°)</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.8 x RFPPZx/2</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>X</td>
<td>-0.4 x RLdFwd x tan(ArgDir=20°)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.4 x RLdFwd</td>
<td></td>
</tr>
</tbody>
</table>

Table continues on next page
Table 30 is used in conjunction with figure 35.

Table 31 is used in conjunction with figure 36.

Table 31: Test points for phase-to-ground C-G (Ohm/Loop)

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>$(2 \times X_{1\text{set}} + X_0)/3$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>$(2 \times X_{1\text{set}} + X_0)/3$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>$2 \times R_{1\text{set}} + R_0/3$</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>$0.8 \times (2 \times X_{1\text{set}} + X_0)/3$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>$0.8 \times (2 \times R_{1\text{set}} + R_0)/3 + R_{\text{RFPGset}}$</td>
<td></td>
</tr>
</tbody>
</table>

Table continues on next page

Figure 36: Distance protection characteristic with test points for phase-to-ground measurements
<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>4</td>
<td>X</td>
<td>0.5 x (2 x X1\textsubscript{set} + R0\textsubscript{set})/3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5 x (2 x R1\textsubscript{set} + R0\textsubscript{set})/3 + RFPG\textsubscript{set}</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>0.85 x RFPG\textsubscript{set} x \tan(Ld\textsubscript{Angle}set)</td>
<td>Ld\textsubscript{Angle} = angle for the maximal load transfer</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.85 x RFPG</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>X</td>
<td>RLdFwd\textsubscript{set} x \tan(Ld\textsubscript{Angle}Set)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFwd\textsubscript{set}</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFwd\textsubscript{set}</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>X</td>
<td>-0.2143 x RLdFwd\textsubscript{set}</td>
<td>Exact: 0.8 x RFPEZ\textsubscript{x} x \tan(\text{ArgDir}=20°)</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.8 x RLdFwd\textsubscript{set}</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>X</td>
<td>-0.8 x RLdFwd\textsubscript{set} x \tan(\text{ArgDir}=20°)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.8 x RLdFwd\textsubscript{set}</td>
<td></td>
</tr>
<tr>
<td>10</td>
<td>X</td>
<td>0.17 x (2 x X1\textsubscript{set} + X0\textsubscript{set})</td>
<td>Exact: 0.5 x (2 x X1\textsubscript{set} X0\textsubscript{set})/3</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>-0.36 x (2 x X1\textsubscript{set} + X0\textsubscript{set})</td>
<td>Exact: 0.5 x (2X1\textsubscript{set} + X0\textsubscript{set})/(3 x \tan(Ag\text{NegDir}=30°))</td>
</tr>
<tr>
<td>11</td>
<td>X</td>
<td>0.27 x (2 x X1\textsubscript{set} + X0\textsubscript{set})</td>
<td>Exact: 0.8 x (2X1\textsubscript{set} + X0\textsubscript{set})/(3 x \tan(Ag\text{NegDir}=30°))</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>-0.57 x (2 x X1\textsubscript{set} + X0\textsubscript{set})</td>
<td></td>
</tr>
<tr>
<td>12</td>
<td>X</td>
<td>0.5 x (2 x X1\textsubscript{set} + X0\textsubscript{set})/3</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5 x (2 x R1\textsubscript{set} + R0\textsubscript{set})/3</td>
<td></td>
</tr>
<tr>
<td>13</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RFPG</td>
<td></td>
</tr>
</tbody>
</table>

### 11.4.7.1 Measuring the operating limit of set values

**Procedure:**

1. Subject the IED to healthy normal load conditions for at least two seconds.
2. Apply the fault condition and slowly decrease the measured impedance to find the operating value of the phase-to-phase fault for zone 1 according to test point 1 in figure 35 and table 30. Compare the result of the measurement with the set value.
3. Repeat steps 1 to 2 to find the operating value for test points 2, 3 in table 30. Observe that the zones that are not tested have to be blocked and the zone that is tested has to be released.
4. Repeat steps 1 to 3 to find the operating value for the phase-to-ground fault C-G according to figure 36 and table 31.
Test points 8, 9, 10 and 11 are intended to test the directional lines of impedance protection. Since directionality is a common function for all six measuring zones, it is only necessary to test 8, 9, 10 and 11 once, in the forward direction in order to test the accuracy of directionality (directional angles). Directional functionality testing (trip inside, no-trip outside) should always be made for all impedance zones set with directionality (forward or reverse).

11.4.7.2 Measuring the operating time of distance protection zones

Procedure:

1. Subject the IED to healthy normal load conditions for at least two seconds.
2. Apply the fault condition to find the operating time for the phase-to-phase fault according to test point 12 in figure 35 and table 30 for zone 1. Compare the result of the measurement to the setting \( t_{PPZ1} \).
3. Repeat steps 1 to 2 to find the operating time for the phase-to-ground fault according to test point 12 in figure 36 and table 31. Compare the result of the measurement to the setting \( t_{1PG} \).
4. Repeat steps 1 to 2 to find the operating time for all other used measuring zones. The zones that are not tested have to be blocked and the zone that is tested has to be released.

11.4.7.3 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.4.8 High Speed distance for series compensated line zones, quadrilateral and mho characteristic ZMFCPDIS (21)

Prepare the IED for verification of settings outlined in Section “Preparing the IED to verify settings”.

Measure operating characteristics during constant current conditions. Keep the measured current as close as possible to its rated value or lower. But make sure it is higher than the set minimum operating current.

Ensure that the maximum continuous current to the IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

The test procedure has to take into consideration that the shaped load encroachment characteristic is active. It is therefore necessary to check the setting. To verify the settings with the shaped load encroachment characteristic the test should be carried out according to figures 37 and 38 and tables 32 and 33. In cases where the load encroachment characteristic is activated tests according to the adjusted figures should be carried out.

To verify the settings for the operating points according to the following fault types should be tested:
- One phase-to-phase fault
- One phase-to-ground fault

The shape of the operating characteristic depends on the values of the setting parameters.

The figures illustrating the characteristic for the distance protection function can be used for settings with and without load encroachment. The solid lines designate the diagram applicable when the load current compensation is active. The solid line and all test points except 13 are valid for this case. When the load current compensation is inactive, the dotted lines and test point 13 are valid. Test points 5, 6, and 7 are not valid for this measurement.

![Diagram with test points](ANSI05000368-1-en.vsd)

**Figure 37: Distance protection characteristic with test points for phase-to-phase measurements**

**Table 32: Test points for phase-to-phase loops A-B (Ohm/Loop)**

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Set value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>$X_{\text{set}}$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>$X_{\text{set}}$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>$R_{\text{set}}$</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>0.8 x $X_{\text{set}}$</td>
<td>0.8 x $R_{\text{set}}$ + RFPPZx/2</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.8 x $R_{\text{set}}$</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>X</td>
<td>0.5 x $X_{\text{set}}$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0.5 x $R_{\text{set}}$</td>
<td>0.5 x RFPPZx/2</td>
</tr>
</tbody>
</table>

Table continues on next page
Table 32 is used in conjunction with figure 37.

Table 33 is used in conjunction with figure 38.
### Table 33: Test points for phase-to-ground C-G (Ohm/Loop)

<table>
<thead>
<tr>
<th>Test point</th>
<th>Reach</th>
<th>Value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>X</td>
<td>((2 \times X1_{\text{set}} + X0_{\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>X</td>
<td>((2 \times X1_{\text{set}} + X0_{\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(2 \times R1_{\text{set}} + R0_{\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>X</td>
<td>(0.8 \times (2 \times X1_{\text{set}} + X0_{\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.8 \times (2 \times R1_{\text{set}} + R0_{\text{set}})/3) + RFPGZ_{set}</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>X</td>
<td>(0.5 \times (2 \times X1_{\text{set}} + R0_{\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.5 \times (2 \times R1_{\text{set}} + R0_{\text{set}})/3) + RFPGZ_{set}</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>X</td>
<td>(0.85 \times X_{\text{set}} \times \tan(Ld\text{Angle}_{\text{set}}))</td>
<td>LdAngle = angle for the maximal load transfer</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.85 \times \text{RFPGZ}_{x})</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>X</td>
<td>RLdFwd_{set} \times \tan(Ld\text{Angle}_{\text{set}})</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFwd_{set}</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>RLdFwd_{set}</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>X</td>
<td>(-0.2143 \times RLdFwd_{set})</td>
<td>Exact: (0.8 \times \text{RFPGZ}_{x} \times \tan(\text{ArgDir}=20^\circ))</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.8 \times RLdFwd_{set})</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>X</td>
<td>(-0.8 \times RLdFwd_{set} \times \tan(\text{ArgDir}=20^\circ))</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.8 \times RLdFwd_{set})</td>
<td></td>
</tr>
<tr>
<td>10</td>
<td>X</td>
<td>(0.17 \times (2 \times X1_{\text{set}} + X0_{\text{set}}))</td>
<td>Exact: (0.5 \times (2 \times X1_{\text{set}} + X0_{\text{set}})/3)</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(-0.36 \times (2 \times X1_{\text{set}} + X0_{\text{set}}))</td>
<td>Exact: (0.5 \times (2 \times X1_{\text{set}} + X0_{\text{set}})/(3 \times \tan(\text{AgNegDir}=30^\circ))</td>
</tr>
<tr>
<td>11</td>
<td>X</td>
<td>(0.27 \times (2 \times X1_{\text{set}} + X0_{\text{set}}))</td>
<td>Exact: (0.8 \times (2 \times X1_{\text{set}} + X0_{\text{set}})/3)</td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(-0.57 \times (2 \times X1_{\text{set}} + X0_{\text{set}}))</td>
<td>Exact: (0.8 \times (2 \times X1_{\text{set}} + X0_{\text{set}})/(3 \times \tan(\text{AgNegDir}=30^\circ))</td>
</tr>
<tr>
<td>12</td>
<td>X</td>
<td>(0.5 \times (2 \times X1_{\text{set}} + X0_{\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td>(0.5 \times (2 \times R1_{\text{set}} + R0_{\text{set}})/3)</td>
<td></td>
</tr>
<tr>
<td>13</td>
<td>X</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>R</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### 11.4.8.1 Measuring the operating limit of set values

**Procedure:**

1. **Testing functionality by secondary injection**
2. **Transformer protection RET670**
3. **Commissioning manual**
1. Subject the IED to healthy normal load conditions for at least two seconds.
2. Apply the fault condition and slowly decrease the measured impedance to find the operating value of the phase-to-phase fault for zone 1 according to test point 1 in figure 37 and table 32. Compare the result of the measurement with the set value.
3. Repeat steps 1 to 2 to find the operating value for test points 2, 3 in table 32. Observe that the zones that are not tested have to be blocked and the zone that is tested has to be released.
4. Repeat steps 1 to 3 to find the operating value for the phase-to-ground fault C-G according to figure 38 and table 33.

Test points 8, 9, 10 and 11 are intended to test the directional lines of impedance protection. Since directionality is a common function for all six measuring zones, it is only necessary to test points 8, 9, 10 and 11 once, in the forward direction in order to test the accuracy of directionality (directional angles). Directional functionality testing (trip inside, no-trip outside) should always be made for all impedance zones set with directionality (forward or reverse).

11.4.8.2 Measuring the operating time of distance protection zones

Procedure:

1. Subject the IED to healthy normal load conditions for at least two seconds.
2. Apply the fault condition to find the operating time for the phase-to-phase fault according to test point 10 in figure 37 and table 32 for zone 1. Compare the result of the measurement with the setting tIPP.
3. Repeat steps 1 to 2 to find the operating time for the phase-to-ground fault according to test point 10 in figure 38 and table 33. Compare the result of the measurement with the setting tIPG.
4. Repeat steps 1 to 2 to find the operating time for all other used measuring zones. Observe that the zones that are not tested have to be blocked and the zone that is tested has to be released.

11.4.8.3 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.4.9 Power swing detection ZMRPSB (68)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Values of the logical signals for ZMRPSB are available on the local HMI under Main menu/Test/Function status/Impedance protection/PowerSwingDetection(68,Zpsb)/ZMRPSB(68;Zpsb):1

The Signal Monitoring tool in PCM600 shows same signals that are available on the local HMI.
11.4.9.1 **Verifying the signal and settings**

Measure the operating characteristics during constant current conditions. Keep the measured current as close as possible to the rated value of the IED or lower. Ensure that it is higher than the set minimum operating current.

Ensure that the maximum continuous current to the IED does not exceed four times of its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

The test is mainly divided into two parts, one aim is to verify that the settings are in accordance with the selectivity plan and another aim is to verify the operation of ZMRPSB.

Before starting this testing process, all impedance measuring zones shall be set and in operation. Ensure that the inner zone of ZMRPSB must cover all impedance measuring zones that are set for operation.

Test of the interactions or combinations that are not configured are not considered in this instruction.

Always set *Operation* to *On* to check the performance of power swing detection during testing.

**Testing the operating characteristics**

**Preconditions**

The following output signals shall be configured to binary output available: *ZOUT*, measured impedance within outer impedance boundary and *ZIN*, measured impedance within inner impedance boundary.

1. Set *X1InFw, R1LIn, X1InRv, R1FinFw, R1FinRv, ArgLd, RLdOutFw, RLdOutRv, kLdRFw* and *kLdRRv* to values that are in accordance with the selectivity plan and the corresponding operating characteristic is as shown in Figure 39.
2. Set *OperationLdCh* to *On*.
3. The test points that are to be considered for the measurement accuracy of set resistive and reactive reaches for outer and inner boundaries are shown in Figure 39, Table 34 and Table 35.
Figure 39: Proposed test points to measure the outer and inner boundaries of operating characteristics

Where,

\[
\Delta Fw = RLdOutFw - (kLdRFw * RLdOutFw)
\]
\[
\Delta Rv = RLdOutRv - (kLdRRv * RLdOutRv)
\]
\[
RLdInFw = kLdRFw * RLdOutFw
\]
\[
RLdInRv = kLdRFw * RLdOutFw
\]
\[
X1OutFw = X1InFw + \Delta Fw
\]
\[
X1OutRv = X1InRv + \Delta Rv
\]

Table 34: Testing points to measure the outer boundary

<table>
<thead>
<tr>
<th>Test point</th>
<th>R</th>
<th>X</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>P1</td>
<td>0.0</td>
<td>X1OutFw</td>
<td></td>
</tr>
<tr>
<td>P2</td>
<td>0.8* R1In + R1FinFw + \Delta Fw</td>
<td>0.8* X1OutFw</td>
<td></td>
</tr>
<tr>
<td>P3</td>
<td>0.8* (R1FinFw + \Delta Fw) * tan(\text{ArgLd})</td>
<td>0.8* (R1FinFw + \Delta Fw) * tan(\text{ArgLd})</td>
<td></td>
</tr>
<tr>
<td>P4</td>
<td>RLdOutFw</td>
<td>0.0</td>
<td></td>
</tr>
<tr>
<td>P5</td>
<td>R1FinFw + \Delta Fw</td>
<td>0.0</td>
<td>If OperationLdCh = Off</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Test point</th>
<th>R</th>
<th>X</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>P6</td>
<td>0.8 * (R1FInFw + ΔFw)</td>
<td>0.8 * (R1FInFw + ΔFw) * tan(ArgLd)</td>
<td></td>
</tr>
<tr>
<td>P7</td>
<td>R1FInFw + ΔFw</td>
<td>-0.8 * X1OutRv</td>
<td></td>
</tr>
<tr>
<td>P8</td>
<td>0.0</td>
<td>-X1OutRv</td>
<td></td>
</tr>
<tr>
<td>P9</td>
<td>-(0.8 * R1LIn + R1FInRv + ΔRv)</td>
<td>-0.8 * X1OutRv</td>
<td></td>
</tr>
<tr>
<td>P10</td>
<td>-0.8 * (R1FInRv + ΔRv)</td>
<td>-0.8 * (R1FInRv + ΔRv) * tan(ArgLd)</td>
<td></td>
</tr>
<tr>
<td>P11</td>
<td>-RLdOutRv</td>
<td>0.0</td>
<td></td>
</tr>
<tr>
<td>P12</td>
<td>-R1FInRv + ΔRv</td>
<td>0.0</td>
<td>If OperationLdCh = Off</td>
</tr>
<tr>
<td>P13</td>
<td>-0.8 * (R1FInRv + ΔRv)</td>
<td>0.8 * (R1FInRv + ΔRv) * tan(ArgLd)</td>
<td></td>
</tr>
<tr>
<td>P14</td>
<td>-(R1FInRv + ΔRv)</td>
<td>0.8 * X1OutFw</td>
<td></td>
</tr>
</tbody>
</table>

**Table 35: Testing points to measure the inner boundary**

<table>
<thead>
<tr>
<th>Test point</th>
<th>R</th>
<th>X</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>P15</td>
<td>0.0</td>
<td>X1InFw</td>
<td></td>
</tr>
<tr>
<td>P16</td>
<td>0.8 * R1LIn + R1FInFw</td>
<td>0.8 * X1InFw</td>
<td></td>
</tr>
<tr>
<td>P17</td>
<td>0.8 * R1FInFw</td>
<td>(0.8 * R1FInFw + ΔFw) * tan(ArgLd)</td>
<td></td>
</tr>
<tr>
<td>P18</td>
<td>RLdInFw</td>
<td>0.0</td>
<td></td>
</tr>
<tr>
<td>P19</td>
<td>R1FInFw</td>
<td>0.0</td>
<td>If OperationLdCh = Off</td>
</tr>
<tr>
<td>P20</td>
<td>0.8 * R1FInFw</td>
<td>-(0.8 * R1FInFw + ΔFw) * tan(ArgLd)</td>
<td></td>
</tr>
<tr>
<td>P21</td>
<td>R1FInFw</td>
<td>-0.8 * X1InRv</td>
<td></td>
</tr>
<tr>
<td>P22</td>
<td>0.0</td>
<td>-X1InRv</td>
<td></td>
</tr>
<tr>
<td>P23</td>
<td>-0.8 * R1LIn + R1FInRv</td>
<td>-0.8 * X1InRv</td>
<td></td>
</tr>
<tr>
<td>P24</td>
<td>-0.8 * R1FInRv</td>
<td>-(0.8 * -R1FInRv + ΔRv) * tan(ArgLd)</td>
<td></td>
</tr>
<tr>
<td>P25</td>
<td>-RLdInRv</td>
<td>0.0</td>
<td></td>
</tr>
<tr>
<td>P26</td>
<td>-R1FInRv</td>
<td>0.0</td>
<td>If OperationLdCh = Off</td>
</tr>
<tr>
<td>P27</td>
<td>-0.8 * R1FInRv</td>
<td>(0.8 * -R1FInRv + ΔRv) * tan(ArgLd)</td>
<td></td>
</tr>
<tr>
<td>P28</td>
<td>-R1FInRv</td>
<td>0.8 * X1InFw</td>
<td></td>
</tr>
</tbody>
</table>

4. Change the magnitude and angle of three phase voltages to achieve the impedances at test points P1, P2,…., P28.
5. For test points P1 to P14, observe the operation value for the signal ZOUT and compare the operation value with the set value.
6. For test points P15 to P28, observe the operation value for the signal ZIN and compare the operation value with the set value.
Testing the power swing detection logic ZMRPSB (68)

Preconditions

The following output signals shall be configured to binary outputs: ZOUT, measured impedance within outer impedance boundary and ZIN, measured impedance within inner impedance boundary and PICKUP, power swing detection.

1. Set $X1InFw$, $R1LIn$, $X1InRv$, $R1FlInFw$, $R1FlInRv$, $ArgLd$, $RLdOutFw$, $RLdOutRv$, $kLdRFw$ and $kLdRRv$ to values that are in accordance with the selectivity plan and the corresponding operating characteristic is shown in Figure 40.
2. Set $OperationLdCh$ to On and set timers $tP1$, $tP2$, $tW$ and $tH$ to their default values.
3. Enable the input signal REL1PH to detect the power swing in one of the three phases.
4. The test points that are to be considered for the power swing detection are shown in Figure 40.

![Proposed test points to detect power swing conditions](IEC18000100-2-en.vsdx)

Figure 40: Proposed test points to detect power swing conditions

Where,

$$\phi = \tan^{-1} \left( \frac{X1InFw}{R1LIn} \right)$$

5. Inject voltage in one of the three phases in accordance with the test point P1 by keeping constant current in three phases throughout testing and maintaining rated voltage for other two phases.
6. Vary the faulty phase voltage to achieve the impedance at the test point P2. At this point, observe that the output ZOUT is activated and then continue to inject for a duration greater than tP1.

7. Inject the faulty phase voltage to achieve the impedance at the test point P3. At this point, observe that the outputs ZIN and START are activated. It indicates that power swing has been detected.

8. Inject the faulty phase voltage in accordance with the test point P1.

9. At this condition, the outputs ZIN and ZOUT get deactivated and START signal will be maintained for a set duration of tH.

10. To detect consecutive power swings, repeat steps 5, 6, 7 and 8. With this condition, the output START will be activated.

11. Repeat step 6 and inject before the expiration of timer tW. The output ZOUT will be activated.

12. Repeat step 7 after a set duration of tP2. The output ZIN will be activated.

13. At this condition, it will be detected as consecutive power swing and the output signal START will be maintained.

14. Repeat step 8 and the outputs ZOUT and ZIN get deactivated. The output signal START will be disappeared after set duration of tH from the instance of step 13.

15. Repeat the above steps to test and detect power swing condition in at least two phases by enabling input REL2PH.

16. By enabling the input BLK1H, operation mode for power swing detection in any phase will be blocked.

17. Similarly, operation mode for power swing detection in at least two phases will be blocked by enabling the input BLK2PH.

18. Repeat steps 5, 6 and 7, and enable the input signal BLOCK. At this condition, the outputs START, ZOUT and ZIN will be disappeared.

19. Output signal START will also get activated instantaneously by enabling input EXTERNAL and this input signal can be configured in ACT to the output derived from some external logics used to detect power swings. By enabling the input signal BLOCK, the output signal START will be deactivated.

**Testing the inhibit logic of power swing detection function**

The following output signals shall be configured to binary output available: ZOUT, measured impedance within outer impedance boundary and ZIN, measured impedance within inner impedance boundary and START, power swing detection.

1. Set X1InFw, R1LIn, X1InRv, R1FInFw, R1FInRv, ArgLd, RLdOutFw, RLdOutRv, kLdRFw and kLdRRv to values that are in accordance with the selectivity plan and the corresponding operating characteristic is shown in Figure 40.

2. Set OperationLdCh to On and set timers tP1, tP2, tW, tH, tR1, tR2 and tEF to their default values.

3. Enable the input signal REL1PH to detect the power swing in one of the three phases.

**Inhibit START output by the presence of residual current**

**Preconditions**

The input signal I0CHECK, residual current (3I0) detection used to inhibit the start output, must be configured to the output signal STPE on the FDPSPDIS, FRPSPDIS, ZMFPDIS or ZMFCPDIS function.

The input signal BLK102, block inhibit of the start output for subsequent residual current detection, is connected to FALSE.
1. Create a test sequence such that power swing has been detected, which can be done by referring to steps 5, 6 and 7 described in section "Testing the power swing detection logic ZMRPSB (68)" and inject voltage in one of the phases accordingly by keeping constant current in all three phases. With this condition, the output signal START will be activated.

2. Create a condition such that residual current is measured (that is, earth fault occurs in the power system) and its value should be above the value seen during unbalanced loading condition. It can be done by increasing current in one of the phases without disturbing faulty phase current.

3. With this condition, I0CHECK connected to STPE output of FDPSPDIS, FRPSPDIS, ZMFPDIS or ZMFCPDIS function, will be activated and I0CHECK should be activated before the expiration of timer tR1.

4. By doing this, the output START will be disappeared after a set time delay of tR1.

5. If required to block inhibit of start output by the presence of residual current, BLKI02 must be configured to TRUE and thereby, the output signal START will be maintained as long as the power swing condition exists.

Inhibit START output by the presence of slow power swings

Preconditions

The input signal BLKI01, block inhibit of the start output for slow power swings is connected to FALSE.

1. Create a test sequence such that the power swing should be detected. This can be done by referring to steps 5, 6, and 7 described in section "Testing the power swing detection logic ZMRPSB (68)" and inject voltage in one of the phases accordingly by keeping constant current in all three phases. With this condition, the output signal START will be activated.

2. After expiration of the set time delay by a timer tR2, the output START will disappear.

3. If required to block inhibit of start output by the presence of slow power swings, BLKI01 must be configured to TRUE. Thereby, the output signal START will be maintained as long as the power swing condition exists.

Inhibit START output by the single pole reclosing

Preconditions

The input signal I0CHECK, residual current (3I0) detection used to inhibit the start output, must be configured to the output signal STPE on the FDPSPDIS, FRPSPDIS, ZMFPDIS or ZMFCPDIS function.

The input signal BLKI02, block inhibit of the start output for subsequent residual current detection and BLKI01, block inhibit of the start output for slow power swings must be connected to TRUE.

1. Create a phase-to-earth fault such that the input signal I0CHECK will be activated and a trip signal from any impedance protection function will be generated.

2. With this condition, the input signal TRSP connected to a tripping function is activated.

3. Clear the fault by injecting zero current magnitude in the fault phase and maintain healthy conditions in other phases. With this, I0CHECK and TRSP will be deactivated.

4. Make a test sequence to create power swing condition in healthy phases in accordance with steps 5, 6, and 7 described in section "Testing the power swing detection logic ZMRPSB (68)". With this condition, the output signal START will be activated.

5. Repeat step 1 to create phase-to-earth fault and thereby, I0CHECK will be activated again.
6. Steps 4 and 5 must be performed before the expiration of set time delay of $t_EF$. There may exist a two phase power swing during single pole autoreclosing time and then switched-on-to the persistent phase-to-earth fault after single pole autoreclosing time.

7. By doing this, the output signal START will be inhibited.

8. If I0CHECK appears the expiration of a timer $t_EF$, the condition will be seen as power swing and thereby, the output signal START will be maintained as long as the power swing exists in the power system.

11.4.9.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.4.10 Power swing logic PSLPSCH

Prepare the IED for verification of settings as outlined in section "Requirements" and section "Preparing for test" in this chapter.

Most readily available test equipment does not permit simulation of power-swing conditions and the simultaneous occurrence of different faults with controlled fault impedance. For this reason it is necessary to enable the logic by connecting the PUPSD input signal to some other functional signal, which is used for testing purposes.

Make sure that the existing configuration permits monitoring of the CS, TRIP signals on the binary outputs of the IED. If not, configure connections to unused binary outputs, for test purposes.

11.4.10.1 Testing the carrier send and trip signals

Procedure

1. Set the operation of all distance zones, which are supposed to be blocked by the operation of ZMRPSB (78), to Disabled.

2. Configure the PUPSD functional inputs to the TRIP output of the underreaching power-swing zone, if the underreaching communication scheme is used.

3. Start instantaneously any kind of fault within the underreaching power-swing zone and check that:
   - The CS signal appears after the time delay, which is equal to the sum of set time delays for the underreaching zone $tnPP$ or $tnPG$ (dependent on the type of fault) and for the carrier send security timer $tCS$. Also add the usual trip time for the underreaching zone (approximately 30ms).
   - The TRIP signal appears after the time delay, which is equal to the sum of set time delays for the underreaching zone $tnPP$ or $tnPG$ (dependent on the type of fault) and for the trip security timer $tTrip$. Also add the usual operate time for the underreaching zone (approximately 30ms).

4. Simulate the receiving of the carrier signal so that the functional input signal CR becomes a logical one.

5. Configure the PUPSD input to connect to the output PICKUP of the carrier accelerating zone (Power-swing overreaching zone).

6. Initiate any kind of fault within the carrier accelerating zone and check that the TRIP signal appears after the time, which is equal to the time delay set on the trip timer $tTrip$. Also consider the (average) trip time of the carrier acceleration zone (approximately 30ms).
11.4.10.2 Testing the influence of the residual overcurrent protection

Additionally connect the IED according to the test instructions for the four step residual overcurrent protection function EF4PTOC (51N/67N), if the Power swing logic (PSLPSCH) is configured in a way that is controlled by this protection.

Procedure

1. Initiate a single phase-to-ground fault within both power-swing zones.
   Make sure that none of CS or TRIP output signals appear after the time delays \( t_{CS} \) and \( t_{Trip} \). BLKZMUR must appear together with the fault and must remain active until the fault has been switched off plus the time delay, as set on the \( t_{BlkTr} \) timer.
2. Initiate a phase-to-phase fault within the operating area of both power-swing zones.
   Make sure that CS and TRIP output signals appear after the time delays \( t_{CS} \).
3. Switch the operation of the zone 1 distance protection function on and fulfill all the conditions for single-pole autoreclosing.
4. Simulate a single phase-to-ground fault within the reach of zone 1 and both power-swing zones.
   The fault should cause a single-pole tripping with the normal operating time of zone 1.
5. Repeat the fault within the dead time of single-pole autoreclosing.
   Make sure, that PSLPSCH generates a BLKZMUR signal and no CS and TRIP.

11.4.10.3 Checking the underreaching zone

Procedure

1. Set the operation of all normal distance protection zones to Enabled.
2. Simulate a fault without fault resistance in the middle of distance protection zone 1.
   Make sure that the trip appears within the operate time for the distance protection zone 1 and no BLKZMOR output signal appears.
3. Switch off the fault and prepare a new fault without fault resistance within the normal distance protection zone 2 operate area, but outside the zone 1 operate area.
4. Switch on the fault and move it into the zone 1 operate area with a time delay longer than the time set on the \( t_{DZ} \) timer and faster than the time set on timer \( t_{ZL} \).
5. Observe the trip time, which must be equal to the trip time of zone 1, after the measured impedance enters its operate area.
   No delayed operation of zone 1 must be observed.
6. Configure the PUPSD functional input to connect to the PICKUP functional output and repeat the previous fault.
   Fast trip, caused by the operation of zone 1 must appear with a time delay, which is equal to the set time delay on the timer \( t_{ZL} \) plus zone 1 normal otrip time. Also, observe the BLKZMOR functional output signal, which must appear for a short time.
7. Be sure to establish the original configuration of the IED and the original settings of all setting parameters.

11.4.10.4 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled.
Restore connections and settings to their original values, if they were changed for testing purposes.
**11.4.11 Pole slip protection PSPPPAM (78)**

Prepare the IED for verification of settings as outlined in section "Requirements" and section "Preparing for test" in this chapter.

**11.4.11.1 Verifying the settings**

It is assumed that setting of the pole slip protection function PSPPPAM (78) is done according to impedances as seen in figure 41 and figure 42.

The test is done by means of injection of three-phase current and three-phase voltage from a modern test device. This test device shall be able to give voltage and current with the possibility to change voltage and current amplitude and the angle between the injected voltage and current. The parameter setting shall be according to the real application chosen values.

Procedure

1. **Feed the IED with current and voltage corresponding to a normal operation point**
   - Injected voltage $V$ equal to base voltage ($V_{Base}$) and the injected current $I$ equal to half the base current ($I_{Base}$). The angle between the voltage and current shall be $0^\circ$.

2. **With maintained amplitude of the injected voltage the current amplitude and angle is changed to a value $Z_C/2$.**
   - This is done with a speed so that the final impedance is reached after 1 second. As the injected voltage is higher than $0.92\, V_{Base}$ no PICKUP signal should be activated.

3. **With reduced amplitude of the injected voltage to $0.8\, V_{Base}$ the current amplitude and angle is changed to a value $Z_C/2$.**
   - This is done with a speed so that the final impedance is reached after 1 second. As the injected voltage is lower than $0.92\, V_{Base}$ the PICKUP signal should be activated.

4. **With reduced amplitude of the injected voltage to $0.8\, V_{Base}$ the current amplitude and angle is changed via $Z_C/2$ to a value corresponding to half $I_{Base}$ and $180^\circ$ between the injected current and voltage.**
   - This is done with a speed so that the final impedance is reached after 1 second. As the injected voltage is lower than $0.92\, V_{Base}$ the PICKUP signal should be activated. In addition to this the signal ZONE1 should be activated.

5. **Set $N1Limit$ to 1 and repeat step 4.**
   - Now the signals TRIP1 and TRIP should be activated.

6. **With reduced amplitude of the injected voltage to $0.8\, V_{Base}$ the current amplitude and angle is changed via $Z_C + (Z_A - Z_C)/2$ to a value corresponding to half $I_{Base}$ and $180^\circ$ between the injected current and voltage.**
   - This is done with a speed so that the final impedance is reached after 1s. As the injected voltage is lower than $0.92\, V_{Base}$ the PICKUP signal should be activated. In addition to this the signal ZONE2 should be activated.

7. **Set $N2Limit$ to 1 and repeat step 6.**
   - Now the signals TRIP2 and TRIP should be activated.
Figure 41: Setting of the pole slip protection PSPPPAM (78)
11.4.11.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.4.12 Out-of-step protection OOSPPAM

The out-of-step protection (OOSPPAM (78)) function in the IED can be used for both generator protection and as well for line protection applications.

The main purpose of the OOSPPAM (78) function is to detect, evaluate, and take the required action during pole slipping occurrences in the power system.

The OOSPPAM (78) function detects pole slip conditions and trips the generator as fast as possible, after the first pole-slip if the center of oscillation is found to be in zone 1, which normally includes the generator and its step-up power transformer. If the center of oscillation is found to be further out in the power system, in zone 2, more than one pole-slip is usually allowed before the generator-transformer unit is disconnected. A parameter setting is available to take into account the circuit breaker opening time. If there are several out-of-step relays in the power system, then the one which finds the center of oscillation in its zone 1 should operate first.

Two current channels I3P1 and I3P2 are available in OOSPPAM function to allow the direct connection of two groups of three-phase currents; that may be needed for very powerful
generators, with stator windings split into two groups per phase, when each group is equipped with current transformers. The protection function performs a simple summation of the currents of the two channels I3P1 and I3P2.

11.4.12.1 Verifying the settings

The test of the out-of-step protection function is made to verify that the trip is issued if the following events happen.

- the impedance, seen by the function, enters the lens characteristic from one side and leaves it from the opposite side
- the trip is issued according to the settings TripAngle and tBreaker

The tripping zone needs to be detected and confirmed. The test may be performed by taking into account the following key points that are shown in Figure 43:

- the point RE (RE = Receiving End)
- the intersection between the line segment SE-RE and the X-line, which is defined through the setting ReachZ1
- the point SE (SE = Sending End)

![Figure 43: Trajectory of the impedance Z(R, X) for the injected current with two components: a 50 Hz component and a 49.5 Hz current component](image)

The test of the out-of-step protection function requires the injection of the analog quantities for a quite long time. The rating of the analogue channels is considered in order to avoid any hardware damage. The test current is lower than the continuous permissive overload current $I_{ovrl}$ of the protection current channels of the transformer module.

If the rated secondary current $I_{rs}$ of the analog channel is 1 A, then the maximum current test $I_{ts}$ is

$$I_{ts} \leq I_{ovrl} = 4 \times I_{rs} = 4A$$

(Equation 5)

If the CT of the generator has ratio 9000/1 A, then in primary values
I_t ≤ I_{ovrl,p} = I_{ovrl} \times \frac{I_w}{I_n} = 4 \times \frac{9000}{1} = 36000A

(Equation 6)

Reference is made to the numerical values of the example, explained in the “Setting guidelines” of the Application Manual. A test current equal to 2.5 time the base current of the generator is chosen; this choice is related to the selected test voltage that is applied while testing the point SE and RE.

I_t = 2.5 \times I_{base} = 2.5 \times 8367 = 20918A

(Equation 7)

The parameter ReachZ1 defines the boundary between zone 1 and zone 2; it is expressed in percent of the parameter ForwardX. If the setting of ReachZ1 = 12%, then corresponding primary value of the reactance is

\[ X_{RZ1} = \frac{ReachZ1}{100} \times \frac{ForwardX}{100} \times Z_{Base} = \frac{12}{100} \times \frac{59.33}{100} \times 0.9522 = 0.068 \Omega \]

(Equation 8)

The calculation of the test voltage, that is related to ReachZ1, is based on the impedance \( Z_{RZ1} \) that has imaginary part \( X_{RZ1} \) and real part \( R_{RZ1} \):

\[ RRZ1 = \frac{ReachZ1}{100} \times \frac{ForwardR}{100} \times Z_{Base} = \frac{12}{100} \times \frac{8.19}{100} \times 0.9522 = 0.009 \Omega \]

(Equation 9)

The magnitude of the impedance \( Z_{RZ1} \) is:

\[ Z_{RZ1} = \sqrt{R^2_{RZ1} + X^2_{RZ1}} = \sqrt{0.009^2 + 0.068^2} = 0.069 \Omega \]

(Equation 10)

Hence the reference voltage of the test of the boundary between zone 1 and zone 2 is

\[ V_{t,RZ1} = Z_{RZ1} \times I_t = 0.069 \times 20918 = 1435V \]

(Equation 11)

If the test voltage is lower than \( V_{t,RZ1} \) (or in opposition), then the test is related to the zone 1; if the test voltage is higher than \( V_{t,RZ1} \), then the test is related to the zone 2.

Considering the resistances and reactances which are related to the settings (ForwardR, ForwardX) and (ReverseR, ReverseX):

\[ R_{Fwd} = \frac{ForwardR}{100} \times Z_{Base} = \frac{8.19}{100} \times 0.9522 = 0.078 \Omega \]

(Equation 12)

\[ X_{Fwd} = \frac{ForwardX}{100} \times Z_{Base} = \frac{59.33}{100} \times 0.9522 = 0.565 \Omega \]

(Equation 13)
The previous calculations are in primary values. They are transferred to secondary values to perform injections by a test set. Primary values are transferred to secondary values by taking into account the CT ratio and the VT ratio (respectively 9000/1 A and 13.8/0.1 kV in the example).

The magnitude of the secondary voltages, that are related to the points RE and SE of the R-X plane, needs to be checked.

**RE (R\_FwdR, X\_FwdX):**

\[
V_{r,\text{FwdZ}} = V_{r,\text{FwdZ}} \times \frac{V_{T,s}}{V_{T,p}} = 11931 \times \frac{0.1}{13.8} = 86.45V
\]

(Equation 18)

**SE (R\_RvsR, X\_RvsX):**

\[
V_{r,\text{RvsZ}} = V_{r,\text{RvsZ}} \times \frac{V_{T,s}}{V_{T,p}} = 5899 \times \frac{0.1}{13.8} = 42.75V
\]

(Equation 19)

The tests, which are described in this section, may require voltages that have magnitude equal to 110% of the previous values. The continuous permissive overload voltage of the protection voltage channels of the TRM module is 420 V; so the previous voltages may be applied to the analog channels of the IED continuously. Limitations may be related to the available test set; the current \( I_t \) was calculated by using a factor 2.5 (instead of the maximum value 4) in order to reduce the magnitude of the test voltage for the points RE and SE.

Test sets usually do not have a feature to simulate a real network during a power swing and apply the related analog quantities at the terminal of the generator. The scope of the present test is not a simulation of a real network. Voltages and currents are supplied in order to measure an impedance that changes in the time and traverses the plane R-X and, in particular, the area inside the lens characteristic. The test may be performed by applying:

- Symmetric three-phase voltage at 50 Hz. The magnitude depends on the point of the characteristic that needs to be verified. The following three main points of the line segment SE-RE need to be checked:
• the point RE ($R_{FwdR}$, $X_{FwdX}$)
• a point which is related to the parameter $ReachZI$ (boundary between zone 1 and zone 2)
• the point SE ($R_{RvsR}$, $X_{RvsX}$)

The phase angle of the test voltages is equal to:
• arctan ($ForwardX$/ForwardR) for tests in the quadrant 1 and 2 of the R-X plane
• arctan ($ReverseX$/ReverseR) -180° for tests in the quadrant 3 and 4 of the R-X plane

• Symmetric three-phase current, where the current is the summation of two currents that have the same magnitude, but different frequencies.

\[ I_{50} = I_{f} = \frac{I}{2} = \frac{20918}{2} = 10459 \text{ A} \]

(Equation 20)

The first current $I_{50}$ has frequency 50 Hz, magnitude 10459 A (that is, 1.162 A secondary) and phase angle 0º.

The second current $I_{f}$ has magnitude 10459 A (that is, 1.162 A secondary), phase angle 180º (at the starting time of the test) and frequency:
• 49.5 Hz for the test as generator in the quadrant 1 and 2 of the R-X plane
• 50.5 Hz for the test as generator in the quadrant 3 and 4 of the R-X plane

When the trajectory of the impedance, that is seen by the protection function, traverses the lens characteristic then a pole slipping is detected. The present procedure avoids tests of points of the line SE-RE that are too close to the R-axis because in that case the voltage is close to zero and, therefore, the impedance may approach a not defined quantity 0/0.

The accuracy of the impedance reach is ±2% of the base impedance; that is considered while evaluating the test results.

For the test as motor the frequency current may have 50.5 Hz in the quadrant 1 and 2 of the R-X plane and 49.5 Hz in the quadrant 3 and 4.

Verifying the settings by secondary injection

It is advised to connect the analog output channels of the function block OOSPPAM to the internal disturbance recorder (and in particular to the function block A4RADR) in order to perform a better analysis of the tests.

If the device is in test mode, the recording of the disturbances are enabled by the setting in Main menu/Settings/IED Settings/Monitoring/Disturbance report/DisturbanceReport/DRPRDRE:1: set the parameter $OpModeTest$ to On.
1. Check the Application Configuration: verify that hardware voltage and current channels of the IED are properly connected to SMAI function blocks, and that the proper analog outputs of SMAI's are connected to the analog inputs of the function block OOSPPAM.
2. Connect three-phase voltage channels of the test set to the appropriate IED terminals.
3. Connect in parallel two groups of three-phase currents of the test set to the appropriate IED terminals.
4. Connect the appropriate trip output of the IED to the input channel of the test set that monitors the trip.
5. Go to Main menu/Settings/IED Settings/Impedance protection/OutOfStep(78,Ucos)/OOSPPAM(78,Ucos):1, and make sure that the function is enabled, that is, Operation is set to On.

### 11.4.12.2 Test of point RE \( (R_{\text{FwdR}}, X_{\text{FwdX}}) \)

The trajectory of the impedance does not enter the lens characteristic.

**Preliminary steady state test at 50 Hz**

- Go to Main menu/Test/Function status/Impedance protection/OutOfStep(78,Ucos)/OOSPPAM(78,Ucos):1/Outputs to check the available service values of the function block OOSPPAM.
- Apply the following three-phase symmetrical quantities (the phase angle is related to phase L1):
  \[
  V_\alpha = 1.1 \times V_{\text{FwdZ}} \times \frac{V_{V_{\text{Fwd}}}}{V_{V_{\text{P}}}} = 1.1 \times 11931 \times \frac{0.1}{13.8} = 95.1V
  \]
  (Equation 21)
  \[
  \angle V_\alpha = \arctan \frac{\text{ForwardX}}{\text{ForwardR}} = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ
  \]
  (Equation 22)
  frequency of \( V_{ts} = 50 \) Hz
  \[
  I_{so} = I_{so} \times \frac{I_{ct}}{I_{c}} = 10459 \times \frac{1}{9000} = 1.162A
  \]
  (Equation 23)
  \[
  \angle I_{50s}=0^\circ
  \]
  frequency of \( I_{50s} = 50 \) Hz
  \[
  I_{tf} = I_{t} \times \frac{I_{ct}}{I_{c}} = 10459 \times \frac{1}{9000} = 1.162A
  \]
  (Equation 24)
  \[
  \angle I_{tfs}=0^\circ
  \]
  frequency of \( I_{tfs} = 50 \) Hz
  - Check that the service values (VOLTAGE, CURRENT, R(%), X(%)) are according to the injected quantities and that ROTORANG is close to 0 rad. For this particular injection the service values are:
• VOLTAGE = 13.12 kV
• CURRENT = 20918 A
• R = 9.01%
• X = 65.27%
• ROTORANG = 0.04 rad

Note that these values identify a point outside the lens characteristic, even if it is close to the point RE. Neither START nor TRIP is issued.

Execution of the dynamic test
The test may be performed by using two states of a sequence tool that is a basic feature of test sets.

• State 1: pre-test condition.
  Steady voltage and current are applied in order to get a steady high impedance. This is a point in the plane R-X that is far away from the lens characteristic. Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[
V = 1.1 \times V_{\text{Fwd}} \times \frac{V_{\text{Fwd}}}{V_{\text{Fwd}}} = 1.1 \times 11931 \times \frac{0.1}{13.8} = 95.1V
\]

(Equation 25)

\[
\angle V = \arctan \left( \frac{\text{ForwardX}}{\text{ForwardR}} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ
\]

(Equation 26)

frequency of \( V_{ts} = 50 \) Hz
\( I_{50S} = 0 \) A
\( I_{ts} = 0 \) A

• State 2: main test step.
  Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[
V = 1.1 \times V_{\text{Fwd}} \times \frac{V_{\text{Fwd}}}{V_{\text{Fwd}}} = 1.1 \times 11931 \times \frac{0.1}{13.8} = 95.1V
\]

(Equation 27)

\[
\angle V = \arctan \left( \frac{\text{ForwardX}}{\text{ForwardR}} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ
\]

(Equation 28)

frequency of \( V_{ts} = 50 \) Hz
\( I_{50S} = 0 \) A
\( I_{ts} = 0 \) A

\[
I_{50S} = I_{sO} \times \frac{I_{cs}}{I_{csp}} = 10459 \times \frac{1}{9000} = 1.162 A
\]

(Equation 29)

\[
\angle I_{50S} = 0^\circ
\]

frequency of \( I_{50S} = 50 \) Hz
\[ I_{t_p} = I_g \times \frac{I_{ct}}{I_{ctp}} = 10459 \times \frac{1}{9000} = 1.162 \text{A} \]

(Equation 30)

\[ \angle I_{tfs} = 180^\circ \]

frequency of \( I_{tfs} = 49.5 \text{ Hz} \)

Expected result: the protection function does not issue either start or trip.

The trajectory of the impedance traverses the lens characteristic in zone 2

Preliminary steady state test at 50 Hz

- Go to Main menu/Test/Function status/Impedance protection/OutOfStep(78, Ucos)/OOSPPAM(78, Ucos):1/Outputs to check the available service values of the function block OOSPPAM.
- Apply the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[ V_o = 0.9 \times V_{t, fwd} \times \frac{V_{ts, s}}{V_{ts, p}} = 0.9 \times 11931 \times \frac{0.1}{13.8} = 77.81 \text{V} \]

(Equation 31)

\[ \angle V_o = \arctan \left( \frac{\text{ForwardX}}{\text{ForwardR}} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ \]

(Equation 32)

frequency of \( V_{ts} = 50 \text{ Hz} \)

\[ I_{50s} = I_g \times \frac{I_{ct}}{I_{ctp}} = 10459 \times \frac{1}{9000} = 1.162 \text{A} \]

(Equation 33)

\[ \angle I_{50s} = 0^\circ \]

frequency of \( I_{50s} = 50 \text{ Hz} \)

\[ I_{t_p} = I_g \times \frac{I_{ct}}{I_{ctp}} = 10459 \times \frac{1}{9000} = 1.162 \text{A} \]

(Equation 34)

\[ \angle I_{tfs} = 0^\circ \]

frequency of \( I_{tfs} = 50 \text{ Hz} \)

- Check that the service values (VOLTAGE, CURRENT, R(%), X(%)) are according to the injected quantities and that ROTORANG is close to 3.14 rad. For this particular injection the service values are:
  - VOLTAGE = 10.74 kV
  - CURRENT = 20918 A
  - R = 7.37%
  - X = 53.40%
  - ROTORANG = -3.09 rad
Note that these values identify a point inside the lens characteristic, in the zone 2, that is close to the point RE. The START is issued, but no TRIP is performed.

Execution of the dynamic test
The test may be performed by using two states of a sequence tool that is a basic feature of test sets.

• State 1: pre-test condition.
  Steady voltage and current are applied in order to get a steady high impedance, that is, a point in the plane R-X which is far away from the lens characteristic. Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[ V_n = 0.9 \times V_{i1,na} \times \frac{V_{V,xy}}{V_{V,p}} = 0.9 \times 11931 \times \frac{0.1}{13.8} = 77.81V \]  

(Equation 35)

\[ \angle V_n = \arctan \left( \frac{\text{ForwardX}}{\text{ForwardR}} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ \]  

(Equation 36)

frequency of \( V_{ts} = 50 \) Hz
\( I_{50s} = 0 \) A
\( I_{tfs} = 0 \) A

• State 2: main test step.
  Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[ V_n = 0.9 \times V_{i1,na} \times \frac{V_{V,xy}}{V_{V,p}} = 0.9 \times 11931 \times \frac{0.1}{13.8} = 77.81V \]  

(Equation 37)

\[ \angle V_n = \arctan \left( \frac{\text{ForwardX}}{\text{ForwardR}} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ \]  

(Equation 38)

frequency of \( V_{ts} = 50 \) Hz

\[ I_{50s} = 0 \] A

\[ I_{tfs} = 0 \] A

\[ \angle \]
Expected result: start of the protection function and trip in zone 2, when trip conditions are fulfilled.

### 11.4.12.3 Test of the boundary between zone 1 and zone 2, which is defined by the parameter ReachZ1

The trajectory of the impedance traverses the lens characteristic in zone 2

Preliminary steady state test at 50 Hz

- Go to Main menu/Test/Function status/Impedance protection/OutOfStep(78,Ucos)/OOSPPAM(78,Ucos):1/Outputs to check the available service values of the function block OOSPPAM.
- Apply the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[
V_u = 1.1 \times V_{V1} \times \frac{V_{V1}}{V_{V2}} = 1.1 \times 1435 \times \frac{0.1}{13.8} = 11.44V
\]

(Equation 41)

\[
\angle V_u = \arctan \left( \frac{\text{ForwardX}}{\text{ForwardR}} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ
\]

(Equation 42)

frequency of \(V_{ts}\) = 50 Hz

\[
I_{so} = I_s \times \frac{I_{so}}{I_{clp}} = 10459 \times \frac{1}{9000} = 1.162A
\]

(Equation 43)

\(\angle I_{so} = 0^\circ\)

frequency of \(I_{so}\) = 50 Hz

\[
I_{tp} = I_t \times \frac{I_{tp}}{I_{clp}} = 10459 \times \frac{1}{9000} = 1.162A
\]

(Equation 44)

\(\angle I_{tp} = 0^\circ\)

frequency of \(I_{tp}\) = 50 Hz

- Check that the service values (VOLTAGE, CURRENT, R(%), X(%) ) are according to the injected quantities and that ROTORANG is close to 3.14 rad. For this particular injection the service values are:
  - VOLTAGE = 1.58 kV
  - CURRENT = 20918 A
  - R = 1.08%
  - X = 7.85%
  - ROTORANG = -3.04 rad

Note that these values identify a point inside the lens characteristic, in the Zone 2, that is close to the boundary between zone 1 and zone 2. The START is issued, but no TRIP is performed.
Execution of the dynamic test

The test may be performed by using two states of a sequence tool that is a basic feature of test sets.

- **State 1**: pre-test condition.
  Steady voltage and current are applied in order to get a steady high impedance, that is a point in the plane R-X that is far away from the lens characteristic. Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):

  \[
  V_n = 1.1 \times V_{i, R1} \times \frac{V_{VT,s}}{V_{VT,p}} = 1.1 \times 1435 \times \frac{0.1}{13.8} = 11.44V
  \]  

  \[
  \angle V_n = \arctan \left( \frac{\text{ForwardX}}{\text{ForwardR}} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ
  \]

  (Equation 45)

  Frequency of \(V_1\) = 50 Hz.

  \(I_{50S} = 0\ A\)

  \(I_{50S} = 0\ A\)

- **State 2**: main test step.
  Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):

  \[
  V_n = 1.1 \times V_{i, R1} \times \frac{V_{VT,s}}{V_{VT,p}} = 1.1 \times 1435 \times \frac{0.1}{13.8} = 11.44V
  \]  

  \[
  \angle V_n = \arctan \left( \frac{\text{ForwardX}}{\text{ForwardR}} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ
  \]

  (Equation 47)

  Frequency of \(V_1\) = 50 Hz

\[
I_{50S} = I_{50} \times \frac{I_{CS}}{I_{CS}} = 10459 \times \frac{1}{9000} = 1.162\ A
\]  

  (Equation 49)

\[
\angle I_{50S} = 0^\circ
\]

  Frequency of \(I_{50S}\) = 50 Hz

\[
I_{50S} = I_{5} \times \frac{I_{CS}}{I_{CS}} = 10459 \times \frac{1}{9000} = 1.162\ A
\]  

  (Equation 50)

\[
\angle I_{50S} = 180^\circ
\]

  Frequency of \(I_{50S}\) = 49.5 Hz

Expected result: start of the protection function and trip in zone 2 when trip conditions are fulfilled.
The trajectory of the impedance traverses the lens characteristic in zone 1

Preliminary steady state test at 50 Hz

- Go to Main menu/Test/Function status/Impedance protection/OutOfStep(78,Ucos)/OOSPPAM(78,Ucos):1/Outputs to check the available service values of the function block OOSPPAM.
- Apply the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[
V_v = 0.9 \times V_r,821 \times \frac{V_{v,r}}{V_{r,p}} = 0.9 \times 1435 \times \frac{0.1}{13.8} = 9.36V
\]

(Equation 51)

\[
\angle V_v = \arctan \left( \frac{ForwardX}{ForwardR} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ
\]

(Equation 52)

frequency of \( V_{ts} \) = 50 Hz

\[
I_{So} = I_s \times \frac{I_{ct,s}}{I_{ct,p}} = 10459 \times \frac{1}{9000} = 1.162A
\]

(Equation 53)

\( \angle I_{So} = 0^\circ \)

frequency of \( I_{So} \) = 50 Hz

\[
I_{tf} = I_f \times \frac{I_{ct,f}}{I_{ct,p}} = 10459 \times \frac{1}{9000} = 1.162A
\]

(Equation 54)

\( \angle I_{tf} = 0^\circ \)

frequency of \( I_{tf} \) = 50 Hz

- Check that the service values (VOLTAGE, CURRENT, R(%), X(%)) are according to the injected quantities and that ROTORANG is close to 3.14 rad. For this particular injection the service values are:
  - VOLTAGE = 1.29 kV
  - CURRENT = 20918 A
  - R = 0.89%
  - X=6.42%
  - ROTORANG = -3.04 rad

Note that these values identify a point inside the lens characteristic in zone 1, that is close to the boundary between zone 1 and zone 2. The START is issued, but no TRIP is performed.

Execution of the dynamic test
The test may be performed by using two states of a sequence tool that is a basic feature of test sets.

- State 1: pre-test condition.
  Steady voltage and current are applied in order to get a steady high impedance, that is a point in the plane R-X which is far away from the lens characteristic. Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):
\[ V_u = 0.9 \times V_{VTRZ} \times \frac{V_{VTRZ}}{V_{VT,p}} = 0.9 \times 1435 \times \frac{0.1}{13.8} = 9.36 \text{V} \]
\[ \angle V_u = \arctan \left( \frac{\text{ForwardX}}{\text{ForwardR}} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ \]

(Equation 55)

frequency of \( V_{ts} = 50 \text{ Hz} \)

\( I_{S0S} = 0 \text{ A} \)

\( I_{tfS} = 0 \text{ A} \)

- State 2: main test step.
  Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[ V_u = 0.9 \times V_{VTRZ} \times \frac{V_{VTRZ}}{V_{VT,p}} = 0.9 \times 1435 \times \frac{0.1}{13.8} = 9.36 \text{V} \]

(Equation 57)

\[ \angle V_u = \arctan \left( \frac{\text{ForwardX}}{\text{ForwardR}} \right) = \arctan \left( \frac{59.33}{8.19} \right) = 82.14^\circ \]

(Equation 58)

frequency of \( V_{ts} = 50 \text{ Hz} \)

\[ I_{S0S} = I_{so} \times \frac{I_{CYP}}{I_{CP}} = 10459 \times \frac{1}{9000} = 1.162 \text{A} \]

(Equation 59)

\[ \angle I_{S0S} = 0^\circ \]

frequency of \( I_{S0S} = 50 \text{ Hz} \)

\[ I_{tfS} = I_{fo} \times \frac{I_{CYP}}{I_{CP}} = 10459 \times \frac{1}{9000} = 1.162 \text{A} \]

(Equation 60)

\[ \angle I_{tfS} = 180^\circ \]

frequency of \( I_{tfS} = 49.5 \text{ Hz} \)

Expected result: start of the protection function and trip in zone 1 when trip conditions are fulfilled.
11.4.12.4 Test of the point SE (\(R_{\text{RvsR}}\), \(X_{\text{RvsX}}\))

The trajectory of the impedance traverses the lens characteristic in zone 1

Preliminary steady state test at 50 Hz

- Go to Main menu/Test/Function status/Impedance protection/OutOfStep(78, Ucos)/ OOSPPAM(78, Ucos):1/Outputs to check the available service values of the function block OOSPPAM.
- Apply the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[
V_o = 0.9 \times V_{ts} \times \frac{V_{ts,R}}{V_{ts,P}} = 0.9 \times 5899 \times \frac{0.1}{13.8} = 38.47 V
\]

(Equation 61)

\[
\angle V_o = \arctan \left( \frac{\text{ReverseX}}{\text{ReverseR}} \right) - 180^\circ = \arctan \left( \frac{29.60}{0.29} \right) - 180^\circ = -90.56^\circ
\]

(Equation 62)

frequency of \(V_{ts} = 50\) Hz

\[
I_{\text{SOs}} = I_o \times \frac{I_{CTs}}{I_{CTp}} = 10459 \times \frac{1}{9000} = 1.162 A
\]

(Equation 63)

\[
\angle I_{\text{SOs}} = 0^\circ
\]

frequency of \(I_{\text{SOs}} = 50\) Hz

\[
I_{f\text{fs}} = I_g \times \frac{I_{CTs}}{I_{CTp}} = 10459 \times \frac{1}{9000} = 1.162 A
\]

(Equation 64)

\[
\angle I_{f\text{fs}} = 0^\circ
\]

frequency of \(I_{f\text{fs}} = 50\) Hz

- Check that the service values (VOLTAGE, CURRENT, R(%), X(%)) are according to the injected quantities and that ROTORANG is close to 3.14 rad. For this particular injection the service values are:
  - VOLTAGE = 5.31 kV
  - CURRENT = 20918 A
  - R = -0.266
  - X = -26.65%
  - ROTORANG = -3.06 rad

Note that these values identify a point inside the lens characteristic in zone 1 that is close to the point SE. The START is issued, but no TRIP is performed.

Execution of the dynamic test

The test may be performed by using two states of a sequence tool that is a basic feature of test sets.

- State 1: pre-test condition.
Steady voltage and current are applied in order to get a steady high impedance, that is a point in the plane R-X which is far away from the lens characteristic. Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[
V_u = 0.9 \times V_{\text{a,n,d}} \times \frac{V_{\text{VT,s}}}{V_{\text{VT,p}}} = 0.9 \times 5899 \times \frac{0.1}{13.8} = 38.47V
\]

(Equation 65)

\[
\angle V_u = \arctan \left( \frac{\text{ReverseX}}{\text{ReverseR}} \right) - 180^\circ = \arctan \left( \frac{29.60}{0.29} \right) - 180^\circ = -90.56^\circ
\]

(Equation 66)

frequency of \(V_{ts} = 50\) Hz
\(I_{SO5} = 0\) A
\(I_{TFS} = 0\) A

- State 2: main test step.
Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[
V_u = 0.9 \times V_{\text{a,n,d}} \times \frac{V_{\text{VT,s}}}{V_{\text{VT,p}}} = 0.9 \times 5899 \times \frac{0.1}{13.8} = 38.47V
\]

(Equation 67)

\[
\angle V_u = \arctan \left( \frac{\text{ReverseX}}{\text{ReverseR}} \right) - 180^\circ = \arctan \left( \frac{29.60}{0.29} \right) - 180^\circ = -90.56^\circ
\]

(Equation 68)

frequency of \(V_{ts} = 50\) Hz
\(I_{SO5} = 0\) A
\(I_{TFS} = 0\) A

\[
I_{SS} = I_s \times \frac{I_{CT}}{I_{CTP}} = 10459 \times \frac{1}{9000} = 1.162 A
\]

(Equation 69)

\(\angle I_{SO5} = 0^\circ\)
frequency of \(I_{SO5} = 50\) Hz

\[
I_f = I_f \times \frac{I_{CT}}{I_{CTP}} = 10459 \times \frac{1}{9000} = 1.162 A
\]

(Equation 70)

\(\angle I_{TFS} = 180^\circ\)
frequency of \(I_{TFS} = 50.5\) Hz

Expected result: start of the protection function and trip in zone 1 when trip conditions are fulfilled.
The trajectory of the impedance does not enter the lens characteristic

Preliminary steady state test at 50 Hz

- Go to Main menu/Test/Function status/Impedance protection/OutOfStep(78,Ucos)/OOSPPAM(78,Ucos):1/Outputs to check the available service values of the function block OOSPPAM.
- Apply the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[
V_v = 1.1 \times V_{r, Ref} \times \frac{V_{V,R}}{V_{V,P}} = 1.1 \times 5899 \times \frac{0.1}{13.8} = 47.02 \text{V}
\]

\[
\angle V_v = \arctan \left( \frac{ReverseX}{ReverseR} \right) = 180^\circ - \arctan \left( \frac{29.60}{0.29} \right) - 180^\circ = -90.56^\circ
\]

(Equation 71)

frequency of \( V_{ts} = 50 \text{ Hz} \)

\[
I_{s0} = I_o \times \frac{I_{c0}}{I_{cP}} = 10459 \times \frac{1}{9000} = 1.162 \text{ A}
\]

(Equation 73)

\( \angle k_{0S} = 0^\circ \)

frequency of \( k_{0S} = 50 \text{ Hz} \)

\[
I_{t0} = I_g \times \frac{I_{c0}}{I_{cP}} = 10459 \times \frac{1}{9000} = 1.162 \text{ A}
\]

(Equation 74)

\( \angle k_{ts} = 0^\circ \)

frequency of \( k_{t} = 50 \text{ Hz} \)

- Check that the service values (VOLTAGE, CURRENT, R(%), X(%)) are according to the injected quantities and that ROTORANG is close to 0 rad. For this particular injection the service values are:
  - VOLTAGE= 6.49 kV
  - CURRENT= 20918 A
  - R= –0.32%
  - X=–32.57%
  - ROTORANG= 0.08 rad

Note that these values identify a point outside the lens characteristic, even if it is close to the point SE. Neither START nor TRIP is issued.

Execution of the dynamic test
The test may be performed by using two states of a sequence tool that is a basic feature of test sets.

- State 1: pre-test condition.
  Steady voltage and current are applied in order to get a steady high impedance, that is, a point in the plane R-X which is far away from the lens characteristic. Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):
\[ V_u = 1.1 \times V_{i,\text{av2}} \times \frac{V_{v,\text{av}}}{V_{v,\text{p}}} = 1.1 \times 5899 \times \frac{0.1}{13.8} = 47.02 \text{V} \]  
(Equation 75)

\[ \angle V_u = \arctan \left( \frac{\text{Reverse} X}{\text{Reverse} R} \right) - 180^\circ = \arctan \left( \frac{29.60}{0.29} \right) - 180^\circ = -90.56^\circ \]  
(Equation 76)

Frequency of \( V_{ts} = 50 \text{ Hz} \)

\( I_{50S} = 0 \text{ A} \)

\( I_{tfs} = 0 \text{ A} \)

- State 2: main test step.

Define the following three-phase symmetrical quantities (the phase angle is related to phase L1):

\[ V_u = 1.1 \times V_{i,\text{av2}} \times \frac{V_{v,\text{av}}}{V_{v,\text{p}}} = 1.1 \times 5899 \times \frac{0.1}{13.8} = 47.02 \text{V} \]  
(Equation 77)

\[ \angle V_u = \arctan \left( \frac{\text{Reverse} X}{\text{Reverse} R} \right) - 180^\circ = \arctan \left( \frac{29.60}{0.29} \right) - 180^\circ = -90.56^\circ \]  
(Equation 78)

Frequency of \( V_{ts} = 50 \text{ Hz} \)

\[ I_{s1s} = I_s \times \frac{l_{\text{ctp}}}{l_{\text{ctp}}} = 10459 \times \frac{1}{9000} = 1.162 \text{ A} \]  
(Equation 79)

\( \angle I_{50S} = 0^\circ \)

Frequency of \( I_{50S} = 50 \text{ Hz} \)

\[ I_{tfs} = I_t \times \frac{I_{\text{ctp}}}{l_{\text{ctp}}} = 10459 \times \frac{1}{9000} = 1.162 \text{ A} \]  
(Equation 80)

\( \angle I_{tfs} = 180^\circ \)

Frequency of \( I_{tfs} = 50.5 \text{ Hz} \)

Expected result: the protection function does not issue either start or trip.

After each test it is possible to download and study the related disturbance recording.
11.4.13 Automatic switch onto fault logic ZCVPSOF

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

The automatic switch onto fault logic function ZCVPSOF is checked using secondary injection tests. ZCVPSOF is activated either by the external input BC or by the internal DLD. FUFSPVC is done with a pre-fault condition where the phase voltages and currents are at zero. A three-phase fault with zero impedance and a three-phase fault with an impedance corresponding to the whole line is applied. This fault shall cause an instantaneous trip and result in a TRIP indication.

11.4.13.1 Activating ZCVPSOF externally

1. Set AutoInitMode to DLD disabled and Mode to Impedance.
2. Activate the switch onto fault BC input.
   During normal operating conditions, the BC input is de-energized.
3. Apply a three-phase fault condition corresponding to a fault at approximately 45% of the line or with impedance at 50% of the used zone setting and current greater than 30% of IBase. The ZACC input is activated.
4. Check that the TRIP output, external signals and indication are obtained.
11.4.13.2 Initiating ZCVPSOF automatically and setting mode to impedance

1. Set AutoInitMode to Voltage and Mode to Impedance.
2. Deactivate the switch onto fault BC input.
3. Set the current and voltage inputs to lower than IphPickup and UVPickup for at least one second.
4. Apply a three-phase fault condition corresponding to a fault at approximately 45% of the line or with impedance at 50% of the used zone setting and current greater than 30% of IBase. Apply a two-phase fault condition corresponding to a fault at approximately 45% of the line or with impedance at 50% of the used zone setting and current greater than 30% of IBase.
The ZACC input is activated.
5. Check that the correct TRIP output, external signals and indication are obtained.

11.4.13.3 Initiating ZCVPSOF automatically and setting mode to VILevel

1. Set AutoInitMode to Voltage and Mode to VILevel.
2. Deactivate the switch onto fault BC input.
3. Set the current and voltage inputs to lower than IphPickup and UVPickup for at least one second.
4. Apply the three-phase currents in such a way that the magnitudes are greater than IphPickup and the three-phase voltages in such a way that the magnitudes and lower than UVPickup at least for the duration of the tDuration setting.
5. Check that the correct TRIP output, external signals and indication are obtained.

11.4.13.4 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.4.14 Phase preference logic PPLPHIZ

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

The Phase preference logic function PPLPHIZ is tested with a three-phase testing equipment for distance protections. PPLPHIZ is tested in co-operation with the Distance protection zone, quadrilateral characteristic function ZMQPDIS (21). The distance protection and the phase preference logic shall be set to values according to the real set values to be used. The test is made by means of injection of voltage and current where the amplitude of both current and voltage and the phase angle between the voltage and current can be controlled.

During the test the following binary signals (outputs) shall be monitored:

- Trip signal from distance protection
- Trip signal from phase preference logic
1. Connect the test set for injection of voltage and current.
2. Inject voltages and currents corresponding to a phase-to-phase to ground fault within zone 1 of the distance protection function. In the test one of the current inputs (one of the faulted phases) is disconnected. The remaining current is the fault current out on the protected line. All combinations of two phase-to-ground faults with one phase current are tested. The result shall be according to table 36. It should be checked that the fault will give phase-to-phase voltage, phase-to-ground voltage, zero-sequence voltage and phase current so that the conditions set for the logic are fulfilled.

<table>
<thead>
<tr>
<th>OperMode</th>
<th>Fault type/Faulted phase current to the IED</th>
</tr>
</thead>
<tbody>
<tr>
<td>A_BG/IA</td>
<td>Trip</td>
</tr>
<tr>
<td>A_BG/IB</td>
<td>Trip</td>
</tr>
<tr>
<td>B_CG/IB</td>
<td>Trip</td>
</tr>
<tr>
<td>B_CG/IC</td>
<td>Trip</td>
</tr>
<tr>
<td>C_CG/IA</td>
<td>Trip</td>
</tr>
<tr>
<td>C_AG/IC</td>
<td>Trip</td>
</tr>
</tbody>
</table>

### 11.4.14.1 Completing the test

Continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

### 11.4.15 Phase preference logic PPL2PHIZ

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

The Phase preference logic function PPL2PHIZ is tested with a three-phase testing equipment for distance protections. PPL2PHIZ is tested in co-operation with the High speed distance protection ZMFPDIS (21). The distance protection and the phase preference logic shall be set to values according to the real set values to be used. The test is made by means of injection of voltage and current where the amplitude of both current and voltage and the phase angle between the voltage and current can be controlled.

During the test the following binary signals (outputs) shall be monitored:

- Trip signal from distance protection
- Trip signal from phase preference logic
1. Connect the test set for injection of voltage and current.
2. Inject voltages and currents corresponding to a phase-to-phase to ground fault within zone 1 of the distance protection function. In the test one of the current inputs (one of the faulted phases) is disconnected. The remaining current is the fault current out on the protected line. All combinations of two phase-to-ground faults with one phase current are tested. The result shall be according to table 37. It should be checked that the fault will give phase-to-phase voltage, phase-to-ground voltage, zero-sequence voltage and phase current so that the conditions set for the logic are fulfilled.

### Table 37: Operation at different combinations of faults and operation mode

<table>
<thead>
<tr>
<th>OperMode</th>
<th>Fault type/Faulted phase current to the IED</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>A_BG/IA</td>
</tr>
<tr>
<td>No Filter</td>
<td>Trip</td>
</tr>
<tr>
<td>No Pref</td>
<td>Trip</td>
</tr>
<tr>
<td>1231c</td>
<td>Trip</td>
</tr>
<tr>
<td>1231c</td>
<td>No Trip</td>
</tr>
<tr>
<td>123a</td>
<td>Trip</td>
</tr>
<tr>
<td>132a</td>
<td>Trip</td>
</tr>
<tr>
<td>213a</td>
<td>No Trip</td>
</tr>
<tr>
<td>231a</td>
<td>No Trip</td>
</tr>
<tr>
<td>312a</td>
<td>Trip</td>
</tr>
<tr>
<td>321a</td>
<td>No Trip</td>
</tr>
</tbody>
</table>

### 11.4.16 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

### 11.4.17 Under impedance protection for Generator ZGVPDIS

Prepare the IED for verification of settings.

Values of the logical signals for ZGVPDIS are available on the local HMI under Main menu/Tests/Function status/Impedance/ZGVPDIS (21G, Z<)/1:ZGVPDIS:ZGVPDIS. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

### 11.4.17.1 Verifying the settings

#### Distance protection zones

While measuring operating characteristics, keep the current constant. Keep the current as close as possible to its rated value or lower, however make sure it is higher than 30% of the rated current.

If the measurement of the operating characteristics runs under constant voltage conditions, ensure that the maximum continuous current in an IED does not exceed four times its rated value.
To verify the zone 1 mho characteristic, at least two points must be tested.

![Diagram showing test points P1, P2, P3, and P4 with impedance angles Z1Fwd and Z1Rev]

**Figure 45: Proposed four test points for phase-to-phase fault**

Where,

- **Z1Fwd** is the forward positive sequence impedance setting for zone 1
- **Z1Rev** is the reverse positive sequence impedance setting for zone 1
- **LineAngle** is the Impedance angle for phase-to-phase fault in degrees

<table>
<thead>
<tr>
<th>Test points</th>
<th>X</th>
<th>R</th>
</tr>
</thead>
<tbody>
<tr>
<td>P1</td>
<td>$Z1Fwd \cdot \sin(\text{LineAngle})$</td>
<td>$Z1Fwd \cdot \cos(\text{LineAngle})$</td>
</tr>
<tr>
<td>P2</td>
<td>$((Z1Fwd - Z1Rev / 2) \cdot \sin(\text{LineAngle}))$</td>
<td>$Z1Fwd / 2 \cdot (1 + \cos(\text{LineAngle})) + Z1Rev / 2 \cdot (1 - \cos(\text{LineAngle}))$</td>
</tr>
<tr>
<td>P3</td>
<td>$((Z1Fwd - Z1Rev / 2) \cdot \sin(\text{LineAngle}))$</td>
<td>$-Z1Fwd / 2 \cdot (1 - \cos(\text{LineAngle})) + Z1Rev / 2 \cdot (1 + \cos(\text{LineAngle}))$</td>
</tr>
<tr>
<td>P4</td>
<td>$-Z1Rev \cdot \sin(\text{LineAngle})$</td>
<td>$-Z1Rev \cdot \cos(\text{LineAngle})$</td>
</tr>
</tbody>
</table>

Change the magnitude and angle of phase-to-phase voltage, to achieve impedances at test points P1, P2, P3 and P4. For each test point, observe that the output signals, PICKUP and PU_Z1 are activated where $x$ refers to the actual phase to be tested. After the trip time delay for the zone 1 has elapsed, the signals TRIP and TRZ1 shall be activated.

To verify zone 2 and zone 3 mho characteristic, at least two points must be tested. The default measuring loop selected is maximum current phase to current loop. Hence the below characteristics are tested with phase-to-ground fault conditions.
Figure 46: Proposed four test points for phase-to-earth fault

Where,

\( Z_{\text{Fwd}} \) is the forward positive sequence impedance setting for zone \( x \) (where, \( x \) is 2-3 depending on the zone selected)

\( Z_{\text{Rev}} \) is the reverse positive sequence impedance setting for zone \( x \) (where \( x \) is 2-3 depending on the zone selected)

\( \text{LineAngle} \) is the Impedance angle for phase-to-phase fault in degrees

<table>
<thead>
<tr>
<th>Test points</th>
<th>X</th>
<th>R</th>
</tr>
</thead>
<tbody>
<tr>
<td>P1</td>
<td>( Z_{\text{Fwd}} \cdot \sin(\text{LineAngle}) )</td>
<td>( Z_{\text{Fwd}} \cdot \cos(\text{LineAngle}) )</td>
</tr>
<tr>
<td>P2</td>
<td>( (Z_{\text{Fwd}} - Z_{\text{Rev}}/2) \cdot \sin(\text{LineAngle}) )</td>
<td>( Z_{\text{Fwd}}/2 \cdot (1 + \cos(\text{LineAngle}) + Z_{\text{Rev}}/2 \cdot (1 - \cos(\text{LineAngle})) )</td>
</tr>
<tr>
<td>P3</td>
<td>( (Z_{\text{Fwd}} - Z_{\text{Rev}}/2) \cdot \sin(\text{LineAngle}) )</td>
<td>( -Z_{\text{Fwd}}/2 \cdot (1 - \cos(\text{LineAngle}) + Z_{\text{Rev}}/2 \cdot (1 + \cos(\text{LineAngle})) )</td>
</tr>
<tr>
<td>P4</td>
<td>( -Z_{\text{Rev}} \cdot \sin(\text{LineAngle}) )</td>
<td>( -Z_{\text{Rev}} \cdot \cos(\text{LineAngle}) )</td>
</tr>
</tbody>
</table>

Change the magnitude and angle of phase-to-phase voltage to achieve impedances at test points P1, P2, P3 and P4. For each test point, observe that the output signals, PICKUP and PU_Zx are activated (where \( x \) is 2 or 3 depending on selected zone). After the trip time delay of the respective zone has elapsed, also the signals TRIP and TRZx (where \( x \) is 2 or 3 depending on selected zone) shall be activated.

Load encroachment

Load encroachment operates on the same measuring principles as the impedance measuring zones. Thus, it is necessary to follow the same principles as for distance protection when performing the secondary injection tests. Measure operating characteristics during constant
current conditions. Keep the injected current as close as possible to the rated value of its associated input transformer or lower. Ensure, however, that it is higher than 30% of the rated current. Ensure that the maximum continuous current of an IED does not exceed four times its rated value, if the measurement of the operating characteristics runs under constant voltage conditions.

**Measuring the operate limit of set values of load encroachment function**

The load encroachment does not have any special output to be verified. It can only be verified with the distance PICKUP and TRIP outputs.

Steps to test the load encroachment:

1. Supply the IED with healthy conditions for at least two seconds.
2. Apply the fault condition and slowly decrease the injected impedance to find the trip value for the phase-to-ground and phase-to-phase loops.

**Under voltage seal-in**

The under voltage seal-in logic can be verified using the 27 Trip and 27 PU outputs.

Steps to verify the logic:

1. Keep all the current and voltage in normal balanced condition.
2. Keeping the phase current at rated value, slowly reduce the voltage till it enters into the respective zone mho characteristics and also below the set value of the under voltage seal-in logic.
3. Before the trip time delay of the respective zone, reduce the current magnitude so that the operating impedance moves out of the mho characteristics.
4. The under voltage seal-in should give a PICKUP and TRIP after the trip time delay is set for the under voltage logic.

**11.4.17.2 Completing the test**

Continue to test another function or end the testing by setting the parameter `TestMode` to `Disabled` under `Main menu/Tests/IED test mode/1: TESTMODE`. If another function is tested, then set the parameter `Blocked` to `No` under `Main menu/Tests/Function test modes/Impedance/ZGVPDIS/1: ZGVPDIS` for the function, or for each individual function in a chain, to be tested next. Remember to set the parameter `Blocked` to `Yes` for each individual function that has been tested.

**11.5 Current protection**

**11.5.1 Instantaneous phase overcurrent protection 3-phase output PHPIOC (50)**

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Values of the logical signals for PHPIOC are available on the local HMI under `Main menu/Tests/Function status/Current protection/InstPhaseOverCurrent(50,3I>>)/PHPIOC(50;3I>>):x`, where x = 1, 2, and 3.
The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

To verify the settings the following fault type should be tested:

- Phase-to-ground fault

Ensure that the maximum continuous current, supplied from the current source used for the test of the IED, does not exceed four times the rated current value of the IED.

### 11.5.1.1 Measuring the trip limit of set values

1. Set the operation mode to 1 out of 3.
2. Inject a phase current into the IED with an initial value below the set value of \( IP_{>>} \) and also make sure that the set value \( IP_{>>} \) is in between \( IP_{>>Min} \) and \( IP_{>>Max} \).
3. Increase the injected current in the \( Ln \) phase until the TR_A (TR_B or TR_C) signal appears.
4. Switch the fault current off.

   ![Info](image)

   Observe: Do not exceed the maximum permitted overloading of the current circuits in the IED.

5. Compare the measured operating current with the set value.
6. Set the operation mode to 2 out of 3 and inject current into one of the phases and then check that no TRLn signal appears.

### 11.5.1.2 Completing the test

Continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

### 11.5.2 Directional phase overcurrent protection, four steps OC4PTOC (51_67)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Values of the logical signals for OC4PTOC are available on the local HMI under Main menu/Tests/Function status/Current protection/PhaseOverCurrent4Step(51_67,4(3I>))/OC4PTOC(51_67;4(3I>):x, where \( x \) = instance number.

The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

### 11.5.2.1 Verifying the settings

The verification of the non-directional phase overcurrent function is done as instructed below, but without applying any polarizing voltage.
1. Connect the test set for current injection to the appropriate IED phases. If there is any configuration logic that is used to enable or block any of the four available overcurrent steps, make sure that the step under test is enabled (for example, end fault protection).
   If 1 out of 3 currents are chosen for operation: Connect the injection current to phases A and neutral.
   If 2 out of 3 currents are chosen for operation: Connect the injection current into phase A and out from phase B.
   If 3 out of 3 currents are chosen for operation: Connect the symmetrical three-phase injection current into phases A, B and C.
2. Connect the test set for the appropriate three-phase voltage injection to the IED phases A, B and C. The protection shall be fed with a symmetrical three-phase voltage.
3. Block higher set stages when testing lower set stages by following the procedure described below:
   3.1. Set the injected polarizing voltage larger than the set minimum polarizing voltage (default is 5% of VBase) and set the injection current to lag the appropriate voltage by an angle of about 80° if forward directional function is selected.
      If 1 out of 3 currents are chosen for operation: The voltage angle of phase A is the reference.
      If 2 out of 3 currents are chosen for operation: The phase angle of the phase-to-phase voltage AB is the reference for APhase.
      If 3 out of 3 currents are chosen for operation: The voltage angle of phase A is the reference.
      If reverse directional function is selected, set the injection current to lag the polarizing voltage by an angle equal to 260° (equal to 80° + 180°).
   3.2. Increase the injected current, note the trip value of the tested step of the function and compare it to the set value.
   3.3. Decrease the current slowly, note the reset value and compare it to the reset ratio 95%.
4. If the test has been performed by injection of current in phase A, repeat the test, injecting current into phases B and C with polarizing voltage connected to phases B, respectively C (1 out of 3 currents for operation).
5. If the test has been performed by injection of current in phases AB, repeat the test, injecting current into phases BC and CA with the appropriate phase angle of injected currents.
6. Connect a trip output contact to a timer.
7. Set the injected current to 200% of the trip level of the tested stage, switch on the current and check the time delay.
   For inverse time curves, check the trip time at a current equal to 110% of the trip current for $tx_{Min}$.
8. Check that all trip and pickup contacts trip according to the configuration (signal matrixes).
9. Reverse the direction of the injected current and check that the protection does not trip.
10. If 2 out of 3 or 3 out of 3 currents are chosen for operation: Check that the function will not trip with current in one phase only.
11. Repeat the above described tests for the higher set stages.
12. Check that pickup and trip information is stored in the event menu.

11.5.2.2 Completing the test
continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.
11.5.3 Instantaneous residual overcurrent protection EFPIOC (50N)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Values of the logical signals for EFPIOC are available on the local HMI under Main menu/Tests/Function status/Current protection/InstResidualOverCurrent(50N,IN=>)/EFPIOC(50N;IN=>):x, where x = 1, 2, and 3.

The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

To verify the settings the following fault type should be tested:

- Phase-to-ground fault

Ensure that the maximum continuous current, supplied from the current source used for the test of the IED, does not exceed four times the rated current value of the IED.

11.5.3.1 Measuring the trip limit of set values

1. Inject a phase current into the IED with an initial value below the set value of $IN=>$ and also make sure that the set value $IN=>$ is in between $IN=>Min$ and $IN=>Max$.
2. Increase the injected current in the Ln or in the neutral (summed current input) phase until the TRIP signal appears.
3. Disable the fault current

   Do not exceed the maximum permitted overloading of the current circuits in the IED.

4. Compare the measured operating current with the set value.

11.5.3.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.5.4 Four step residual overcurrent protection, (Zero sequence or negative sequence directionality) EF4PTOC (51N/67N)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Values of the logical signals for D2PTOC are available on the local HMI under Main menu/Tests/Function status/Current protection/ResidualOverCurr4Step(51N_67N,4(IN=>))/EF4PTOC(51N_67N;4(IN=>)):x, where x = instance number.

The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.
11.5.4.1 Four step directional ground fault protection

1. Connect the test set for single current injection to the appropriate IED terminals.
   Connect the injection current to terminals A and neutral.
2. Set the injected polarizing voltage slightly larger than the set minimum polarizing voltage (5% of Vn) and set the injection current to lag the voltage by an angle equal to the set reference characteristic angle (AngleRCA), if the forward directional function is selected. If reverse directional function is selected, set the injection current to lag the polarizing voltage by an angle equal to RCA + 180°.
3. Increase the injected current and note the value at which the studied step of the function trips.
4. Decrease the current slowly and note the reset value.
5. If the test has been performed by injection of current in phase A, repeat the test, injecting current into terminals B and C with a polarizing voltage connected to terminals B, respectively C.
6. Block lower set steps when testing higher set steps according to the instructions that follow.
7. Connect a trip output contact to a timer.
8. Set the injected current to 200% of the trip level of the tested step, switch on the current and check the time delay.
   For inverse time curves, check the trip time at a current equal to 110% of the trip current for txMin.
9. Check that all trip and pickup contacts trip according to the configuration (signal matrixes)
10. Reverse the direction of the injected current and check that the step does not trip.
11. Check that the protection does not trip when the polarizing voltage is zero.
12. Repeat the above described tests for the higher set steps.
13. Finally, check that pickup and trip information is stored in the event menu.

11.5.4.2 Four step non-directional ground fault protection

1. Do as described in "Four step directional ground fault protection", but without applying any polarizing voltage.

11.5.4.3 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.5.5 Four step negative sequence overcurrent protection NS4PTOC (46I2)

Prepare the IED for verification of settings as outlined in section "Preparing for test" in this chapter.
When inverse time overcurrent characteristic is selected, the trip time of the stage will be the sum of the inverse time delay and the set definite time delay. Thus, if only the inverse time delay is required, it is important to set the definite time delay for that stage to zero.

Procedure

1. Connect the test set for injection of three-phase currents and voltages to the appropriate CT and VT inputs of the IED.
2. Inject pure negative sequence current, that is, phase currents with exactly same magnitude, reversed sequence and exactly 120° phase displaced into the IED with an initial value below negative sequence current pickup level. No output signals should be activated. Check under NS4PTOC function Service Values that correct I2 magnitude is measured by the function.
3. Set the injected negative sequence polarizing voltage slightly larger than the set minimum polarizing voltage (default 5 % of Vn) and set the injection current to lag the voltage by an angle equal to the set reference characteristic angle \(180° - \text{AngleRCA}\) if the forward directional function is selected.
   If reverse directional function is selected, set the injection current to lag the polarizing voltage by an angle equal to RCA.
4. Increase the injected current and note the value at which the studied step of the function trip.
5. Decrease the current slowly and note the reset value.
6. Block lower set steps when testing higher set steps according to the instructions that follow.
7. Connect a trip output contact to a timer.
8. Set the injected current to 200% of the trip level of the tested step, switch on the current and check the time delay.
   For inverse time curves, check the trip time at a current equal to 110% of the trip current in order to test parameter \(txmin\).
9. Check that all trip and pickup contacts trip according to the configuration (signal matrixes)
10. Reverse the direction of the injected current and check that the step does not trip.
11. Check that the protection does not trip when the polarizing voltage is zero.
12. Repeat the above-described tests for the higher set steps.
13. Finally, check that pickup and trip information is stored in the event menu.

### 11.5.1 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

### 11.5.6 Sensitive directional residual overcurrent and power protection SDEPSDE (67N)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".


Fig. 47: Principle connection of the test set

Values of the logical signals belonging to the sensitive directional residual overcurrent and power protection are available on the local HMI under **Main menu/Test/Function status/Current protection/SensDirResOvCurr(67N,IN>/SDEPSDE(67N,IN>):x**

**11.5.6.1 Measuring the trip and time limit for set values**

*Operation mode 3I₀ · cosϕ*

Procedure

1. Set the polarizing voltage to 1.2 · VNRelPU and set the phase angle between voltage and current to the set characteristic angle (*RCADir*). Note that the current lagging the voltage.
   Take setting *RCAComp* into consideration if not equal to 0.
2. Inject current until the function picks up, and make sure that the trip current of the set directional element is equal to the *INcosPhiPU* setting.
   The *I Dir (3I₀ · cosϕ)* function activates the PICKUP and PUNDIN output.
3. Assume that *ϕ´* is the phase angle between injected voltage (3V₀) and current (3I₀) i.e. *ϕ´ = RCADir-ϕ*. Change *ϕ´* to for example 45 degrees. Increase the injected current until the function trips.
4. Compare the result with the set value and make sure that the new injected *3I₀ · cos ϕ* is equal to the setting *INcosPhiPU*.
   Take the set characteristic into consideration, see Fig. 48 and Fig. 49.
5. Measure the trip time of the timer by injecting a current two times the set *INcosPhiPU* value and the polarizing voltage 1.2 · VNRelPU.
6. Compare the result with the expected value. The expected value depends on whether definite or inverse time was selected.
7. Set the polarizing voltage to zero and increase until the boolean output signal UNREL is activated, which is visible in the Application Configuration in PCM600 when the IED is in online mode. Compare the voltage with the set value \( V_{\text{unrel}} \).
8. Continue to test another function or complete the test by setting the test mode to Disabled.

\[
T_{\text{inv}} = \frac{T \cdot S_{\text{ref}}}{3I_0 \cdot 3V_{\text{test}} \cdot \cos(\phi)}
\]

(Equation 81)

Figure 48: Characteristic with \( \text{ROADir} \) restriction
Operation mode $3I_0 \cdot 3V_0 \cdot \cos \phi$

1. Set the polarizing voltage to $1.2 \cdot VNRe/PU$ and set the phase angle between voltage and current to the set characteristic angle ($RCADir$). Note that the current lagging the voltage.

2. Inject current until the function picks up, and make sure that the trip power is equal to the $SN\_PU$ setting for the set directional element. Note that for pick-up, both the injected current and voltage must be greater than the set values $INRe/PU$ and $VNRe/PU$ respectively. The function activates the PICKUP and PUDIRIN outputs.

3. Assume that $\phi'$ is the phase angle between injected voltage ($3V_0$) and current ($3I_0$) i.e. $\phi' = RCADir-\phi$. Change $\phi'$ to for example 45 degrees. Increase the injected current until the function trips.

4. Compare the result with the set value and make sure that the new injected $3I_0 \cdot 3V_0 \cdot \cos \phi$ is equal to the setting $SN\_PU$. Take the set characteristic into consideration, see figure 48 and figure 49.

5. Measure the trip time of the timer by injecting $1.2 \cdot VNRe/PU$ and a current to get two times the set $SN\_PU$ trip value.

---

Figure 49: Explanation of RCAcomp

- $RCADir = 0^\circ$
- Trip area
- Instrument transformer angle error
- Characteristic after angle compensation
- $3I_0$ (prim) $3I_0$ (to prot)
- ANSI06000651-2-en.vsd
\[ T_{inv} = TDSN \cdot SRef / 3I_{0\text{test}} \cdot 3V_{0\text{test}} \cdot \cos(\phi) \]

(Equation 82)

6. Compare the result with the expected value. The expected value depends on whether definite or inverse time was selected.
7. Continue to test another function or complete the test by setting the test mode to Disabled.

**Operation mode 3I\(_0\) and \(\phi\)**

1. Set the polarizing voltage to 1.2 \(\cdot\) \(VN_{RelPU}\) and set the phase angle between voltage and current to the set characteristic angle \((RCAdir)\). Note that the current lagging the voltage.
2. Inject current until the function picks up, and make sure that the trip current is equal to the \(IN_{RelPU}\) setting for the set directional element.

   Note that for pickup, both the injected current and voltage must be greater than the set values \(IN_{RelPU}\) and \(VN_{RelPU}\) respectively.

   The function activates the PICKUP and PUDIRIN output.
3. Measure with angles \(\phi\) around \(RCAdir \pm ROADir\).
4. Compare the result with the set values, refer to figure 50 for example characteristic.
5. Measure the trip time of the timer by injecting a current to get two times the set \(SN\_PU\) trip value.

\[ T_{inv} = TDSN \cdot SRef / 3I_{0\text{test}} \cdot 3V_{0\text{test}} \cdot \cos(\phi) \]

(Equation 83)

6. Compare the result with the expected value. The expected value depends on whether definite or inverse time was selected.
7. Continue to test another function or complete the test by setting the test mode to Disabled.
Non-directional ground fault current protection

Procedure

1. Measure that the trip current is equal to the \( I_{INDirPU} \) setting. The function activates the \( PICKUP \) and \( PUDIRIN \) output.
2. Measure the trip time of the timer by injecting a current of 200% of the trip value.
3. Compare the result with the expected value.
   The expected value depends on whether definite time \( tINNonDir \) or inverse time was selected.
4. Continue to test another function or complete the test by setting the test mode to \textit{Disabled}.

Residual overvoltage release and protection

Procedure

1. Measure that the trip voltage is equal to the \( VN\_PU \) setting. The function activates the \( PICKUP \) and \( PUVN \) signals.
2. Measure the trip time by injecting a voltage 1.2 times set \( VN\_PU \) trip value.
3. Compare the result with the set \( tVN \) trip value.
4. Inject a voltage \( 0.8 \cdot VNRelPU \) and a current high enough to trip the directional function at the chosen angle.
5. Increase the voltage until the directional function is released.
6. Compare the measured value with the set \( VNRelPU \) trip value.

11.5.6.2 Completing the test

continue to test another function or end the test by changing the \textit{TESTMODE} setting to \textit{Disabled}.
Restore connections and settings to their original values, if they were changed for testing purposes.
11.5.7 Thermal overload protection, one time constant, Fahrenheit/ Celsius LFPTTR/LCPTTR (26)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Check that the input logical signal BLOCK is logical zero and that on the local HMI, the logical signal TRIP, PICKUP and ALARM are equal to logical zero.

11.5.7.1 Measuring the trip and time limit of set values

Testing the protection without external temperature compensation (NonComp)

1. Quickly set the measured current (fault current) in one phase to about 300% of IRef (to minimise the trip time), and switch the current off.
2. Reset the thermal memory on the local HMI under Main menu/Reset/Reset temperature/ThermalOverLoad1TimeConst(PTTR,26)/LFPTTR:x, Main menu/Reset/Reset temperature/ThermalOverLoad1TimeConst(PTTR,26)/LCPTTR:x,
3. Switch the fault current on and take note of the temperature, available on the local HMI under Main menu/Test/Function status/Current protection/ThermOverLoad1TimeConst(PTTR,26)/LFPTTR:x/TEMP, Main menu/Test/Function status/Current protection/ThermOverLoad1TimeConst(PTTR,26)/LCPTTR:x/TEMP,
4. Check the time until the actual temperature TEMP has reached the AlarmTemp level during injection.
   Monitor the signal ALARM until it appears on the corresponding binary output or on the local HMI.
5. Measure the LFPTTR/LCPTTR (26) protection trip time.
   Use the TRIP signal from the configured binary output to stop the timer.
6. Take the TEMP readings.
   Compare with the setting of TripTemp.
7. Activate the BLOCK binary input.
   The signals ALARM, PICKUP and TRIP should disappear.
8. Reset the BLOCK binary input.
9. Check the reset limit (TdReset).
   Monitor the signal PICKUP until it disappears on the corresponding binary output or on the local HMI, take the TEMP readings and compare with the setting of RecITemp.
10. Compare the measured trip time with the setting according to the formula.
11. Reset the thermal memory.
12. Continue to test another function or end the test by changing the test mode setting to Disabled.

11.5.7.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.
11.5.8 Thermal overload protection, two time constants TRPTTR (49)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.5.8.1 Checking trip and reset values

1. Connect symmetrical three-phase currents to the appropriate current terminals of the IED.
2. Set the Time constant 1 (Tau1) and Time Constant 2 (Tau2) temporarily to 1 minute.
3. Set the three-phase injection currents slightly lower than the set trip value of stage IBase1, increase the current in phase A until stage IBase1 trips and note the trip value.

Observe the maximum permitted overloading of the current circuits in the IED.

4. Decrease the current slowly and note the reset value.
5. Set the injection current for phase A to 1.50 · IBase1.
6. Set the time constant for IBase1 in accordance with the setting plan.
7. Set the injection current for phase A to 1.50 · IBase1.
8. Connect a trip output contact to the timer and monitor the output of contacts ALARM1 and ALARM2 to digital inputs in test equipment.
9. Read the heat content in the thermal protection from the local HMI and wait until the content is zero.
10. Switch on the injection current and check that ALARM1 and ALARM2 contacts trip at the set percentage level and that the trip time for tripping is in accordance with the set Time Constant 1 (Tau1).

With setting Itr = 101%IBase1 and injection current 1.50 · IBase1, the trip time from zero content in the memory shall be 0.60 · Time Constant 1 (Tau1).
11. Switch off the injection current and check from the service menu readings of thermal status and LOCKOUT that the lockout resets at the set percentage of heat content.
12. Check that all trip and alarm contacts trip according to the configuration logic.
13. Continue to test another function or end the test by changing the TESTMODE setting to Disabled.
14. Restore connections and settings to their original values, if they were changed for testing purposes.
11.5.9 Breaker failure protection, phase segregated activation and output CCRBRF (50BF)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

The Breaker failure protection, 3-phase activation and output function CCRBRF (50BF) should normally be tested in conjunction with some other function that provides an initiate signal. An external BFI_3P signal can also be used.

To verify the settings in the most common back-up trip mode 1 out of 3, it is sufficient to test phase-to-ground faults.

At mode 2 out of 4 the phase current setting, Pickup_PH can be checked by single-phase injection where the return current is connected to the summated current input. The value of residual (ground fault) current IN set lower than Pickup_N is easiest checked in back-up trip mode 1 out of 4.

11.5.9.1 Checking the phase current trip value, Pickup_PH

The check of the Pickup_PH current level is best made in FunctionMode = Current and BuTripMode = 1 out of 3 or 2 out of 4.

1. Apply the fault condition, including BFI_3P of CCRBRF (50BF), with a current below set Pickup_PH.
2. Repeat the fault condition and increase the current in steps until a trip occurs. Note that the INITIATE signal shall be re-applied for each new current value.
3. Compare the result with the set Pickup_PH.

If RetripMode = Off and FunctionMode = Current is set, only backup trip can be used to check set Pickup_PH.

11.5.9.2 Checking the residual (ground fault) current trip value Pickup_N set below Pickup_PH

Check the low set Pickup_N current where setting FunctionMode = Current and setting BuTripMode = 1 out of 4

1. Apply the fault condition, including BFI_3P of CCRBRF (50BF), with a current just below set Pickup_N.
2. Repeat the fault condition and increase the current in steps until trip appears. Note that the INITIATE signal shall be re-applied for each new current value.
3. Compare the result with the set Pickup_N.
4. Disconnect AC and BFI_3P input signals.
11.5.9.3 Checking the re-trip and back-up times

The check of the set times can be made in connection with the check of trip values above.

Choose the applicable function and trip mode, such as FunctionMode = Current and RetripMode = UseFunctionMode.

1. Apply the fault condition, including initiation of CCRBRF (50BF), well above the set current value. Measure time from BFI_3P of CCRBRF (50BF).
2. Check the re-trip t1 and back-up trip times t2 and t3.
   In applicable cases, the back-up trip for multi-phase pickup t2MPh and back-up trip 2, t2 and t3 can also be checked. To check t2MPh, a two-phase or three-phase initiation shall be applied.
3. Disconnect AC and BFI_3P input signals.

11.5.9.4 Verifying the re-trip mode

Choose the mode below, which corresponds to the actual case.

In the cases below it is assumed that FunctionMode = Current is selected.

**Checking the case without re-trip, RetripMode = Off**

1. Set RetripMode = Off.
2. Apply the fault condition, including initiation of CCRBRF (50BF), well above the set current value.
3. Verify that no re-trip, but back-up trip is achieved after set time.
4. Disconnect AC and BFI_3P input signals.

**Checking the re-trip with current check, RetripMode = UseFunctionMode**

1. Set RetripMode = UseFunctionMode.
2. Apply the fault condition, including initiation of CCRBRF (50BF), well above the set current value.
3. Verify that re-trip is achieved after set time t1 and back-up trip after time t2.
4. Apply the fault condition, including initiation of CCRBRF (50BF), with current below set current value.
5. Verify that no re-trip, and no back-up trip is obtained.
6. Disconnect AC and BFI_3P input signals.

**Checking re-trip without current check**

1. Set RetripMode = Always.
2. Apply the fault condition, including initiation of CCRBRF (50BF), well above the set current value.
3. Verify that re-trip is achieved after the set time t1, and the back-up trip after time t2.
4. Apply the fault condition, including initiation of CCRBRF (50BF), with current below set current value.
5. Verify that re-trip is achieved after set time t1, but no back-up trip is obtained.
6. Disconnect AC and BFI_3P input signals.
11.5.9.5  Verifying the back-up trip mode

In the cases below it is assumed that \( \text{FunctionMode} = \text{Current} \) is selected.

Checking that back-up tripping is not achieved at normal CB tripping
Use the actual tripping modes. The case below applies to re-trip with current check.

1. Apply the fault condition, including initiation of CCRBRF (50BF), with phase current well above set value Pickup\_PH.
2. Interrupt the current, with a margin before back-up trip time, \( t2 \). It may be made at issue of re-trip command.
3. Check that re-trip is achieved, if selected, but no back-up trip is obtained.
4. Disconnect AC and BFI\_3P input signals.

The normal mode \( \text{BuTripMode} = 1 \text{ out of 3} \) should have been verified in the tests above. In applicable cases the modes \( 1 \text{ out of 4} \) and \( 2 \text{ out of 4} \) can be checked. Choose the mode below, which corresponds to the actual case.

Checking the case \( \text{BuTripMode} = 1 \text{ out of 4} \)
It is assumed that the ground-fault current setting \( \text{Pickup\_N} \) is below phase current setting \( \text{Pickup\_PH} \).

1. Set \( \text{BuTripMode} = 1 \text{ out of 4} \).
2. Apply the fault condition, including initiation of CCRBRF (50BF), with one-phase current below set \( \text{Pickup\_PH} \) but above \( \text{Pickup\_N} \). The residual ground-fault should then be above set \( \text{Pickup\_N} \).
3. Verify that back-up trip is achieved after set time. If selected, re-trip should also appear.
4. Disconnect AC and BFI\_3P input signals.

Checking the case \( \text{BuTripMode} = 2 \text{ out of 4} \)
The ground-fault current setting \( \text{Pickup\_N} \) may be equal to or below phase-current setting \( \text{Pickup\_PH} \).

1. Set \( \text{BuTripMode} = 2 \text{ out of 4} \).
2. Apply the fault condition, including initiation of CCRBRF (50BF), with one-phase current above set \( \text{Pickup\_PH} \) and residual (ground fault) above set \( \text{Pickup\_N} \).
3. Verify that back-up trip is achieved after set time. If selected, re-trip should also appear.
4. Apply the fault condition, including initiation of CCRBRF (50BF), with at least one-phase current below set \( \text{Pickup\_PH} \) and residual (ground fault) above set \( \text{Pickup\_N} \). The current may be arranged by feeding three- (or two-) phase currents with equal phase angle (I0-component) below \( \text{Pickup\_PH} \), but of such value that the residual (ground fault) current (3I0) will be above set value \( \text{Pickup\_N} \).
5. Verify that back-up trip is not achieved.
6. Disconnect AC and BFI\_3P input signals.

11.5.9.6  Verifying instantaneous back-up trip at CB faulty condition

Applies in a case where a signal from CB supervision function regarding CB being faulty and unable to trip is connected to input 52FAIL.
1. Repeat the check of back-up trip time. Disconnect current and input signals.
2. Activate the input 52FAIL. The output CBALARM (CB faulty alarm) should appear after set time $t_{CBAlarm}$. Keep the input activated.
3. Apply the fault condition, including initiation of CCRBRF (50BF), with current above set current value.
4. Verify that back-up trip is obtained without intentional delay, for example within 20ms from application of initiation.
5. Disconnect injected AC and BFI_3P input signals.

11.5.9.7 Verifying the case FunctionMode = CB Pos

It is assumed that re-trip without current check is selected, RetripMode = UseFunctionMode.

1. Set FunctionMode = CB Pos.
2. Apply input signal for CB closed to relevant input or inputs 52A_A, 52A_B or 52A_C.
3. Apply the input signal, or signals for the start of CCRBRF.
4. Verify that phase selection re-trip and backup trip are achieved after set times.
5. Disconnect the start signal(s). Keep the CB closed signal(s).
6. Apply input signal(s), for start of CCRBRF.
7. Arrange disconnection of CB closed signal(s) well before set backup trip time $t_2$.
8. Verify that backup trip is not achieved.

11.5.9.8 Verifying the case FunctionMode = Current or CB Pos

To be made only when FunctionMode = Current or CB Pos is selected. It is suggested to make the tests in one phase only, or at three-pole trip applications for just three-pole tripping.

Checking the case with fault current above set value Pickup_PH

The operation shall be as in RetripMode = UseFunctionMode.

1. Set FunctionMode = Current or CB Pos.
2. Leave the inputs for CB close inactivated. These signals should not influence.
3. Apply the fault condition, including initiation of CCRBRF (50BF), with current above the set Pickup_PH value.
4. Check that the re-trip, if selected, and back-up trip commands are achieved.
5. Disconnect injected AC and BFI_3P input signals.

Checking the case with fault current below set value Pickup_BlkCBPos

The case shall simulate a case where the fault current is very low and operation will depend on CB position signal from CB auxiliary contact. It is suggested that retrip without current check is used, setting RetripMode = UseFunctionMode Check.

1. Set FunctionMode = Current or CB Pos.
2. Apply input signal for CB closed to relevant input or inputs 52a_A (B or C).
3. Apply the fault condition with input signal(s) for initiation of CCRBRF (50BF). The value of current should be below the set value Pickup_BlkCBPos.
4. Verify that phase selection re-trip (if selected) and back-up trip are achieved after set times. Failure to trip is simulated by keeping the signal(s) CB closed activated.
5. Disconnect the AC and the BFI_3P signal(s). Keep the CB closed signal(s).
6. Apply the fault and the initiation again. The value of current should be below the set value Pickup_BlkCBPos.
7. Arrange disconnection of CB closed signal(s) well before set back-up trip time $t_2$. It simulates a correct CB tripping.
8. Verify that back-up trip is not achieved. Re-trip can appear for example, due to selection $\text{RetripMode} = \text{Always}$.
9. Disconnect injected AC and BFI_3P input signals.

11.5.9.9 Verifying the external start signal has timed out

1. Set $\text{StartMode} = \text{FollowStart}$.
2. Set $\text{FunctionMode} = \text{Current}$.
3. Use default value for $t\text{StartTimeout} = 1.0$ s.
4. Use default value for time delay backup trip $t_2 = 0.150$ s.
5. Leave the inputs for CB close inactivated. These signals should not influence.
6. Apply START signal, and after 1.5 s apply the current above the set $\text{IPPU}$ value.
7. Check that STALARM signal is set after 1.0 s and backup trip is blocked.

11.5.9.10 Verifying that backup signal is released when STALARM is reset

1. Set $\text{StartMode} = \text{FollowStart}$.
2. Set $\text{FunctionMode} = \text{Current}$.
3. Use default value for $t\text{StartTimeout} = 1.0$ s.
4. Use default value for time delay backup trip $t_2 = 0.150$ s.
5. Leave the inputs for CB close inactivated. These signals should not influence.
6. Apply START signal at $t = 0$ s, and after 1.5 s apply the current above the set $\text{IPPU}$ value.
7. Check that STALARM signal is set after 1.0 s and backup trip is blocked.
8. Reset START signal after 3.5 s, ensure that STALARM reset immediately.
9. Set START after 3.6 s, ensure that backup trip is set after $t_2$ backup trip delay has expired (3.750 s).

11.5.9.11 Test of $\text{FollowStart&Mode}$ behaviour

1. Set $\text{StartMode} = \text{FollowStart&Mode}$.
2. Set $\text{FunctionMode} = \text{Current}$.
3. Set $\text{RetripMode} = \text{UseFunctionMode}$.
4. Use default value for $t\text{StartTimeout} = 1.0$ s.
5. Use default value for time delay backup trip $t_2 = 0.150$ s.
6. Use default value for time delay re-trip $t_1 = 0.000$ s.
7. Leave the inputs for CB close inactivated. These signals should not influence.
8. Apply START signal, and after 0.200 s apply the current above the set $\text{IPPU}$ value.
9. Check that STALARM does not come and that re-trip TRRET is instantly set at 0.200 s while the backup trip TRBU set only when $t_2$ expires at 0.200 + 0.150 s.

11.5.9.12 Test of $\text{FollowStart}$ behaviour

1. Set $\text{StartMode} = \text{FollowStart}$.
2. Set $\text{FunctionMode} = \text{Current}$.
3. Set $\text{RetripMode} = \text{UseFunctionMode}$.
4. Use default value for $t\text{StartTimeout} = 1.0$ s.
5. Use for example the default value for time delay backup trip $t_2 = 0.150$ s.
6. Use for example the default value for time delay retrip $t_1 = 0.000$ s.
7. Leave the inputs for CB close inactivated. These signals should not influence.
8. Apply START signal, and after 0.200 s apply the current above the set IPPU value.
9. Check that STALARM does not come and that both re-trip TRRET and backup trip TRBU comes instantly at 0.200 s (that is, as soon as current is given to the function).

11.5.9.13 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.5.10 Stub protection STBPTOC (50STB)

Prepare the IED for verification of settings outlined in Section “Preparing the IED to verify settings”.

Logical signals for STBPTOC (50STB) protection are available on the local HMI underMain menu/ Settings/IED Settings/Current protection/Stub(PTOC,50STB)/STBPTOC:x

11.5.10.1 Measuring the trip limit of set values

1. Check that the input logical signals BLOCK and ENABLE and the output logical signal TRIP are all logical zero.
2. Activate the input ENABLE on the STBPTOC (50STB) function block
3. For a short while inject a current (fault current) in one phase to about 110% of the set operating current, and switch the current off.
   Observe to not exceed the maximum permitted overloading of the current circuits in the IED.
4. Switch the fault current on and measure the operating time of STBPTOC (50STB). Use the TRIP signal from the configured binary output to stop the timer. The operation should be instantaneously.
5. Activate the input BLOCK on the STBPTOC (50STB) function block.
6. Switch on the fault current (110% of the setting).
   No TRIP signal should appear.
7. Switch off the fault current.
8. For a short while inject a current (fault current) in same phase to about 90% of the set operating current, and switch the current off.
9. Switch the fault current on.
   No TRIP signal should appear.
10. Switch off the fault current.
11. Reset the ENABLE binary input.
12. Switch the fault current on.
    No TRIP signal should appear.
13. Disable the fault current.

11.5.10.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.
11.5.11 Pole discrepancy protection CCPDSC (52PD)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.5.11.1 Verifying the settings

1. External detection logic, Contact function selection = ContactSel setting equals CCPDSC (52PD) signal from CB. Activate the EXTPDIND binary input, and measure the operating time of CCPDSC (52PD).
   Use the TRIP signal from the configured binary output to stop the timer.
2. Compare the measured time with the set value tTrip.
3. Reset the EXTPDIND input.
4. Activate the BLKDBYAR binary input.
   This test should be performed together with Autorecloser SMBRREC (79).
5. Activate the EXTPDIND binary input.
   No TRIP signal should appear.
6. Reset both BLKDBYAR and EXTPDIND binary inputs.
7. Activate the BLOCK binary input.
8. Activate EXTPDIND binary input.
   NO TRIP signal should appear.
9. Reset both BLOCK and EXTPDIND binary inputs.
10. If Internal detection logic Contact function selection = ContactSel setting equals Pole position from auxiliary contacts. Then set inputs 52b_A...52a_C in a status that activates the pole discordance logic and repeats step 2 to step 6.
11. Unsymmetrical current detection with CB monitoring: Set measured current in one phase to 110% of current release level. Activate CLOSECMD and measure the operating time of the CCPDSC (52PD) protection.
   Use the TRIP signal from the configured binary output to stop the timer.
12. Deactivate the CLOSECMD: Set measured current in one phase to 90% of Current Release level. Activate CLOSECMD.
   NO TRIP signal should appear.
13. Repeat the previous two steps using OPENCMD instead of CLOSECMD. Asymmetry current detection with CB monitoring: Set all three currents to 110% of Current Release level. Activate CLOSECMD.
   NO TRIP signal should appear due to symmetrical condition.
14. Deactivate the CLOSECMD. Decrease one current with 120% of the current unsymmetrical level compared to the other two phases. Activate CLOSECMD and measure the operating time of the CCPDSC (52PD) protection.
   Use the TRIP signal from the configured binary output to stop the timer.
15. Deactivate the CLOSECMD. Decrease one current with 80% of the current unsymmetrical level compared to the other two phases. Activate CLOSECMD.
   NO TRIP signal should appear.

11.5.11.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled.
Restore connections and settings to their original values, if they were changed for testing purposes.
11.5.12 Directional underpower protection GUPPDUP (37)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.5.12.1 Verifying the settings

The underpower protection shall be set to values according to the real set values to be used.

The test is made by means of injection of voltage and current where the amplitude of both current and voltage and the phase angle between the voltage and current can be controlled. During the test, the analog outputs of active and reactive power shall be monitored.

1. Connect the test set for injection of voltage and current corresponding to the mode to be used in the application. If a three-phase test set is available this could be used for all the modes. If a single-phase current/voltage test set is available the test set should be connected to a selected input for one-phase current and voltage.

Table 38: Calculation modes

<table>
<thead>
<tr>
<th>Set value: Mode</th>
<th>Formula used for complex power calculation</th>
</tr>
</thead>
<tbody>
<tr>
<td>A, B, C</td>
<td>$\bar{S} = \bar{V}_A \cdot \bar{I}_A^* + \bar{V}_B \cdot \bar{I}_B^* + \bar{V}_C \cdot \bar{I}_C^*$ (Equation 84)</td>
</tr>
<tr>
<td>Arone</td>
<td>$\bar{S} = \bar{V}_{AB} \cdot \bar{I}<em>A^* - \bar{V}</em>{BC} \cdot \bar{I}_C^*$ (Equation 85)</td>
</tr>
<tr>
<td>PosSeq</td>
<td>$\bar{S} = 3 \cdot \bar{V}<em>{PosSeq} \cdot \bar{I}</em>{PosSeq}^*$ (Equation 86)</td>
</tr>
<tr>
<td>AB</td>
<td>$\bar{S} = \bar{V}_{AB} \cdot (\bar{I}_A^* - \bar{I}_B^*)$ (Equation 87)</td>
</tr>
<tr>
<td>BC</td>
<td>$\bar{S} = \bar{V}_{BC} \cdot (\bar{I}_B^* - \bar{I}_C^*)$ (Equation 88)</td>
</tr>
<tr>
<td>CA</td>
<td>$\bar{S} = \bar{V}_{CA} \cdot (\bar{I}_C^* - \bar{I}_A^*)$ (Equation 89)</td>
</tr>
</tbody>
</table>

Table continues on next page
### Set value: Mode

<table>
<thead>
<tr>
<th>Mode</th>
<th>Formula used for complex power calculation</th>
</tr>
</thead>
</table>
| A    | \( S = 3 \cdot \overline{V}_A \cdot \overline{I}_A^* \)  
(Equation 90) |
| B    | \( S = 3 \cdot \overline{V}_B \cdot \overline{I}_B^* \)  
(Equation 91) |
| C    | \( S = 3 \cdot \overline{V}_C \cdot \overline{I}_C^* \)  
(Equation 92) |

2. Adjust the injected current and voltage to the set values in % of \( I_{Base} \) and \( V_{Base} \) (converted to secondary current and voltage). The angle between the injected current and voltage shall be set equal to the set direction \( Angle1 \), angle for stage 1 (equal to 0° for low forward power protection and equal to 180° for reverse power protection). Check that the monitored active power is equal to 100% of rated power and that the reactive power is equal to 0% of rated power.

3. Change the angle between the injected current and voltage to \( Angle1 + 90° \). Check that the monitored active power is equal to 0% of rated power and that the reactive power is equal to 100% of rated power.

4. Change the angle between the injected current and voltage back to 0°. Decrease the current slowly until the PICKUP1 signal, pickup of stage 1, is activated.

5. Increase the current to 100% of \( I_{Base} \).

6. Switch the current off and measure the time for activation of TRIP1, trip of stage 1.

7. If a second stage is used, repeat steps 2 to 6 for the second stage.

### 11.5.12.2 Completing the test

Continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

### 11.5.13 Directional overpower protection GOPPDOP (32)

Prepare the IED for verification of settings outlined in Section “Preparing the IED to verify settings”.

### 11.5.13.1 Verifying the settings

The overpower protection shall be set to values according to the real set values to be used. The test is made by means of injection of voltage and current where the amplitude of both current and voltage and the phase angle between the voltage and current can be controlled. During the test the analog outputs of active and reactive power shall be monitored.

1. Connect the test set for injection of voltage and current corresponding to the mode to be used in the application. If a three phase test set is available this could be used for all the
modes. If a single phase current/voltage test set is available the test set should be connected to a selected input for one phase current and voltage.

2. Adjust the injected current and voltage to the set rated values in % of \textit{IBase} and \textit{VBase} (converted to secondary current and voltage). The angle between the injected current and voltage shall be set equal to the set direction \textit{Angle1}, angle for stage 1 (equal to \(0^\circ\) for low forward power protection and equal to \(180^\circ\) for reverse power protection). Check that the monitored active power is equal to 100% of rated power and that the reactive power is equal to 0% of rated power.

3. Change the angle between the injected current and voltage to \(\textit{Angle1} + 90^\circ\). Check that the monitored active power is equal to 0% of rated power and that the reactive power is equal to 100% of rated power.

4. Change the angle between the injected current and voltage back to \textit{Angle1} value. Increase the current slowly from 0 until the \textit{PICKUP1} signal, pickup of stage 1, is activated. Check the injected power and compare it to the set value \textit{Power1}, power setting for stage 1 in % of \textit{Sbase}.

5. Increase the current to 100% of \textit{IBase} and switch the current off.

6. Switch the current on and measure the time for activation of \textit{TRIP1}, trip of stage 1.

7. If a second stage is used, repeat steps 2 to 6 for the second stage.

\textbf{11.5.13.2 Completing the test}

continue to test another function or end the test by changing the \textit{TESTMODE} setting to \textit{Disabled}. Restore connections and settings to their original values, if they were changed for testing purposes.

\textbf{11.5.14 Broken conductor check BRCPTOC (46)}

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

\textbf{11.5.14.1 Measuring the trip and time limit of set values}

1. Check that the input logical signal \textit{BLOCK} to the BRCPTOC (46) function block is logical zero and note on the local HMI that the output signal \textit{TRIP} from the BRCPTOC (46) function block is equal to the logical 0.

2. Set the measured current (fault current) in one phase to about 110% of the set operating current \textit{Pickup\_PH}. Observe to not exceed the maximum permitted overloading of the current circuits in the terminal.

3. Switch on the fault current and measure the operating time of BRCPTOC (46). \textit{TRIP} is controlled by Gate 13 in the configuration. Use the \textit{TRIP} signal from the configured binary output to stop the timer.

4. Compare the measured time with the set value \textit{tOper}.

5. Activate the \textit{BLOCK} binary input.

6. Switch on the fault current (110% of the setting) and wait longer than the set value \textit{tOper}. No \textit{TRIP} signal should appear.

7. Switch off the fault current.

8. Set the measured current (fault current) in same phase to about 90% of the set operating current \textit{Pickup\_PH}. Switch off the current.

9. Switch on the fault current and wait longer than the set value \textit{tOper}.
No TRIP signal should appear.
10. Switch off the fault current.

11.5.14.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.5.15 Capacitor bank protection CBPGAPC

Prepare the IED for verification of settings as outlined in section "Preparing for test" in this chapter.

In this section it is shown how to test the capacitor bank protection function CBPGAPC for application on a 60Hz, 200MVar, 400kV SCB with 500/5A ratio CT.

Note that such SCB is shown in the application manual for this function. The same procedure can be used to test SCB with some other rating and different CT ratio.

As calculated in the application manual the base current for this particular SCB will be 289A on the primary side and 0.578A on the CT secondary side. Before any testing is commenced make sure that setting $I_{Base}$ for this function is set to 289A (that is, setting for the base current corresponds to the rated current of the protected SCB). It will be also assumed that all other settings have values as shown in the setting example in the application manual for this SCB.

Test equipment

Connect the secondary test set to the CT inputs on the IED dedicated for the SCB currents. Single- or three-phase test equipment can be used but it may be required to have facility to vary the frequency of the injected current signal(s).

11.5.15.1 Verifying the settings and operation of the function

Reconnection inhibit feature

1. Inject SCB rated current (that is, 0.587A at 50Hz for this SCB) in at least one phase (preferably perform this test with three phase injection).
2. After couple of seconds stop injection of all currents (that is, set all currents back to 0A).
3. Check that function binary output signal RECNINH is set to logical 1 and that only resets after the set time under parameter $t_{ReconnInhibit}$ (for example 300s for this SCB) has expired.
4. If this binary signal is used to prevent CB closing make sure that it is properly connected/wired into the CB closing circuit.
Overcurrent feature

Note that during testing the overcurrent feature the harmonic voltage overload feature or reactive power overload feature may also give pickup and trip signals depending on their actual settings. Therefore it is best to switch them off during this test.

1. Inject current 20% bigger than the set overcurrent pickup level under setting parameter PU 51 (for example, 1.2 \cdot 1.35 \cdot 0.587A = 0951A at 50Hz for this SCB) in phase A only.
2. Check that function binary output signals PU_51_A and PU_OC are set to one.
3. Check that function binary output signals TROC and TRIP are set to one after the set time under parameter tOC (that is, 30s for this SCB) has expired.
4. If any of these signals are used for tripping, signaling and/or local/remote indication check that all relevant contacts and LEDs have operated and that all relevant GOOSE messages have been sent.
5. Check that service value from the function for current in phase A, on the local HMI under Main menu/Test is approximately 476A (that is, 0.951A \cdot (500/1) = 476A).
6. Stop injection of all currents (that is, set all currents back to 0A).
7. Check that all above mentioned function binary output signals now have logical value zero.
8. Repeat above steps 1-7 for phase B and phase C.

Note that the operation of this feature is based on current peak value. That means that this overcurrent function is also able to trip for the same current magnitude but for different injected frequencies. If required repeat this injection procedure for example for the 3rd harmonic by just simply injecting 3 \cdot 50 = 150Hz currents with the same magnitude. Obtain results shall be the same.

Undercurrent feature

1. Inject SCB rated current (that is, 0.587A at 50Hz for this SCB) in all three phases.
2. Lower phase A current 10% under the set value for setting parameter PU_37 (that is, 0.9 \cdot 0.7 \cdot 0.587A = 0.370A at 50Hz for this SCB).
3. Check that function binary output signals PU_37_A and PU_UC are set to one.
4. Check that function binary output signals TRUC and TRIP are set to one after the set time under parameter tUC (for example, 5s for this SCB) has expired.
5. If any of these signals are used for tripping, signaling and/or local/remote indication check that all relevant contacts and LEDs have operated and that all relevant GOOSE messages have been sent.
6. Check that service value from the function for current in phase A, on the local HMI under Main menu/Test is approximately 185A (that is, 0.370A \cdot (500/1) = 185A).
7. Stop injection of all currents (that is, set all currents back to 0A).
8. Check that all above mentioned function binary output signals now have logical value zero because they will be automatically blocked by operation of built-in reconnection inhibit feature.
9. Repeat above steps 1-8 for phase B and phase C.
Reactive power overload feature

Note that during testing the reactive power overload feature the harmonic voltage overload feature or overcurrent feature may also give pickup and trip signals depending on their actual settings. Therefore it is recommended to switch them off during this test.

1. Inject current equal to the set reactive power overload pickup level under setting parameter UP_QOL (that is, 1.3 · 0.587A = 0.763A at 50Hz for this SCB) in phase A only.
2. Check that function binary output signals PU_QOL_A and PU_QOL are set to one.
3. Check that function binary output signals TRQOL and TRIP are set to one after the set time under parameter tQOL (for example, 60s for this SCB) has expired.
4. If any of these signals are used for tripping, signaling and/or local/remote indication check that all relevant contacts and LEDs have operated and that all relevant GOOSE messages have been sent.
5. Check that service value from the function for current in phase A, on the local HMI under Main menu/Test is approximately 382A (that is, 0.763A · (500/1) = 382A).
6. Check that service value from the function for reactive power in phase A, on the local HMI under Main menu/Test is approximately 169% (that is, 1.3 · 1.3 = 1.69pu = 169%).
7. Stop injection of all currents (that is, set all currents back to 0A).
8. Check that all above mentioned function binary output signals now have logical value zero.
9. Repeat above steps 1 - 8 for phase B and phase C.

Note that operation of this feature is based on injected current and internally calculated true RMS values. That means that this feature is also able to trip for current signals with varying frequency. However due to relatively complex calculation procedure it is recommended to do secondary tests only with fundamental frequency current signals.

The following formula can be used to calculate SCB reactive power in per-unit system when current with different frequency from the rated frequency is injected.

\[
    Q[pu] = \frac{f_{\text{rated}}}{f_{\text{injected}}} \cdot I^2[pu]
\]

(Equation 93)

Harmonic voltage overload feature

Note that during testing the harmonic voltage overload feature the reactive power overload feature or overcurrent feature may also give pickup and trip signals depending on their actual settings. Therefore it is recommended to switch them off during this test.

Procedure to test inverse time delayed step:

The following points on the inverse curve are defined per relevant IEC/ANSI standards for time multiplier value set to \( k_{HOL_IDMT}=1.0 \).
Note that operation of this feature is based on internally calculated voltage peak RMS value. That means that this feature is also able to trip for current signals with varying frequency.

Here will be shown how to test the fourth point from the above table. Other points can be tested in the similar way:

1. Inject 140% of the base current (that is, $1.4 \cdot 0.587\text{A} = 0.822\text{A}$ at 50Hz for this SCB) in phase A only.
2. Check that function binary output signals PU_HOL_IDMT_A and PU_HOL are set to one.
3. Check that function binary output signals TRHOL and TRIP are set to one after the expected time (for example, 15s for this voltage level in accordance with the above table) has expired.
4. If any of these signals are used for tripping, signaling and/or local/remote indication, check that all relevant contacts and LEDs have operated and that all relevant GOOSE messages have been sent.
5. Check that service value for current in phase A, on the local HMI under **Main menu/Test** is approximately 411A (that is, $0.822\text{A} \cdot (500/1) = 411\text{A}$).
6. Check that service value for voltage across SCB in phase A, on the local HMI under **Main menu/Test** is approximately 140%.
7. Stop injection of all currents (that is, set all currents back to 0A).
8. Check that all above mentioned function binary output signals now have logical value zero.
9. Repeat above steps 1 - 8 for phase B and phase C.
10. Repeat above steps 1 - 8 to test different points from the above table.

Operation of this feature is based on internally calculated peak RMS voltage value. That means that this feature is also able to trip for current signals with varying frequency. Note that for the fundamental frequency injection, internally calculated voltage in percent corresponds directly to the injected current value given in percent. However if it is required to test IDMT characteristic with a varying frequency, the magnitude of the injected current must be adjusted accordingly. The following formula can be used to calculate required current RMS value in percent at the desired injection frequency in order to archive voltage percentage value given in the above table:

$$I_{\text{inj}} [%] = \frac{I_{\text{inj}} [\%]}{f_{\text{fund}}} \cdot V [\%]$$

(Equation 94)

Note that it is recommended to test IDMT operating times by injected current with the rated frequency.

Above procedure can also be used to test definite time step. Pay attention that IDMT step can also trip during such injection. Therefore make sure that appropriate settings are entered in order to insure correct test results for definite time step.
11.5.15.2 Completing the test

Continue to test another functions or end the test by changing the Test mode setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes. Make sure that all built-in features for this function, which shall be in operation, are enabled and with correct settings.

11.5.16 Negative-sequence time overcurrent protection for machines NS2PTOC (46I2)

When inverse time overcurrent characteristic is selected, the trip time of the stage will be the sum of the inverse time delay and the set definite time delay. Thus, if only the inverse time delay is required, it is important to set the definite time delay for that stage to zero.

11.5.16.1 Verifying settings by secondary injection

1. Connect the test set for injection of three-phase currents to the appropriate current terminals of the IED.
2. Go to Main menu/Settings/IED Settings/Current protection/NegSeqOverCurr2Step/NSOn/General and make sure that the function is enabled, that is Operation is set to Enabled.
3. Inject current into IEDs in such a way that negative sequence component is created and then verify that negative sequence component of the injected currents is calculated correctly by the function. See example below for 1 A rated current transformer.
4. Inject pure negative sequence current, that is, phase currents with exactly same magnitude, reversed sequence and exactly 120° phase displaced into the IED with an initial value below negative sequence current pickup level. No output signals should be activated. Note: If it is difficult to obtain pure negative sequence current for the secondary injection test, a current corresponding to the two phase short-circuit condition can be used. A two phase short-circuit gives a negative sequence current of a magnitude: magnitude = (1/√3) · fault current.
5. Increase the injected current and note the value at which the step 1 of the function operates. Pickup signal PU_ST1 must be activated when amplitude of the negative sequence current lies slightly above the pickup level I2-1>. Corresponding trip signals TRST1 and TRIP is activated after the pre-set time delay has expired. Note: Block or disable operation of step 2 when testing step 1 if the injected current activates the step 2.
6. Decrease the current slowly and note the reset value.
7. Connect a trip output contact to a timer.
8. Set the current to 200 % of the pickup level of the step 1, switch on the current and check the definite time delay for trip signals TRST1 and TRIP. Once the measured negative sequence current exceeds the set pickup level I2-1>, the settable definite timer t1 starts to count and trip signals is released after the set time delay has elapsed. The same test must be carried out to check the accuracy of definite time delay for ALARM signal. Note: The output ALARM is operated by PICKUP signal.
9. If inverse time is selected the trip signals TRST1 and TRIP operates after a time corresponding to the formula:
This means that if current jumps from 0 to 2 times pickup and negative sequence capability value of generator $K_I$ is set to 10 sec and current pickup level $I^2 - I^*$ is set to 10% of rated generator current, then TRST1 and TRIP signals operates at time equal to 250 s ± tolerance.

10. Repeat the above-described tests for the step 2 of the function excluding the inverse time testing.

11. Finally check that pickup and trip information is stored in the event menu.

**Example**

Given $CT_{prim}$ the CT ratios $CT_{sec}$ for all three phases is 1000 A, $I_{Base}$ is 1000 A, and the following secondary currents are applied:

<table>
<thead>
<tr>
<th>Phase</th>
<th>Ampl</th>
<th>Angl</th>
</tr>
</thead>
<tbody>
<tr>
<td>IA</td>
<td>1.1 A</td>
<td>15 deg</td>
</tr>
<tr>
<td>IB</td>
<td>0.6 A</td>
<td>97 deg</td>
</tr>
<tr>
<td>IC</td>
<td>1.3 A</td>
<td>-135 deg</td>
</tr>
</tbody>
</table>

The service value output $NSCURR$ indicating amplitude of negative sequence current in primary amperes should be 962A approximative.

### 11.5.16.2 Completing the test

Continue to test another functions or end the test by changing the Test mode setting to Off. Restore connections and settings to their original values, if they were changed for testing purposes. Make sure that all built-in features for this function, which shall be in operation, are enabled and with correct settings.

### 11.5.17 Voltage-restrained time overcurrent protection VRPVOC(51V)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

### 11.5.17.1 Verifying the settings

Verifying settings by secondary injection
1. Connect the test set for three-phase current injection and three-phase voltage injection to the appropriate IED terminals.

2. Go to Main menu/Settings/IED Settings/Current protection/VoltageRestOverCurr (51V, 2(l>/V<))/VRPVOC (51V,2(l>/V<)):1/General and make sure that the function is enabled, that is, Operation is set to Enabled.

3. The test of the function may be performed by injecting restrain voltage and increasing the injected current(s). Note the value at which the PICKUP and STOC signals are set.

In the following equations, restrain voltage is the magnitude of the minimum phase-to-phase voltage in secondary volts.

The set trip value in secondary amperes is calculated according to the following equations:

First part of the characteristic (Restrain voltage ≤ 25% of VBase):

\[
\text{Pickup Curr} \left( \frac{100}{100} \times \frac{\text{IBase} \times \text{CTsec}}{\text{CTprim}} \times \frac{\text{VDepFact}_\text{sec}}{100} \right)
\]

(Equation 95)

Second part of the characteristic (25% of VBase ≤ Restrain voltage ≤ VHighLimit/100*VBase), valid when setting parameter VDepMode = Slope:

\[
\left( \frac{\text{Pickup Curr} \times \text{IBase} \times \text{CTsec}}{\text{CTprim}} \times \frac{1}{\text{VDepFact}_\text{sec}} \right) \times \left( \frac{\text{Restrain Voltage} - \frac{25}{100} \times \text{VBase} \times \text{VTsec}}{\text{VTprim}} \right) \times \left( \frac{\text{Pickup Curr} \times \text{IBase} \times \text{CTsec}}{\text{CTprim}} \times \frac{\text{VDepFact}_\text{sec}}{100} \right)
\]

(Equation 96)

Third part of the characteristic (VHighLimit/100*VBase ≤ Restrain voltage):

\[
\text{Pickup Curr} \left( \frac{100}{100} \times \frac{\text{IBase} \times \text{CTsec}}{\text{CTprim}} \right)
\]

(Equation 97)

Example (rated secondary current = 5A):

<table>
<thead>
<tr>
<th>CT ratio</th>
<th>10 000/5A</th>
</tr>
</thead>
<tbody>
<tr>
<td>VT ratio</td>
<td>10 kV/100 V</td>
</tr>
<tr>
<td>Pickup_Curr</td>
<td>100%</td>
</tr>
<tr>
<td>VDepFact</td>
<td>25%</td>
</tr>
<tr>
<td>VHighLimit</td>
<td>100%</td>
</tr>
<tr>
<td>IBase</td>
<td>10 000 A</td>
</tr>
<tr>
<td>VBase</td>
<td>10 kV</td>
</tr>
</tbody>
</table>

A test for each section of the characteristic of the function VRPVOC may be performed; that may be achieved by applying the following voltages:

First section of the characteristic:
If VDepMode = Slope, the minimum measured phase-to-phase voltage is lower than 0,25*VBase; if VDepMode = Step, the minimum measured phase-to-phase voltage is lower than VHighLimit/100*VBase:
• VA: Ampl = 10 / √3; Angle = 0°
• VB: Ampl = 10 / √3; Angle = 240°
• VC: Ampl = 100 / √3; Angle = 120°

Second section of the characteristic only:
If VDepMode = Slope, the minimum measured phase-to-phase voltage is between 0.25*VBase and VHighLimit/100*VBase:
• VA: Ampl = 50 / √3; Angle = 0°
• VB: Ampl = 50 / √3; Angle = 240°
• VC: Ampl = 100 / √3; Angle = 120°

Last section of the characteristic:
If VDepMode = either Slope or Step, the minimum measured phase-to-phase voltage is higher than VHighLimit/100*VBase:
• VA: Ampl = 105 / √3; Angle = 0°
• VB: Ampl = 105 / √3; Angle = 240°
• VC: Ampl = 105 / √3; Angle = 120°

4. Inject the voltages that are related to the first part of the characteristic, and then slowly increase the phase current IA from 0.0 A up to the value the function trips. The PICKUP and STOC signals must be activated when the amplitude of the phase current IA is slightly above 0.25 A on the secondary side. The corresponding trip signals TROC and TRIP will be activated after the pre-set time delay has expired.

5. Decrease the current IA slowly and note the reset value.

6. Repeat steps 4 and 5 applying voltages that are related to the second and the last section of the characteristic; the function trips when IA is slightly higher than: 0.5 A in the second section; 5A in the last section.

7. Connect the trip output contact to the input channel of the test set in order to stop the injection and measure the trip time.

8. If definite time delay is used for the overcurrent step, set the setting Characterist = ANSI Def. Time. Apply the voltages related to the last part of the characteristic and inject a current IA 200% higher than the set tripping level, and check the definite time delay for trip (the signals TROC and TRIP of the protection function VRPVOC are active in trip condition).

9. If inverse time delay is used for the overcurrent step, the parameter setting Characterist shall be properly set; we can refer, for example, to the setting IEC Very inv.; If the IEC Very inverse time characteristic is selected, the trip signals TROC and TRIP will trip after a time defined by the equation:

\[ t[s] = \frac{13.5^* k}{I - Pickup\_Curr} \]

(Equation 98)

where:
- \( t \) Trip time in seconds
- \( I \) Measured value (for example, phase current)
- Pickup\_Curr Set trip value

This means that if the measured phase current jumps from 0 to 2 times the set trip level and time multiplier \( k \) is set to 1.0 s (default value), then the TROC and TRIP signals will trip after a time delay equal to 13.5 s ± tolerance.
Taking into account the above explanation, inject the voltages related to the last part of the characteristic and inject a current IA 200% higher than the set operation level, and check the trip time delay.

If $t_{Def\_OC}$ is set to a value different from 0 s, then this time delay is added to the one that is defined by the IDMT characteristic.

10. Check the pickup and trip information that are stored in the event menu.
11. The previous step 8 or 9 may be repeated also for the first and second section of the characteristic.
12. Supply the IED with symmetric three-phase voltages at their rated values. Go to Main menu/Settings/IED Settings/Current protection/VoltageRestOverCurr(51V,2(>|<)/V<))/VRPVOCS(51V,2(|>|<)):1/Undervoltage and set the setting $Operation\_UV = Enabled$ to activate the undervoltage stage.
13. Slowly decrease the voltage in two phases simultaneously, until the STUV and PICKUP signals appear.
14. Note the trip value. The set trip value in secondary volts is calculated according to the following equation:

$$
\frac{Pickup\_Volt \times VBase}{100} \times \frac{VT\_sec}{\sqrt{3}} \times \frac{VT\_prim}{VT\_sec}
$$

(Equation 99)

If the VRPVOCS function is used as an overcurrent protection with undervoltage seal-in, it is necessary to first inject sufficient current to activate the STOC signal before the under-voltage step is allowed to trip. In order to achieve that, apply symmetric three-phase voltages at their rated value and then inject a current IA that is 200% higher than the set tripping level. Then slowly decrease the voltage in two phases simultaneously, until the STUV and PICKUP signals appear.

15. Increase slowly the applied voltages of the previous two phases and note the reset value.
16. Check that the trip output of the relay is connected to the input channel of the test in order to stop the injection and measure the trip time.
17. Inject symmetric three-phase voltages at their rated value and check that the STUV and PICKUP signals reset as well as the trip signals of the function block (TRIP and TRUV).
18. Instantaneously decrease the voltage in two phases simultaneously to a value 20% lower than the set trip value (take into account the previous note if VRPVOCS is configured in ACT with the undervoltage seal-in feature).
19. Measure the definite time delay for the TRUV and TRIP signals and compare it with the set value $t_{Def\_UV}$.
20. Check that pickup and trip information is stored in the event menu.

### 11.5.17.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.
11.6 Voltage protection

11.6.1 Two step undervoltage protection UV2PTUV (27)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.6.1.1 Verifying the settings

Verification of pickup value and time delay to trip for Step 1

1. Check that the IED settings are appropriate, especially the PICKUP value, the definite time delay and the 1 out of 3 operation mode.
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly decrease the voltage in one of the phases, until the PICKUP signal appears.
4. Note the trip value and compare it with the set value.

   The trip value in secondary volts is calculated according to the following equations:

   For phase-to-ground measurement:
   \[
   \frac{V_{pickup}}{100} < \frac{V_{Base}}{\sqrt{3}} \times \frac{V_{sec}}{V_{prim}}
   \]
   (Equation 100)

   For phase-to-phase measurement:
   \[
   \frac{V_{pickup}}{100} < V_{Base} \times \frac{V_{sec}}{V_{prim}}
   \]
   (Equation 101)

5. Increase the measured voltage to rated load conditions.
6. Check that the PICKUP signal resets.
7. Instantaneously decrease the voltage in one phase to a value about 20% lower than the measured trip value.
8. Measure the time delay for the TRIP signal, and compare it with the set value.
9. Check the inverse time delay by injecting a voltage corresponding to 0.8 × V_{pickup}.

   For example, if the inverse time curve A is selected, the trip signals TRST1 and TRIP trip after a time corresponding to the equation:

   \[
   t(s) = \frac{TD1}{\left(1 - \frac{V}{V_{pickup}}\right)}
   \]
   (Equation 102)
where:

- $t(s)$: Trip time in seconds
- $TD1$: Settable time multiplier of the function for step 1
- $V$: Measured voltage
- $V_{pickup}$: Set pickup voltage for step 1

For example, if the measured voltage jumps from the rated value to 0.8 times the set pickup voltage level and time multiplier $TD1$ is set to 0.05 s (default value), then the TRST1 and TRIP signals trip at a time equal to 0.250 s ± tolerance.

10. The test above can be repeated to check the inverse time characteristic at different voltage levels.

11. Repeat the above described steps for Step 2 of the function.

**Extended testing**
The tests above can be repeated for 2 out of 3 and for 3 out of 3 operation mode.

**11.6.1.2 Completing the test**

Continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

**11.6.2 Two step overvoltage protection OV2PTOV (59)**

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

**11.6.2.1 Verifying the settings**

**Verification of single-phase voltage and time delay to trip for Step 1**

1. Apply single-phase voltage below the set value $Pickup1$.
2. Slowly increase the voltage until the PU_ST1 signal appears.
3. Note the trip value and compare it with the set value $Pickup1$.

The trip value in secondary volts is calculated according to the following equations:

For phase-to-ground measurement:

$$V_{pickup} > \frac{V_{Base}}{\sqrt{3}} \times \frac{VT_{sec}}{VT_{prim}}$$

(Equation 103)

For phase-to-phase measurement:
4. Decrease the voltage slowly and note the reset value.
5. Set and apply about 20% higher voltage than the measured trip value for one phase.
6. Measure the time delay for the TRST1 signal and compare it with the set value.
7. Check the inverse time delay by injecting a voltage corresponding to $1.2 \times V_{\text{pickup}}$.
8. Repeat the test to check the inverse time characteristic at different over-voltage levels.
9. Repeat the above described steps for Step 2 of the function.

11.6.2.2 Extended testing

1. The tests above can be repeated for 2 out of 3 and for 3 out of 3 operation mode.

11.6.2.3 Completing the test

Continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.6.3 Two step residual overvoltage protection ROV2PTOV (59N)

Prepare the IED for verification of settings outlined in Section “Preparing the IED to verify settings”.

11.6.3.1 Verifying the settings

1. Apply a single-phase voltage either to a single-phase voltage input or to a residual voltage input with the pickup value below the set value $\text{Pickup1}$.
2. Slowly increase the value until PU_ST1 appears.
3. Note the trip value and compare it with the set value.
4. Decrease the voltage slowly and note the reset value.
5. Set and apply a 20% higher voltage than the measured trip value for one phase.
6. Measure the time delay for the TRST1 signal and compare it with the set value.
7. Check the inverse time delay by injecting a voltage corresponding to $1.2 \times V_{\text{pickup}}$.

For example, if the inverse time curve A is selected, the trip signals TRST1 and TRIP trip after a time corresponding to the equation:

$$t(s) = \frac{k_1}{\left(\frac{U}{U_1} \cdot 1\right)}$$

(Equation 105)
\[ t(s) = \frac{TD1}{V \cdot \left( V_{\text{pickup}} > V\right)} \]

(Equation 106)

where:
- \( t(s) \): Trip time in seconds
- \( TD1 \): Settable time multiplier of the function for step 1
- \( V \): Measured voltage
- \( V_{\text{pickup}} \): Set pickup voltage for step 1

For example, if the measured voltage jumps from 0 to 1.2 times the set pickup voltage level and time multiplier TD1 is set to 0.05 s (default value), then the TRST1 and TRIP signals trip at a time equal to 0.250 s ± tolerance.

8. Repeat the test for Step 2 of the function.

11.6.3.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.6.4 Overexcitation protection OEXPVPH (24)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.6.4.1 Verifying the settings

1. Enable function.
2. Connect a symmetrical three-phase voltage input from the test set to the appropriate connection terminals of the overexcitation protection OEXPVPH (24) is configured for a three-phase voltage input.
   A single-phase injection voltage is applied if the function is configured for a phase-to-phase voltage input.
   OEXPVPH (24) is conveniently tested using rated frequency for the injection voltage and increasing the injection voltage to get the desired overexcitation level.
3. Connect the alarm contact to the timer and set the time delay \( t_{\text{Alarm}} \) temporarily to zero.
4. Increase the voltage and note the operate value \( \text{Pickup}_1 \).
5. Reduce the voltage slowly and note the reset value.
6. Set the alarm time delay to the correct value according to the setting plan and check the time delay, injecting a voltage corresponding to 1.2 \( \cdot \text{Pickup}_1 \).
7. Connect a trip output contact to the timer and temporarily set the time delay \( t_{\text{MinTripDelay}} \) to 0.5s.
8. Increase the voltage and note the \( \text{Pickup}_2 \) operate value
9. Reduce the voltage slowly and note the reset value.
10. Set the time delay to the correct value according to the setting plan and check the time delay \( t_{MinTripDelay} \), injecting a voltage corresponding to \( 1.2 \cdot Pickup2 \).
11. Check that trip and alarm contacts operate according to the configuration logic.
12. Set the cooling time constant temporarily to min value (1min.) to quickly lower the thermal content.
13. Wait for a period equal to 6 times \( t_{CoolingK} \) switch 20 minutes on a voltage \( 1.15 \cdot Pickup1 \) and check the inverse operate time.
   Wait until the thermal memory is emptied, set the cooling time constant according to the setting plan and check another point on the inverse time curve injecting a voltage \( 1.3 \cdot Pickup1 \).
14. Finally check that PICKUP and TRIP information is stored in the event menu.

11.6.4.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled.
Restore connections and settings to their original values, if they were changed for testing purposes.

11.6.5 Voltage differential protection VDCPTOV (60)

Prepare the IED for verification of settings as outlined in section "Requirements" and section "Preparing for test" in this chapter.

11.6.5.1 Check of undervoltage levels

This test is relevant if the setting BlkDiffAtVLow = Yes.

Check of V1Low

Procedure

1. Connect voltages to the IED according to valid connection diagram and figure 51.
2. Apply voltage higher than the highest set value of \( VDTrip \), \( V1Low \) and \( V2Low \) to the V1 three-phase inputs and to one phase of the V2 inputs according to figure 51.
   The voltage differential PICKUP signal is set.
Figure 51: Connection of the test set to the IED for test of V1 block level

where:
1. is three-phase voltage group1 (V1)
2. is three-phase voltage group2 (V2)

3. Decrease slowly the voltage in phase VA of the test set until the PICKUP signal resets.
4. Check V1 blocking level by comparing the voltage level at reset with the set undervoltage blocking V1Low.
5. Repeat steps 2 to 4 to check V1Low for the other phases.

The connections to V1 must be shifted to test another phase. (VA to VB, VB to VC, VC to VA)

Check of V2Low
Procedure

1. Connect voltages to the IED according to valid connection diagram and figure 52.
Figure 52: Connection of the test set to the IED for test of V2 block level

where:
1. is three-phase voltage group1 (V1)
2. is three-phase voltage group2 (V2)

2. Apply voltage higher than the highest set value of $V_{DTrip}$, $V_{1Low}$ and $V_{2Low}$ to the V1 three-phase inputs and to one phase of the V2 inputs according to figure 52. The voltage differential PICKUP signal is set.
3. Decrease slowly the voltage in phase VC of the test set until the PICKUP signal resets.
4. Check V2 blocking level by comparing the voltage level at reset with the set undervoltage blocking $V_{2Low}$.

11.6.5.2 Check of voltage differential trip and alarm levels

Procedure

1. Connect voltages to the IED according to valid connection diagram and figure 53.
Figure 53: Connection of the test set to the IED for test of alarm levels, trip levels and trip timer

where:
1 is three-phase voltage group1 (V1)
2 is three-phase voltage group2 (V2)

2. Apply $1.2 \cdot V_n$ (rated voltage) to the V1 and V2 inputs.
3. Decrease slowly the voltage of in phase VA of the test set until the ALARM signal is activated.

   The ALARM signal is delayed with timer $t_{Alarm}$

4. Check the alarm operation level by comparing the differential voltage level at ALARM with the set alarm level $VD_{Alarm}$.
5. Continue to slowly decrease the voltage until PICKUP signal is activated.
6. Check the differential voltage operation level by comparing the differential voltage level at PICKUP with the set trip level $VD_{Trip}$.
7. Repeat steps 1 to 6 to check the other phases.
   Observe that the connections to V1 must be shifted to test another phase. (VA to VB, VB to VC, VC to VA)

11.6.5.3 Check of trip and trip reset timers

Procedure
1. Connect voltages to the IED according to valid connection diagram and figure 53.
2. Set Vn (rated voltage) to the V1 inputs and increase V2 voltage until differential voltage is 1.5 · operating level (VDTrip).
3. Switch on the test set. Measure the time from activation of the PICKUP signal until TRIP signal is activated.
4. Check the measured time by comparing it to the set trip time tTrip.
5. Increase the voltage until PICKUP signal resets. Measure the time from reset of PICKUP signal to reset of TRIP signal.
6. Check the measured time by comparing it to the set trip reset time tReset.

11.6.5.4 Final adjustment of compensation for VT ratio differences

Procedure

1. With the protection in test mode, view the differential voltage service values in each phase on the local HMI under Main menu/Test/Function status/Voltage protection/VoltageDiff(PTOV,60)/VDCPTOV:x.

The IED voltage inputs should be connected to the VTs according to valid connection diagram.

2. Record the differential voltages.
3. Calculate the compensation factor RF_x for each phase.
   For information about calculation of the compensation factor, see the application manual.
4. Set the compensation factors on the local HMI under Main menu/Settings/Settings group N/Voltage protection/VoltageDiff(PTOV,60)/VDCPTOV:x
5. Check that the differential voltages are close to zero.

11.6.5.5 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled.
Restore connections and settings to their original values, if they were changed for testing purposes.

11.6.6 Loss of voltage check LOVPTUV (27)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.6.6.1 Measuring the trip limit of set values

1. Check that the input logical signals BLOCK, CBOPEN and BLKU are logical zero.
2. Supply a three-phase rated voltage in all three phases and note on the local HMI that the TRIP logical signal is equal to the logical 0.
3. Switch off the voltage in all three phases.
   After set tTrip time a TRIP signal appears on the corresponding binary output or on the local HMI.
Note that TRIP at this time is a pulse signal, duration should be according to set \( t_{\text{Pulse}} \).

4. Inject the measured voltages at rated values for at least set \( t_{\text{Restore}} \) time.
5. Activate the CBOPEN binary input.
6. Simultaneously disconnect all the three-phase voltages from the IED. No TRIP signal should appear.
7. Inject the measured voltages at rated values for at least set \( t_{\text{Restore}} \) time.
8. Activate the BLKU binary input.
9. Simultaneously disconnect all the three-phase voltages from the IED. No TRIP signal should appear.
10. Reset the BLKU binary input.
11. Inject the measured voltages at rated values for at least set \( t_{\text{Restore}} \) time.
12. Activate the BLOCK binary input.
13. Simultaneously disconnect all the three-phase voltages from the IED. No TRIP signal should appear.
14. Reset the BLOCK binary input.

11.6.6.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.7 Frequency protection

11.7.1 Underfrequency protection SAPTUF (81)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.7.1.1 Verifying the settings

Verification of PICKUP value and time delay to trip

1. Check that the IED settings are appropriate, for example the pickup value and the time delay.
2. Supply the IED with three-phase voltages at their rated values and initial frequency. The initial frequency is calculated using Equation 107.

\[
\text{StartFrequency} + 0.02 + \text{floor}[f, -\text{StartFrequency}] \times 0.04
\]

(Equation 107)

3. Slowly decrease the voltage frequency by steps of 40 mHz until the TRIP signal appears; during each step apply the voltage signal for a time that is either at least 10% longer than \( (t_{\text{Delay}}+100\text{ms}) \) or a suitable time to monitor the function.
4. Note the frequency value at which the START signal appears and compare it with the set value \( \text{StartFrequency} \).
5. Note the frequency value at which the TRIP signal appears and compare it with the set value \( \text{StartFrequency} \).
6. Increase the frequency until its rated value is reached.
7. Check that the PICKUP signal resets.
8. Supply the IED with three-phase voltages at their rated values and frequency 20 mHz over the set value *StartFrequency*.
9. Decrease the frequency with a 40 mHz step, applying it for a time that is at least 10% longer than (*tDelay*+100ms).
10. Measure the time delay of the TRIP signal, and compare it with the set value *tDelay*. Note that the measured time consists of the set value of the time delay plus the minimum trip time of the pickup start function (80 - 90 ms).

Extended testing

1. The test above can be repeated to check the time to reset.
2. The tests above can be repeated to test the frequency dependent inverse time characteristic.

Verification of the low voltage magnitude blocking

1. Check that the IED settings are appropriate, for example the *PUFrequency*, *VMin*, and the *tDelay*.
2. Supply the IED with three-phase voltages at rated values.
3. Slowly decrease the magnitude of the applied voltage, until the BLKDMAGN signal appears.
4. Note the voltage magnitude value and compare it with the set value *VMin*.
5. Slowly decrease the frequency of the applied voltage, to a value below *PUFrequency*.
6. Check that the PICKUP signal does not appear.
7. Wait for a time corresponding to *tDelay*; make sure that the TRIP signal does not appear.

11.7.2 Completing the test

Continue to test another function or end the test by changing the TESTMODE setting to *Disabled*. Restore connections and settings to their original values, if they were changed for testing purposes.

11.7.2 Overfrequency protection SAPTOF (81)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.7.2.1 Verifying the settings

Verification of PICKUP value and time delay to trip

1. Check that the IED settings are appropriate, for example the pickup value and the time delay.
2. Supply the IED with three-phase voltages at their rated values and initial frequency.
3. Slowly increase the frequency of the applied voltage with a 40 mHz step, applying it for a period that is 10% longer than *tDelay*.
4. Note the trip value and compare it with the set value.
5. Decrease the frequency to rated operating conditions.
6. Check that the PICKUP signal resets.
7. Set the frequency to 20 mHz under the trip value.
8. Increase the frequency with a 40 mHz step, applying it for a period that is 10% longer than \( t_{\text{Delay}} \).
9. Measure the time delay for the TRIP signal, and compare it with the set value. Note that the measured time consists of the set value for time delay plus minimum trip time of the pickup function (80 - 90 ms).

Extended testing

1. The test above can be repeated to check the time to reset.

Verification of the low voltage magnitude blocking

1. Check that the settings in the IED are appropriate, for example the \( PUFrequency \) and the \( t_{\text{Delay}} \).
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly decrease the magnitude of the applied voltage, until the BLKDMAGN signal appears.
4. Note the voltage magnitude value and compare it with the set value.
5. Slowly increase the frequency of the applied voltage, to a value above \( PUFrequency \).
6. Check that the PICKUP signal does not appear.
7. Wait for a time corresponding to \( t_{\text{Delay}} \), make sure that the TRIP signal does not appear.

11.7.2.2 Completing the test

continue to test another function or end the test by changing the \( TESTMODE \) setting to \( \text{Disabled} \). Restore connections and settings to their original values, if they were changed for testing purposes.

11.7.3 Rate-of-change frequency protection SAPFRC (81)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.7.3.1 Verifying the settings

PICKUP value and time delay to trip

1. Check that the settings in the IED are appropriate, especially the PICKUP value and the definite time delay. Set \( \text{PickupFreqGrad} \), to a rather small negative value.
2. Supply the IED with three-phase voltages at their rated values.
3. Slowly decrease the frequency of the applied voltage, with an increasing rate-of-change that finally exceeds the setting of \( \text{PickupFreqGrad} \), and check that the PICKUP signal appears.
4. Note the trip value and compare it with the set value.
5. Increase the frequency to rated operating conditions, and zero rate-of-change.
6. Check that the PICKUP signal resets.
7. Instantaneously decrease the frequency of the applied voltage to a value about 20% lower than the nominal value.
8. Measure the time delay for the TRIP signal, and compare it with the set value.

Extended testing
1. The test above can be repeated to check a positive setting of \textit{PickupFreqGrad}.
2. The tests above can be repeated to check the time to reset.
3. The tests above can be repeated to test the \textit{RESTORE} signal, when the frequency recovers from a low value.

11.7.3.2 Completing the test

Continue to test another function or end the test by changing the \textit{TESTMODE} setting to \textit{Disabled}. Restore connections and settings to their original values, if they were changed for testing purposes.

11.8 Multipurpose protection

11.8.1 General current and voltage protection CVGAPC

Prepare the IED for verification of settings as outlined in section "Requirements" and section "Preparing for test" in this chapter.

One of the facilities within the general current and voltage protection function CVGAPC is that the value, which is processed and used for evaluation in the function, can be chosen in many different ways by the setting parameters \textit{CurrentInput} and \textit{VoltageInput}.

These setting parameters decide what kind of preprocessing the connected three-phase CT and VT inputs shall be subjected to. That is, for example, single-phase quantities, phase-to-phase quantities, positive sequence quantities, negative sequence quantities, maximum quantity from the three-phase group, minimum quantity from the three-phase group, difference between maximum and minimum quantities (unbalance) can be derived and then used in the function.

Due to the versatile possibilities of CVGAPC itself, but also the possibilities of logic combinations in the application configuration of outputs from more than one CVGAPC function block, it is hardly possible to define a fully covering general commissioning test.

11.8.1.1 Built-in overcurrent feature (non-directional)

Procedure

1. Go to Main menu/Test/Function test modes/Multipurpose protection/\textit{GeneralCurrentVoltage(GAPC)/CVGAPC:x} and make sure that CVGAPC to be tested is unblocked and other functions that might disturb the evaluation of the test are blocked.
2. Connect the test set for injection of three-phase currents to the appropriate current terminals of the IED.
3. Inject current(s) in a way that relevant measured current (according to setting parameter \textit{CurrentInput}) is created from the test set. Increase the current(s) until the low set stage trips and check against the set trip value.
4. Decrease the current slowly and check the reset value.
5. Block high set stage if the injection current will activate the high set stage when testing the low set stage according to below.
6. Connect a TRIP output contact to the timer.
7. Set the current to 200\% of the trip value of low set stage, switch on the current and check the time delay.
For inverse time curves, check the trip time at a current equal to 110% of the trip current at \(t_{\text{MinTripDelay}}\).

8. Check that TRIP and PICKUP contacts trip according to the configuration logic.
9. Release the blocking of the high set stage and check the trip and reset value and the time delay for the high set stage in the same way as for the low set stage.
10. Finally check that PICKUP and TRIP information is stored in the event menu.

Information on how to use the event menu is found in the operator's manual.

### 11.8.1.2 Overcurrent feature with current restraint

The current restraining value has also to be measured or calculated and the influence on the operation has to be calculated when the testing of the trip value is done.

**Procedure**

1. **Trip value measurement**
   - The current restraining value has also to be measured or calculated and the influence on the operation has to be calculated when the testing of the trip value is done.

### 11.8.1.3 Overcurrent feature with voltage restraint

**Procedure**

1. **Connect the test set for injection of three-phase currents and three-phase voltages to the appropriate current and voltage terminals of the IED.**
2. **Inject current(s) and voltage(s) in a way that relevant measured (according to setting parameter `CurrentInput` and `VoltageInput`) currents and voltages are created from the test set.**
   - Overall check in principal as above (non-directional overcurrent feature)
3. **Trip value measurement**
   - The relevant voltage restraining value (according to setting parameter `VoltageInput`) has also to be injected from the test set and the influence on the trip value has to be calculated when testing of the trip value is done.
4. **Trip time measurement**
   - Definite times may be tested as above (non-directional overcurrent feature). For inverse time characteristics the PICKUP value (to which the overcurrent ratio has to be calculated) is the actual pickup value as got with actual restraining from the voltage restraining quantity.

### 11.8.1.4 Overcurrent feature with directionality

Please note that the directional characteristic can be set in two different ways either just dependent on the angle between current and polarizing voltage (setting parameter `DirPrinc_OC1` or `DirPrinc_OC2` set to or in a way that the trip value also is dependent on the angle between current and polarizing voltage according to the \(I \cdot \cos(\theta)\) law (setting parameter `DirPrinc_OC1` or `DirPrinc_OC2` set to \(I \cdot \cos(\theta)\)). This has to be known if a more detailed measurement of the directional characteristic is made, than the one described below.
Procedure

1. Connect the test set for injection of three-phase currents and three-phase voltages to the appropriate current and voltage terminals of the IED.
2. Inject current(s) and voltage(s) in a way that relevant measured (according to setting parameter CurrentInput and VoltageInput) currents and voltages are created from the test set.
3. Set the relevant measuring quantity current to lag or lead (lag for negative RCA angle and lead for positive RCA angle) the relevant polarizing quantity voltage by an angle equal to the set IED characteristic angle (rca-dir) when forward directional feature is selected and the CTWYEpoint configuration parameter is set to ToObject.
   If reverse directional feature is selected or CTWYEpoint configuration parameter is set to FromObject, the angle between current and polarizing voltage shall be set equal to rca-dir +180°.
4. Overall check in principal as above (non-directional overcurrent feature)
5. Reverse the direction of the injection current and check that the protection does not trip.
6. Check with low polarization voltage that the feature becomes non-directional, blocked or with memory according to the setting.

11.8.1.5 Over/Undervoltage feature

Procedure

1. Connect the test set for injection three-phase voltages to the appropriate voltage terminals of the IED.
2. Inject voltage(s) in a way that relevant measured (according to setting parameter VoltageInput) voltages are created from the test set.
3. Overall check in principal as above (non-directional overcurrent feature) and correspondingly for the undervoltage feature.

11.8.1.6 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled.
Restore connections and settings to their original values, if they were changed for testing purposes.

11.9 Secondary system supervision

11.9.1 Current circuit supervision CCSSPVC (87)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

The Current circuit supervision function CCSSPVC (87) is conveniently tested with the same three-phase test set as used when testing the measuring functions in the IED.

The condition for this procedure is that the setting of IMinOp is lower than the setting of Pickup_Block.
11.9.1.1 Verifying the settings

1. Check the input circuits and the trip value of the $I_{MinOp}$ current level detector by injecting current, one phase at a time.
2. Check the phase current blocking function for all three phases by injecting current, one phase at a time. The output signals shall reset with a delay of 1 second when the current exceeds 1.5 $\cdot I_{Base}$.
3. Inject a current 0.1 $\cdot I_{Base}$ to the reference current input Analogue channel ID current input 5.
4. Increase slowly the current in one of the phases and check that FAIL output is obtained when the current is above 0.9 $\cdot I_{Base}$.

11.9.1.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.9.2 Fuse failure supervision FUFSPVC

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

The verification is divided in two main parts. The first part is common to all fuse failure supervision options, and checks that binary inputs and outputs trip as expected according to actual configuration. In the second part the relevant set trip values are measured.

11.9.2.1 Checking that the binary inputs and outputs trip as expected

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Connect the nominal dc voltage to the 89bS binary input.
   • The signal BLKV should appear with almost no time delay.
   • The signals BLKZ and 3PH should not appear on the IED.
   • Only the distance protection function can trip.
   • Undervoltage-dependent functions must not trip.
3. Disconnect the dc voltage from the 89b binary input terminal.
4. Connect the nominal dc voltage to the MCBOP binary input.
   • The BLKV and BLKZ signals should appear without any time delay.
   • All undervoltage-dependent functions must be blocked.
5. Disconnect the dc voltage from the MCBOP binary input terminal.
6. Disconnect one of the phase voltages and observe the logical output signals on the binary outputs of the IED.
   BLKV and BLKZ signals should appear simultaneously weather the BLKV and BLKZ reset depends on the setting SealIn “on” or “off”. If “on” no reset, if “off” reset.
7. After more than 5 seconds disconnect the remaining two-phase voltages and all three currents.
There should be no change in the high status of the output signals BLKV and BLKZ.
The signal 3PH will appear.

8. Establish normal voltage and current operating conditions simultaneously and observe the corresponding output signals. They should change to logical 0 as follows:
- Signal 3PH after about 25ms
- Signal BLKV after about 50ms
- Signal BLKZ after about 200ms

11.9.2.2 Measuring the trip value for the negative sequence function

Measure the trip value for the negative sequence function, if included in the IED.

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Slowly decrease the measured voltage in one phase until the BLKV signal appears.
3. Record the measured voltage and calculate the corresponding negative-sequence voltage according to the equation (observe that the voltages in the equation are phasors):

\[
3 \cdot V_z = V_a + a^2 \cdot V_b + a \cdot V_c
\]

(Equation 108)

Where:
- \( V_a, V_b \) and \( V_c \) are the measured phase voltages
- \( a = 1 \cdot e^{\frac{2\pi}{3}} = -0.5 + j \frac{\sqrt{3}}{2} \)

4. Compare the result with the set value of the negative-sequence operating voltage (consider that the set value \( 3V_{2PU} \) is in percentage of the base voltage \( V_{Base} \)).
5. Repeat steps 1 and 2. Then slowly increase the measured current in one phase until the BLKV signal disappears.
6. Record the measured current and calculate the corresponding negative-sequence current according to the equation (observe that the currents in the equation are phasors):

\[
3 \cdot I_z = I_a + a^2 \cdot I_b + a \cdot I_c
\]

(Equation 109)

Where:
- \( I_a, I_b \) and \( I_c \) are the measured phase currents
7. Compare the result with the set value of the negative-sequence operating current. Consider that the set value \(3I_2<\) is in percentage of the base current \(I_{\text{Base}}\).

### 11.9.2.3 Measuring the trip value for the zero-sequence function

Measure the trip value for the zero-sequence function, if included in the IED.

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Slowly decrease the measured voltage in one phase until the BLKV signal appears.
3. Record the measured voltage and calculate the corresponding zero-sequence voltage according to the equation (observe that the voltages in the equation are phasors):
   \[
   3\cdot V_0 = V_A + V_B + V_C
   \]
   (Equation 110)

   Where:
   \(V_A, V_B, \text{ and } V_C\) are the measured phase voltages

4. Compare the result with the set value of the zero-sequence tripping voltage (consider that the set value \(3V_0\text{Pickup}\) is in percentage of the base voltage.)
5. Repeat steps 1 and 2. Then slowly increase the measured current in one phase until the BLKV signal disappears.
6. Record the measured current and calculate the corresponding zero-sequence current according to the equation (observe that the currents in the equation are phasors):
   \[
   3\cdot I_0 = I_A + I_B + I_C
   \]
   (Equation 111)

   Where:
   \(I_A, I_B, \text{ and } I_C\) are the measured phase currents

7. Compare the result with the set value of the zero-sequence trip current. Consider that the set value \(3I_0<\) is in percentage of the base current \(I_{\text{Base}}\).

### 11.9.2.4 Measuring the trip value for the dead line detection function

1. Apply three-phase voltages with their rated value and zero currents.
2. Decrease the measured voltage in one phase until the DLD1PH signal appears.
3. This is the point at which the dead line condition is detected. Check the value of the decreased voltage with the set value VLDPU (VLDPU is in percentage of the base voltage VBase).
4. Apply three-phase currents with their rated value and zero voltages.
5. Decrease the measured current in one phase until the DLD1PH signal appears.
6. This is the point at which the dead line condition is detected. Check the value of the decreased current with the set value IDLDPU (IDLDPU is in percentage of the base current IBase).

11.9.2.5 Checking the operation of the dv/dt and di/dt based function

Check the operation of the dv/dt and di/dt based function if included in the IED.

1. Simulate normal operating conditions with the three-phase currents in phase with their corresponding phase voltages and with all of them equal to their rated values.
2. Change the voltages and currents in all three phases simultaneously.
   The voltage change must be higher than the set value DVPU and the current change must be lower than the set value DIPU.
   • The BLKV and BLKZ signals appear without any time delay. The BLKZ signal will be activated only if the internal deadline detection is not activated at the same time.
   • 3PH should appear after 5 seconds, if the remaining voltage levels are lower than the set VDLDPU of the dead line detection function.

3. Apply normal conditions as in step 1.
   The BLKV, BLKZ and 3PH signals should reset, if activated, see step 1 and 2.
4. Change the voltages and currents in all three phases simultaneously.
   The voltage change must be higher than the set value DVPU and the current change must be higher than the set value DIPU.
   The BLKV, BLKZ and 3PH signals should not appear.
5. Repeat step 2.
6. Connect the nominal voltages in all three phases and feed a current below the trip level in all three phases.
7. Keep the current constant. Disconnect the voltage in all three phases simultaneously.
   The BLKV, BLKZ and 3PH signals should not appear.
8. Change the magnitude of the voltage and current for phase 1 to a value higher than the set value DVPU and DIPU.
9. Check that the pickup output signals PU_DV_A and PU_DI_A and the general pickup signals PU_DV or PU_DI are activated.
10. Check that the pickup output signals for the current and voltage phases 2 and 3 are activated by changing the magnitude of the voltage and current for phases 2 and 3.

11.9.2.6 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled.
Restore connections and settings to their original values, if they were changed for testing purposes.

11.9.3 Fuse failure supervision

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".
Checking the operation of binary input and output

1. Simulate normal operation conditions with three-phase voltage on the main fuse group and the pilot fuse group. Ensure the values are equal to their rated values.
2. Disconnect one of the phase voltage from the main fuse group or the pilot fuse group. Observe the binary outputs of the IED. The MAINFUF or the PILOTFUF signals are simultaneously activated. Only the output circuit related to the open phase will be active i.e either MAINFUF or PILOTFUF.
3. Establish a normal voltage operating condition and observe the corresponding output signals. MAINFUF or PILOTFUF should change to 0 in about 27 ms.
4. Set normal conditions as mentioned in step 1.
5. Enable the BLOCK binary input and repeat step 2. MAINFUF or PILOTFUF should not appear.

Checking the operation of MAINFUF and PILOTFUF

1. Simulate normal operation conditions with three-phase voltage on the main fuse group and the pilot fuse group. Ensure the values are equal to their rated values.
2. Decrease one of the three-phase voltages on main fuse group or pilot fuse group. The voltage change must be greater than the set value for Vdif Main block or Vdif Pilot alarm. MAINFUF or PILOTFUF signals are activated without any time delay.
3. Set normal conditions as mentioned in step 1. MAINFUF or PILOTFUF signals should reset.
4. Set SealIn to On, Vdif Main block to 20% of VBase and VSealIn to 70% of VBase.
5. Apply three-phase voltages with the value slightly below VSealIn level.
6. Decrease one of the three-phase voltages on main fuse group. The voltage change must be greater than the set value for Vdif Main block. MAINFUF signal is activated.
7. After more than 5 seconds increase the measured voltage back to the value slightly below VSealIn level. MAINFUF signal should not reset.
8. Slowly increase measured voltage to the value slightly above VSealIn until MAINFUF signal resets.
9. Record the measured voltage and compare with the set value VSealIn.

11.9.3.1 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.9.4 Voltage based delta supervision DELVSPVC

Prepare the IED for verification of settings outlined in Section “Preparing the IED to verify settings”.

11.9.4.1 Verifying the signals and settings

Make sure that the function is connected to SMAI function with V3P signal.

Delta supervision function has 6 different modes of operation. Proceed as follows to test the function in a particular mode.

1. Set the following parameters:
- **Operation** = Enabled
- **MeasMode** = Phase-to-ground
- **Umin** = 10% of VBase
- **DelU>** = 50% of VBase
- **DelUang>** = 10°
- **Delta T** = 2
- **tHold** = 100

2. Set the constant voltage input of UL1 = 63.5V at 0° and UL2 = 63.5V at 120° and UL3 at 63.5 -120° secondary at rated frequency.

3. Based on the mode of operation, carry out the following changes on input condition of Step 2 to detect pickup condition.
   Each mode is given for increase and decrease operation. (3-a and 3-b)

<table>
<thead>
<tr>
<th>Step No.</th>
<th>OpMode</th>
<th>Changes after step 2</th>
<th>Expected output</th>
</tr>
</thead>
<tbody>
<tr>
<td>3-a</td>
<td>Instantaneous 1 cycle or Instantaneous 2 cycle</td>
<td>Change UL1 to 20V</td>
<td>STL1, STLOW, BFI_3P signal should be TRUE for 100 ms</td>
</tr>
<tr>
<td>3-b</td>
<td>Instantaneous 1 cycle or Instantaneous 2 cycle</td>
<td>Change UL1 back to 63.5V</td>
<td>STL1, STRISE, BFI_3P signal should be TRUE for 100 ms</td>
</tr>
<tr>
<td>3-a</td>
<td>RMS or DFT Mag</td>
<td>Change UL1 to 20V</td>
<td>STL1, STLOW, BFI_3P signal should be TRUE for 100 ms</td>
</tr>
<tr>
<td>3-b</td>
<td>RMS or DFT Mag</td>
<td>Change UL1 back to 63.5V</td>
<td>STL1, STRISE, BFI_3P signal should be TRUE for 100 ms</td>
</tr>
<tr>
<td>3-a</td>
<td>DFT Angle (vector shift)</td>
<td>Change UL1 to 63.5V at 15° without changing frequency</td>
<td>STL1, BFI_3P signal will be TRUE for 100 ms.</td>
</tr>
<tr>
<td>3-b</td>
<td>DFT Angle (Vector shift)</td>
<td>Change UL1 to 63.5V at 0° without changing frequency</td>
<td>STL1, BFI_3P signal will be TRUE for 100 ms</td>
</tr>
</tbody>
</table>

4. Repeat Step 3 with UL2 changes for different mode.

11.9.4.2 **Completing the test**

Continue to test another function or end the test by changing the TestMode setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.9.5 **Current based delta supervision DELISPVC(7I)**

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.9.5.1 **Verifying the signals and settings**

Make sure that the function is connected to SMAI function with I3P signal.
Delta supervision function has four different modes of operation. Proceed as follows to test the function in a particular mode.

1. Set the following parameters:
   - Operation = ON
   - MeasMode = Phase-to-ground
   - Imin = 10% of IBase
   - Dell> = 80% of IBase
   - DeltaT = 2
   - tHold = 100

2. Set the constant current input of IL1 = 1A at 0° and IL2 = 1A at 120° and IL3 at -120° secondary at rated frequency.

3. Based on the mode of operation, carry out the following changes on input condition of Step 2 to detect pickup condition. Each mode is given for increase and decrease operation. (3-a and 3-b)

<table>
<thead>
<tr>
<th>Step No.</th>
<th>OpMode</th>
<th>Changes after step 2</th>
<th>Expected output</th>
</tr>
</thead>
<tbody>
<tr>
<td>3-a</td>
<td>Instantaneous 1 cycle or</td>
<td>Change IL1 to 2 A</td>
<td>STL1, STRISE, BFI_3P signal should be TRUE for 100 ms</td>
</tr>
<tr>
<td></td>
<td>Instantaneous 2 cycle</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3-b</td>
<td>Instantaneous 1 cycle or</td>
<td>Change IL1 back to 1A</td>
<td>STL1, STLOW, BFI_3P signal should be TRUE for 100 ms</td>
</tr>
<tr>
<td></td>
<td>Instantaneous 2 cycle</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3-a</td>
<td>RMS or DFT Mag</td>
<td>Change IL1 to 2 A</td>
<td>STL1, STRISE, BFI_3P signal should be TRUE for 100 ms</td>
</tr>
<tr>
<td>3-b</td>
<td>RMS or DFT Mag</td>
<td>Change IL1 back to 1A</td>
<td>STL1, STLOW, BFI_3P signal should be TRUE for 100 ms</td>
</tr>
</tbody>
</table>

4. Repeat Step 3 with IL2 changes for different mode.

11.9.5.2 Completing the test

Continue to test another function or end the test by changing the TestMode setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.9.6 Delta supervision of real input DELSPVC

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.9.6.1 Verifying the signals and settings

Make sure that the function is connected to any of the available real derived outputs, for example the P output signal of the CMMXU function.

1. Set the following parameters:
2. Set a constant voltage input of UL1 = 63.5 V at 0° and UL2 = 63.5 V at 180° secondary and a current signal at IL1 = 1A at 0° and IL2 = 1A at 180°.

3. Ensure that the P output of CMMXU function has exceeded 10 MW and remains stable.

4. Change the current signal at IL1= 2A at 0° and IL2= 2A at 180°. Both START and STARTRISE signals should be active for 100 ms.

5. Change the current signal at IL1= 1A at 0° and IL2= 1A at 180°. Both START and STARTLOW signals should be active for 100 ms.

### 11.9.6.2 Completing the test

Continue to test another function or end the test by changing the TestMode setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

### 11.10 Control

#### 11.10.1 Synchrocheck, energizing check, and synchronizing SESRSYN (25)

This section contains instructions on how to test the synchrocheck/synchronism check, energizing check, and synchronizing function SESRSYN (25) for single, double and breaker-and-a-half arrangements.

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

At commissioning and periodical checks, the functions shall be tested with the used settings. To test a specific function, it might be necessary to change some setting parameters, for example:

- `AutoEnerg = Disabled/DLLB/DBLL/Both`
- `ManEnerg = Disabled`
- `Operation = Disabled/Enabled`
- Activation of the voltage selection function if applicable

The tests explained in the test procedures below describe the settings, which can be used as references during testing before the final settings are specified. After testing, restore the equipment to the normal or desired settings.

A secondary injection test set with the possibility to alter the phase angle and amplitude of the voltage is needed. The test set must also be able to generate different frequencies on different outputs.

The description below applies for a system with a nominal frequency of 60 Hz but can be directly applicable to 50 Hz. SESRSYN (25) can be set to use different phases, phase to ground or phase to phase. Use the set voltages instead of what is indicated below.
Figure 54 shows the general test connection principle, which can be used during testing. This description describes the test of the version intended for one bay.

Figure 55 shows the general test connection for a breaker-and-a-half diameter with one-phase voltage connected to the line side.

Figure 54: General test connection with three-phase voltage connected to the line side

Figure 55: General test connection for a breaker-and-a-half diameter with one-phase voltage connected to the line side
11.10.1.1 Testing the synchronizing function

The voltage inputs used are:

- V3PL1: VA, VB or VC line 1 voltage inputs on the IED
- V3PBB1: Bus1 voltage input on the IED

Testing the frequency difference

The frequency difference test should verify that operation is achieved when the frequency difference between bus and line is less than set value of $FreqDiffMax$ and above set value of $FreqDiffMin$. The test procedure below will depend on the settings used. Input STARTSYN must be activated during the test.

$FreqDiffMax = 50.2 \text{ Hz}$

$FreqDiffMin = 50.01 \text{ Hz}$

$tBreaker = 0.080 \text{ s}$

1. Apply voltages
   1.1. V-Line = 100% $V_{BaseLine}$ and f-Line = 60.0 Hz
   1.2. V-Bus = 100% $V_{BaseBus}$ and f-Bus = 60.15Hz
2. Check that a closing pulse is submitted at a closing angle equal to calculated phase angle value from the formula below. Modern test sets will evaluate this automatically.
   \[
   \text{Closing Angle} = \left| \left( \frac{(f_{Bus} - f_{Line}) \cdot tBreaker \cdot 360 \text{ degrees}}{} \right) \right|
   \]
   \[
   f_{Bus} = \text{Bus frequency}
   \]
   \[
   f_{Line} = \text{Line frequency}
   \]
   \[
   tBreaker = \text{Set closing time of the breaker}
   \]
3. Repeat with
   3.1. V-Bus = 100% $V_{BaseBus}$ and f-bus = 60.25 Hz, to verify that the function does not trip when frequency difference is above limit.
4. Verify that the closing command is not issued when the frequency difference is less than the set value $FreqDiffMin$.

11.10.1.2 Testing the synchrocheck functionality

During the test of SESRSYN (25) for a single bay arrangement, these voltage inputs are used:

- V-Line: VA, VB or VC line 1 voltage input on the IED according to the connection in SMT
- V-Bus: V5 voltage input on the IED according to the connection in SMT

Testing the voltage difference

Set the voltage difference to 0.15 p.u. on the local HMI, and the test should check that operation is achieved when the voltage difference $VDiffSC$ is lower than 0.15 p.u.

The settings used in the test shall be final settings. The test shall be adapted to site setting values instead of values in the example below.
Test with no voltage difference between the inputs.

Test with a voltage difference higher than the set $VDiffSC$.

1. Apply voltages $V\text{-Line}$ (for example) = 80% $GblBaseSelLine$ and $V\text{-Bus}$ = 80% $GblBaseSelBus$, with the same phase-angle and frequency.
2. Check that the AUTOSYOK and MANSYOK outputs are activated.
3. The test can be repeated with different voltage values to verify that the function trips within the set $VDiffSC$. Check with both $V\text{-Line}$ and $V\text{-Bus}$ respectively lower than the other.
4. Increase the $V\text{-Bus}$ to 110% $GblBaseSelBus$, and the $V\text{-Line}$ = 90% $GblBaseSelLine$ and also the opposite condition.
5. Check that the two outputs for manual and auto synchronism are not activated.

**Testing the phase angle difference**

The phase angle differences $PhaseDiffM$ and $PhaseDiffA$ respectively are set to their final settings and the test should verify that operation is achieved when the phase angle difference is lower than this value both leading and lagging.

Test with no voltage difference.

1. Apply voltages $V\text{-Line}$ (for example) = 100% $GblBaseSelLine$ and $V\text{-Bus}$ = 100% $GblBaseSelBus$, with a phase difference equal to 0 degrees and a frequency difference lower than $FreqDiffA$ and $FreqDiffM$.
2. Check that the AUTOSYOK and MANSYOK outputs are activated.
The test can be repeated with other phase difference values to verify that the function trips for values lower than the set ones, $PhaseDiffM$ and $PhaseDiffA$. By changing the phase angle on the voltage connected to $V\text{-Bus}$, between $\pm d\phi$ degrees, the user can check that the two outputs are activated for a phase difference lower than the set value. It should not trip for other values. See figure 56.

![Figure 56: Test of phase difference](en05000551_ansi.vsd)

3. Change the phase angle between $+d\phi$ and $-d\phi$ and verify that the two outputs are activated for phase differences between these values but not for phase differences outside, see figure 56.

**Testing the frequency difference**

The frequency difference test should verify that operation is achieved when the $FreqDiffA$ and $FreqDiffM$ frequency difference is lower than the set value for manual and auto synchronizing...
check, FreqDiffA and FreqDiffM respectively and that operation is blocked when the frequency difference is greater.

Test with frequency difference = 0 mHz

Test with a frequency difference outside the set limits for manual and auto synchronizing check respectively.

1. Apply voltages V-Line equal to 100% GblBaseSelLine and V-Bus equal to 100% GblBaseSelBus, with a frequency difference equal to 0 mHz and a phase difference lower than the set value.
2. Check that the AUTOSYOK and MANSYOK outputs are activated.
3. Apply voltage to the V-Line equal to 100% GblBaseSelLine with a frequency equal to 50 Hz and voltage V-Bus equal to 100% GblBaseSelBus, with a frequency outside the set limit.
4. Check that the two outputs are not activated. The test can be repeated with different frequency values to verify that the function trips for values lower than the set ones. If a modern test set is used, the frequency can be changed continuously.

Testing the reference voltage

1. Use the same basic test connection as in figure 54. The voltage difference between the voltage connected to V-Bus and V-Line should be 0%, so that the AUTOSYOK and MANSYOK outputs are activated first.
2. Change the V-Line voltage connection to V-Line2 without changing the setting on the local HMI. Check that the two outputs are not activated.

11.10.1.3 Testing the energizing check

During the test of the energizing check function for a single bay arrangement, these voltage inputs are used:

V-Line VA, VB or VC line1 voltage inputs on the IED
V-Bus Bus voltage input on the IED

General
When testing the energizing check function for the applicable bus, arrangement shall be done for the energizing check functions. The voltage is selected by activation of different inputs in the voltage selection logic.

Live voltage level is fixed to 80% VBase and dead voltage level to fixed 40% VBase.

The test shall be performed according to the settings for the station. Test the alternatives below that are applicable.

Testing the dead line live bus (DLLB)
The test should verify that the energizing check function trips for a low voltage on the V-Line and for a high voltage on the V-Bus. This corresponds to the energizing of a dead line to a live bus.
1. Apply a single-phase voltage 100% $GblBaseSelBus$ to the V-Bus, and a single-phase voltage 30% $GblBaseSelLine$ to the V-Line.
2. Check that the AUTOENOK and MANENOK outputs are activated after set $tAutoEnerg$ respectively $tManEnerg$.
3. Increase the V-Line to 60% $GblBaseSelLine$ and V-Bus to be equal to 100% $GblBaseSelBus$. The outputs should not be activated.
4. The test can be repeated with different values on the V-Bus and the V-Line.

**Testing the dead bus live line (DBLL)**
The test should verify that the energizing check function trips for a low voltage on the V-Bus and for a high voltage on the V-Line. This corresponds to an energizing of a dead bus to a live line.

1. Verify the settings $AutoEnerg$ or $ManEnerg$ to be DBLL.
2. Apply a single-phase voltage of 30% $GblBaseSelBus$ to the V-Bus and a single-phase voltage of 100% $GblBaseSelLine$ to the V-Line.
3. Check that the AUTOENOK and MANENOK outputs are activated after set $tAutoEnerg$ respectively $tManEnerg$.
4. Decrease the V-Line to 60% $GblBaseSelLine$ and keep the V-Bus equal to 30% $GblBaseSelBus$. The outputs should not be activated.
5. The test can be repeated with different values on the V-Bus and the V-Line.

**Testing both directions (DLLB or DBLL)**

1. Verify the local HMI settings $AutoEnerg$ or $ManEnerg$ to be Both.
2. Apply a single-phase voltage of 30% $GblBaseSelLine$ to the V-Line and a single-phase voltage of 100% $GblBaseSelBus$ to the V-Bus.
3. Check that the AUTOENOK and MANENOK outputs are activated after set $tAutoEnerg$ respectively $tManEnerg$.
4. Change the connection so that the V-Line is equal to 100% $GblBaseSelLine$ and the V-Bus is equal to 30% $GblBaseSelBus$. The outputs should still be activated.
5. The test can be repeated with different values on the V-Bus and the V-Line.

**Testing the dead bus dead line (DBDL)**
The test should verify that the energizing check function trips for a low voltage on both the V-Bus and the V-Line, that is, closing of the breaker in a non-energized system. Test is valid only when this function is used.

1. Verify the local HMI setting $AutoEnerg$ to be Disabled and $ManEnerg$ to be DBLL.
2. Set the parameter $ManEnergDBDL$ to Enabled.
3. Apply a single-phase voltage of 30% $GblBaseSelBus$ to the V-Bus and a single-phase voltage of 30% $GblBaseSelLine$ to the V-Line.
4. Check that the MANENOK output is activated after set $tManEnerg$.
5. Increase the V-Bus to 80% $GblBaseSelBus$ and keep the V-Line equal to 30% $GblBaseSelLine$. The outputs should not be activated.
6. Repeat the test with $ManEnerg$ set to DLLB with different values on the V-Bus and the V-Line voltage.

**11.10.1.4 Testing the voltage selection**
**Testing the voltage selection for single CB arrangements**

This test should verify that the correct voltage is selected for the measurement in the SESRSYN function used in a double-bus arrangement. Apply a single-phase voltage of 100% $GblBaseSelLine$ to the V-Line and a single-phase voltage of 100% $GblBaseSelBus$ to the V-Bus.

If the VB1/2OK inputs for the fuse failure are used, they must be activated, during tests below. Also verify that deactivation prevents operation and gives an alarm.

1. Connect the signals above to binary inputs and binary outputs.
2. Connect the voltage inputs to the analog inputs used for each bus or line depending of the type of busbar arrangement and verify that correct output signals are generated.

**Testing the voltage selection for double breaker**

This test should verify that correct voltage is selected for the measurement in the SESRSYN function used for a diameter in a Breaker-and-a-half arrangement. Apply a single-phase voltage of 100% $GblBaseSelLine$ to the V-Line and a single-phase voltage of 100% $GblBaseSelBus$ to the V-Bus. Verify that correct output signals are generated.

1. Connect the analog signals to the voltage inputs, in pair of two for V1 and V2. (Inputs V3PB1, V3PB2, V3PL1, V3PL2)
2. Activate the binary signals according to the used alternative. Verify the measuring voltage on the synchronizing check function SESRSYN (25). Normally it can be good to verify synchronizing check with the same voltages and phase angles on both voltages. The voltages should be verified to be available when selected and not available when another input is activated so connect only one voltage transformer reference at each time.
3. Record the voltage selection tests in a matrix table showing read values and AUTOSYOK/MANSYOK signals to document the test performed.

**Testing the voltage selection for 1 1/2 CB arrangements**

At test of the SESRSYN (25) function for a breaker-and-a-half diameter the following alternative voltage inputs can be used for the three SESRSYN (SESRSYN 1, SESRSYN 2, SESRSYN 3) functions. These three SESRSYN functions can either be in one, two or three different IEDs. Table 39 describes the scenario when SESRSYN 1, SESRSYN 2 and SESRSYN 3 all are in the same IED. If SESRSYN 3 is in another IED, WA1 will be considered as WA2 and LINE2 as LINE1. The voltage is selected by activation of different inputs in the voltage selection logic as shown in table 39 and figure 57.

<table>
<thead>
<tr>
<th>SESRSYN</th>
<th>CBConfig setting</th>
<th>Section to be synchronize d</th>
<th>Activated B1QCLD input on IED from</th>
<th>Activated B2QCLD input on IED from</th>
<th>Activated LN1QCLD input on IED from</th>
<th>Activated LN2QCLD input on IED from</th>
<th>Indication from SESRSYN on IED</th>
</tr>
</thead>
<tbody>
<tr>
<td>SESRSYN 1 (Operates on CB1 52)</td>
<td>breaker-and-a-half bus CB</td>
<td>Bus1 – Line1</td>
<td></td>
<td></td>
<td>LN1 989</td>
<td></td>
<td>B1SEL, LN1SEL</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Bus1 – Line2</td>
<td>CB2 252</td>
<td></td>
<td>LN2 989</td>
<td></td>
<td>B1SEL, LN2SEL</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Bus1 – Bus2</td>
<td>CB2 252</td>
<td>CB3 352</td>
<td></td>
<td></td>
<td>B1SEL, B2SEL</td>
</tr>
</tbody>
</table>

Table continues on next page
| SESRSYN 2 (Operates on CB2 252) | Tie CB | Line1 – Line2 | LN1 989 | LN2 989 | LN1SEL, LN2SEL |
| SESRSYN 3 (Operates on CB3 352) | breaker-and-a-half bus alt. CB (mirrored) | Bus2 – Line2 | LN2 989 | B2SEL, LN2SEL |

**Figure 57: Objects used in the voltage selection logic**

### 11.10.1.5 Completing the test

Continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.
11.10.2 **Apparatus control APC**

The apparatus control function consists of four types of function blocks, which are connected in a delivery-specific way between bays and to the station level. For that reason, test the total function in a system, that is, either in a complete delivery system as an acceptance test (FAT/SAT) or as parts of that system.

If a block/unblock command is sent from remote to function, while the IED is shut down, this command will not be recognized after the start up, thus the command that was sent prior to the shut down is used. In such cases, where there is a mismatch, the user is advised to make a complete cycle of block/unblock operations to align the statuses.

11.10.3 **Voltage control TR1ATCC, TR8ATCC, TCMYLTC, TCLYLTC**

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

The automatic voltage control for tap changer, single control TR1ATCC (90) is based on a transformer configuration that consists of one tap changer on a single two-winding power transformer.

The automatic voltage control for tap changer, parallel control TR8ATCC (90), if installed, may be set to operate in Master Follower (MF) mode, or Minimise Circulating Current (MCC) mode. The commissioning tests for each parallel control mode are addressed separately in the following procedure.

Secondary injection of load current ($I_L$) and secondary bus voltage ($U_B$) equivalent quantities are required during installation and commissioning tests. The test consists mainly of:

1. Increasing or decreasing the injected voltage or current at the analogue inputs of the IED.
2. Checking that the corresponding commands (Lower or Raise) are issued by the voltage control function.

Setting confirmation is an important step for voltage control in the installation and commissioning phase to ensure consistency of power systems base quantities, alarm/blocking conditions and parallel control settings for each transformer control function.

Before starting any test, verify the following settings in PCM600 or the local HMI for TR1ATCC (90), TR8ATCC (90) and TCMYLTC (84) and TCLYLTC (84).

- Confirm power system base quantities $I1Base$, $I2Base$, $VBase$.

Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x/ TR8ATCC:x/General

and

Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x.
• Confirm that the setting for short circuit impedance \( Xr2 \) for TR1ATCC (90) or TR8ATCC (90) is in accordance with transformer data:
  • Short circuit impedance, available on the local HMI under Main menu/Settings/IED settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x/TR8ATCC:x/Xr2.

• Confirm that the setting for TCMYLTC (84) or TCLYLTC (84) is in accordance with transformer data:
  • Tap change timeout duration - effectively the maximum transformer tap change time, \( tTCTimeout \), available on the local HMI under Main menu/Settings/IED Settings/Control/TransformerTapControl(YLTC,84)/TCMYLTC:x/TCLYLTC:x/tTCTimeout.
  • Load tap changer pulse duration - required length of pulse from IED to load tap changer, \( tPulseDur \), available on the local HMI under Main menu/Settings/IED Settings/Control/TransformerTapControl(YLTC,84)/TCMYLTC:x/TCLYLTC:x/tPulseDur.
  • Transformer tap range, LowVoltTap and HighVoltTap, available on the local HMI under Main menu/Settings/IED Settings/Control/TransformerTapControl(YLTC,84)/TCMYLTC:x/TCLYLTC:x/HighVoltTap and .
  • Load tap changer code type - method for digital feedback of tap position, CodeType, available on the local HMI under Main menu/Settings/IED settings/Control/TransformerTapControl(YLTC,84)/TCMYLTC:x/TCLYLTC:x/CodeType.

During the installation and commissioning, the behavior of the voltage control functions for different tests may be governed by a parameter group, available on the local HMI under Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x/TR8ATCC:x. These parameter settings can cause a Total Block, Automatic Block or Alarm for a variety of system conditions including over and under voltage, over current and tap changer failure. It is important to review these settings and confirm the intended response of the voltage control function for different secondary injection tests.

**Terminology**

The busbar voltage \( V_B \) is a shorter notation for the measured voltages \( V_a, V_b, V_c \) or \( V_{ij} \), where \( V_{ij} \) is the phase-phase voltage, \( V_{ij} = V_i - V_j \), or \( V_i \), where \( V_i \) is one single-phase-to-ground voltage.

\( I_i \) is a shorter notation for the measured load current; it is to be used instead of the three-phase quantities \( I_a, I_b, I_c \) or the two-phase quantities \( I_i \) and \( I_j \), or single-phase current \( I_i \).

Also note that for simplicity, the Parameter Setting menu structures included in the following procedure are referred to universally as VCP1, for example, Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x/TR8ATCC:x/Time/t1 and t2l.

For cases where single-mode voltage control is implemented, the Parameter Setting menu structure includes TR1ATCC:1 instead of the parallel designator TR8ATCC:1.

**11.10.3.1 Secondary test**

The voltage control function performs basic voltage regulation by comparing a calculated load voltage \( V_L \) against a voltage range defined by setting \( V_{Deadband} \) (with upper and lower limits \( V_2 \) and \( V_1 \) respectively). The calculated load voltage \( V_L \) represents the secondary transformer bus voltage \( V_B \) adjusted for Load drop compensation (LDC) where enabled in settings.
Note that when LDC is disabled, $V_B$ equals $V_L$.

When the load voltage $V_L$ stays within the interval between $V1$ and $V2$, no action will be taken.

If $V_L < V1$ or $V_L > V2$, a command timer will pickup, which is constant time or inverse time defined by setting $t1$ and $t1Use$. The command timer will trip while the measured voltage stays outside the inner deadband (defined by setting $V_{DeadbandInner}$).

If $V_L$ remains outside of the voltage range defined by $V_{Deadband}$ and the command timer expires, the voltage control will execute a raise or lower command to the transformer tap changer. This command sequence will be repeated until $V_L$ is brought back within the inner deadband range.

### 11.10.3.2 Check the activation of the voltage control operation

1. Confirm Transformer Tap Control = Enable and Transformer Voltage Control = Enable
   - Direct tap change control
     
     **Main menu/Settings/IED Settings/Control/TransformerTapChanger(YLTC,84)/TCMYLTC:x/TCLYLTC:x/Operation**
   - Automatic transformer voltage control
     
     **Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x/TR8ATCC:x/General/Operation**
   - Enable Tap Command

2. Apply the corresponding voltage
   Confirm the analog measuring mode prior to undertaking secondary injection (positive sequence, phase-to-phase, or phase-to-ground). This measuring mode is defined in the local HMI under **Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC, 90)/TR1ATCC:x/TR8ATCC:x/General/MeasMode**
   The application of nominal voltage $VSet$ according to set $MeasMode$ to the IEDs should cause the alarm or blocking condition for undervoltage to reset.

### 11.10.3.3 Check the normal voltage regulation function
1. Review the settings for $V_{Deadband}$ (based on percentage of nominal bus voltage) and calculate the upper ($V_2$) and lower ($V_1$) voltage regulation limits for which a tap change command will be issued.

2. Review the expected time for first ($t_1$) and subsequent ($t_2$) tap change commands from the voltage control function on the local HMI under **Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x/TR8ATCC:x/Time/t1 and t2**

3. Lower the voltage 1% below $V_1$ and wait for the issue of a Raise command from the voltage control after the expiry of a constant or inverse time delay set by $t_1$. Detection of this command will involve locating the allocated binary output for a raise pulse command in the Signal Matrix in PCM600 and monitoring a positive from this output.

4. After the issue of the raise command, return the applied voltage to $V_{Set}$ (nominal value).

5. Raise the voltage 1% above the upper deadband limit $V_2$ and wait for the issue of a lower command from the voltage control after the expiry of a constant or inverse time delay set by $t_1$. Detection of this command will involve locating the allocated binary output for a low pulse command in the Signal Matrix in PCM600 and monitoring a positive from this output.

6. Return the applied voltage to $V_{Set}$.

### 11.10.3.4 Check the undervoltage block function

1. Confirm the setting for $V_{block}$, nominally at 80% of rated voltage.

2. Confirm the voltage control function response to an applied voltage below $V_{block}$, by reviewing the setting in the local HMI under **Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x/TR8ATCC:x/UVBk** that may cause an alarm, total or automatic block of the voltage control function to be displayed on the local HMI.

3. Apply a voltage slightly below $V_{block}$ and confirm the response of the voltage control function.

### 11.10.3.5 Check the upper and lower busbar voltage limit

1. Confirm the settings for $V_{min}$ and $V_{max}$ in the local HMI under **Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x/TR8ATCC:x/Voltage/Umax or Umin** and **Main menu/Settings/IED Settings/Control/TR8ATCC (90)/TR8ATCC:n/Voltage/Umax**

2. Confirm the voltage control function response to an applied voltage below $V_{min}$ and above $V_{max}$, by reviewing the settings in the local HMI under **Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x/TR8ATCC:x/UVPartBk** and **Main menu/IED Settings/Control/TransformerVoltageControl/TR1ATCC:x/TR8ATCC:x/OVPartBk**. These conditions may cause an alarm or total block of the voltage control function to be displayed on the local HMI.

3. Decrease the injected voltage slightly below the $V_{min}$ value and check for the corresponding blocking or alarm condition on the local HMI. For an alarm condition, the voltage regulation function is not blocked and a raise command should be issued from the IED.

4. Increase the applied voltage slightly above the $V_{max}$ value and check for the corresponding blocking or alarm condition on the local HMI. For an alarm condition, the voltage regulation function is not blocked and a lower command should be issued from the IED.

### 11.10.3.6 Check the overcurrent block function
1. Confirm the setting for \( I_{\text{block}} \) in the local HMI under Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,91)/TR1ATCC:x/TR8ATCC:x/TCCtrl/Iblock

2. Confirm the voltage control function response to an applied current above \( I_{\text{block}} \), by reviewing the settings in the local HMI under Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,91)/TR1ATCC:x/TR8ATCC:x/OVPartBk. This condition may cause an alarm or total block of the voltage control function to be displayed on the local HMI.

3. Inject a current higher than the \( I_{\text{block}} \) setting and confirm the alarm or blocking condition is present on the local HMI. If an automatic or total blocking condition occurs, change the applied secondary voltage and confirm that no tap change commands are issued from the associated binary outputs. This situation can also be confirmed through reviewing the disturbance and service reports on the local HMI.

11.10.3.7 Single transformer

Load drop compensation

1. Confirm that OperationLDC is set to Enabled.
2. Confirm settings for \( R_{\text{line}} \) and \( X_{\text{line}} \).
3. Calculate the expected load voltage \( V_L \) (displayed as a measured value on the local HMI) based on secondary injection of transformer secondary voltage \( (V_B = V_{\text{Set}}) \) and rated load current \( (I_L = I_{\text{Base}}) \), in accordance with equation 112.

\[
V_L = V_B - (R_{\text{line}} + jX_{\text{line}}) \cdot I_L
\]

(Equation 112)

where:

\( V_L, I_L = \text{Re}(I_L) + j\text{Im}(I_L) \) are complex phase quantities

When all secondary phase-to-ground voltages are available, use the positive-sequence components of voltage and current. By separation of real and imaginary parts:

\[
v_l, re = v_b, re - r_{\text{line}} \cdot i_l, re + x_{\text{line}} \cdot i_l, im
\]

(Equation 113)

\[
v_l, im = v_b, im - x_{\text{line}} \cdot i_l, re - r_{\text{line}} \cdot i_l, im
\]

(Equation 114)

where:

\( v_b \) is the complex value of the busbar voltage

\( i_l \) is the complex value of the line current (secondary side)

\( r_{\text{line}} \) is the value of the line resistance

\( x_{\text{line}} \) is the value of the line reactance
For comparison with the set-point value, the modulus of $U_L$ are according to equation 115.

\[ |V_L| = \sqrt{(vl, re)^2 + (vl, im)^2} \]

(Equation 115)

4. Inject voltage for $V_B$ equal to setting $V_{Set}$.
5. Inject current equal to rated current $I_{2\text{Base}}$.
6. Confirm on the local HMI that service values for bus voltage and load current are equal to injected quantities.
7. Confirm that the calculated value for load voltage, displayed on the local HMI, is equal to that derived through hand calculations.
8. When setting $OperationLDC$ set to $Enabled$, the voltage regulation algorithm uses the calculated value for load voltage as the regulating quantity to compare against $V_{Set}$ and the voltage deadband limits $V_{Deadband}$ and $V_{DeadbandInner}$.
9. While injecting rated current $I_{2\text{Base}}$ into the IED, inject a quantity for $V_B$ that is slightly higher than $V_{Set} + |(R_{\text{line}} + jX_{\text{Line}}) \cdot I_L|$. This will ensure that the regulating voltage $V_L$ is higher than $V_{Set}$, and hence no tap change command should be issued from the IED.
10. Reduce the injected voltage for $V_B$ slightly below $V_{Set} + |(R_{\text{line}} + jX_{\text{Line}}) \cdot I_L|$ and confirm that the calculated value for load voltage is below $V_{Set}$ and a tap change command is issued from the IED.

11.10.3.8 Parallel voltage regulation

Master follower voltage regulation

1. For the transformers connected in the parallel group, confirm that $OperationPAR$ is set to $MF$.
2. For parallel operation, it is also recommended to confirm for parallel group membership, defined by setting $TnRXOP$ in the local HMI under $Main\ menu/Settings/IED\ Settings/Control/TransformerVoltageControl(ATCC,90)/TR8ATCC:x/ParCtrl$.
   The general parallel arrangement of transformers are defined by setting $TnRXOP$ to $Enabled$ or $Disabled$. The following rules are applicable on the settings $T1RXOP$ – $T4RXOP$. If IED $T1$ and $T2$ are connected,
   • $T1RXOP$ shall be set to $Enabled$ in instance 2 of TR8ATCC (90),
   • $T2RXOP$ shall be set to $Enabled$ in instance 3 of TR8ATCC (90),
   • $T2RXOP$ and $T3RXOP$ shall be set to $Enabled$ in instance 1 of TR8ATCC (90), and so on.

   The parameter corresponding to the own IED must not be set. $T1RXOP$ should thus not be set in IED $T1$, $T2RXOP$ not in IED $T2$, and so on.

3. The lowest transformer number in the parallel group is by default set as the Master – confirm that this is the case by reviewing the setting in the local HMI.
4. Review the settings for $V_{Deadband}$ (based on percentage of nominal bus voltage) and calculate the upper ($V_2$) and lower ($V_1$) voltage regulation limits for which a tap change command will be issued from the master transformer in the group.
5. Review the expected time for first ($t_1$) and subsequent ($t_2$) tap change commands from the master transformer in the local HMI under $Main\ menu/Settings/IED\ Settings/Control/TransformerVoltageControl(ATCC,90)/TR1ATCC:x/TR8ATCC:x/Time/t1\ and\ t2$.
6. Apply a voltage 1% below $V_1$ and wait for the issue of a raise command from the voltage control after the expiry of a constant or inverse time delay set by $t_1$. Detection of this
command will involve locating the allocated binary output for a raise command in the Signal Matrix in PCM600 and monitoring a positive from this output. Confirm the timing of this command correlates with the setting \( t_1 \).

7. After the issue of the raise command, confirm that all follower transformers in the group change tap in accordance with the command issued from the master transformer.

8. Inject a voltage \( V_B \) for the master transformer that is 1% above the upper deadband limit \( V_2 \) and wait for the issue of a lower command from the voltage control after the expiry of a constant or inverse time delay set by \( t_2 \).

9. Confirm that all follower transformers in the group change tap in accordance with this command.

**Circulating current voltage regulation**

This instruction for confirmation of circulating current voltage regulation assumes two transformers in the parallel group. Setting confirmation through secondary injection requires calculation of circulating currents for each transformer based on impedance values and respective compensating factors, and is therefore more complex for greater than two transformers.

1. Confirm that \( \text{OperationPAR} \) is set to \( CC \) for the transformers in the parallel group.

2. For parallel operation, it is also recommended that settings be confirmed for parallel group membership, governed by setting \( TnRXOP \) in the local HMI under **Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR8ATCC:x/ParCtrl**. The general parallel arrangement of transformers are defined by setting \( TnRXOP \) to Enabled or Disabled. The following rules are applicable on the settings \( T1RXOP - T4RXOP \).

   - If IED \( T1 \) and \( T2 \) are connected,
     - \( T1RXOP \) shall be set to Enabled in instance 2 of TR8ATCC (90), and
     - \( T2RXOP \) shall be set to Enabled in instance 1 of TR8ATCC (90).

   - If \( T1 - T3 \) are available,
     - \( T1RXOP \) and \( T2RXOP \) shall be set to Enabled in instance 3 of TR8ATCC (90),
     - \( T2RXOP \) and \( T3RXOP \) shall be set to Enabled in instance 1 of TR8ATCC (90) and so on.

   The parameter corresponding to the own IED **must not** be set. \( TIRXOP \) should thus **not** be set in IED \( T1, T2RXOP \)** not in IED \( T2 \) and so on.

3. Review the settings for \( V\text{Deadband} \) (based on percentage of nominal bus voltage) and calculate the upper \( V_2 \) and lower \( V_1 \) voltage regulation limits for which a tap change command will be issued from the master transformer in the group.

4. Review the expected time for first \( t_1 \) and subsequent \( t_2 \) tap change commands from the master transformer in the local HMI under **Main menu/Settings/IED Settings/TransformerVoltageControl(ATCC,90)/TR8ATCC:x/ParCtrl**.

5. Inject a voltage \( V_B \) equal to \( V\text{Set} \) for each transformer.

6. Inject a load current for Transformer 1 that is equal to rated load current \( I_2\text{Base} \) and a load current for Transformer 2 that is equal to 95% of rated load current \( I_2\text{Base} \). This will have the effect of producing a calculated circulating current that flows from HV to LV side for Transformer 1 and LV to HV side for Transformer 2.

7. Confirm that a circulating current is measured on the local HMI that is equal in magnitude to 5% of \( I_2\text{Base} \), with polarity as discussed in step 6.

8. Confirm the settings for \( C_i \) (Compensation Factor) and \( X_i \) (Transformer Short Circuit Impedance). Using these setting values and the measured quantity of circulating current from the local HMI \( (I_{cc,i}) \), calculate the value for circulating current voltage adjustment \( Vci \).
\[ Vdi = Ci \cdot Icc_i \cdot Xi \]  
(Equation 116)

The voltage regulation algorithm then increases (for transformer T2) or decreases (for transformer T1) the measured voltage by \( Vdi \) and compares \( Vi \) against the voltage deadband limits \( V1 \) and \( V2 \) for the purposes of voltage regulation.

\[ Vi = VB + Vdi \]  
(Equation 117)

9. To cause a tap change, the calculated value for circulating current voltage adjustment must offset the injected quantity for bus voltage \( V_B \) so that \( Vi \) is outside the voltage deadband created by setting \( V\text{Deadband} \). Expressed by equation 118 and equation 119.

\[ Vdi > V2 - VB \]  
(Equation 118)

\[ VB = V_{set} \]  
(for the purposes of this test procedure)  
(Equation 119)

Therefore:

\[ Ci \cdot Icc_i \cdot Xi > V2 - V_{set} \]  
(Equation 120)

\[ |Icc_i| > \left( \frac{V2 - V_{set}}{Ci \cdot Xi} \right) \]  
(Equation 121)

10. Using the settings for \( V_{Set} \), \( V\text{Deadband} \), \( C \) (Compensating factor) and \( Xr2 \) (transformer short circuit impedance) calculate the magnitude of \( Icc_i \) necessary to cause a tap change command.

11. Inject current equal to \( I2\text{Base} \) for Transformer 1 and \( (I2\text{Base} - |Icc_i|) \) for Transformer 2 so that the magnitude of calculated circulating current will cause a raise command to be issued for Transformer 2 and a lower command for Transformer 1. Magnitude and direction of circulating currents measured for each transformer can be observed as service values on the local HMI and raise/lower commands detected from the binary output mapped in the Signal Matrix.
The voltage injection equal to $V_{Set}$ is required for both transformers during this test.

12. Confirm that a tap change command is issued from the voltage control function to compensate for the circulating current.

13. Injected currents can be reversed such that the direction of calculated circulating currents change polarity, which will cause a lower command for Transformer 2 and a raise command for Transformer 1.

### Circulating current limit

1. Confirm that $Operation_{PAR}$ is set to $CC$ for each transformer in the parallel group.
2. Confirm that $OperCCBlock$ is set to $Enable$ for each transformer in the parallel group.
3. Review the setting for $CircCurrLimit$.
4. Review the setting for $CircCurrBk$ to confirm whether a circulating current limit will result in an $Alarm$ state, $Auto$ Block or $Auto&Man$ Block of the automatic voltage control for tap changer, for parallel control function TR8ATCC (90).
5. Inject a voltage $V_B$ equal to $V_{Set}$ for each transformer.
6. Inject a load current for Transformer 1 that is equal to rated load current $I_{2Base}$ and a load current for Transformer 2 that is 1% less than $(I_{2Base} - (I_{2Base} \cdot CircCurrLimit))$
7. Confirm that the automatic voltage control for tap changer, for parallel control function TR8ATCC (90) responds in accordance with the setting for $CircCurrBk$. Alarm and blocking conditions can be confirmed through interrogation of the event menu or the control menu on the local HMI.

### $VTmismatch$ during parallel operation

1. Confirm that $Operation_{PAR}$ is set to $MF$ for each transformer in the parallel group.
2. Review the setting for $VTmismatch$ and $tVTmismatch$.
3. Inject a voltage $V_B$ equal to $V_{Set}$ for Transformer 1 and a voltage less than $(V_{Set} - (VTmismatch \cdot V_{Set}))$ for Transformer 2.
4. This condition should result in a $VTmismatch$ which will mutually block the operation of the automatic voltage control for tap changer, parallel control function TR8ATCC (90) for all transformers connected in the parallel group, which can be confirmed through interrogation of the local HMI.
5. Confirm that the automatic voltage control for tap changer, parallel control function TR8ATCC (90) responds in accordance with the setting for $CircCurrBk$.

#### 11.10.3.9 Completing the test

Continue to test another function or end the test by changing the $TESTMODE$ setting to $Disabled$. Restore connections and settings to their original values, if they were changed for testing purposes.

#### 11.10.4 Single command, 16 signals SINGLECMD

For the single command function block, it is necessary to configure the output signal to corresponding binary output of the IED. The operation of the single command function (SINGLECMD) is then checked from the local HMI by applying the commands with $Mode = Off$, $Steady$ or $Pulse$, and by observing the logic statuses of the corresponding binary output.
Command control functions included in the operation of different built-in functions must be tested at the same time as their corresponding functions.

### 11.10.5 Interlocking

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Values of the logical signals are available on the local HMI under **Main menu/Tests/Function status/Control/Apparatus control/Interlocking**. The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

The interlocking function consists of a bay-level part and a station-level part. The interlocking is delivery specific and is realized by bay-to-bay communication over the station bus. For that reason, test the function in a system, that is, either in a complete delivery system as an acceptance test (FAT/SAT) or as parts of that system.

### 11.11 Scheme communication

#### 11.11.1 Scheme communication logic for distance or overcurrent protection ZCPSCH (85)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Check the scheme logic during the secondary injection test of the impedance or overcurrent protection functions.

Activation of the different zones verifies that the CS signal is issued from the intended zones. The CS signal from the independent tripping zone must have a $t_{SendMin}$ minimum time.

Check the tripping function by activating the CR and CR_GUARD inputs with the overreaching zone used to achieve the PLTR_CRD signal.

It is sufficient to activate the zones with only one type of fault with the secondary injection.

#### 11.11.1.1 Testing permissive underreaching

1. Activate the receive (CR) signal in the IED.
2. Apply healthy normal load conditions to the IED for at least two seconds.
3. Apply a fault condition within the permissive zone.
4. Check that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.
5. Check that other zones operate according to their zone timers and that the send (CS) signal is obtained only for the zone configured to generate the actual signal.
6. Deactivate the receive (CR) signal in the IED.
7. Check that the trip time complies with the zone timers and that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.
11.11.2 Testing permissive overreaching

1. Activate the receive (CR) signal in the IED.
2. Apply healthy normal load conditions to the IED for at least two seconds.
3. Apply a fault condition within the permissive zone.
4. Check that correct trip outputs, external signals, and indication are obtained for the actual type of fault generated.
5. Check that the other zones operate according to their zone timer and that the send (CS) signal is obtained only for the zones that are configured to give the actual signal. Also the zone connected to CS underreach is giving CS in this mode.
6. Deactivate the IED receive (CR) signal.
7. Apply healthy normal load conditions to the IED for at least two seconds.
8. Apply a fault condition within the permissive zone.
9. Check that trip time complies with the zone timers and that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.

11.11.3 Testing blocking scheme

1. Deactivate the receive (CR) signal of the IED.
2. Apply healthy normal load conditions to the IED for at least two seconds.
3. Apply a fault condition within the forward directed zone used for scheme communication tripping.
4. Check that correct trip outputs and external signals are obtained for the type of fault generated and that the trip time complies with the tCoord timer (plus relay measuring time).
5. Check that the other zones trip according to their zone times and that a send (CS) signal is only obtained for the reverse zone.
6. Activate the IED receive (CR) signal.
7. Apply a fault condition in the forward directed zone used for scheme communication tripping.
8. Check that the no trip from scheme communication occurs.
9. Check that the trip time from the forward directed zone used for scheme communication tripping complies with the zone timer and that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.

11.11.4 Testing delta blocking scheme

1. Deactivate the receive (CR) signal of the IED.
2. Apply healthy normal load conditions to the IED for at least two seconds.
3. Apply a fault condition within the forward directed zone used for scheme communication tripping.
4. Check that correct trip outputs and external signals are obtained for the type of fault generated and that the trip time complies with the tCoord timer (plus relay measuring time).
5. Check that the other zones trip according to their zone times and that a send (CS) signal is only obtained for the reverse zone.
6. Check that for a forward fault, carrier send (CS) signal is first obtained from the delta based fault inception detection (as it is not directional), and immediately inhibited by the forward zone.
7. Activate the IED receive (CR) signal.
8. Apply a fault condition in the forward directed zone used for scheme communication tripping.
9. Check that the no trip from scheme communication occurs.
10. Check that the trip time from the forward directed zone used for scheme communication tripping complies with the zone timer and that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.

11.11.1.5 Checking of unblocking logic

Check the unblocking function (if the function is required) when checking the communication scheme.

Command function with continuous unblocking (Unblock = 1)

Procedure

1. Activate the guard input signal (CR_GUARD) of the IED.
2. Using the scheme selected, check that a signal accelerated trip (TRIP) is obtained when the guard signal is deactivated.

11.11.1.6 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.11.2 Phase segregated scheme communication logic for distance or overcurrent protection ZC1PPSCH (85)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Check the scheme logic during the secondary injection test of the impedance protection functions.

Activating of the different zones and phases verifies that the CS_x signal are issued from the intended zones. The CS_x signal from the independent tripping zone must have a tSendMin minimum time.

Check the tripping function by activating the CRLx inputs with the overreaching zone used to achieve the CACCLx signals.

It is sufficient to activate the zones with all types of fault with the secondary injection.

11.11.2.1 Testing permissive underreaching

1. Activate the receive (CRLx) signal in the IED.
2. Apply healthy normal load conditions to the IED for at least two seconds.
3. Apply a fault condition within the permissive zone.
4. Check that correct trip outputs, external signals, and indications are obtained for all the actual types of fault generated.
5. Check that other zones operate according to their zone timers and that the send (CS_x) signal is obtained only for the zone and phase configured to generate the actual signal.
6. Deactivate the receive (CRLx) signal in the IED.
7. Check that the trip time complies with the zone timers and that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.

11.11.2.2 Testing permissive overreaching

1. Activate the receive (CRLx) signal in the IED.
2. Apply healthy normal load conditions to the IED for at least two seconds.
3. Apply a fault condition within the permissive zone.
4. Check that correct trip outputs, external signals, and indication are obtained for the all actual types of fault generated.
5. Check that the other zones operate according to their zone timer and that the send (CS_x) signal is obtained only for the zones and phases that are configured to give the actual signal. Also the zone connected to CS_x underreach is giving CS_x in this mode.
6. Deactivate the IED receive (CRLx) signal.
7. Apply healthy normal load conditions to the IED for at least two seconds.
8. Apply a fault condition within the permissive zone.
9. Check that trip time complies with the zone timers and that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.

11.11.2.3 Testing blocking scheme

1. Deactivate the receive (CRLx) signal of the IED.
2. Apply healthy normal load conditions to the IED for at least two seconds.
3. Apply a fault condition within the forward directed zone used for scheme communication tripping.
4. Check that correct trip outputs and external signals are obtained for the type of fault generated and that the operate time complies with the tCoord timer (plus relay measuring time).
5. Check that the other zones operate according to their zone times and that a send (CS_x) signal is only obtained for the reverse zone.
6. Activate the IED receive (CRLx) signal.
7. Apply a fault condition in the forward directed zone used for scheme communication tripping.
8. Check that the no trip from scheme communication occurs.
9. Check that the trip time from the forward directed zone used for scheme communication tripping complies with the zone timer and that correct trip outputs, external signals, and indications are obtained for the actual type of fault generated.

11.11.2.4 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.
11.11.3 Current reversal and Weak-end infeed logic for distance protection 3-phase ZCRWPSCH (85)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Values of the logical signals for ZCRWPSCH are available on the local HMI under Main menu/Tests/Function status / Scheme communication / ZCRWPSCH(85) /ZCRWPSCH:1.

The Signal Monitoring in PCM600 shows signals that are available on the Local HMI.

The current reversal logic and the weak-end infeed functions are tested during the secondary-injection test of the impedance or overcurrent protection zones together with the scheme communication logic for the distance protection function ZCPSCH (85).

11.11.3.1 Current reversal logic

It is possible to check the delay of the CS send signal with $t_{DelayRev}$ by changing from a reverse to a forward fault.

By continuously activating the CR input and changing from a reverse to a forward fault, the delay $t_{DelayRev}$ can be checked.

**Checking of current reversal**

1. Set the healthy condition to an impedance at 50% of the reach of the reverse zone connected to IRV.
2. Activate the receive (CRL) signal.
3. After the pickup condition is obtained for reverse zone, apply a fault at 50% of the reach of the forward zone connected to WEIBLK2.
4. Check that correct trip outputs and external signals are obtained for the type of fault generated. The operation time should be about the $t_{DelayRev}$ setting longer than the carrier accelerated trip (TRIP) recorded for the permissive overreach scheme communication.
5. Restore the forward and reverse zone timer to its original setting.

11.11.3.2 Weak end infeed logic

**Weak-end infeed logic at permissive overreach schemes**
1. Check the blocking of the echo with the injection of a CRL signal >40ms after a reverse fault is applied.
2. Measure the duration of the echoed CS signal by applying a CRL receive signal.
3. Check the trip functions and the voltage level for trip by reducing a phase voltage and applying a CRL receive signal.

**Testing conditions**
Only one type of fault is sufficient, with the current reversal and weak-end infeed logic for distance protection function ZCRWPSCH (85). Apply three faults (one in each phase). For phase A-G fault, set these parameters:

<table>
<thead>
<tr>
<th>Phase</th>
<th>I (Amps)</th>
<th>Phase-angle (Deg)</th>
<th>V (Volts)</th>
<th>Phase-angle (Deg)</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>0</td>
<td>0</td>
<td>Set less than UPN&lt;PU27PN</td>
<td>0</td>
</tr>
<tr>
<td>B</td>
<td>0</td>
<td>240</td>
<td>69.3</td>
<td>240</td>
</tr>
<tr>
<td>C</td>
<td>0</td>
<td>120</td>
<td>69.3</td>
<td>120</td>
</tr>
</tbody>
</table>

If wanted, change all settings cyclically for other faults (BG and CG).

The setting parameter WEI is set to *Echo & Trip*.

1. Apply input signals according Table 40.
2. Activate the receive (CR) signal.
3. After the IED has operated, turn off the input signals.
4. Check that trip, send signal, and indication are obtained.

The ECHO output gives only a 200 ms pulse.

### 11.11.3.3 Completing the test

**Completing the test**

continue to test another function or end the test by changing the **TESTMODE** setting to **Disabled**. Restore connections and settings to their original values, if they were changed for testing purposes.

### 11.11.4 Scheme communication logic for residual overcurrent protection ECPSCH (85)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

Before testing the communication logic for residual overcurrent protection function ECPSCH (85), the four step residual overcurrent protection function EF4PTOC (51N/67N) has to be tested according to the corresponding instruction. Once this is done, continue with the instructions below.
If the current reversal and weak-end infeed logic for ground-fault protection is included, proceed with the testing according to the corresponding instruction after testing the communication logic for residual overcurrent protection. The current reversal and weak-end-infeed functions shall be tested together with the permissive scheme.

### 11.11.4.1 Testing the directional comparison logic function

#### Blocking scheme

1. Inject the polarizing voltage 3V0 at 5% of $V_{Base}$ (EF4PTOC, 51N67N) where the current is lagging the voltage by 65°.
2. Inject current (65° lagging the voltage) in one phase at about 110% of the set operating current, and switch the current off with the switch.
3. Switch the fault current on and measure the operating time of the communication logic. Use the TRIP signal from the configured binary output to stop the timer.
4. Compare the measured time with the set value $t_{Coord}$.
5. Activate the CR binary input.
6. Check that the CRL output is activated when the CR input is activated.
7. Switch the fault current on (110% of the set operating current) and wait longer than the set value $t_{Coord}$.

No TRIP signal should appear.

8. Switch the fault current off.
9. Reset the CR binary input.
10. Activate the BLOCK digital input.
11. Switch the fault current on (110% of the set operating current) and wait for a period longer than the set value $t_{Coord}$.

No TRIP signal should appear.

12. Switch the fault current and the polarizing voltage off.
13. Reset the BLOCK digital input.

#### Permissive scheme

1. Inject the polarizing voltage 3V0, which is 5% of $V_{Base}$ (EF4PTOC, 51N67N) where the current is lagging the voltage by 65°.
2. Inject current (65° lagging the voltage) into one phase at about 110% of the set operating current, and switch the current off with the switch.
3. Switch the fault current on, (110% of the set operating current) and wait longer than the set value $t_{Coord}$.

No TRIP signal should appear, and the CS binary output should be activated.
4. Switch the fault current off.
5. Activate the CR binary input.
6. Switch the fault current on (110% of the set operating current) and measure the operating time of the ECP SCH (85) logic.
   Use the TRIP signal from the configured binary output to stop the timer.
7. Compare the measured time with the setting for $t_{Coord}$.
8. Activate the BLOCK digital input.
9. Switch the fault current on (110% of the set operating current) and wait for a period longer than the set value $t_{Coord}$.

No TRIP signal should appear.

10. Switch the fault current and the polarizing voltage off.
11. Reset the CR binary input and the BLOCK digital input.

### 11.11.4.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

### 11.11.5 Current reversal and weak-end infeed logic for residual overcurrent protection ECRWPSCH (85)

Prepare the IED for verification of settings as outlined in section "Requirements" and section "Preparing for test" in this chapter.

Values of the logical signals for ECRWPSCH are available on the Local HMI under Main menu/Tests/Function status/Scheme communication/ECRWPSCH(85)/ECRWPSCH:1.

The Signal Monitoring in PCM600 shows service values that are available on the Local HMI as well.

First, test the four step residual overcurrent protection function EF4PTOC (51N/67N) and then the current reversal and weak-end infeed logic according to the corresponding instructions. Then continue with the instructions below.

#### 11.11.5.1 Testing the current reversal logic

1. Inject the polarizing voltage 3V0 to 5% of $V_{Base}$ and the phase angle between voltage and current to 155°, the current leads the voltage.
2. Inject current ($180° - AngleRCA$) in one phase to about 110% of the set operating current of the four step residual overcurrent protection ($INDi r$).
3. Check that the IRVL output is activated in the disturbance recorder after the set time $t_{PickUpRev}$.
4. Abruptly reverse the current to $AngleRCA$ setting lagging the voltage, to operate the forward directional element.
5. Check that the IRVL output still is activated after the reversal with a time delay that complies with the setting $t_{DelayRev}$.
6. Switch off the polarizing voltage and the current.
11.11.5.2 Testing the weak-end infeed logic

If setting \textit{WEI = Echo}

1. Inject the polarizing voltage $3V0$ to $(180^\circ - \text{AngleRCA})$ of $V_{\text{Base}}$ and the phase angle between voltage and current to $155^\circ$, the current leads the voltage.
2. Inject current $(180^\circ - \text{AngleRCA})$ in one phase to about $110\%$ of the setting operating current ($\text{INDir}$).
3. Activate the CRL binary input.
   
   No ECHO and CS should appear.

4. Abruptly reverse the current to the setting of $\text{AngleRCA}$ setup lagging the voltage, to operate the forward directional element.
   
   No ECHO and CS should appear.

5. Switch off the current and check that the ECHO and CS appear on the corresponding binary output during $200$ms after resetting the directional element.
6. Switch off the CRL binary input.
7. Activate the BLOCK binary input.
8. Activate the CRL binary input.
   
   No ECHO and CS should appear.

9. Switch off the polarizing voltage and reset the BLOCK and CRL binary input.

If setting \textit{WEI = Echo & Trip}

1. Inject the polarizing voltage $3V0$ to about $90\%$ of the setting ($3V0PU$) operating voltage.
2. Activate the CRL binary input.
   
   No ECHO, CS and TRWEI outputs should appear.

3. Increase the injected voltage to about $110\%$ of the setting ($3V0PU$) operating voltage.
4. Activate the CRL binary input.
5. Check that the ECHO, CS and TRWEI appear on the corresponding binary output or on the local HMI.
6. Reset the CRL binary input.
7. Activate the BLOCK binary input.
8. Activate the CRL binary input.
No ECHO, CS and TRWEI outputs should appear.

9. Reset the CRL and BLOCK binary input.
10. Inject the polarizing voltage 3V0 to about 110% of the setting (3VOPU>) and adjust the phase angle between the voltage and current to (180°- AnglRCA) setting, the current leads the voltage.
11. Inject current in one phase to about 110% of the set trip current (INDir).
12. Activate the CRL binary input.

No ECHO and TRWEI should appear.

13. Abruptly reverse the current to 65° lagging the voltage to trip the forward directional element.

No ECHO and TRWEI should appear.

14. Switch the current off and check that the ECHO, CS and TRWEI appear on the corresponding binary output during 200ms after resetting the directional element. If EF4PTOC also trips in forward direction, CS should be obtained.
15. Switch the polarizing voltage off and reset the CRL binary input.

11.11.5.3 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.12 Logic

11.12.1 Tripping logic, common 3-phase output SMPPTRC (94)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

This function is functionality tested together with other protection functions (line differential protection, ground-fault overcurrent protection, and so on) within the IED. It is recommended that the function is tested together with the autorecloser function, when built into the IED or when a separate external unit is used for reclosing purposes.

11.12.1.1 3 phase operating mode

1. Check that AutoLock and TripLockout are both set to Disabled.
2. Initiate a three-phase fault.
An adequate time interval between the faults should be considered, to overcome a reset time caused by the possible activation of the autorecloser function SMBRREC (79). The function must issue a three-pole trip in all cases when trip is initiated by any protection or some other built-in or external function. The following functional output signals must always appear simultaneously: TRIP, TR_A, TR_B, TR_C and TR3P.

11.12.1.2 1p/3p operating mode

In addition to various other tests, the following tests should be performed. They depend on the complete configuration of an IED:

Procedure

1. Make sure that TripLockout and AutoLock are both set to Disabled.
2. Initiate different single-phase-to-ground faults one at a time.
3. Initiate different phase-to-phase and three-phase faults.
   Consider using an adequate time interval between faults, to overcome a reset time, which is activated by SMBRREC (79). A three-pole trip should occur for each separate fault and all of the trips. Functional outputs TRIP, all TR_A, TR_B, TR_C and TR3P should be active at each fault. No other outputs should be active.

4. Initiate a single phase-to-ground fault and switch it off immediately when the trip signal is issued for the corresponding phase. Initiate the same fault once again within the reset time of the used SMBRREC (79).
5. Initiate a single phase-to-ground fault and switch it off immediately when the trip signal is issued for the corresponding phase. Initiate the second single phase-to-ground fault in one of the remaining phases within the time interval, shorter than $t_{EvolvingFault}$ (default setting 2.0s) and shorter than the dead-time of SMBRREC (79), when included in the protection scheme. Check that the second trip is a three-pole trip and that a three-phase autoreclosing attempt is given after the three-phase dead time. Functional outputs TRIP, TR_A, TR_B, TR_C and TR1P should be active during the first fault. No other outputs should be active. Functional outputs TRIP, all TR_A, TR_B, TR_C and TR3P should be active during second fault.

11.12.1.3 Circuit breaker lockout

The following tests should be carried out when the built-in lockout function is used in addition to possible other tests, which depends on the complete configuration of an IED:

1. Check that AutoLock and TripLockout are both set to Disabled.
2. Activate shortly the set lockout (SETLKOUT) signal in the IED.
3. Check that the circuit breaker lockout (CLLKOUT) signal is set.
4. Activate shortly thereafter, the reset lockout (RSTLKOUT) signal in the IED.
5. Check that the circuit breaker lockout (CLLKOUT) signal is reset.
6. Initiate a three-phase fault.
   A three- trip should occur and all trip outputs TR_A, TR_B, TR_C should be activated. Functional outputs TRIP and TR3P should be active at each fault. The output CLLKOUT should not be set.
7. Activate the automatic lockout function, set AutoLock = Enabled and repeat. Besides the TRIP outputs, CLLKOUT should be set.
8. Reset the lockout signal by activating the reset lockout (RSTLKOUT) signal.
9. Activate the trip signal lockout function, set TripLockout = Enabled and repeat. All trip outputs (TR_A, TR_B, TR_C) and functional outputs TRIP and TR3P must be active and stay active after each fault, CLLKOUT should be set.
10. Reset the lockout. All functional outputs should reset.
11. Deactivate the trip signal lockout function, set TripLockout = Disabled and the automatic lockout function, set AutoLock = Disabled.

### 11.12.1.4 Completing the test

Continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

### 11.13 Monitoring

#### 11.13.1 Gas medium supervision SSIMG

Prepare the IED for verification of settings as outlined in Section "Preparing the IED to verify settings" in this chapter.

Values of logical signals for SSIMG protection are available on the local HMI under Main menu/Tests/Function status/Monitoring/InsulationGas(63)/SSIMG(63):x, where x = 1, 2,.....21.

The Signal Monitoring in PCM600 shows the same signals that are available on the local HMI.

Check that the input logical signal BLOCK is logical zero and that on the local HMI, the logical signals PRESALM, PRESLO, TEMPALM, TEMPO, ALARM and LOCKOUT are logical zero.

Using service kit, prepare the IED for verification of settings as outlined in Section "Preparing the IED to verify settings" in this chapter.

#### 11.13.1.1 Testing the gas medium supervision for pressure alarm and pressure lockout conditions

1. Connect binary inputs to consider gas pressure and gas density to initiate the alarms.
2. Consider the analogue pressure input SENPRES and set SENPRES to a value lower than PresAlmLimit or activate binary input signal SENPRESALM, check that outputs PRESALM and ALARM are activated after a set time delay of tPressureAlarm.
3. Gas pressure lockout input SETPLO can be used to set PRESLO.
4. Also, reduce further the pressure level input below PresLOLimit or activate the binary input signal SENPRESLO, check that PRESLO signal appears after a set time delay of tPressureLO.
5. Activate BLOCK binary input and check that the outputs PRESALM, PRESLO, ALARM and LOCKOUT disappear.
6. Reset the BLOCK binary input.
7. Make sure that pressure lockout condition exists and then activate the reset lock out input RESETLO and check that the outputs PRESLO and LOCKOUT reset.
11.13.1.2 Testing the gas medium supervision for temperature alarm and temperature lock out conditions

1. Consider the analogue temperature input \textit{SENTEMP} and set \textit{SENTEMP} to a value higher than \textit{TempAlarmLimit}, check that outputs \textit{TEMPALM} and \textit{ALARM} are activated after a set time delay of \textit{tTempAlarm}.
2. Temperature lockout input \textit{SETTLO} can be used to set \textit{TEMPLO} signal.
3. Also, increase further the temperature input above \textit{TempLOLimit}, check that the outputs \textit{TEMPLO} and \textit{LOCKOUT} appears after a set time delay of \textit{tTempLockOut}.
4. Activate \textit{BLOCK} binary input and check that the outputs \textit{TEMPALM}, \textit{TEMPLO}, \textit{ALARM} and \textit{LOCKOUT} disappear.
5. Reset the \textit{BLOCK} binary input.
6. Make sure that temperature lockout condition exists and then activate the reset lock out input \textit{RESETLO} and check that the outputs \textit{TEMPLO} and \textit{LOCKOUT} reset.

11.13.1.3 Completing the test

Continue to test another function or end the test by changing the \textit{TestMode} setting to \textit{Off}. Restore connections and settings to their original values, if they were changed for testing purposes.

11.13.2 Liquid medium supervision SSIML

Prepare the IED for verification of settings as outlined in section "Preparing the IED to verify settings" in this chapter.

Values of logical signals for SSIMG protection are available on the local HMI under Main menu/Tests/Function status/Monitoring/InsulationLiquid(71)/SSIML(71):x, where \( x = 1, 2, ..., 4 \).

Check that the input logical signal \textit{BLOCK} is logical zero and that on the local HMI, the logical signals \textit{LVLALM}, \textit{LVLLO}, \textit{TEMPALM}, \textit{TEMPLO}, \textit{ALARM} and \textit{LOCKOUT} are logical zero.

11.13.2.1 Testing the liquid medium supervision for level alarm and level lockout conditions

1. Connect the binary inputs to consider liquid level to initiate the alarms.
2. Consider the analogue level input \textit{SENLEVEL} and set \textit{SENLEVEL} to a value lower than \textit{LevelAlmLimit} or activate binary input signal \textit{SENLVLALM}, check that outputs \textit{LVLALM} and \textit{ALARM} are activated after a set time delay of \textit{tLevelAlarm}.
3. Liquid level lockout input \textit{SENLVLLO} can be used to set \textit{LVLLO}.
4. Also, reduce the liquid level input below \textit{LevelLOLimit} or activate the binary input signal \textit{SENLVLLO}, check that \textit{LVLLO} signal after a set time delay of \textit{tLevelLockOut}.
5. Activate \textit{BLOCK} binary input and check that the outputs \textit{LVLALM}, \textit{LVLLO}, \textit{ALARM} and \textit{LOCKOUT} disappears.
6. Reset the \textit{BLOCK} binary input.
7. Make sure that level lockout condition exists and then activate the reset lock out input \textit{RESETLO} and check that the outputs \textit{PRESLO} and \textit{LOCKOUT} reset.
### 11.13.2.2 Testing the liquid medium supervision for temperature alarm and temperature lock out conditions

1. Consider the analogue temperature input \text{SENTEMP} and set \text{SENTEMP} to a value higher than \text{TempAlarmLimit}, check that outputs \text{TEMPALM} and \text{ALARM} are activated after a set time delay of tTempAlarm.
2. Temperature lockout input \text{SETTLO} can be used to set \text{TEMPLO} signal.
3. Also, increase further the temperature input above \text{TempLOLimit}, check that the outputs \text{TEMPLO} and \text{LOCKOUT} appears after a set time delay of tTempLockOut.
4. Activate \text{BLOCK} binary input and check that the outputs \text{TEMPALM}, \text{TEMPLO}, \text{ALARM} and \text{LOCKOUT} disappear.
5. Reset the \text{BLOCK} binary input.
6. Make sure that temperature lockout condition exists and then activate the reset lock out input \text{RESETLO} and check that the outputs \text{TEMPLO} and \text{LOCKOUT} reset.

### 11.13.2.3 Completing the test

Continue to test another function or end the test by changing the \text{TestMode} setting to \text{Off}. Restore connections and settings to their original values, if they were changed for testing purposes.

### 11.13.3 Breaker monitoring SSCBR

Prepare the IED for verification of settings outlined in section “Testing the IED operation”.

The Signal Monitoring tool in PCM600 shows the service values that are available on the Local HMI as well.

Values of the logical signals belong to the breaker monitoring are available on the local HMI under: \text{Main menu/Test/Function status/Monitoring/BreakerMonitoring/SSCBR:x}

#### 11.13.3.1 Verifying the settings

1. Connect the test set for the injection of a three-phase current to the appropriate current terminals of the IED.
2. If current need to be injected for a particular test, it should be done in the phase selected by the \text{PhSel} parameter.
3. Follow the sequence for positioning the auxiliary contacts before testing:

<table>
<thead>
<tr>
<th>POSCLOSE</th>
<th>0</th>
<th>1</th>
<th>0</th>
</tr>
</thead>
<tbody>
<tr>
<td>POSOPEN</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
</tbody>
</table>

4. Test of CB contact travel time

4.1. Test the set timing defined by \text{OpenTimeCorr}, \text{CloseTimeCorr}, \text{tTrOpenAlm} and \text{tTrCloseAlm}.

4.2. Change the status of the auxiliary contacts such that travel time to open \text{TTRVOP} and travel time to close \text{TTRVCL} exceed the respective set values (\text{tTrOpenAlm} and \text{tTrCloseAlm}). The measured travel time for opening and closing is shown on \text{TTRVOP} and \text{TTRVCL} respectively.

4.3. Check that \text{TRVTOPAL} and \text{TRVTCLAL} are activated.

5. Test of CB status
5.1. Test the set current level defined by AccStopCurr.
5.2. Check the CLOSEPOS output by changing the POSOPEN to 0 and POSCLOSE to 1.
5.3. Check the OPENPOS output by changing the POSOPEN to 1 and POSCLOSE to 0 and also inject the current in the selected phase slightly lower and higher than AccStopCurr set value. Only for a current lower than set AccStopCurr should activate the output POSOPEN.
5.4. Check the circuit breaker is in INVDPOS if auxiliary contacts read same value or CB is open and inject the current in selected phase more than AccStopCurr set value.

6. Test of remaining life of CB
6.1. Test the set timing defined by RatedOperCurr, RatedFltCurr, OperNoRated, OperNoFault, DirCoef, CBLifeAlmLevel.
6.2. Vary the phase current in the selected phase from below rated operated current, RatedOperCurr to above rated fault current, RatedFltCurr of a breaker.
6.3. The remaining life of CB output CBLIFEPH is estimated when the CB is changed from closed to open position. Check that the output CBLIFEPH is decreased with a value that corresponds to the injected current.
6.4. CBLIFEAL is activated as soon as CBLIFEPH is below the set CBLifeAlmLevel value.

7. Test of accumulated energy
7.1. Test the actual set values defined by AccSelCal to Aux Contact, ContTrCorr and AlmAccCurrPwr.
7.2. Inject phase current in the selected phase such that its value is greater than set AccStopCurr value.
7.3. When the breaker goes to open position, accumulated energy IPOWPH is calculated. The calculated value can be seen on the output IPOWPH.
7.4. Alarm signal IPOWALPH appears when IPOWPH is greater than set AlmAccCurrPwr value.
7.5. Lockout signal IPOWLOPH appears if IPOWPH exceeds further to the threshold value LOAccCurrPwr.
7.6. Calculation of accumulated energy IPOWPH is stopped when injected current is lower than set AccStopCurr value.

8. Test of CB operation cycles
8.1. Test the actual set values defined by OperAlmLevel and OperLOLevel.
8.2. The operation counter, NOOPER is updated for every close-open sequence of the breaker by changing the position of auxiliary contacts POSCLOSE and POSOPEN.
8.3. OPERALM is activated when NOOPER value exceeds the set OperAlmLevel value. The actual value can be read on the output NOOPER.
8.4. OPERLO is activated when NOOPER value exceeds the set OperLOLevel value.

9. Test of CB spring charge monitoring
9.1. Test the actual set value defined by SpChAlmTime.
9.2. Enable SPRCHRST input. Also activate SPRCHRD after a time greater than set time SpChAlmTime.
9.3. At this condition, SFCHALM is activated.

10. Test of CB gas pressure indication
10.1. Test the actual set value defined by tDGasPresAlm and tDGasPresLO.
10.2. The output GPRESALM is activated after a time greater than set time of tDGasPresAlm value if the input PRESALM is enabled.
10.3. The output GPRESLO is activated after a set time of tDGasPresLO value if the input PRESLO is enabled.
11.13.3.2 Completing the test

1. Continue to test another function or end the test by changing the Test mode setting to Disabled.
2. Restore connections and settings to their original values if they were changed for testing purposes.

11.13.4 Event function EVENT

Prepare the IED for verification of settings as outlined in section “Preparing for test” in this chapter.

During testing, the IED can be set when in test mode from PST. The functionality of the event reporting during test mode is set in the Parameter Setting tool in PCM600.

- Use event masks
- Report no events
- Report all events

In test mode, individual event blocks can be blocked from PCM600.

11.13.5 Fault locator LMBRFLO

Prepare the IED for verification of settings outlined in Section “Preparing the IED to verify settings”.

The Fault locator function LMBRFLO depends on other functions to work properly, that is, phase selection information from distance protection function and analog information supplied by the trip value recorder function. Check that proper binary initiate (pickup or tripping) and phase selection signals are connected and voltage and current signals are configured (parameter settings).

The result is displayed on the local HMI or via PCM600. Distances to faults for the last 100 recorded disturbances can be found on the local HMI under Main menu/Disturbance Records/Disturbance #n(n = 1–100)/General Information

If PCM600 is used, the result is displayed on the recording list after upload, including loop selection information.
### 11.13.5.1 Measuring the Operate Limit

1. Set the test point (|Z| fault impedance and ZΦ impedance phase angle) for a condition that meets the requirements in Table 41.
2. Subject the IED to healthy normal load conditions for at least two seconds.
3. Apply a fault condition.

Check that the distance-to-fault value displayed on the HMI complies with equations 122, 123, and 124:

\[
p = \frac{Z_x}{X1} \times 100
\]

(Equation 122)

in % for two- and three-pole faults

\[
p = \frac{3 \cdot Z_x}{X0 + 2 \cdot X1} \times 100
\]

(Equation 123)

in % for single-phase-to-ground faults

\[
p = \frac{3 \cdot Z_x}{X0 + 2 \cdot X1 \pm XM} \times 100
\]

(Equation 124)

in % for single-phase-to-ground faults with mutual zero sequence currents.

Where:

- \( p \) = the expected value of a distance to fault in percent
- \( Z_x \) = set test point on the test set

Table continues on next page
X₀ = set zero-sequence reactance of a line
X₁ = set positive-sequence reactance of a line
XM = set mutual zero-sequence impedance of a line

11.13.5.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.13.6 Limit counter L4UFCNT

The Limit counter function L4UFCNT can be tested by connecting a binary input to the counter and applying pulses to the counter. The speed of the pulses must not exceed the cycle time of the function. Normally the counter will be tested when testing the function that the counter is connected to, such as the trip function. When the function is configured, test it together with the function that operates it. Trigger the function and check that the counter result corresponds to the number of operations.

11.13.6.1 Completing the test

Continue to test another function or end the test by changing the Test mode setting to Off. Restore connections and settings to their original values, if they were changed for testing purposes.

11.13.7 Estimation of transformer insulation life LOLSPTR (26/49HS)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.13.7.1 Verifying the signals and settings

Time measurement and the injection of current and voltage can be done using common test equipment.

Verifying HPTMPMAX and TOTCALC outputs

1. Connect the test set for injection of three-phase currents to the appropriate current terminals of the IED.
2. Activate the TOTVALID input and set some temperature value in TOTEMP input.
3. Supply the IED with three-phase currents slightly more than the rated value.
4. Make sure that the hot spot temperature (HPTMPMAX) is more than the set value of TOTEMP.
5. Activate the AMBVALID input and set some temperature value in AMBTEMP input.
6. Make sure that the top oil temperature calculated (TOTCALC) is more than the set value of AMBTEMP.
Verifying WARNINGx and ALARMx outputs

1. Connect the test set for injection of three-phase currents to the appropriate current terminals of the IED.
2. Make sure that the settings in the IED are appropriate, especially the warning temperature levels (WrnHPTmpLevX) and alarm time settings (tDelayToAlarmX).
3. Supply three-phase currents nearly zero to the IED and take note of hot spot temperature (HPTMPMAX).
4. Make sure that the WARNINGx and ALARMx signals remain low.
5. Supply three-phase currents slightly more than the rated value to the IED and take note of hot spot temperature (HPTMPMAX). Observe the maximum permitted overloading of the current circuits in the IED.
6. Check the time until the warning limit has reached WrnHPTmpLev1 level during injection. Monitor the signal WARNING1 until it appears on the corresponding binary output.
7. Compare the measured temperature (HPTMPMAX) with WrnHPTmpLev1 setting.
8. Check the time until the warning limit has reached WrnHPTmpLev2 level during injection. Monitor the signal WARNING2 until it appears on the corresponding binary output.
9. Compare the measured temperature (HPTMPMAX) with WrnHPTmpLev2 setting.
10. Wait for a time corresponding to tDelayToAlarm1 from the appearance of WARNING1 signal. Make sure that the ALARM1 signal appears.
11. Note the time taken for the appearance of ALARM1 signal from the appearance of WARNING1 signal, and compare it with set value (tDelayToAlarm1).
12. Wait for a time corresponding to tDelayToAlarm2 from the appearance of WARNING2 signal, and make sure that the ALARM2 signal appears.
13. Note the time taken for the appearance of ALARM2 signal from the appearance of WARNING2 signal, and compare it with set value (tDelayToAlarm2).
14. Activate the BLKWRN binary input. The WARNING1 and WARNING2 signals should disappear.
15. Activate the BLKALM binary input. The ALARM1 and ALARM2 signals should disappear.
16. Reset the BLKWRN and BLKALM binary inputs. Once again warning and alarm signals should appear.
17. Reduce the three-phase currents suddenly to value lower than the rated current and take note of hot spot temperature.
18. Check the warnings reset limit (HystAbsHPT). Monitor the WARNING1 and WARNING2 signals until they disappear on the corresponding binary outputs. Take the HPTMPMAX readings and compare with the reset level settings.

Verifying LOLINDAY and LOLINYRS outputs

1. Connect the test set for injection of three-phase currents to the appropriate current terminals of the IED.
2. Make sure that the settings in the IED are appropriate, especially set the EnaAgeCalc setting as Enable, TimeToUpdate setting as 1 hour and AgeingRateMeth setting as IEC.
3. Activate the TOTVALID input and set some temperature value in TOTEMP input.
4. Supply the IED with three-phase currents slightly more than the rated value.
5. Note the hot spot temperature (HPTMPMAX) and maintain it for another 1 hour.
6. Check the LOLINDAY and LOLINYRS outputs after 1 hour and compare it with the calculated loss of life value using IEC standard method.

11.13.7.2 Completing the test

Continue to test another function or end the test by changing the Test mode setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.
11.13.8 Through fault monitoring PTRSTHR (51TF)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.13.8.1 Verifying the signals and settings

The voltage or current can be injected using a common test equipment.

**Verifying the through fault detection**

1. Supply the IED with three-phase current at rated value.
2. Ensure that the relevant settings for through fault detection such as $W1\ I$ pickup, $W2\ I$ pickup and $W3\ I$ pickup are set correctly.
3. Ensure that the BLOCK and INHIBIT inputs are not activated.
4. Set $t_{\text{MinTripDelay}}$ as 0 s to check the pickup level of through fault detection.
5. Slowly increase any phase current of any winding until ALARM signal appears.
6. Compare the injected phase current value with the set threshold limit value.
7. Bring the injected three-phase currents at the rated value.
8. Change the $t_{\text{MinTripDelay}}$ setting to a particular value.
9. Slowly increase any phase current of any winding to the threshold limit and wait until the ALARM signal appears.
10. Compare the wait time with the set $t_{\text{MinTripDelay}}$ value.
11. Bring the injected three-phase currents to the rated value.

**Verifying the fault $I_t^2$ and cumulative $I_t^2$ alarms**

1. Supply the IED with three-phase current at rated value.
2. Ensure that the relevant settings for fault $I_t^2$ alarm such as $\text{MaxI}_t^2\text{W}_1$, $\text{MaxI}_t^2\text{W}_2$ and $\text{MaxI}_t^2\text{W}_3$ are set correctly.
3. Ensure that the BLOCK and INHIBIT inputs are not activated.
4. Slowly increase any phase current of any winding until ALARM signal appears.
5. Continue to inject the same level of current until the I2TALM signal appears.
6. Compute the time between the ALARM signal raise to I2TALM signal raise and multiply with the squared injected current.
7. Compare the calculated value with the set $\text{MaxI}_t^2\text{W}_x$ value.
8. Bring the injected three-phase currents to the rated value.
9. Ensure that the relevant settings for fault cumulative $I_t^2$ alarm such as $\text{MaxI}_t^2\text{CmlW}_1$, $\text{MaxI}_t^2\text{CmlW}_2$ and $\text{MaxI}_t^2\text{CmlW}_3$ are set correctly.
10. Slowly increase any phase current of any winding to the threshold limit and wait until the ALARM signal appears.
11. Continue to inject the same level of current until the CMLI2TALM signal appears.
12. Compute the time between the ALARM signal raise to CMLI2TALM signal raise and multiply with the squared injected current.
13. Add the calculated value with previously calculated $I_t^2$ value.
14. Compare the calculated value with the set $\text{MaxI}_t^2\text{CmlW}_x$ value.
15. Bring the injected three-phase currents to the rated value.
11.13.8.2 Completing the test

Continue to test another function or end the test by changing the TestMode setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.13.9 Current harmonic monitoring CHMMHAI(ITHD)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.13.9.1 Verifying the signals and settings

The current can be injected using common test equipment.

Verifying the warning and alarm time limit of THD, WrnLimitTHD and tDelayAlmTHD

1. Supply the IED with current at rated value.
2. Apply harmonics at the numerical multiple of fundamental frequency along with the injected current signal.
3. Slowly increase the harmonic amplitudes until the THDWRN signal appears.
4. Compare the harmonic amplitude level value with the set warning limit value.
5. Continue to inject the same level of harmonics level until the THDALM signal appears and note down the time from THDWRN set to THDALM set.
6. Compare the noted time value with the set time limit value of alarm.

Verifying the warning and alarm time limit of TDD, WrnLimitTDD and tDelayAlmTDD

1. Supply the IED with current at rated value.
2. Apply harmonics at the numerical multiple of fundamental frequency along with the injected current signal.
3. Slowly increase the harmonic amplitudes until the TDDWRN signal appears.
4. Compare the harmonic amplitude level value with the set warning limit value.
5. Continue to inject the same level of harmonics level until the TDDALM signal appears and note down the time from TDDWRN set to TDDALM set.
6. Compare the noted time value with the set time limit value of alarm.

Verifying the warning and alarm time limit of individual harmonic distortions IHD, WrnLimit#HD and tDelayAlm#HD (where # = 2nd, 3rd...5th)

1. Supply the IED with current at rated value.
2. Apply 2nd order harmonic along with injected current signal.
3. Slowly increase the harmonic amplitude until the 2NDHDWRN signal appears.
4. Compare the harmonic amplitude level value with the set warning limit value.
5. Continue to inject the same level of harmonics level until the 2NDHDALM signal appears and note down the time from 2NDHDWRN signal set to 2NDHDALM signal set.
6. Compare the noted time value with the set time limit value of alarm.
7. Repeat Steps 2 to 6 for the harmonics until 5th order.
11.13.9.2 Completing the test

Continue to test another function or end the test by changing the TestMode setting to Off. Restore connections and settings to their original values, if they were changed for testing purposes.

11.13.10 Voltage harmonic monitoring VHMMHAI(VTHD)

Prepare the IED for verification of settings outlined in Section "Preparing the IED to verify settings".

11.13.10.1 Verifying the signals and settings

The voltage can be injected using common test equipment.

Verifying the warning and alarm time limit of THD, WrnLimitTHD and tDelayAlmTHD

1. Supply the IED with voltage at rated value.
2. Apply harmonics at the numerical multiple of fundamental frequency along with the injected voltage signal.
3. Slowly increase the harmonic amplitudes until the THDWRN signal appears.
4. Compare the harmonic amplitude level value with the set warning limit value.
5. Continue to inject the same level of harmonics level until the THDALM signal appears and note down the time from THDWRN set to THDALM set.
6. Compare the noted time value with the set time limit value of alarm.

Verifying the warning and alarm time limit of individual harmonic distortions IHD, WrnLimit#HD and tDelayAlm#HD (where # = 2nd, 3rd…5th)

1. Supply the IED with voltage at rated value.
2. Apply 2nd order harmonic along with injected voltage signal.
3. Slowly increase the harmonic amplitude until the 2NDHDWRN signal appears.
4. Compare the harmonic amplitude level value with the set warning limit value.
5. Continue to inject the same level of harmonics level until the 2NDHDAHM signal appears and note down the time from 2NDHDWRN signal set to 2NDHDAHM signal set.
6. Compare the noted time value with the set time limit value of alarm.
7. Repeat Steps 2 to 6 for the harmonics until 5th order.

11.13.10.2 Completing the test

Continue to test another function or end the test by changing the TestMode setting to Off. Restore connections and settings to their original values, if they were changed for testing purposes.
11.14 Metering

11.14.1 Pulse-counter logic PCFCNT

The test of the Pulse-counter logic function PCFCNT requires the Parameter Setting tool in PCM600 or an appropriate connection to the local HMI with the necessary functionality. A known number of pulses with different frequencies are connected to the pulse counter input. The test should be performed with settings Operation = Enable or Operation = Disable and the function blocked or unblocked. The pulse counter value is then checked in PCM600 or on the local HMI.

11.14.2 Function for energy calculation and demand handling ETPMMTR

Prepare the IED for verification of settings as outlined in section "Overview " and section "Preparing for test" in this chapter.

11.14.2.1 Verifying the settings

Common test equipment can be used to determine the injection of current and voltage and time measurement.

Verification of EAFACC & ERFACC output

1. Connect the test set for injection of three-phase currents and three-phase voltage to the appropriate current and voltage terminals of the IED.
2. Ensure the instantaneous values of active and reactive power from CVMMXN function block are connected to ETPMMTR function block active and reactive power inputs.
3. Enable the EnaAcc setting and set tEnergy as 1 minute.
4. Activate the STARTACC input and supply the IED with three-phase currents and voltages at their rated value.
5. Check that the ACCINPRG signal appears continuously.
6. Note the EAFACC and ERFACC value after 1 minute and compare it with calculated energy value.
7. Similarly check after each 1 minute whether the calculated integrated energy value and EAFACC and ERFACC outputs are matching.
8. After some time (multiple of minute) remove the current and voltage input from CVMMXN function block.
9. Check the EAFACC and ERFACC output in the next 1 minute cycle for the retaining the same value.
10. Activate STOPACC input after some time and supply the IED with same current and voltage.
11. Check that the ACCINPRG signal disappears immediately and EAFACC and ERFACC outputs also stop updating.
12. Similarly the testing can be done for EAFACC and ERFACC outputs by changing the power inputs directions through direction settings.

Verification of MAXPAFD & MAXPRFD outputs

1. Repeat the above test steps 1 to 2.
2. Set tEnergy setting as 1 minute and supply the IED with three-phase currents and voltages at their rated value till 1 minute.
3. Check the MAXPAFD and MAXPRFD outputs after 1 minute and compare it with last 1 minute average power values.
4. Increase either three-phase current or voltage above the last 1 minute value.
5. After 1 minute check the MAXPAFD and MAXPRFD whether it is showing the last 1 minute average power value as maximum.
6. Next 1 minute cycle reduce the current or voltage below previous value.
7. Check after 1 minute whether the MAXPAFD and MAXPRFD outputs are retaining the old maximum value.
8. Similarly the testing can be done for MAXPAFD and MAXPRFD outputs by changing the power inputs directions through direction settings.

**Verification of EAFALM & ERFALM outputs**

1. Repeat the above test steps 1 to 2.
2. Set \textit{tEnergy} setting as 1 minute and supply the IED with three-phase currents and voltages at their rated value till 1 minute.
3. Ensure that the active and reactive energy values are less than the \textit{EALim} and \textit{ERlim} setting default values respectively.
4. Check that EAFALM and ERFALM are low.
5. Increase the supply currents or voltage in next 1 minute cycle such that the active or reactive energy values are greater than the \textit{EALim} and \textit{ERlim} setting default values respectively.
6. Check that EAFALM and ERFALM are high after 1 minute.
7. Similarly the testing can be done for EARALM and ERRALM outputs by changing the power inputs directions through direction settings.

11.14.2.2 **Completing the test**

continue to test another function or end the test by changing the \textit{TESTMODE} setting to \textit{Disabled}. Restore connections and settings to their original values, if they were changed for testing purposes.

11.15 **Station communication**

11.15.1 **Multiple command and transmit MULTICMDRCV / MULTICMDSND**

The multiple command and transmit function (MULTICMDRCV / MULTICMDSND) is only applicable for horizontal communication.

Test of the multiple command function block and multiple transmit is recommended to be performed in a system, that is, either in a complete delivery system as an acceptance test (FAT/SAT) or as parts of that system, because the command function blocks are connected in a delivery-specific way between bays and the station level and transmit.

Command and transmit function blocks included in the operation of different built-in functions must be tested at the same time as their corresponding functions.

11.16 **Remote communication**

11.16.1 **Binary signal transfer**

Prepare the IED for verification of settings as outlined in section "Preparing the IED to verify settings".
To perform a test of the binary signal transfer functions, the hardware (LDCM) and binary input and output signals to transfer must be configured as required by the application.

There are two types of internal self-supervision of the binary signal transfer:

- The I/O-circuit board is supervised as an I/O module. For example it generates FAIL if the board is not inserted. I/O-modules not configured are not supervised.
- The communication is supervised and the signal COMFAIL is generated if a communication error is detected.

Status for inputs and outputs as well as self-supervision status are available from the local HMI under

- Self-supervision status: **Main menu/Diagnostics/Internal events**
- Status for inputs and outputs: **Main menu/Test/Function status**, browse to the function group of interest.
- Remote communication related signals: **Main menu/Test/Function status/Communication/Remote communication**

Test the correct functionality by simulating different kind of faults. Also check that sent and received data is correctly transmitted and read.

A test connection is shown in figure 58. A binary input signal (BI) at End1 is configured to be transferred through the communication link to End2. At End2 the received signal is configured to control a binary output (BO). Check at End2 that the BI signal is received and the BO operates.

Repeat the test for all the signals configured to be transmitted over the communication link.
11.17 Basic IED functions

11.17.1 Parameter setting group handling SETGRPS

Prepare the IED for verification of settings as outlined in section "Preparing for test" in this chapter.

11.17.1.1 Verifying the settings

1. Check the configuration of binary inputs that control the selection of the active setting group.
2. Browse to the ActiveGroup menu to achieve information about the active setting group. The ActiveGroup menu is located on the local HMI under Main menu/Test/Function status/Setting groups/ActiveGroup. The ActiveGroup menu is located on the PCM600 under Main menu/Test/Function status/Setting groups/ActiveGroup.
3. Connect the appropriate dc voltage to the corresponding binary input of the IED and observe the information presented on the local HMI. The displayed information must always correspond to the activated input.
4. Check that the corresponding output indicates the active group.
Operating procedures for the PC aided methods of changing the active setting groups are described in the corresponding PCM600 documents and instructions for the operators within the SCS are included in the SCS documentation.

11.17.1.2 Completing the test

continue to test another function or end the test by changing the TESTMODE setting to Disabled. Restore connections and settings to their original values, if they were changed for testing purposes.

11.18 Exit test mode

The following procedure is used to return to normal operation.

After exiting the IED test mode, make sure that the MU is returned to normal mode.

1. Navigate to the test mode folder.
2. Change the Enable setting to Disable. Press the 'E' key and the left arrow key.
3. Answer YES, press the 'E' key and exit the menus.
Section 12 Primary injection testing

Whenever it becomes necessary to work on primary equipment, it is essential that all the necessary switching, locking, grounding and safety procedures are observed and obeyed in a rigid and formalized manner. Operating and testing procedures should be strictly followed in order to avoid exposure to live equipment.

A test with primary current through the protected zone is usually a final check that the current circuits are correctly connected to the IED protection scheme. It is important to have an appropriate source, which is able to inject sufficient current in the primary circuit in order to distinguish between noise and real injected current. Therefore it is recommended that the injection current should be at least 10% of rated CT primary current.

12.1 Transformer Voltage Control ATCC

12.1.1 Load drop compensation function, LDC

The load drop compensation function can be tested directly with operational currents, in other words, with the power transformer in service and loaded.

When the system is carrying load there is a difference between the busbar voltage (VB) and the voltage at the load point (VL). This difference is load dependent and can be compensated by the ATCC function.

The load current is fed into the ATCC function where parameters corresponding to the line data for resistance and inductance are set. The voltage drop calculated by the LDC is proportional to the voltage drop in the system up to the load point.

In the IED, this voltage is subtracted from the measured busbar voltage (VB) and the result, corresponding to the voltage at the load point (UL) is presented to the ATCC function for the purposes of voltage regulation. This voltage will be lower (if resistive or inductive load current is applied) than the Vset voltage and ATCC increases the voltage in order to achieve the correct system voltage at the load point.

1. Confirm the PST settings for \textit{Rline} and \textit{Xline}.
2. Open the voltage control display on the LHMI in \textit{Main Menu/Control/Commands/TransformerVoltageControl (ATCC, 90)}. 

3. In this view, check the following settings:

3.1. Check that Control Mode is set to Manual.
3.2. Operate the tap changer so that the Load Voltage corresponds to the Voltage Set Point.
3.3. Check that neither the Raise or the Lower command are operating. Voltage Deviation should be less than 100%.
3.4. Compare the values of Bus Voltage and Load Voltage.

UL can be obtained by a vector calculation where IL is the load current and values for RL and XL are the settings given in primary system ohms.

For inductive load, the Bus Voltage should be higher than the Load Voltage. If not, current circuits are incorrect and must be reversed. Incorrect current polarity may be an issue with physical wiring or the current direction convention selected for the CT analogue input as part of the setting in the PST setting.

## 12.1.2 Voltage control of Parallel Transformers

In parallel operation, each transformer protection IED must be connected to the station communication bus for data exchange. The following procedure assumes that the necessary pre-configuration in the Parameter Setting tool and the Signal Matrix tool has been performed and
tested to enable data exchange between IEDs or between instances of voltage control in the same IED.

For parallel operation, it is also recommended to confirm the setting for the general parallel arrangement of transformers in Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR8ATCC:x/ParCtrl. General parallel arrangement of transformers are defined by setting TnRXOP to On or Off. The following rules are applicable for the settings of T1RXOP – T8RXOP.

• In each instance of TR8ATCC, set all TnRXOP parameters corresponding to the connected IEDs to Enabled except the parameter corresponding to the IED itself. For example, do not set T1RXOP to Enabled in IED T1 or T2RXOP to Enabled in IED T2.

For example, if IEDs T1, T2 and T3 are connected,

• In instance 1 of TR8ATCC (90), set T2RXOP and T3RXOP to Enabled.
• In instance 2 of TR8ATCC (90), set T1RXOP and T3RXOP to Enabled.
• In instance 3 of TR8ATCC (90), set T1RXOP and T2RXOP to Enabled.

12.1.3 Minimum Circulating Current (MCC) method

Voltage control of parallel transformers with the circulating current method means minimizing the circulating current at a given voltage target value. The busbar voltage (VB) is measured individually for each transformer in the parallel group by its associated TR8ATCC function. These measured values are then exchanged between the transformers, and in each TR8ATCC block, the mean value of all VB values is calculated. The resulting value VBobm is then used in each IED instead of VB for the voltage regulation.

1. Check the overcurrent blocking level (Iblock) in Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR8ATCC:x/TCCtrl/Iblock.
2. Check that the circulating current blocking (OperCCBlock) is set On, and the check the correct settings for circulating current limit (CircCurLimit) in Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR8ATCC:x/ParCtrl/CircCurLimit.
3. Connect all transformers in the parallel group to the same busbar on the secondary side.
4. Open the test display for Transformer Voltage Control on the LHMI in Main menu/Test/Function status/Control/TransformerVoltageControl(ATCC,90).

5. Manually execute Raise commands to step up the tap changer for transformer T1 to two steps above the setting for the other transformers in the parallel group.

6. Check that the value of IPRIM is below the overcurrent blocking level (Iblock).

7. Check that the value of ICIRCUL is below the circulating current limit (CircCurLimit).

8. Set the control mode to Automatic for all transformers.

9. For transformer T1, adjust the parameter Comp on the local HMI in Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR8ATCC:x/ParCtrl/Comp so that the LOWER output is activated due to circulating current.

   Comp is a setting for circulating current Compensating Factor, and it is effectively a multiplier value to change the sensitivity of the voltage regulation function to measured values of circulating current. A nominal value of 200 for Comp should be appropriate to achieve sensitive voltage regulation. Smaller values may lead to a normal state where tap changers for parallel transformers are well out of step, and values significantly higher will cause over-sensitivity in the voltage control function and tap changer hunting behavior. It is an important outcome of the testing process that the compensating factor is checked for each transformer to ensure sensitive but stable operation for circulating currents.

   For example, if there are three transformers connected in parallel, and the tap changer of transformer T1 is two steps over the tap changer of T2 and T3, the circulating current detected by the VCTR for T1 will be the sum (with opposite sign) of the current measured at T2 and T3. The currents measured at T2 and T3 will ideally be about the same values. If the voltage is close to the upper limit of the VDeadband, the tap changer of T1 will try to decrease the controlled voltage. In the opposite case, that is, the voltage is close to the lower limit of VDeadband, the tap changer at T1 will not try to decrease the controlled voltage. The tap changer for T2 and T3 will not operate due to the fact that the detected circulating current will be half of the current detected at T1. The setting of the parameter Comp then might need to be increased a little. At least one tap changer step difference between the different transformers should be allowed in order to avoid the tap changers to operate too often. If the allowed difference is, for example, two steps, the tap changer shall be stepped up three steps when setting the parameter Comp.

10. Set the Comp parameter for T2 and T3 in the same manner as for T1. According to the described procedure when the tap changer of one transformer is two steps above the others, it shall automatically step down. When there are only two transformers in the group either shall one step down or the other step up depending on the voltage level at the ATCC.

12.1.4 Master Follower (MF) method

The Master Follower method requires a Master to be nominated in a parallel group. It is responsible for measuring secondary bus voltage and executing commands to raise and lower tap changers, which are repeated by Follower transformers in the group.

1. Confirm the voltage blocking levels OVPartBK and UVPartBK in Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR8ATCC:x/Voltage/Umax,Umin.

2. Check that MFMode is set to Follow Tap or Follow Cmd in Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR8ATCC:x/MFMode.

   If this setting is Follow Tap, all Follower transformers shall match the actual tap setting of the Master, while Follow Cmd requires that Follower transformers follow the RAISE and LOWER commands executed by the Master.

3. Confirm the maximum difference in tap positions for parallel transformers. The number of taps is determined by the setting in the Parameter Setting tool under Main menu/Settings/IED Settings/Control/TransformerVoltageControl(ATCC,90)/TR8ATCC:x/ParCtrl/MFPosDiffLim.
4. Connect all transformers in the parallel group to the same busbar on both the primary and secondary sides.

5. Set the control mode to Manual for each transformer in the parallel control group.

6. Open the test display for Transformer Voltage Control on the LHMI in Main menu/Test/Function status/Control/TransformerVoltageControl (ATCC, 90).

```
| T8PC | 0 |
| RAISE | 0 |
| LOWER | 0 |
| BUSVOLT | 35.428 kV |
| VOLTDEV | 12.196 % |
| TRLDCURR | 998.996 A |
| USETOUT | 33.000 kV |
| ULOD | 33.048 kV |
| P | 53.089 MW |
| Q | 30.651 MVAR |
| IPRIM | 300.002 A |
| CCARVolt | 33.048 kV |
| USETPAR | 0.000 kV |
| ICIRCUL | 0.000 A |
```

7. Manually execute Raise commands to step up the tap changer one step for all transformers in the parallel group.

8. Check that the value of BUSVOLT is below overvoltage blocking level OVPartBK and above the undervoltage blocking level UVPartBK.

9. Set the control mode to Auto for each transformer in the parallel control group. The Master in the parallel group will issue a Lower command after time delay t1, which will also be repeated by the Follower transformers in the group.

10. Set Transformer 2 to Master of the parallel group and T1 to Follower (in that order) and repeat steps 7 to 10. Undertake this same test by setting each Transformer in turn to Master.

11. Note that voltage regulation will be mutually blocked if no transformer is set to Master in the parallel group. To confirm this function, set T1 as Master and the other transformers in the parallel group as Follower. Without allocating a new Master, set T1 to Follower and note the Automatic block state on the local HMI for all parallel transformers.

12. Restore T1 to Master of the parallel transformer group and ensure that control mode for each transformer is Automatic.

**12.1.5 Completing the test**

After the test is completed and relevant disturbance records are saved to document the test, go to Main menu/Clear on the local HMI and clear all LEDs, the process eventlist and disturbances from the IED.
Section 13 Checking the directionality

13.1 Overview

Before starting this process, all individual devices that are involved in the fault clearance process of the protected object must have been individually tested and must be set in operation. The circuit breaker must be ready for an open-close-open cycle.

The directional test is performed when the protected object is energized and a certain amount of load current is available. It is also necessary to know the flow of the load current (import or export, i.e. forward or reverse) by help of the indication from an external instrument (energy-meter, or SCADA information).

The design of the test procedure depends on the type of protection function to be tested. Some items that can be used as guidelines are the following.

13.2 Testing the directionality of the distance protection

The test is performed by looking at the information given by the High speed distance protection ZMFPDIS, ZMFCPDIS or the directional function ZDRDIR or ZDMRDIR whichever is used.

Procedure:

1. Make sure that all control and protection functions that belong to the object that are going to be energized have been tested and are set to be in operation
2. Make sure that the primary load current fulfills the following conditions (by using an external equipment):
   - The magnitude of the primary load current must be higher than the minimum operating current set for the directional elements in the IED. In case of default settings this means:
     - load current > 5% of base current
     - Otherwise the settings $I_{\text{MinOpPG}}$ and $I_{\text{MinOpPP}}$ for ZDRDIR or ZDMRDIR are available under the HMI menu: Main menu/Settings/IED Settings/Impedance protection/DirectionalImpedance
   - The primary load impedance must have an angle (PHI) between the setting angles for the directional lines. In case of default settings this means:
     - for forward (exported) load: $-15 \, \text{deg} < \Phi < 115 \, \text{deg}$
     - for reverse (imported) load: $165 \, \text{deg} < \Phi < 295 \, \text{deg}$
   - The settings for forward load: $-\text{ArgDir} < \Phi < \text{ArgNegRes}$ and the settings for reverse load: $180 \, \text{deg} - \text{ArgDir} < \Phi < 180 \, \text{deg} + \text{ArgNegRes}$ included in the directional functions ZDRDIR or ZDMRDIR are available under the HMI menu: Main menu/Settings/IED Settings/Impedance protection/DirectionalImpedance
3. The directionality of the load current is shown by the High speed distance protection ZMFPDIS or ZMFCPDIS under the HMI menu: Main menu/Test/Function status/Impedance protection/HighSpeedDistance or by the directional function ZDRDIR or ZDMRDIR and it is
available under the HMI menu: `Main menu/Test/Function status/Impedance protection/Directional impedance`
The following will be shown if the load current flows in forward (exporting) direction:
- L1Dir = Forward
- L2Dir = Forward
- L3Dir = Forward

The following will be shown if the load current flows in the reverse direction (importing):
- L1Dir = Reverse
- L2Dir = Reverse
- L3Dir = Reverse

Compare this result with the information given by the external equipment; it must be the same. If the direction of the three phases is not the same, this is a sign of incorrect connection of the voltage or current transformers serving the distance protection function. It is also possible that there is a wrong setting for the earthing point for one or more of the CTs serving distance protection (the setting name is: `CTStarPoint` available under the HMI menu: `Main menu/Configuration/Analog modules`).

If the directional function shows forward when it should show reverse (or vice-versa) for all the three phases, this probably means a wrong connection of CTs and/or VTs serving the distance protection, or it can mean a wrong setting of earthing point (the setting name is: `CTStarPoint`) for all the three CTs, or it could mean a wrong setting for the pre-processing blocks (`3PhaseAnalogGroup` under the HMI menu: `Main menu/Configuration/Analog modules`) connected to the CTs/VTs serving the distance protection (verify that no wrong negation has been set; the setting name is: `Negation`).

If the directional function shows “No direction” for all the three phases it can mean that the load current is below the minimum operating current or that the load impedance has an angle which is outside the above given valid angles for determining forward or reverse direction.

If the directional function shows “No direction” for only some of the three phases, this probably means a wrong CTs/VTs connection.

4. The measured impedance information is available under the same menu. These values are not affected by the minimum operating current setting of ZDRDIR or ZDMRDIR and the measured values are shown any time the load current is higher than 3% of the base current:
- L1R
- L1X
- L2R
- L2X
- L3R
- L3X

The measured impedance information can still be used to determine the direction of the load. A positive resistance measured in all phases indicates a forward (exporting) resistive load (active power), while a negative sign indicates a reverse (importing) resistive load (active power). Usually it is enough to look at the resistive values to get information of the load direction, that must anyway be compared with the indication given by external equipment measuring the same power flow.
Section 14    Commissioning and maintenance of the fault clearing system

14.1    Commissioning tests

During commissioning all protection functions shall be verified with the setting values used at each plant. The commissioning tests must include verification of all circuits by highlighting the circuit diagrams and the configuration diagrams for the used functions.

Further, the settings for protection functions are tested and recorded carefully as outlined for the future periodic maintenance tests.

The final testing includes primary verification of all directional functions where load currents is checked on the local HMI and in PCM600. The magnitudes and angles of all currents and voltages should be checked and the symmetry verified.

Directional functions have information about the measured direction and, for example, measured impedance. These values must be checked and verified as correct with the export or import of power available.

Finally, final trip tests must be performed. This involves activation of protection functions or tripping outputs with the circuit breaker closed and the tripping of the breaker verified. When several breakers are involved, each breaker must be checked individually and it must be verified that the other involved breakers are not tripped at the same time.

14.2    Periodic maintenance tests

The periodicity of all tests depends on several factors, for example the importance of the installation, environmental conditions, simple or complex equipment, static or electromechanical IEDs, and so on.

The normal maintenance practices of the user should be followed. However, ABB's recommendation is as follows:

Every second to third year

• Visual inspection of all equipment.
• Removal of dust on ventilation louvres and IEDs if necessary.
• Periodic maintenance test for protection IEDs of object where no redundant protections are provided.

Every four to six years

• Periodic maintenance test for protection IEDs of objects with redundant protection system.
First maintenance test should always be carried out after the first half year of service.

When protection IEDs are combined with built-in control, the test interval can be increased drastically, up to for instance 15 years, because the IED continuously reads service values, operates the breakers, and so on.

14.2.1 Visual inspection

Prior to testing, the protection IEDs should be inspected to detect any visible damage that may have occurred (for example, dirt or moisture deposits, overheating).

Make sure that all IEDs are equipped with covers.

14.2.2 Maintenance tests

To be made after the first half year of service, then with the cycle as proposed above and after any suspected maloperation or change of the IED setting.

Testing of protection IEDs shall preferably be made with the primary circuit de-energized. The IED cannot protect the circuit during testing. Trained personnel may test one IED at a time on live circuits where redundant protection is installed and de-energization of the primary circuit is not allowed.

ABB protection IEDs are preferably tested by aid of components from the COMBITEST testing system or FT test systems described in information B03-9510 E. Main components are RTXP 8/18/24 test switch usually located to the left in each protection IED and RTXH 8/18/24 test handle, which is inserted in test switch at secondary testing. All necessary operations such as opening of trip circuits, short-circuiting of current circuits and opening of voltage circuits are automatically performed in the right order to allow for simple and safe secondary testing even with the object in service.

Important components of FT test system are FT1, FTx, FT19, FT19RS, FR19RX switches and assemblies as well as FT-1 test plug.

14.2.2.1 Preparation

Before starting maintenance testing, the test engineers should scrutinize applicable circuit diagrams and have the following documentation available:

- Test instructions for protection IEDs to be tested
- Test records from previous commissioning and maintenance tests
- List of valid settings
- Blank test records to fill in measured values
14.2.2.2 Recording

It is of utmost importance to carefully record the test results. Special test sheets covering the frequency of test, date of test and achieved test values should be used. IED setting list and protocols from previous tests should be available and all results should be compared for differences. At component failures, spare equipment is used and set to the requested value. A note of the exchange is made and the new measured values are recorded. Test records for several years of testing should be stored in a common file for a station, or a part of a station, to give a simple overview of the period of testing and achieved test values. These test records are valuable when analysis of service disturbances shall be done.

14.2.2.3 Secondary injection

The periodic maintenance test is done by secondary injection from a portable test set. Each protection shall be tested according to the secondary injection test information for the specific protection IED. Only the setting values adopted shall be checked for each protection function. If the discrepancy between obtained value and requested set value is too big the setting should be adjusted, the new value recorded and a note should be made in the test record.

14.2.2.4 Alarm test

When inserting the test handle of RTXP or using FT plugs, the alarm and event signalling is normally blocked. This is done in the IED by setting the event reporting to Disabled during the test. This can be done when the test handle is inserted or the IED is set to test mode from the local HMI. At the end of the secondary injection test it should be checked that the event and alarm signalling is correct by activating the events and performing some selected tests.

14.2.2.5 Self supervision check

Once secondary testing has been completed, it should be checked that no self-supervision signals are activated continuously or sporadically. Especially check the time synchronization system, GPS or other, and communication signals, both station communication and remote communication.

14.2.2.6 Trip circuit check

When the protection IED undergoes an operational check, a tripping pulse is normally obtained on one or more of the output contacts and preferably on the test switch. The healthy circuit is of utmost importance for the protection operation. If the circuit is not provided with a continuous trip-circuit supervision, it is possible to check that circuit is really closed when the test-plug handle has been removed by using a high-ohmic voltmeter and measuring between the plus and the trip output on the panel. The measurement is then done through the trip coil of the circuit breaker and therefore the complete trip circuit is checked.

Note that the breaker must be closed.
Please observe that the test system does not provide built-in security during this test. If the instrument should be set on Amp instead of Volts, the circuit breaker naturally is tripped, therefore, great care is necessary.

Trip circuit from trip IEDs to circuit breaker is often supervised by trip-circuit supervision. It can then be checked that a circuit is healthy by opening tripping output terminals in the cubicle. When the terminal is opened, an alarm shall be achieved on the signal system after a delay of some seconds.

Remember to close the circuit directly after the test and tighten the terminal carefully.

**14.2.2.7 Measurement of service currents**

After a maintenance test it is recommended to measure the service currents and service voltages recorded by the protection IED. The service values are checked on the local HMI or in PCM600. Ensure that the correct values and angles between voltages and currents are recorded. Also check the direction of directional functions such as Distance and directional overcurrent functions.

For transformer differential protection, the achieved differential current value is dependent on the tap changer position and can vary between less than 1% up to perhaps 10% of rated current. For line differential functions, the capacitive charging currents can normally be recorded as a differential current.

The zero-sequence current to ground-fault protection IEDs should be measured. The current amounts normally very small but normally it is possible to see if the current circuit is “alive”.

The neutral-point voltage to an ground-fault protection IED is checked. The voltage is normally 0.1 to 1V secondary. However, voltage can be considerably higher due to harmonics. Normally a CVT secondary can have around 2.5 - 3% third-harmonic voltage.

**14.2.2.8 Restoring**

Maintenance is very important to improve the availability of the protection system by detecting failures before the protection is required to operate. There is however little point in testing healthy equipment and then putting it back into service with an open terminal, with a removed fuse or open miniature circuit breaker with an open connection, wrong setting, and so on.

Thus a list should be prepared of all items disturbed during test so that all can be put back into service quickly and without overlooking something. It should be put back into service item by item and signed by the responsible engineer.
Section 15  Troubleshooting

15.1 Checking the self supervision signals

15.1.1 Checking the self supervision function

15.1.1.1 Determine the cause of an internal failure

This procedure describes how to navigate the menus in order to find the cause of an internal failure when indicated by the flashing green LED on the HMI module.

Procedure

1. Display the general diagnostics menu.
   Navigate the menus to:
   Diagnostics/IED status/General
2. Scroll the supervision values to identify the reason for the failure.
   Use the arrow buttons to scroll between values.

15.1.2 Self supervision HMI data

15.1.2.1 General IED status

The following table shows the general IED status signals.

Table 42: Signals from the General menu in the diagnostics tree.

<table>
<thead>
<tr>
<th>Indicated result</th>
<th>Possible reason</th>
<th>Proposed action</th>
</tr>
</thead>
<tbody>
<tr>
<td>Internal fail Off</td>
<td>No problem detected.</td>
<td>None.</td>
</tr>
<tr>
<td>Internal fail On</td>
<td>A failure has occurred.</td>
<td>Check the rest of the indicated results to find the fault.</td>
</tr>
<tr>
<td>Internal warning Off</td>
<td>No problem detected.</td>
<td>None.</td>
</tr>
<tr>
<td>Internal warning On</td>
<td>A warning has been issued.</td>
<td>Check the rest of the indicated results to find the fault.</td>
</tr>
<tr>
<td>Time synch Ready</td>
<td>No problem detected.</td>
<td>None.</td>
</tr>
<tr>
<td>Time synch Fail</td>
<td>No time synchronization.</td>
<td>Check the synchronization source for problems. If the problem persists, contact your ABB representative for service.</td>
</tr>
<tr>
<td>Real time clock Ready</td>
<td>No problem detected.</td>
<td>None.</td>
</tr>
<tr>
<td>Real time clock Fail</td>
<td>The real time clock has been reset.</td>
<td>Set the clock.</td>
</tr>
<tr>
<td>ADC-module OK</td>
<td>No problem detected.</td>
<td>None.</td>
</tr>
<tr>
<td>ADC-module Fail</td>
<td>The AD conversion module has failed.</td>
<td>Contact your ABB representative for service.</td>
</tr>
</tbody>
</table>

Table continues on next page
## 15.2 Fault tracing

### 15.2.1 Internal fault indications

If an internal fault has occurred, the local HMI displays information under **Main menu/Diagnostics/IED status/General**

Under the Diagnostics menus, indications of a possible internal failure (serious fault) or internal warning (minor problem) are listed.

Indications regarding the faulty unit are outlined in table 43.

**Table 43: Self-supervision signals on the local HMI**

<table>
<thead>
<tr>
<th>HMI Signal Name:</th>
<th>Status</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INT Fail</td>
<td>OFF / ON</td>
<td>This signal will be active if one or more of the following internal signals are active; INT--LMERROR, INT--WATCHDOG, INT--APERROR, INT--RTEERROR, or any of the HW dependent signals</td>
</tr>
<tr>
<td>INT Warning</td>
<td>OFF / ON</td>
<td>This signal will be active if one or more of the following internal signals are active; INT--RTCERROR, INT--IEC61850ERROR, INT--TIMESYNCHERROR</td>
</tr>
<tr>
<td>ADMnn</td>
<td>READY / FAIL</td>
<td>Analog input module n failed. Signal activation will reset the IED</td>
</tr>
<tr>
<td>BIMnn</td>
<td>READY / FAIL</td>
<td>BIM error. Binary input module Error status. Signal activation will reset the IED</td>
</tr>
<tr>
<td>BOMn</td>
<td>READY / FAIL</td>
<td>BOM error. Binary output module Error status.</td>
</tr>
<tr>
<td>IOMn</td>
<td>READY / FAIL</td>
<td>IOM-error. Input/Output Module Error status.</td>
</tr>
<tr>
<td>MIMn</td>
<td>READY / FAIL</td>
<td>mA input module MIM1 failed. Signal activation will reset the IED</td>
</tr>
<tr>
<td>RTC</td>
<td>READY / FAIL</td>
<td>This signal will be active when there is a hardware error with the real time clock.</td>
</tr>
<tr>
<td>HMI Signal Name</td>
<td>Status</td>
<td>Description</td>
</tr>
<tr>
<td>-----------------</td>
<td>----------</td>
<td>-----------------------------------------------------------------------------</td>
</tr>
<tr>
<td>Time Sync</td>
<td>READY / FAIL</td>
<td>This signal will be active when the source of the time synchronization is lost, or when the time system has to make a time reset.</td>
</tr>
<tr>
<td>Application</td>
<td>READY / FAIL</td>
<td>This signal will be active if one or more of the application threads are not in the state that Runtime Engine expects. The states can be CREATED, INITIALIZED, RUNNING, etc.</td>
</tr>
<tr>
<td>RTE</td>
<td>READY / FAIL</td>
<td>This signal will be active if the Runtime Engine failed to do some actions with the application threads. The actions can be loading of settings or parameters for components, changing of setting groups, loading or unloading of application threads.</td>
</tr>
<tr>
<td>IEC61850</td>
<td>READY / FAIL</td>
<td>This signal will be active if the IEC 61850 stack did not succeed in some actions like reading IEC 61850 configuration, startup etc.</td>
</tr>
<tr>
<td>LMD</td>
<td>READY / FAIL</td>
<td>LON network interface, MIP/DPS, is in an unrecoverable error state.</td>
</tr>
<tr>
<td>LDCMxxx</td>
<td>READY / FAIL</td>
<td>Line Differential Communication Error status</td>
</tr>
<tr>
<td>OEM</td>
<td>READY / FAIL</td>
<td>Optical Ethernet Module error status</td>
</tr>
</tbody>
</table>

Also the internal signals, such as INT--FAIL and INT--WARNING can be connected to binary output contacts for signalling to a control room.

In the IED Status - Information, the present information from the self-supervision function can be viewed. Indications of failure or warnings for each hardware module are provided, as well as information about the external time synchronization and the internal clock. All according to table 43. Loss of time synchronization can be considered as a warning only. The IED has full functionality without time synchronization.

When settings are changed in the IED, the protection and control applications restart in order to take effect of the changes. During restart, internal events get generated and Runtime App error will be displayed. These events are only indications and will be for short duration during the restart.

**IED will not be operational during applications restart.**

### 15.2.2 Using front-connected PC

When an internal fault has occurred, extensive information about the fault can be retrieved from the list of internal events available in the SMS part:

**TRM-STAT TermStatus - Internal Events**
The list of internal events provides valuable information, which can be used during commissioning and fault tracing.

The internal events are time tagged with a resolution of 1ms and stored in a list. The list can store up to 40 events. The list is based on the FIFO principle, when it is full, the oldest event is overwritten. The list cannot be cleared and its content cannot be erased.

The internal events in this list not only refer to faults in the IED, but also to other activities, such as change of settings, clearing of disturbance reports, and loss of external time synchronization.

The information can only be retrieved from the Parameter Setting software package. The PC can be connected either to the port at the front or at the rear of the IED.

These events are logged as internal events.

### Table 44: Events available for the internal event list in the IED

<table>
<thead>
<tr>
<th>Event message:</th>
<th>Description</th>
<th>Generating signal:</th>
</tr>
</thead>
<tbody>
<tr>
<td>INT--FAIL Off</td>
<td>Internal fail status</td>
<td>INT--FAIL (reset event)</td>
</tr>
<tr>
<td>INT--FAIL</td>
<td></td>
<td>INT--FAIL (set event)</td>
</tr>
<tr>
<td>INT--WARNING Off</td>
<td>Internal warning status</td>
<td>INT--WARNING (reset event)</td>
</tr>
<tr>
<td>INT--WARNING</td>
<td></td>
<td>INT--WARNING (set event)</td>
</tr>
<tr>
<td>IOG--Error Off</td>
<td>In/Out module No. n status</td>
<td>IOG--Error (reset event)</td>
</tr>
<tr>
<td>IOG--Error</td>
<td></td>
<td>IOG--Error (set event)</td>
</tr>
<tr>
<td>ADMn-Error Off</td>
<td>Analog/Digital module No. n status</td>
<td>ADMn-Error (reset event)</td>
</tr>
<tr>
<td>ADMn-Error</td>
<td></td>
<td>ADMn-Error (set event)</td>
</tr>
<tr>
<td>MIM1-Error Off</td>
<td>mA-input module status</td>
<td>MIM1-Error (reset event)</td>
</tr>
<tr>
<td>MIM1-Error</td>
<td></td>
<td>MIM1-Error (set event)</td>
</tr>
<tr>
<td>INT--RTC Off</td>
<td>Real Time Clock (RTC) status</td>
<td>INT--RTC (reset event)</td>
</tr>
<tr>
<td>INT--RTC</td>
<td></td>
<td>INT--RTC (set event)</td>
</tr>
<tr>
<td>INT--TSYNC Off</td>
<td>External time synchronization status</td>
<td>INT--TSYNC (reset event)</td>
</tr>
<tr>
<td>INT--TSYNC</td>
<td></td>
<td>INT--TSYNC (set event)</td>
</tr>
<tr>
<td>INT--SETCHGD</td>
<td>Any settings in IED changed</td>
<td></td>
</tr>
<tr>
<td>DRPC-CLEARED</td>
<td>All disturbances in Disturbance report cleared</td>
<td></td>
</tr>
</tbody>
</table>

The events in the internal event list are time tagged with a resolution of 1ms.

This means that, when using the PC for fault tracing, it provides information on the:

- Module that should be changed.
- Sequence of faults, if more than one unit is faulty.
- Exact time when the fault occurred.
15.2.3 Diagnosing the IED status via the LHMI hint menu

In order to help the user, there is an LHMI page labeled 'Hints'. This page is located under Main menu/Diagnostics/IED status/Hints. For each activated hint there is a headline. From the headline view, an explanation page can be entered, giving the user more information and hints about the particular topic.

For example, if there is a configuration to use IEC 61850 9–2 analog data, but no data arrives on the access point, then the IED will use substituted data and most protection functions will be blocked. This condition will be indicated with a sub-menu under Hints, where details about this condition are shown. The Hint menu is a way to assist the user in troubleshooting.

The Hint menu is currently only available in English. All the entries are in English, regardless of which language is selected.

The supported list of hints are as follows:

Table 45: Hint menu

<table>
<thead>
<tr>
<th>Headline</th>
<th>Explanation</th>
</tr>
</thead>
<tbody>
<tr>
<td>Incorrect setting of SyncLostMode</td>
<td>There are two explanations possible:</td>
</tr>
<tr>
<td></td>
<td><em>SyncLostMode</em> is set to <em>Block</em>, no time source is configured to achieve the required accuracy. Unless a high accuracy time source is selected, the function dependent on high time accuracy will be blocked.</td>
</tr>
<tr>
<td></td>
<td><em>SyncLostMode</em> is set to <em>BlockOnLostUTC</em>, but there is no UTC capable synch source (GPS, IRIG-B) used. Unless a UTC capable time source is selected, the function dependent on high time accuracy will be blocked.</td>
</tr>
<tr>
<td>Sampled value substituted</td>
<td>&lt;Access Point&gt;&lt;Hardware Module Identifier&gt;&lt;svID&gt; Where the Hardware Module Identifier is the same as given in PCM600, e.g. API1: MU1_9201 svID: &lt;ABB_MU0101&gt;</td>
</tr>
<tr>
<td>Time diff: IED vs Sampled value</td>
<td>&lt;Access Point&gt;&lt;Hardware Module Identifier&gt;&lt;svID&gt; Where the Hardware Module Identifier is the same as given in PCM600, e.g. API1: MU1_9201 svID: &lt;ABB_MU0101&gt;</td>
</tr>
<tr>
<td>Frequency diff: IED vs Sampled value</td>
<td>&lt;Access Point&gt;&lt;Hardware Module Identifier&gt;&lt;svID&gt; Where the Hardware Module Identifier is the same as given in PCM600, e.g. API1: MU1_9201 svID: &lt;ABB_MU0101&gt;</td>
</tr>
<tr>
<td>Wrong cycle time for PMU report</td>
<td>Wrong cycle time on SMAI or 3PHSUM block connected to Phasor Report block. The SMAI or 3PHSUM block should have the same cycle time as that of Phasor Report.</td>
</tr>
<tr>
<td>PMU not connected to 3ph output</td>
<td>The PMU phasor report input(s) must be connected to the 3ph output of SMAI or 3PHSU.</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Headline</th>
<th>Explanation</th>
</tr>
</thead>
<tbody>
<tr>
<td>Invalid value set for PMU Parameters</td>
<td>There are two explanations possible:</td>
</tr>
<tr>
<td></td>
<td>Check if the following parameters are set correctly on PMUREPORT: ReportRate or SvcClass or parameter PRIMVAL:1.FrequencySel is not set as 50Hz / 60Hz.</td>
</tr>
<tr>
<td></td>
<td>Check if the following parameters are set correctly on PMUREPORT: ReportRate or SvcClass or RptTimetag or parameter PRIMVAL:1.FrequencySel is not set as 50Hz / 60Hz.</td>
</tr>
<tr>
<td>Invalid phase angle reference</td>
<td>The selected PhaseAngleRef corresponds to an analog channel that is not configured. Please configure a valid reference channel.</td>
</tr>
<tr>
<td>GOOSE is configured on a disabled port</td>
<td>At least one of the access points configured for GOOSE is disabled. The port can be disabled either through changing the access point operation to off or by unchecking the GOOSE protocol from the access point in the Ethernet configuration in PCM600 or LHMI. Please enable GOOSE on access points: AP_FRONT, AP_1.</td>
</tr>
<tr>
<td>LDCM not running the application image</td>
<td>&lt;slot number&gt; is running the factory image instead of the application image. The factory image is older and does not contain the latest updates and fixes. Please reboot the IED. If the problem persists update the LDCM firmware or replace the board.</td>
</tr>
<tr>
<td>LDCM version is not accepted</td>
<td>&lt;device name&gt; firmware version &lt;version string&gt; is not accepted. The minimum accepted version is &lt;version string&gt;. Please update the LDCM firmware or replace the board.</td>
</tr>
<tr>
<td>OEM not running the application image</td>
<td>OEM in slot &lt;slot number&gt; is running the factory image instead of the application image. The factory image is older and does not contain the latest updates and fixes. Please reboot the IED. If the problem persists, update the OEM firmware or replace the board.</td>
</tr>
<tr>
<td>SFP unplugged from the slot</td>
<td>SFP has been unplugged from the slot. Please check the connection. Corresponding hardware(s) is set to fail.</td>
</tr>
<tr>
<td>SFP replaced with other type</td>
<td>Configured and the detected SFP(s) are different. Corresponding hardware(s) is set to fail. Please restart IED and consider Reconfigure HW modules to get updated hardware list.</td>
</tr>
<tr>
<td>Non ABB vendor SFP detected</td>
<td>Non ABB vendor SFP detected. Corresponding hardware(s) is set to fail. Please use ABB approved SFP's.</td>
</tr>
</tbody>
</table>

### 15.2.4 Hardware re-configuration

When adding, removing or moving a hardware modules in an IED (for example, I/O modules, communication modules or time synchronization modules) a set of procedures must be followed.

**Adding a new module in to an IED**

Procedure:
1. Switch the IED off and insert the new module.
2. Switch the IED on, wait for it to start, and then perform a HW reconfig.
3. Perform a license update in PCM600.

The new module is now available in PCM600 and is ready to be configured.

**Removing a module from an IED**

Procedure:

1. Remove all existing configuration for the module in PCM, and write that configuration to the IED.
2. Switch the IED off and remove the HW module.
3. Switch the IED on, wait for it to start, and then perform a HW reconfig.
4. Perform a license update in PCM 600.

If any configuration that makes the module needed remains, then the HW reconfig will not remove the module. The module will still be needed. An error indication for the module will appear, if the module is physically removed from the IED and the IED is restarted with some part of the configuration still requiring the module.

**Moving a module in an IED from one position to another**

Procedure:

1. Remove all existing configuration for the module in PCM, and write that configuration to the IED.
2. Switch the IED off and move the HW module.
3. Switch the IED on, wait for it to start, and then perform a HW reconfig.
4. Perform a license update in PCM600.

The new module is now available in PCM600 at the new position and is ready to be configured.

**15.3 Repair instruction**

- Never disconnect the secondary connection of a current transformer circuit without short-circuiting the transformer's secondary winding. Operating a current transformer with the secondary winding open will cause a massive potential build up that may damage the transformer and may cause injuries to humans.

- Never connect or disconnect a wire and/or a connector to or from a IED during normal service. Hazardous voltages and currents are present that may be lethal. Operation may be disrupted and IED and measuring circuitry may be damaged.
An alternative is to open the IED and send only the faulty circuit board to ABB for repair. When a printed circuit board is sent to ABB, it must always be placed in a metallic, ESD-proof, protection bag. The user can also purchase separate replacement modules.

Strictly follow the company and country safety regulations.

Most electronic components are sensitive to electrostatic discharge and latent damage may occur. Please observe usual procedures for handling electronics and also use an ESD wrist strap. A semi-conducting layer must be placed on the workbench and connected to ground.

Disassemble and reassemble the IED accordingly:

1. Switch off the dc supply.
2. Short-circuit the current transformers and disconnect all current and voltage connections from the IED.
3. Disconnect all signal wires by removing the female connectors.
4. Disconnect the optical fibers.
5. Unscrew the main back plate of the IED.
6. If the transformer module is to be changed:
   • Remove the IED from the panel if necessary.
   • Remove the rear plate of the IED.
   • Remove the front plate.
   • Remove the screws of the transformer input module, both front and rear.
7. Pull out the faulty module.
8. Check that the new module has a correct identity number.
9. Check that the springs on the card rail are connected to the corresponding metallic area on the circuit board when the new module is inserted.
10. Reassemble the IED.

If the IED has been calibrated with the system inputs, the calibration procedure must be performed again to maintain the total system accuracy.

15.4 Repair support

If an IED needs to be repaired, the whole IED must be removed and sent to an ABB Logistic Center. Please contact the local ABB representative to get more details.

15.5 Maintenance

The IED is self-supervised. No special maintenance is required.

Instructions from the power network company and other maintenance directives valid for maintenance of the power system must be followed.
# Section 16 Glossary

<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Full Form</th>
</tr>
</thead>
<tbody>
<tr>
<td>AC</td>
<td>Alternating current</td>
</tr>
<tr>
<td>ACC</td>
<td>Actual channel</td>
</tr>
<tr>
<td>ACT</td>
<td>Application configuration tool within PCM600</td>
</tr>
<tr>
<td>A/D converter</td>
<td>Analog-to-digital converter</td>
</tr>
<tr>
<td>ADBS</td>
<td>Amplitude deadband supervision</td>
</tr>
<tr>
<td>ADM</td>
<td>Analog digital conversion module, with time synchronization</td>
</tr>
<tr>
<td>AI</td>
<td>Analog input</td>
</tr>
<tr>
<td>ANSI</td>
<td>American National Standards Institute</td>
</tr>
<tr>
<td>AR</td>
<td>Autoreclosing</td>
</tr>
<tr>
<td>ASCT</td>
<td>Auxiliary summation current transformer</td>
</tr>
<tr>
<td>ASD</td>
<td>Adaptive signal detection</td>
</tr>
<tr>
<td>ASDU</td>
<td>Application service data unit</td>
</tr>
<tr>
<td>AWG</td>
<td>American Wire Gauge standard</td>
</tr>
<tr>
<td>BBP</td>
<td>Busbar protection</td>
</tr>
<tr>
<td>BFOC/2,5</td>
<td>Bayonet fiber optic connector</td>
</tr>
<tr>
<td>BFP</td>
<td>Breaker failure protection</td>
</tr>
<tr>
<td>BI</td>
<td>Binary input</td>
</tr>
<tr>
<td>BIM</td>
<td>Binary input module</td>
</tr>
<tr>
<td>BOM</td>
<td>Binary output module</td>
</tr>
<tr>
<td>BOS</td>
<td>Binary outputs status</td>
</tr>
<tr>
<td>BR</td>
<td>External bistable relay</td>
</tr>
<tr>
<td>BS</td>
<td>British Standards</td>
</tr>
<tr>
<td>BSR</td>
<td>Binary signal transfer function, receiver blocks</td>
</tr>
<tr>
<td>BST</td>
<td>Binary signal transfer function, transmit blocks</td>
</tr>
<tr>
<td>C37.94</td>
<td>IEEE/ANSI protocol used when sending binary signals between IEDs</td>
</tr>
<tr>
<td>CAN</td>
<td>Controller Area Network. ISO standard (ISO 11898) for serial communication</td>
</tr>
<tr>
<td>CB</td>
<td>Circuit breaker</td>
</tr>
<tr>
<td>CBM</td>
<td>Combined backplane module</td>
</tr>
<tr>
<td>CCITT</td>
<td>Consultative Committee for International Telegraph and Telephony. A United Nations-sponsored standards body within the International Telecommunications Union</td>
</tr>
<tr>
<td>CCM</td>
<td>CAN carrier module</td>
</tr>
<tr>
<td>CCVT</td>
<td>Capacitive Coupled Voltage Transformer</td>
</tr>
<tr>
<td>Abbreviation</td>
<td>Description</td>
</tr>
<tr>
<td>--------------</td>
<td>-------------</td>
</tr>
<tr>
<td>Class C</td>
<td>Protection Current Transformer class as per IEEE/ ANSI</td>
</tr>
<tr>
<td>CMPPS</td>
<td>Combined megapulses per second</td>
</tr>
<tr>
<td>CMT</td>
<td>Communication Management tool in PCM600</td>
</tr>
<tr>
<td>CO cycle</td>
<td>Close-open cycle</td>
</tr>
<tr>
<td>Codirectional</td>
<td>Way of transmitting G.703 over a balanced line. Involves two twisted pairs making it possible to transmit information in both directions</td>
</tr>
<tr>
<td>COM</td>
<td>Command</td>
</tr>
<tr>
<td>COMTRADE</td>
<td>Standard Common Format for Transient Data Exchange format for Disturbance recorder according to IEEE/ANSI C37.111, 1999 / IEC 60255-24</td>
</tr>
<tr>
<td>Contra-directional</td>
<td>Way of transmitting G.703 over a balanced line. Involves four twisted pairs, two of which are used for transmitting data in both directions and two for transmitting clock signals</td>
</tr>
<tr>
<td>COT</td>
<td>Cause of transmission</td>
</tr>
<tr>
<td>CPU</td>
<td>Central processing unit</td>
</tr>
<tr>
<td>CR</td>
<td>Carrier receive</td>
</tr>
<tr>
<td>CRC</td>
<td>Cyclic redundancy check</td>
</tr>
<tr>
<td>CROB</td>
<td>Control relay output block</td>
</tr>
<tr>
<td>CS</td>
<td>Carrier send</td>
</tr>
<tr>
<td>CT</td>
<td>Current transformer</td>
</tr>
<tr>
<td>CU</td>
<td>Communication unit</td>
</tr>
<tr>
<td>CVT or CCVT</td>
<td>Capacitive voltage transformer</td>
</tr>
<tr>
<td>DAR</td>
<td>Delayed autoreclosing</td>
</tr>
<tr>
<td>DARPA</td>
<td>Defense Advanced Research Projects Agency (The US developer of the TCP/IP protocol etc.)</td>
</tr>
<tr>
<td>DBDL</td>
<td>Dead bus dead line</td>
</tr>
<tr>
<td>DBLL</td>
<td>Dead bus live line</td>
</tr>
<tr>
<td>DC</td>
<td>Direct current</td>
</tr>
<tr>
<td>DFC</td>
<td>Data flow control</td>
</tr>
<tr>
<td>DFT</td>
<td>Discrete Fourier transform</td>
</tr>
<tr>
<td>DHCP</td>
<td>Dynamic Host Configuration Protocol</td>
</tr>
<tr>
<td>DIP-switch</td>
<td>Small switch mounted on a printed circuit board</td>
</tr>
<tr>
<td>DI</td>
<td>Digital input</td>
</tr>
<tr>
<td>DLLB</td>
<td>Dead line live bus</td>
</tr>
<tr>
<td>DNP</td>
<td>Distributed Network Protocol as per IEEE Std 1815-2012</td>
</tr>
<tr>
<td>DR</td>
<td>Disturbance recorder</td>
</tr>
<tr>
<td>DRAM</td>
<td>Dynamic random access memory</td>
</tr>
<tr>
<td>DRH</td>
<td>Disturbance report handler</td>
</tr>
<tr>
<td>DSP</td>
<td>Digital signal processor</td>
</tr>
<tr>
<td>DTT</td>
<td>Direct transfer trip scheme</td>
</tr>
<tr>
<td>Abbreviation</td>
<td>Description</td>
</tr>
<tr>
<td>--------------</td>
<td>-----------------------------------------------------------------------------</td>
</tr>
<tr>
<td>ECT</td>
<td>Ethernet configuration tool</td>
</tr>
<tr>
<td>EHV network</td>
<td>Extra high voltage network</td>
</tr>
<tr>
<td>EIA</td>
<td>Electronic Industries Association</td>
</tr>
<tr>
<td>EMC</td>
<td>Electromagnetic compatibility</td>
</tr>
<tr>
<td>EMF</td>
<td>Electromotive force</td>
</tr>
<tr>
<td>EMI</td>
<td>Electromagnetic interference</td>
</tr>
<tr>
<td>EnFP</td>
<td>End fault protection</td>
</tr>
<tr>
<td>EPA</td>
<td>Enhanced performance architecture</td>
</tr>
<tr>
<td>ESD</td>
<td>Electrostatic discharge</td>
</tr>
<tr>
<td>F-SMA</td>
<td>Type of optical fiber connector</td>
</tr>
<tr>
<td>FAN</td>
<td>Fault number</td>
</tr>
<tr>
<td>FCB</td>
<td>Flow control bit; Frame count bit</td>
</tr>
<tr>
<td>FOX 20</td>
<td>Modular 20 channel telecommunication system for speech, data and protection signals</td>
</tr>
<tr>
<td>FOX 512/515</td>
<td>Access multiplexer</td>
</tr>
<tr>
<td>FOX 6Plus</td>
<td>Compact time-division multiplexer for the transmission of up to seven duplex channels of digital data over optical fibers</td>
</tr>
<tr>
<td>FPN</td>
<td>Flexible product naming</td>
</tr>
<tr>
<td>FTP</td>
<td>File Transfer Protocol</td>
</tr>
<tr>
<td>FUN</td>
<td>Function type</td>
</tr>
<tr>
<td>G.703</td>
<td>Electrical and functional description for digital lines used by local telephone companies. Can be transported over balanced and unbalanced lines</td>
</tr>
<tr>
<td>GCM</td>
<td>Communication interface module with carrier of GPS receiver module</td>
</tr>
<tr>
<td>GDE</td>
<td>Graphical display editor within PCM600</td>
</tr>
<tr>
<td>GI</td>
<td>General interrogation command</td>
</tr>
<tr>
<td>GIS</td>
<td>Gas-insulated switchgear</td>
</tr>
<tr>
<td>GOOSE</td>
<td>Generic object-oriented substation event</td>
</tr>
<tr>
<td>GPS</td>
<td>Global positioning system</td>
</tr>
<tr>
<td>GSAL</td>
<td>Generic security application</td>
</tr>
<tr>
<td>GSE</td>
<td>Generic substation event</td>
</tr>
<tr>
<td>HDLC protocol</td>
<td>High-level data link control, protocol based on the HDLC standard</td>
</tr>
<tr>
<td>HFBR connector type</td>
<td>Plastic fiber connector</td>
</tr>
<tr>
<td>HLV circuit</td>
<td>Hazardous Live Voltage according to IEC60255-27</td>
</tr>
<tr>
<td>HMI</td>
<td>Human-machine interface</td>
</tr>
<tr>
<td>HSAR</td>
<td>High speed autoreclosing</td>
</tr>
<tr>
<td>HSR</td>
<td>High-availability Seamless Redundancy</td>
</tr>
<tr>
<td>HV</td>
<td>High-voltage</td>
</tr>
<tr>
<td>HVDC</td>
<td>High-voltage direct current</td>
</tr>
</tbody>
</table>
IDBS  Integrating deadband supervision
IEC  International Electrical Committee
IEC 60044-6  IEC Standard, Instrument transformers – Part 6: Requirements for protective current transformers for transient performance
IEC 60870-5-103  Communication standard for protection equipment. A serial master/slave protocol for point-to-point communication
IEC 61850  Substation automation communication standard
IEC 61850–8–1  Communication protocol standard
IEEE  Institute of Electrical and Electronics Engineers
IEEE 802.12  A network technology standard that provides 100 Mbits/s on twisted-pair or optical fiber cable
IEEE P1386.1  PCI Mezzanine Card (PMC) standard for local bus modules. References the CMC (IEEE P1386, also known as Common Mezzanine Card) standard for the mechanics and the PCI specifications from the PCI SIG (Special Interest Group) for the electrical EMF (Electromotive force).
IEEE 1686  Standard for Substation Intelligent Electronic Devices (IEDs) Cyber Security Capabilities
IED  Intelligent electronic device
IET600  Integrated engineering tool
I-GIS  Intelligent gas-insulated switchgear
IOM  Binary input/output module
Instance  When several occurrences of the same function are available in the IED, they are referred to as instances of that function. One instance of a function is identical to another of the same kind but has a different number in the IED user interfaces. The word “instance” is sometimes defined as an item of information that is representative of a type. In the same way an instance of a function in the IED is representative of a type of function.
IP 1. Internet protocol. The network layer for the TCP/IP protocol suite widely used on Ethernet networks. IP is a connectionless, best-effort packet-switching protocol. It provides packet routing, fragmentation and reassembly through the data link layer.
  2. Ingression protection, according to IEC 60529
IP 20  Ingression protection, according to IEC 60529, level IP20- Protected against solid foreign objects of 12.5mm diameter and greater.
IP 40  Ingression protection, according to IEC 60529, level IP40-Protected against solid foreign objects of 1mm diameter and greater.
IP 54  Ingression protection, according to IEC 60529, level IP54-Dust-protected, protected against splashing water.
IRF  Internal failure signal
IRIG-B:  InterRange Instrumentation Group Time code format B, standard 200
ITU  International Telecommunications Union
LAN  Local area network
<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>LIB 520</td>
<td>High-voltage software module</td>
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<tr>
<td>LCD</td>
<td>Liquid crystal display</td>
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<tr>
<td>LDCM</td>
<td>Line data communication module</td>
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<td>LDD</td>
<td>Local detection device</td>
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<tr>
<td>LED</td>
<td>Light-emitting diode</td>
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<tr>
<td>LNT</td>
<td>LON network tool</td>
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<tr>
<td>LON</td>
<td>Local operating network</td>
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<tr>
<td>MCB</td>
<td>Miniature circuit breaker</td>
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<tr>
<td>MCM</td>
<td>Mezzanine carrier module</td>
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<tr>
<td>MIM</td>
<td>Milli-ampere module</td>
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<tr>
<td>MPM</td>
<td>Main processing module</td>
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<tr>
<td>MVAL</td>
<td>Value of measurement</td>
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<tr>
<td>MVB</td>
<td>Multifunction vehicle bus. Standardized serial bus originally developed for use in trains.</td>
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<tr>
<td>NCC</td>
<td>National Control Centre</td>
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<tr>
<td>NOF</td>
<td>Number of grid faults</td>
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<tr>
<td>NUM</td>
<td>Numerical module</td>
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<tr>
<td>OCO cycle</td>
<td>Open-close-open cycle</td>
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<td>OCP</td>
<td>Overcurrent protection</td>
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<td>OEM</td>
<td>Optical Ethernet module</td>
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<tr>
<td>OLTC</td>
<td>On-load tap changer</td>
</tr>
<tr>
<td>OTEV</td>
<td>Disturbance data recording initiated by other event than start/pick-up</td>
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<tr>
<td>OV</td>
<td>Overvoltage</td>
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<tr>
<td>Overreach</td>
<td>A term used to describe how the relay behaves during a fault condition. For example, a distance relay is overreaching when the impedance presented to it is smaller than the apparent impedance to the fault applied to the balance point, that is, the set reach. The relay “sees” the fault but perhaps it should not have seen it.</td>
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<tr>
<td>PCI</td>
<td>Peripheral component interconnect, a local data bus</td>
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<td>PCM</td>
<td>Pulse code modulation</td>
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<td>PCM600</td>
<td>Protection and control IED manager</td>
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<tr>
<td>PC-MIP</td>
<td>Mezzanine card standard</td>
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<tr>
<td>PELV circuit</td>
<td>Protected Extra-Low Voltage circuit type according to IEC60255-27</td>
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<tr>
<td>PMC</td>
<td>PCI Mezzanine card</td>
</tr>
<tr>
<td>POR</td>
<td>Permissive overreach</td>
</tr>
<tr>
<td>POTT</td>
<td>Permissive overreach transfer trip</td>
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<tr>
<td>Process bus</td>
<td>Bus or LAN used at the process level, that is, in near proximity to the measured and/or controlled components</td>
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<td>PRP</td>
<td>Parallel redundancy protocol</td>
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<tr>
<td>Abbreviation</td>
<td>Description</td>
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<td>PSM</td>
<td>Power supply module</td>
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<td>PST</td>
<td>Parameter setting tool within PCM600</td>
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<td>PTP</td>
<td>Precision time protocol</td>
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<tr>
<td>PT ratio</td>
<td>Potential transformer or voltage transformer ratio</td>
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<tr>
<td>PTT</td>
<td>Permissive underreach transfer trip</td>
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<tr>
<td>RASC</td>
<td>Synchrocheck relay, COMBIFLEX</td>
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<td>RCA</td>
<td>Relay characteristic angle</td>
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<tr>
<td>RISC</td>
<td>Reduced instruction set computer</td>
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<td>RMS value</td>
<td>Root mean square value</td>
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<td>RS422</td>
<td>A balanced serial interface for the transmission of digital data in point-to-point connections</td>
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<td>RS485</td>
<td>Serial link according to EIA standard RS485</td>
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<td>RTC</td>
<td>Real-time clock</td>
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<tr>
<td>RTU</td>
<td>Remote terminal unit</td>
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<tr>
<td>SA</td>
<td>Substation Automation</td>
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<tr>
<td>SBO</td>
<td>Select-before-operate</td>
</tr>
<tr>
<td>SC</td>
<td>Switch or push button to close</td>
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<tr>
<td>SCL</td>
<td>Short circuit location</td>
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<td>SCS</td>
<td>Station control system</td>
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<tr>
<td>SCADA</td>
<td>Supervision, control and data acquisition</td>
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<tr>
<td>SCT</td>
<td>System configuration tool according to standard IEC 61850</td>
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<tr>
<td>SDU</td>
<td>Service data unit</td>
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<tr>
<td>SELV circuit</td>
<td>Safety Extra-Low Voltage circuit type according to IEC60255-27</td>
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<tr>
<td>SFP</td>
<td>Small form-factor pluggable (abbreviation) Optical Ethernet port (explanation)</td>
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<tr>
<td>SLM</td>
<td>Serial communication module.</td>
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<tr>
<td>SMA connector</td>
<td>Subminiature version A, A threaded connector with constant impedance.</td>
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<td>SMT</td>
<td>Signal matrix tool within PCM600</td>
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<td>SMS</td>
<td>Station monitoring system</td>
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<tr>
<td>SNTP</td>
<td>Simple network time protocol – is used to synchronize computer clocks on local area networks. This reduces the requirement to have accurate hardware clocks in every embedded system in a network. Each embedded node can instead synchronize with a remote clock, providing the required accuracy.</td>
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<tr>
<td>SOF</td>
<td>Status of fault</td>
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<tr>
<td>SPA</td>
<td>Strömberg Protection Acquisition (SPA), a serial master/slave protocol for point-to-point and ring communication.</td>
</tr>
<tr>
<td>SRY</td>
<td>Switch for CB ready condition</td>
</tr>
<tr>
<td>ST</td>
<td>Switch or push button to trip</td>
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<tr>
<td><strong>Starpoint</strong></td>
<td>Neutral/Wye point of transformer or generator</td>
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<td>-----------------</td>
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<tr>
<td><strong>SVC</strong></td>
<td>Static VAr compensation</td>
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<tr>
<td><strong>TC</strong></td>
<td>Trip coil</td>
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<tr>
<td><strong>TCS</strong></td>
<td>Trip circuit supervision</td>
</tr>
<tr>
<td><strong>TCP</strong></td>
<td>Transmission control protocol. The most common transport layer protocol used on Ethernet and the Internet.</td>
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<tr>
<td><strong>TCP/IP</strong></td>
<td>Transmission control protocol over Internet Protocol. The de facto standard Ethernet protocols incorporated into 4.2BSD Unix. TCP/IP was developed by DARPA for Internet working and encompasses both network layer and transport layer protocols. While TCP and IP specify two protocols at specific protocol layers, TCP/IP is often used to refer to the entire US Department of Defense protocol suite based upon these, including Telnet, FTP, UDP and RDP.</td>
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<tr>
<td><strong>TEF</strong></td>
<td>Time delayed ground-fault protection function</td>
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<tr>
<td><strong>TLS</strong></td>
<td>Transport Layer Security</td>
</tr>
<tr>
<td><strong>TM</strong></td>
<td>Transmit (disturbance data)</td>
</tr>
<tr>
<td><strong>TNC connector</strong></td>
<td>Threaded Neill-Concelman, a threaded constant impedance version of a BNC connector</td>
</tr>
<tr>
<td><strong>TP</strong></td>
<td>Trip (recorded fault)</td>
</tr>
<tr>
<td><strong>TPZ, TPY, TPX, TPS</strong></td>
<td>Current transformer class according to IEC</td>
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<tr>
<td><strong>TRM</strong></td>
<td>Transformer Module. This module transforms currents and voltages taken from the process into levels suitable for further signal processing.</td>
</tr>
<tr>
<td><strong>TYP</strong></td>
<td>Type identification</td>
</tr>
<tr>
<td><strong>UMT</strong></td>
<td>User management tool</td>
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<tr>
<td><strong>Underreach</strong></td>
<td>A term used to describe how the relay behaves during a fault condition. For example, a distance relay is underreaching when the impedance presented to it is greater than the apparent impedance to the fault applied to the balance point, that is, the set reach. The relay does not “see” the fault but perhaps it should have seen it. See also Overreach.</td>
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<tr>
<td><strong>UTC</strong></td>
<td>Coordinated Universal Time. A coordinated time scale, maintained by the Bureau International des Poids et Mesures (BIPM), which forms the basis of a coordinated dissemination of standard frequencies and time signals. UTC is derived from International Atomic Time (TAI) by the addition of a whole number of “leap seconds” to synchronize it with Universal Time 1 (UT1), thus allowing for the eccentricity of the Earth’s orbit, the rotational axis tilt (23.5 degrees), but still showing the Earth’s irregular rotation, on which UT1 is based. The Coordinated Universal Time is expressed using a 24-hour clock, and uses the Gregorian calendar. It is used for aeroplane and ship navigation, where it is also sometimes known by the military name, “Zulu time.” “Zulu” in the phonetic alphabet stands for “Z”, which stands for longitude zero.</td>
</tr>
<tr>
<td><strong>UV</strong></td>
<td>Undervoltage</td>
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<tr>
<td><strong>WEI</strong></td>
<td>Weak end infeed logic</td>
</tr>
<tr>
<td><strong>VT</strong></td>
<td>Voltage transformer</td>
</tr>
<tr>
<td><strong>X.21</strong></td>
<td>A digital signalling interface primarily used for telecom equipment</td>
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</tbody>
</table>
Three times zero-sequence current. Often referred to as the residual or the ground-fault current

Three times the zero sequence voltage. Often referred to as the residual voltage or the neutral point voltage.