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This product complies with the directive of the Council of the European Communities on the approximation of the laws of the Member States relating to electromagnetic compatibility (EMC Directive 2004/108/EC) and concerning electrical equipment for use within specified voltage limits (Low-voltage directive 2006/95/EC). This conformity is the result of tests conducted by ABB in accordance with the product standard EN 60255-26 for the EMC directive, and with the product standards EN 60255-1 and EN 60255-27 for the low voltage directive. The product is designed in accordance with the international standards of the IEC 60255 series and ANSI C37.90.
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Section 1  Introduction

1.1  This manual

The application manual contains application descriptions and setting guidelines sorted per function. The manual can be used to find out when and for what purpose a typical protection function can be used. The manual can also provide assistance for calculating settings.

1.2  Intended audience

This manual addresses the protection and control engineer responsible for planning, pre-engineering and engineering.

The protection and control engineer must be experienced in electrical power engineering and have knowledge of related technology, such as protection schemes and communication principles.
1.3 Product documentation

1.3.1 Product documentation set

Figure 1: The intended use of manuals throughout the product lifecycle

The engineering manual contains instructions on how to engineer the IEDs using the various tools available within the PCM600 software. The manual provides instructions on how to set up a PCM600 project and insert IEDs to the project structure. The manual also recommends a sequence for the engineering of protection and control functions, as well as communication engineering for IEC 61850.

The installation manual contains instructions on how to install the IED. The manual provides procedures for mechanical and electrical installation. The chapters are organized in the chronological order in which the IED should be installed.

The commissioning manual contains instructions on how to commission the IED. The manual can also be used by system engineers and maintenance personnel for assistance during the testing phase. The manual provides procedures for the checking of external circuitry and energizing the IED, parameter setting and configuration as well as verifying settings by secondary injection. The manual describes the process of testing an IED in a substation which is not in service. The chapters are organized in the chronological order in which the IED should be commissioned. The relevant procedures may be followed also during the service and maintenance activities.
The operation manual contains instructions on how to operate the IED once it has been commissioned. The manual provides instructions for the monitoring, controlling and setting of the IED. The manual also describes how to identify disturbances and how to view calculated and measured power grid data to determine the cause of a fault.

The application manual contains application descriptions and setting guidelines sorted per function. The manual can be used to find out when and for what purpose a typical protection function can be used. The manual can also provide assistance for calculating settings.

The technical manual contains operation principle descriptions, and lists function blocks, logic diagrams, input and output signals, setting parameters and technical data, sorted per function. The manual can be used as a technical reference during the engineering phase, installation and commissioning phase, and during normal service.

The communication protocol manual describes the communication protocols supported by the IED. The manual concentrates on the vendor-specific implementations.

The point list manual describes the outlook and properties of the data points specific to the IED. The manual should be used in conjunction with the corresponding communication protocol manual.

The cyber security deployment guideline describes the process for handling cyber security when communicating with the IED. Certification, Authorization with role based access control, and product engineering for cyber security related events are described and sorted by function. The guideline can be used as a technical reference during the engineering phase, installation and commissioning phase, and during normal service.

### 1.3.2 Document revision history

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<tr>
<td>–/May 2017</td>
<td>First release</td>
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<tr>
<td>A/October 2017</td>
<td>Information updated</td>
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<tr>
<td>B/March 2018</td>
<td>2.2 Maintenance release 1</td>
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<tr>
<td>C/June 2018</td>
<td>Added new functions and resolved bugs</td>
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<tr>
<td>D/November 2018</td>
<td>Added harmonic, delta, and PSTPDIF functions</td>
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### 1.3.3 Related documents

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<td>Test system, COMBITEST</td>
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1.4 Document symbols and conventions

1.4.1 Symbols

- The electrical warning icon indicates the presence of a hazard which could result in electrical shock.

- The warning icon indicates the presence of a hazard which could result in personal injury.

- The caution hot surface icon indicates important information or warning about the temperature of product surfaces.

- Class 1 Laser product. Take adequate measures to protect the eyes and do not view directly with optical instruments.

- The caution icon indicates important information or warning related to the concept discussed in the text. It might indicate the presence of a hazard which could result in corruption of software or damage to equipment or property.

- The information icon alerts the reader of important facts and conditions.
The tip icon indicates advice on, for example, how to design your project or how to use a certain function.

Although warning hazards are related to personal injury, it is necessary to understand that under certain operational conditions, operation of damaged equipment may result in degraded process performance leading to personal injury or death. It is important that the user fully complies with all warning and cautionary notices.

### 1.4.2 Document conventions

- Abbreviations and acronyms in this manual are spelled out in the glossary. The glossary also contains definitions of important terms.
- Push button navigation in the LHMI menu structure is presented by using the push button icons.
  
  For example, to navigate between the options, use ⬆️ and ⬇️.
- HMI menu paths are presented in bold.
  
  For example, select **Main menu/Settings**.
- LHMI messages are shown in Courier font.
  
  For example, to save the changes in non-volatile memory, select **Yes** and press ⬅️.
- Parameter names are shown in italics.
  
  For example, the function can be enabled and disabled with the **Operation** setting.
- Each function block symbol shows the available input/output signal.
  
  - the character ^ in front of an input/output signal name indicates that the signal name may be customized using the PCM600 software.
  - the character * after an input signal name indicates that the signal must be connected to another function block in the application configuration to achieve a valid application configuration.
- Dimensions are provided both in inches and millimeters. If it is not specifically mentioned then the dimension is in millimeters.

### 1.5 IEC 61850 edition 1 / edition 2 mapping

Function block names are used in **ACT** and **PST** to identify functions. Respective function block names of Edition 1 logical nodes and Edition 2 logical nodes are shown in the table below.

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Section 2  Application

2.1  General IED application

The Intelligent Electronic Device (IED) is designed for the selective, reliable and fast differential protection of busbars, T-connections and meshed corners in up to 6 zones. It can be used for protection of single, double and triple busbar with or without transfer bus, double circuit breaker or breaker-and-a-half stations. The IED is applicable for the protection of medium voltage (MV), high voltage (HV) and extra high voltage (EHV) installations at a power system frequency of 50Hz or 60Hz. It can detect all types of internal phase-to-phase and phase-to-ground faults in solidly grounded or low impedance grounded power systems, as well as all internal multi-phase faults in isolated or high-impedance grounded power systems.

Ordering of PT inputs inside of the busbar protection IED will allow integration of voltage related functionality like under-voltage release, residual over-voltage, power functions, metering and voltage recording during the faults. However attention shall be given to the fact that inclusion of PT inputs will reduce number of available CT inputs (in total 24 analogue inputs are the product limit). Consequently when PT inputs are ordered the busbar protection IED will be applicable for buses with a fewer number of bays. Practically the number of available CT inputs will limit the size of the station which can be protected.

The IED has very low requirements on the main current transformers (that is, CTs) and no interposing current transformers are necessary. For all applications, it is possible to include and mix main CTs with 1A and 5A rated secondary current within the same protection zone. Typically, CTs with up to 10:1 ratio difference can be used within the same differential protection zone. Adjustment for different main CT ratios is achieved numerically by a parameter setting.

The numerical, low-impedance differential protection function is designed for fast and selective protection for faults within protected zone. All connected CT inputs are provided with a restraint feature. The minimum pick-up value for the differential current is set to give a suitable sensitivity for all internal faults. The pick-up setting for the fault current must be less than 80% of the lowest fault current that can occur on the protected bus bars. If the minimum fault current is high enough, the set value should be set higher than the maximum load current. This setting is made directly in primary amperes. The operating slope for the differential operating characteristic is fixed to 53% in the algorithm.

The fast tripping time (minimum trip time is 5ms when SOM output module is used) of the low-impedance differential protection function is especially advantageous for power system networks with high fault levels or where fast fault clearance is required for power system stability.

All CT inputs are provided with a restraint feature. The operation is based on the well-proven RADSS percentage restraint stabilization principle, with an extra stabilization feature to stabilize for very heavy CT saturation. Stability for external faults is guaranteed if a CT is not saturated for at least two milliseconds during each power system cycle.

The advanced open CT detection algorithm detects instantly the open CT secondary circuits and prevents differential protection operation without any need for additional check zone.

Differential protection zones in the IED include a sensitive operational pickup. This sensitive operational pickup is designed to be able to detect internal busbar ground faults in low impedance grounded power systems (that is, power systems where the ground-fault current is
limited to a certain level, typically between 300A and 2000A primary by a neutral point reactor or resistor. Alternatively this sensitive pickup can be used when high sensitivity is required from busbar differential protection (that is, energizing of the bus via long line).

Overall operating characteristic of the differential function in the IED is shown in figure 2.

Figure 2: IED operating characteristic

Integrated overall check zone feature, independent from any disconnector position, is available. It can be used in double or triple busbar stations to secure stability of the busbar differential protection in case of entirely wrong status indication of busbar disconnector in any of the feeder bays.

Flexible, software based dynamic Zone Selection enables easy and fast adaptation to the most common substation arrangements such as single busbar with or without transfer bus, double busbar with or without transfer bus, triple busbar with or without transfer bus, breaker-and-a-half stations, double busbar-double breaker stations, ring busbars, and so on. The software based dynamic Zone Selections ensures:

- Dynamic linking of measured CT currents to the appropriate differential protection zone as required by substation topology
- Efficient merging of the two or more differential zones when required by substation topology (that is load-transfer)
- Easy zone merging initiated by closing of bus-sectionalizing disconnectors
- Selective operation of busbar differential protection ensures tripping only of circuit breakers connected to the faulty zone
- Correct marshaling of backup-trip commands from internally integrated or external circuit breaker failure protections to all surrounding circuit breakers
- Easy incorporation of bus-section and/or bus-coupler bays (that is, tie-breakers) with one or two sets of CTs into the protection scheme
- Disconnector and/or circuit breaker status supervision
Advanced Zone Selection logic accompanied by optionally available end-fault and/or circuit breaker failure protections ensure minimum possible tripping time and selectivity for faults within the blind spot or the end zone between bay CT and bay circuit breaker. Therefore the IED offers best possible coverage for such faults in feeder and bus-section/bus-coupler bays.

Optionally available circuit breaker failure protection, one for every CT input into the IED, offers secure local back-up protection for the circuit breakers in the station.

Optionally available four-stage, non-directional overcurrent protections, one for every CT input into the IED, provide remote backup functionality for connected feeders and remote-end stations.

Optionally available voltage and frequency protection functions enable to include voltage release criterion for busbar protection or to integrate independent over-, under-voltage protection for the bus in the busbar protection IED.

Optionally available over-current, thermal overload and capacitor bank protection functions open possibilities to integrate protection of shunt reactors and shunt capacitor banks into the busbar protection IED.

It is normal practice to have just one busbar protection IED per busbar. Nevertheless some utilities do apply two independent busbar protection IEDs per zone of protection. This IED fits both solutions.

A simplified bus differential protection for multi-phase faults and ground faults can be obtained by using a single, one-phase IED with external auxiliary summation current transformers.

The IED can be used in applications with IEC/UCA 61850-9-2LE process bus with up to eight merging units (MU) depending on the other functionality included in the IED. Each MU has eight analog channels, four currents and four voltages. Conventional and Merging Unit channels can be mixed freely in the application.

Forcing of binary inputs and outputs is a convenient way to test wiring in substations as well as testing configuration logic in the IEDs. Basically it means that all binary inputs and outputs on the IED I/O modules (BOM, BIM, IOM & SOM) can be forced to arbitrary values.

Central Account Management is an authentication infrastructure that offers a secure solution for enforcing access control to IEDs and other systems within a substation. This incorporates management of user accounts, roles and certificates and the distribution of such, a procedure completely transparent to the user.

The Flexible Product Naming allows the customer to use an IED-vendor independent IEC 61850 model of the IED. This customer model will be exposed in all IEC 61850 communication, but all other aspects of the IED will remain unchanged (e.g., names on the local HMI and names in the tools). This offers significant flexibility to adapt the IED to the customers' system and standard solution.

Optional apparatus control for up to 30 objects can provide a facility to draw simplified single line diagram (SLD) of the station on the local HMI.
Note that customized REB670 is delivered without any configuration. Thus the complete IED engineering shall be done by the customer or its system integrator. In order to secure proper operation of the busbar protection it is strictly recommended to always start engineering work from the PCM600 project for the pre-configured REB670 which is the closest to the actual application. Then, necessary modifications shall be applied in order to adopt the customized IED configuration to suite the actual station layout. The PCM600 project for the pre-configured REB670 IEDs is available in the Connectivity Package DVD.

The following tables list all the functions available in the IED. Those functions that are not exposed to the user or do not need to be configured are not described in this manual.

### 2.2 Main protection functions

#### Table 2: Example of quantities

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#### Function description

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<td>CCRBF</td>
<td>50BF</td>
<td>Breaker failure protection</td>
<td>0-8</td>
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<tr>
<td>CCSRBF</td>
<td>50BF</td>
<td>Breaker failure protection, single phase version</td>
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</tr>
<tr>
<td>GUPPDUP</td>
<td>37</td>
<td>Directional underpower protection</td>
<td>0-4</td>
</tr>
<tr>
<td>GOPPDOP</td>
<td>32</td>
<td>Directional overpower protection</td>
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<tr>
<td>CBPGAPC</td>
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<td>Capacitor bank protection</td>
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#### Voltage protection

<table>
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<tr>
<td>UV2PTUV</td>
<td>27</td>
<td>Two step undervoltage protection</td>
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<tr>
<td>OV2PTOV</td>
<td>59</td>
<td>Two step overvoltage protection</td>
<td>0-2</td>
</tr>
<tr>
<td>ROV2PTOV</td>
<td>59N</td>
<td>Two step residual overvoltage protection</td>
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<td>VDCPTOV</td>
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### 2.4 Control and monitoring functions

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<td>LOVPTUV</td>
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<td>Loss of voltage check</td>
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**Frequency protection**

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<tr>
<td>SAPTUF</td>
<td>81</td>
<td>Underfrequency protection</td>
<td>0-6</td>
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<tr>
<td>SAPTOF</td>
<td>81</td>
<td>Overfrequency protection</td>
<td>0-6</td>
</tr>
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<td>SAPFRC</td>
<td>81</td>
<td>Rate-of-change of frequency protection</td>
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**Multipurpose protection**

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<tr>
<td>CVGAPC</td>
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<td>General current and voltage protection</td>
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1) 67 requires voltage
2) 67N requires voltage

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<thead>
<tr>
<th>IEC 61850 or function name</th>
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<tbody>
<tr>
<td>AUTOBITS</td>
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<td>Automation bits, command function for DNP3.0</td>
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<td>SINGLECMD</td>
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<td>Single command, 16 signals</td>
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<td>I103CMD</td>
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<td>Function commands for IEC 60870-5-103</td>
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<td>I103POSCMDV</td>
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<td>I103USRCMD</td>
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**Secondary system supervision**

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<tr>
<td>FUFSPVC</td>
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<td>Fuse failure supervision</td>
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<td>VDSPVC</td>
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<td>Fuse failure supervision based on voltage difference</td>
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<td>DELVSPVC 7V_78</td>
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<td>Voltage delta supervision, 2 phase</td>
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<td>DELISPVC</td>
<td>71</td>
<td>Current delta supervision, 2 phase</td>
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<td>DELSPVC 78</td>
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<td>Real delta supervision, real</td>
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**Logic**

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<td>TMAGAPC</td>
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<td>ALMCALH</td>
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<td>Logic for group alarm</td>
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<td>WRNCALH</td>
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<td>Logic for group warning</td>
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<tr>
<td>INDCALH</td>
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<td>Logic for group indication</td>
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<tr>
<td>AND, GATE, INV, LLD, OR, PULSETIMER, RSMEMORY, SRMEMORY, TIMERSET, XOR</td>
<td>Basic configurable logic blocks (see Table 3)</td>
<td>40-496</td>
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<thead>
<tr>
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<th>Function description</th>
<th>Busbar</th>
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<tbody>
<tr>
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<td>REB670 (Customized)</td>
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<tr>
<td>ANDQT, INDCOMBSPTQT,</td>
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<td>Configurable logic blocks Q/T (see Table 5)</td>
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<tr>
<td>INDEXTSPTQT, INVALIDQT,</td>
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<td></td>
<td></td>
</tr>
<tr>
<td>INVERTERQT, ORQT,</td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>PULSETIMERQT, RSMEMORYQT,</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SRMEMORYQT, TIMERSETQT,</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>XORQT</td>
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<td></td>
<td></td>
</tr>
<tr>
<td>AND, GATE, INV, LLD, OR,</td>
<td></td>
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<td>0-1</td>
</tr>
<tr>
<td>PULSETIMER, RSMEMORY,</td>
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<tr>
<td>SLGAPC, SRMEMORY,</td>
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<tr>
<td>TIMERSET, VSGAPC, XOR</td>
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<td>FXDSIGN</td>
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<td>Fixed signal function block</td>
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<td>Boolean to integer conversion, 16 bit</td>
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<tr>
<td>BTIGAPC</td>
<td></td>
<td>Boolean to integer conversion with logical node representation, 16 bit</td>
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<tr>
<td>IB16</td>
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<td>Integer to Boolean 16 conversion</td>
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<tr>
<td>BCTZCONN</td>
<td></td>
<td>Integer to Boolean conversion for six-zone busbar</td>
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<tr>
<td>ITBGAPC</td>
<td></td>
<td>Integer to Boolean 16 conversion with Logic Node representation</td>
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<td>TEIGAPC</td>
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<td>Elapsed time integrator with limit transgression and overflow supervision</td>
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<tr>
<td>INTCOMP</td>
<td></td>
<td>Comparator for integer inputs</td>
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<tr>
<td>REALCOMP</td>
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<td>Comparator for real inputs</td>
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**Table 3: Total number of instances for basic configurable logic blocks**

<table>
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<th>Basic configurable logic block</th>
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<tr>
<td>AND</td>
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<tr>
<td>GATE</td>
<td>64</td>
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<tr>
<td>INV</td>
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<tr>
<td>LLD</td>
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<table>
<thead>
<tr>
<th>Basic configurable logic block</th>
<th>Total number of instances</th>
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</thead>
<tbody>
<tr>
<td>OR</td>
<td>496</td>
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<tr>
<td>PULSETIMER</td>
<td>40</td>
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<tr>
<td>RSMEMORY</td>
<td>40</td>
</tr>
<tr>
<td>SRMEMORY</td>
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<tr>
<td>TIMERSET</td>
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<td>XOR</td>
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Table 4: Number of function instances in APC30

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<tr>
<th>Function name</th>
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<td>SCILO</td>
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<tr>
<td>A1A2_BS</td>
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<td>4</td>
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<td>A1A2_DC</td>
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<td>6</td>
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<tr>
<td>ABC_BC</td>
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<td>2</td>
</tr>
<tr>
<td>BH_CONN</td>
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<td>2</td>
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<tr>
<td>BH_LINE_A</td>
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<tr>
<td>BH_LINE_B</td>
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<td>2</td>
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<td>DB_BUS_A</td>
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<td>3</td>
</tr>
<tr>
<td>DB_BUS_B</td>
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<td>DB_LINE</td>
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<td>ABC_LINE</td>
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<tr>
<td>AB_TRAFO</td>
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<td>SCSWI</td>
<td>Switch controller</td>
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<td>SXSWI</td>
<td>Circuit switch</td>
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<td>QCRSV</td>
<td>Apparatus control</td>
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<td>RESIN1</td>
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<td>1</td>
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<td>RESIN2</td>
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<td>POS_EVAL</td>
<td>Evaluation of position indication</td>
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<td>QCBAY</td>
<td>Bay control</td>
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<td>LOCREM</td>
<td>Handling of LR-switch positions</td>
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<td>XLNPROXY</td>
<td>Proxy for signals from switching device via GOOSE</td>
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<td>GOOSEXLNRCV</td>
<td>GOOSE function block to receive a switching device</td>
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### Table 5: Total number of instances for configurable logic blocks Q/T

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<td>INDEXTSPQT</td>
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<td>ORQT</td>
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<tr>
<td>PULSETIMERQT</td>
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<tr>
<td>RSMEMORYQT</td>
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<tr>
<td>SRMEMORYQT</td>
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<td>TIMERSETQT</td>
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<tr>
<td>XORQT</td>
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### Table 6: Total number of instances for extended logic package

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<th>Extended configurable logic block</th>
<th>Total number of instances</th>
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<tr>
<td>GATE</td>
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<td>INV</td>
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<tr>
<td>OR</td>
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<tr>
<td>PULSETIMER</td>
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<td>VSGAPC</td>
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<tr>
<td>XOR</td>
<td>89</td>
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<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
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<th>Busbar</th>
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<tbody>
<tr>
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<td>REB670 (Customized)</td>
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<td>VMMXU</td>
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<td>Voltage measurement phase-phase</td>
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<td>CMSQI</td>
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<td>Voltage measurement phase-ground</td>
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<td>EVENT</td>
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<td>Event function</td>
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<td>SP16GAPC</td>
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<td>Generic communication function for Single Point indication 16 inputs</td>
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<td>MVGAPC</td>
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<td>Generic communication function for measured values</td>
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<td>RANGE_XP</td>
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<td>Measured value expander block</td>
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<td>SSIML</td>
<td>71</td>
<td>Insulation supervision for liquid medium</td>
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<td>SSCBR</td>
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<td>Circuit breaker condition monitoring</td>
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<td>I103FLTPROT</td>
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### 2.5 Communication

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<th>Function description</th>
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<td>Running hour meter</td>
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<td>CHMMHAI</td>
<td>ITHD</td>
<td>Current harmonic monitoring, 3 phase</td>
<td>0-3</td>
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<tr>
<td>VHMMHAI</td>
<td>VTHD</td>
<td>Voltage harmonic monitoring, 3 phase</td>
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<tr>
<td>Metering</td>
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<td>Pulse-counter logic</td>
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<td>Function for energy calculation and demand handling</td>
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<th>Function description</th>
<th>Busbar</th>
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<td></td>
<td></td>
<td>IEC 61850-9-2 Process bus communication, 8 merging units</td>
<td>0-1</td>
</tr>
<tr>
<td>ACTIVLOG</td>
<td></td>
<td>Activity logging</td>
<td>1</td>
</tr>
<tr>
<td>ALTRK</td>
<td></td>
<td>Service tracking</td>
<td>1</td>
</tr>
<tr>
<td>PRP</td>
<td></td>
<td>IEC 62439-3 Parallel redundancy protocol</td>
<td>0-1</td>
</tr>
<tr>
<td>HSR</td>
<td></td>
<td>IEC 62439-3 High-availability seamless redundancy</td>
<td>0-1</td>
</tr>
<tr>
<td>PTP</td>
<td></td>
<td>Precision time protocol</td>
<td>1</td>
</tr>
<tr>
<td>SCHLCCH</td>
<td></td>
<td>Access point diagnostic for non-redundant Ethernet port</td>
<td>6</td>
</tr>
<tr>
<td>RCHLCCH</td>
<td></td>
<td>Access point diagnostic for redundant Ethernet ports</td>
<td>3</td>
</tr>
<tr>
<td>Remote communication</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LDCMRecBinStat1</td>
<td></td>
<td>Receive binary status from remote LDCM</td>
<td>6/3/3</td>
</tr>
<tr>
<td>LDCMRecBinStat2</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LDCMRecBinStat3</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### 2.6 Basic IED functions

**Table 7: Basic IED functions**

<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INERRSIG</td>
<td>Self supervision with internal event list</td>
</tr>
<tr>
<td>TIMESYNCHGEN</td>
<td>Time synchronization module</td>
</tr>
<tr>
<td>BININPUT, SYNCHCAN, SYNCHGPS, SYNCHCMPPS, SYNCHLON, SYNCHPPH, SYNCHPPS, SNTP, SYNCHSPA</td>
<td>Time synchronization</td>
</tr>
<tr>
<td>TIMEZONE</td>
<td>Time synchronization</td>
</tr>
<tr>
<td>IRIG-B</td>
<td>Time synchronization</td>
</tr>
<tr>
<td>SETGRPS</td>
<td>Number of setting groups</td>
</tr>
<tr>
<td>ACTVGRP</td>
<td>Parameter setting groups</td>
</tr>
<tr>
<td>TESTMODE</td>
<td>Test mode functionality</td>
</tr>
<tr>
<td>CHNGLCK</td>
<td>Change lock function</td>
</tr>
<tr>
<td>SMBI</td>
<td>Signal matrix for binary inputs</td>
</tr>
<tr>
<td>SMBO</td>
<td>Signal matrix for binary outputs</td>
</tr>
<tr>
<td>SMMI</td>
<td>Signal matrix for mA inputs</td>
</tr>
<tr>
<td>SMAI1 - SMAI12</td>
<td>Signal matrix for analog inputs</td>
</tr>
</tbody>
</table>

Table continues on next page
### Table 8: Local HMI functions

<table>
<thead>
<tr>
<th>IEC 61850 or function name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LHMICTRL</td>
<td>Local HMI signals</td>
</tr>
<tr>
<td>LANGUAGE</td>
<td>Local human machine language</td>
</tr>
<tr>
<td>SCREEN</td>
<td>Local HMI Local human machine screen behavior</td>
</tr>
<tr>
<td>FNKEYTY1–FNKEYTY5 FNKEYMD1–FNKEYMD5</td>
<td>Parameter setting function for HMI in PCM600</td>
</tr>
<tr>
<td>LEDGEN</td>
<td>General LED indication part for LHMI</td>
</tr>
<tr>
<td>OPENCLOSE_LED</td>
<td>LHMI LEDs for open and close keys</td>
</tr>
<tr>
<td>GRP1_LED1–GRP1_LED15</td>
<td>Basic part for CP HW LED indication module</td>
</tr>
<tr>
<td>GRP2_LED1–GRP2_LED15</td>
<td></td>
</tr>
<tr>
<td>GRP3_LED1–GRP3_LED15</td>
<td></td>
</tr>
</tbody>
</table>
Section 3  Configuration

3.1  Description of configuration REB670

3.1.1  Available ACT configurations for pre-configured REB670

Three configurations have been made available for pre-configured REB670 IED with two zones. Product variant with six zone is available as customized product only. It shall be noted that all three configurations include the following features:

- fully configured for the total available number of bays in each REB670 variant
- facility to take any bay out of service via the local HMI or externally via binary input
- facility to block any of the two zones via the local HMI or externally via binary input
- facility to block all bay trips via the local HMI or externally via binary input, but leaving all other function in service (that is BBP Zones, BFP and OCP where applicable)
- facility to externally initiate built-in disturbance recorder
- facility to connect external breaker failure backup trip signal from every bay
- facility to connect external bay trip signal

3.1.2  Configuration X01

This configuration includes only busbar protection for simple stations layouts (in other words, breaker-and-a-half, double breaker or single breaker stations). Additionally it can be used for double busbar-single breaker stations where disconnector replica is done by using only b auxiliary contact from every disconnector and/or circuit breakers. As a consequence no disconnector/breaker supervision will be available. It is as well possible to adapt this configuration by the signal matrix tool to be used as direct replacement of RED521 terminals. This configuration is available for all five REB670 variants (that is A20, A31, B20, B21 & B31). It shall be noted that optional functions breaker failure protection CCRBRF (50BF), end fault protection and overcurrent protection PH4SPTOC (51) can be ordered together with this configuration, but they will not be pre-configured. Thus these optional functions shall be configured by the end user.

3.1.3  Configuration X02

This configuration includes only busbar protection for double busbar-single breaker stations, where Zone Selection is done by using a and b auxiliary contacts from every disconnector and/or circuit breaker. Thus full disconnector/breaker supervision is available. This configuration is available for only three REB670 variants (that is A31, B21 and B31). It shall be noted that optional functions breaker failure protection CCRBRF (50BF), end fault protection and overcurrent protection PH4SPTOC (51) can be ordered together with this configuration, but they will not be pre-configured. Thus these optional functions shall be configured by the end user.

3.1.4  Configuration X03

This configuration includes BBP with breaker failure protection CCRBRF (50BF), end fault protection and overcurrent protection PH4SPTOC (51) for double busbar-single breaker stations,
where Zone Selection is done by using a and b auxiliary contacts from every disconnectors and/or circuit breakers. Thus full disconnector/breaker supervision is available. This configuration is available for only three REB670 variants (that is A31, B21 and B31).

In order to use X03 configuration, optional breaker failure and overcurrent functions must be ordered.

### 3.1.5 Description of 3 ph package A20A

Three-phase version of the IED with two low-impedance differential protection zones and four three-phase CT inputs A20A. The version is intended for simpler applications such as T-connections, meshed corners, and so on.
### 3.1.6 Description of 3 ph package A31A

Three-phase version of the IED with two low-impedance differential protection zones and eight three-phase CT inputs A31A. The version is intended for applications on smaller busbars, with up to two zones and eight CT inputs.

---

**Figure 4:** Configuration diagram for A31A, configuration X00

---

<table>
<thead>
<tr>
<th>VERSION OF REB670</th>
<th>NUMBER OF FEDERS IN BOTH BUSBAR SECTIONS</th>
<th>NUMBER OF REB670 REQUIRED BY THE SCHEME</th>
</tr>
</thead>
<tbody>
<tr>
<td>REB670 ANSI(A20A – X00) 3-Phase, 4 Bays, 2 Zones for Simple Station Layout 12 AI</td>
<td>6*</td>
<td>2</td>
</tr>
<tr>
<td>REB670 ANSI(A31A – X00) 3-Phase, 8 Bays, 2 Zones for Simple Station Layout 24 AI</td>
<td>14*</td>
<td>2</td>
</tr>
</tbody>
</table>

* With Just one CT in the Bus Section Bay
Figure 5: Configuration diagram for A31, configuration X01

<table>
<thead>
<tr>
<th>VERSION OF REB670</th>
<th>NUMBER OF FEEDERS IN BOTH BUSBAR SECTIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>REB670(A31 – X01)</td>
<td>3-Phase, 8 Bays, 2 Zones for Simple Station Layout 24 AI</td>
</tr>
</tbody>
</table>

* With just one CT in the Bus Section Bay
**Figure 6:** Configuration diagram for A31, configuration X01_1
Figure 7: Configuration diagram for A31, configuration X02
3.1.7 Description of 1 ph package B20A

One-phase version of the IED with two low-impedance differential protection zones and twelve CT inputs B20.

- Due to three available binary input modules, the B20A is intended for applications without need for dynamic Zone Selection such as substations with single busbar with or without bus-section breaker, breaker-and-a-half or double breaker arrangements. Three such IEDs offer cost effective solutions for such simple substation arrangements with up to twelve CT inputs.
• This version can be used with external auxiliary 3-phase to 1-phase summation current transformers with different turns ratio for each phase.

Figure 9: Configuration diagram for B20, configuration X01
### Figure 10: Configuration diagram for B21, configuration X01

#### 3.1.8 Description of 1 ph package B31A

One-phase version of the IED with two low-impedance differential protection zones and twenty-four CT inputs B31A.
• The IED is intended for busbar protection applications in big substations where dynamic Zone Selection, quite large number of binary inputs and outputs and many CT inputs are needed. The IED includes two differential zones and twenty-four CT inputs. Note that binary inputs can be shared between phases by including the LDCM communication module. This simplifies panel wiring and saves IO boards.

• This version can be used with external auxiliary 3-phase to 1-phase summation current transformers with different turns ratio for each phase.
Figure 11: Configuration diagram for B31, configuration X01

<table>
<thead>
<tr>
<th>Version of REB670</th>
<th>Number of Feeders in Both Busbar Sections</th>
<th>Number of REB670 Required by the Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>REB670(B20 – X01) 1-Phase, 12 Bays, 2 Zones for Simple Station Layout 12 Al</td>
<td>22*</td>
<td>6</td>
</tr>
<tr>
<td>REB670(B21 – X01) 1-Phase, 12 Bays, 2 Zones for Simple Station Layout 12 Al</td>
<td>22*</td>
<td>6</td>
</tr>
<tr>
<td>REB670(B31 – X01) 1-Phase, 24 Bays, 2 Zones for Simple Station Layout 24 Al</td>
<td>46*</td>
<td>6</td>
</tr>
</tbody>
</table>

* With Just one CT in the Bus Section Bay
Figure 12: Configuration diagram for B31, configuration X02
Figure 13: Configuration diagram for B31, configuration X03
Section 4 Analog inputs

4.1 Introduction

Analog input channels must be configured and set properly in order to get correct measurement results and correct protection operations. For power measuring, all directional and differential functions, the directions of the input currents must be defined in order to reflect the way the current transformers are installed/connected in the field (primary and secondary connections). Measuring and protection algorithms in the IED use primary system quantities. Setting values are in primary quantities as well and it is important to set the data about the connected current and voltage transformers properly.

An AISVBAS reference PhaseAngleRef can be defined to facilitate service values reading. This analog channel's phase angle will always be fixed to zero degrees and remaining analog channel's phase angle information will be shown in relation to this analog input. During testing and commissioning of the IED, the reference channel can be changed to facilitate testing and service values reading.

The IED has the ability to receive analog values from primary equipment, that are sampled by Merging units (MU) connected to a process bus, via the IEC 61850-9-2 LE protocol.

The availability of VT inputs depends on the ordered transformer input module (TRM) type.

4.2 Setting guidelines

The available setting parameters related to analog inputs are depending on the actual hardware (TRM) and the logic configuration made in PCM600.

If a second TRM is used, at least one TRM channel must be configured to get the service values. However, the MU physical channel must be configured to get service values from that channel.
### 4.2.1 Setting of the phase reference channel

All phase angles are calculated in relation to a defined reference. An appropriate analog input channel is selected and used as phase reference. The parameter `PhaseAngleRef` defines the analog channel that is used as phase angle reference.

#### 4.2.1.1 Example

Usually the A phase-to-ground voltage connected to the first VT channel number of the transformer input module (TRM) is selected as the phase reference. The first VT channel number depends on the type of transformer input module.

For a TRM with 6 current and 6 voltage inputs the first VT channel is 7. The setting `PhaseAngleRef=7` shall be used if the phase reference voltage is connected to that channel.

### 4.2.2 Setting of current channels

The direction of a current to the IED is depending on the connection of the CT. Unless indicated otherwise, the main CTs are supposed to be Wye (star) connected and can be connected with the grounding point to the object or from the object. This information must be set in the IED. The convention of the directionality is defined as follows: A positive value of current, power, and so on means that the quantity has the direction into the object and a negative value means direction out from the object. For directional functions the direction into the object is defined as Forward and the direction out from the object is defined as Reverse. See Figure 14.

A positive value of current, power, and so on (forward) means that the quantity flows towards the object. A negative value of current, power, and so on (reverse) means that the quantity flows away from the object. See Figure 14.

![Diagram showing directionality convention](en05000456-2.vsd)

**Figure 14:** Internal convention of the directionality in the IED

With correct setting of the primary CT direction, `CT_WyePoint` set to `FromObject` or `ToObject`, a positive quantities always flowing towards the protected object and a direction defined as Forward always is looking towards the protected object. The following examples show the principle.
### 4.2.2.1 Example 1

Two IEDs used for protection of two objects.

![Diagram](image1.png)

**Figure 15**: Example how to set CT_WyePoint parameters in the IED

Figure 15 shows the normal case where the objects have their own CTs. The settings for CT direction shall be done according to the figure. To protect the line, direction of the directional functions of the line protection shall be set to *Forward*. This means that the protection is looking towards the line.

### 4.2.2.2 Example 2

Two IEDs used for protection of two objects and sharing a CT.

**Figure 16**: Example how to set CT_WyePoint parameters in the IED

This example is similar to example 1, but here the transformer is feeding just one line and the line protection uses the same CT as the transformer protection does. The CT direction is set with different reference objects for the two IEDs though it is the same current from the same CT that is feeding the two IEDs. With these settings, the directional functions of the line protection shall be set to *Forward* to look towards the line.

### 4.2.2.3 Example 3

One IED used to protect two objects.
**Figure 17: Example how to set CT_WyePoint parameters in the IED**

In this example, one IED includes both transformer and line protections and the line protection uses the same CT as the transformer protection does. For both current input channels, the CT direction is set with the transformer as reference object. This means that the direction *Forward* for the line protection is towards the transformer. To look towards the line, the direction of the directional functions of the line protection must be set to *Reverse*. The direction *Forward/Reverse* is related to the reference object that is the transformer in this case.

When a function is set to *Reverse* and shall protect an object in reverse direction, it shall be noted that some directional functions are not symmetrical regarding the reach in forward and reverse direction. It is in first hand the reach of the directional criteria that can differ. Normally it is not any limitation but it is advisable to have it in mind and check if it is acceptable for the application in question.

If the IED has sufficient number of analog current inputs, an alternative solution is shown in Figure 18. The same currents are fed to two separate groups of inputs and the line and transformer protection functions are configured to the different inputs. The CT direction for the current channels to the line protection is set with the line as reference object and the directional functions of the line protection shall be set to *Forward* to protect the line.
Figure 18: Example how to set CT_WyePoint parameters in the IED
For busbar protection, it is possible to set the \( CT_{\text{WyePoint}} \) parameters in two ways.

The first solution will be to use busbar as a reference object. In that case for all CT inputs marked with 1 in Figure 19, set \( CT_{\text{WyePoint}} = \text{ToObject} \), and for all CT inputs marked with 2 in Figure 19, set \( CT_{\text{WyePoint}} = \text{FromObject} \).

The second solution will be to use all connected bays as reference objects. In that case for all CT inputs marked with 1 in Figure 19, set \( CT_{\text{WyePoint}} = \text{FromObject} \), and for all CT inputs marked with 2 in Figure 19, set \( CT_{\text{WyePoint}} = \text{ToObject} \).

Regardless which one of the above two options is selected, busbar differential protection will behave correctly.

The main CT ratios must also be set. This is done by setting the two parameters \( CT_{\text{sec}} \) and \( CT_{\text{prim}} \) for each current channel. For a 1000/5 A CT, the following settings shall be used:
• \( CT_{prim} = 1000 \) (value in A)
• \( CT_{sec} = 5 \) (value in A).

### 4.2.2.4 Examples on how to connect, configure and set CT inputs for most commonly used CT connections

Figure 20 defines the marking of current transformer terminals commonly used around the world:

![Diagram of CT terminals](en06000641.vsd)

In the SMAI function block, you have to set if the SMAI block is measuring current or voltage. This is done with the parameter: \textit{AnalogInputType}: Current/Voltage. The \textit{ConnectionType}: phase-phase/phase-ground and \textit{GlobalBaseSel}.

**Figure 20: Commonly used markings of CT terminals**

Where:

a) is symbol and terminal marking used in this document. Terminals marked with a square indicates the primary and secondary winding terminals with the same (that is, positive) polarity

b) and c) are equivalent symbols and terminal marking used by IEC (ANSI) standard for CTs. Note that for these two cases the CT polarity marking is correct!

It shall be noted that depending on national standard and utility practices, the rated secondary current of a CT has typically one of the following values:

- 1A
- 5A

However, in some cases, the following rated secondary currents are used as well:

- 2A
- 10A

The IED fully supports all of these rated secondary values.
It is recommended to:
- use 1A rated CT input into the IED in order to connect CTs with 1A and 2A secondary rating
- use 5A rated CT input into the IED in order to connect CTs with 5A and 10A secondary rating

4.2.2.5 Example on how to connect a wye connected three-phase CT set to the IED

Figure 21 gives an example about the wiring of a wye connected two-phase CT set to the IED. It gives an overview of the actions which are needed to make this measurement available to the built-in protection and control functions within the IED as well.

For correct terminal designations, see the connection diagrams valid for the delivered IED.

Figure 21: Wye connected three-phase CT set with wye point towards the protected object
Where:

1) The drawing shows how to connect three individual phase currents from a wye connected three-phase CT set to the three CT inputs of the IED.

2) The current inputs are located in the TRM. It shall be noted that for all these current inputs the following setting values shall be entered for the example shown in Figure 21.
   - \( CT_{prim} = 600A \)
   - \( CT_{sec} = 5A \)
   - \( CT_{StarPoint} = ToObject \)

   Ratio of the first two parameters is only used inside the IED. The third parameter (\( CT_{StarPoint} = ToObject \)) as set in this example causes no change on the measured currents. In other words, currents are already measured towards the protected object.

3) These three connections are the links between the three current inputs and the three input channels of the preprocessing function block 4). Depending on the type of functions, which need this current information, more than one preprocessing block might be connected in parallel to the same three physical CT inputs.

4) The preprocessing block that has the task to digitally filter the connected analog inputs and calculate:
   - fundamental frequency phasors for all three input channels
   - harmonic content for all three input channels
   - positive, negative and zero sequence quantities by using the fundamental frequency phasors for the first three input channels (channel one taken as reference for sequence quantities)

   These calculated values are then available for all built-in protection and control functions within the IED, which are connected to this preprocessing function block. For this application most of the preprocessing settings can be left to the default values.
   - If frequency tracking and compensation is required (this feature is typically required only for IEDs installed in power plants), then the setting parameters DFTReference shall be set accordingly.
   - Section SMAI in this manual provides information on adaptive frequency tracking for the signal matrix for analogue inputs (SMAI).

5) \( AI3P \) in the SMAI function block is a grouped signal which contains all the data about the phases A, B, C and neutral quantity; in particular the data about fundamental frequency phasors, harmonic content and positive sequence, negative and zero sequence quantities are available.

   \( AI1, AI2, AI3, AI4 \) are the output signals from the SMAI function block which contain the fundamental frequency phasors and the harmonic content of the corresponding input channels of the preprocessing function block.

   \( AIN \) is the signal which contains the fundamental frequency phasors and the harmonic content of the neutral quantity. In this example, \( GRP2N \) is not connected so this data is calculated by the preprocessing function block on the basis of the inputs \( GRP\_A, GRP\_B \) and \( GRP\_C \). If \( GRP2N \) is connected, the data reflects the measured value of \( GRP2N \).

Another alternative is to have the wye point of the three-phase CT set as shown in figure 22:
Figure 22: Wye connected three-phase CT set with its wye point away from the protected object

In the example, everything is done in a similar way as in the above described example (Figure 21). The only difference is the setting of the parameter CTStarPoint of the used current inputs on the TRM (item 2 in Figure 22 and 21):

- CTprim=600A
- CTsec=5A
- CTWyePoint=FromObject

The ratio of the first two parameters is only used inside the IED. The third parameter as set in this example will negate the measured currents in order to ensure that the currents are measured towards the protected object within the IED.

A third alternative is to have the residual/neutral current from the three-phase CT set connected to the IED as shown in Figure 22.
Figure 23: Wye connected three-phase CT set with its wye point away from the protected object and the residual/neutral current connected to the IED

Where:

1) Shows how to connect three individual phase currents from a wye connected three-phase CT set to the three CT inputs of the IED.

2) Shows how to connect residual/neutral current from the three-phase CT set to the fourth input in the IED. It shall be noted that if this connection is not made, the IED will still calculate this current internally by vectorial summation of the three individual phase currents.

3) Is the TRM where these current inputs are located. It shall be noted that for all these current inputs the following setting values shall be entered.

- $CT_{prim}=800\, \text{A}$
- $CT_{sec}=1\, \text{A}$
- $CTStarPoint=\text{FromObject}$
- $ConnectionType=\text{Ph-N}$

The ratio of the first two parameters is only used inside the IED. The third parameter as set in this example will have no influence on measured currents (that is, currents are already measured towards the protected object).

4) Are three connections made in the Signal Matrix tool (SMT) and Application configuration tool (ACT), which connects these three current inputs to the first three input channels on the preprocessing function block 6). Depending on the type of functions, which need this current information, more than one preprocessing block might be connected in parallel to these three CT inputs.

Table continues on next page
5) Is a connection made in the Signal Matrix tool (SMT) and Application configuration tool (ACT), which connects the residual/neutral current input to the fourth input channel of the preprocessing function block 6). Note that this connection in SMT shall not be done if the residual/neutral current is not connected to the IED.

6) Is a Preprocessing block that has the task to digitally filter the connected analog inputs and calculate:
   - fundamental frequency phasors for all input channels
   - harmonic content for all input channels
   - positive, negative and zero sequence quantities by using the fundamental frequency phasors of the first three input channels (channel one taken as reference for sequence quantities)

These calculated values are then available for all built-in protection and control functions within the IED, which are connected to this preprocessing function block in the configuration tool. For this application, most of the preprocessing settings can be left to the default values. If frequency tracking and compensation is required (this feature is typically required only for IEDs installed in the generating stations), then the setting parameters DFTReference shall be set accordingly.

### 4.2.2.6 Example how to connect delta connected three-phase CT set to the IED

Figure 24 gives an example how to connect a delta connected three-phase CT set to the IED. It gives an overview of the required actions by the user in order to make this measurement available to the built-in protection and control functions in the IED as well.

For correct terminal designations, see the connection diagrams valid for the delivered IED.
Figure 24: Delta DAB connected three-phase CT set

Where:

1) shows how to connect three individual phase currents from a delta connected three-phase CT set to three CT inputs of the IED.

2) is the TRM where these current inputs are located. It shall be noted that for all these current inputs the following setting values shall be entered.

   - $CT_{\text{prim}}=600\, \text{A}$
   - $CT_{\text{sec}}=5\, \text{A}$

   - $CTWyePoint=\text{ToObject}$
   - $ConnectionType=\text{Ph-Ph}$

3) are three connections made in Signal Matrix Tool (SMT), Application configuration tool (ACT), which connect these three current inputs to first three input channels of the preprocessing function block 4). Depending on the type of functions which need this current information, more than one preprocessing block might be connected in parallel to these three CT inputs.

4) is a Preprocessing block that has the task to digitally filter the connected analog inputs and calculate:

   - fundamental frequency phasors for all three input channels
   - harmonic content for all three input channels
   - positive, negative and zero sequence quantities by using the fundamental frequency phasors for the first three input channels (channel one taken as reference for sequence quantities)

These calculated values are then available for all built-in protection and control functions within the IED, which are connected to this preprocessing function block. For this application most of the preprocessing settings can be left to the default values. If frequency tracking and compensation is required (this feature is typically required only for IEDs installed in the generating stations) then the setting parameters $DFTReference$ shall be set accordingly.
Another alternative is to have the delta connected CT set as shown in figure 25:

![Diagram of Delta DAC connected three-phase CT set]

**Figure 25: Delta DAC connected three-phase CT set**

In this case, everything is done in a similar way as in the above described example, except that for all used current inputs on the TRM the following setting parameters shall be entered:

\[
CT_{\text{prim}} = 800\text{A}
\]

\[
CT_{\text{sec}} = 1\text{A}
\]

- \text{CTWyePoint} = \text{ToObject}
- \text{ConnectionType} = \text{Ph-Ph}

It is important to notice the references in SMAI. As inputs at \text{Ph-Ph} are expected to be A-B, B-C respectively C-A we need to tilt 180° by setting \text{ToObject}.

### 4.2.2.7 Example how to connect single-phase CT to the IED

Figure 26 gives an example how to connect the single-phase CT to the IED. It gives an overview of the required actions by the user in order to make this measurement available to the built-in protection and control functions within the IED as well.

For correct terminal designations, see the connection diagrams valid for the delivered IED.
Figure 26: Connections for single-phase CT input

Where:

1) shows how to connect single-phase CT input in the IED.

2) is TRM where these current inputs are located. It shall be noted that for all these current inputs the following setting values shall be entered.

   For connection (a) shown in Figure 26:
   \[ CT_{prim} = 1000 \text{ A} \]
   \[ CT_{sec} = 1 \text{ A} \]
   \[ CT_{WyePoint} = ToObject \]

   For connection (b) shown in Figure 26:
   \[ CT_{prim} = 1000 \text{ A} \]
   \[ CT_{sec} = 1 \text{ A} \]
   \[ CT_{WyePoint} = FromObject \]

3) shows the connection made in SMT tool, which connect this CT input to the fourth input channel of the preprocessing function block 4).

4) is a Preprocessing block that has the task to digitally filter the connected analog inputs and calculate values. The calculated values are then available for all built-in protection and control functions within the IED, which are connected to this preprocessing function block.

If frequency tracking and compensation is required (this feature is typically required only for IEDs installed in the power plants) then the setting parameters DFTReference shall be set accordingly.

4.2.3 Relationships between setting parameter Base Current, CT rated primary current and minimum pickup of a protection IED

Note that for all line protection applications (e.g. distance protection or line differential protection) the parameter Base Current (i.e. IBase setting in the IED) used by the relevant protection function, shall always be set equal to the largest rated CT primary current among all CTs involved in the protection scheme. The rated CT primary current value is set as parameter CTPrim under the IED TRM settings.

For all other protection applications (e.g. generator, shunt reactor, shunt capacitor and transformer protection) it is typically desirable to set IBase parameter equal to the rated current
of the protected object. However this is only recommended to do if the rated current of the protected object is within the range of 40% to 120% of the selected CT rated primary current. If for any reason (e.g. high maximum short circuit current) the rated current of the protected object is less than 40% of the rated CT primary current, it is strongly recommended to set the parameter \( i_{\text{Base}} \) in the IED to be equal to the largest rated CT primary current among all CTs involved in the protection scheme and installed on the same voltage level. This will effectively make the protection scheme less sensitive; however, such measures are necessary in order to avoid possible problems with loss of the measurement accuracy in the IED.

Regardless of the applied relationship between the \( i_{\text{Base}} \) parameter and the rated CT primary current, the corresponding minimum pickup of the function on the CT secondary side must always be verified. It is strongly recommended that the minimum pickup of any instantaneous protection function (e.g. differential, restricted earth fault, distance, instantaneous overcurrent, etc.) shall under no circumstances be less than 4% of the used IED CT input rating (i.e. 1A or 5A). This corresponds to 40mA secondary for IED 1A rated inputs and to 200mA secondary for IED 5A rated inputs used by the function. This shall be individually verified for all current inputs involved in the protection scheme.

Note that exceptions from the above 4% rule may be acceptable for very special applications (e.g. when Multipurpose filter SMAIHPAC is involved in the protection scheme).

### 4.2.4 Setting of voltage channels

As the IED uses primary system quantities, the main VT ratios must be known to the IED. This is done by setting the two parameters \( V_{\text{sec}} \) and \( V_{\text{prim}} \) for each voltage channel. The phase-to-phase value can be used even if each channel is connected to a phase-to-ground voltage from the VT.

#### 4.2.4.1 Example

Consider a VT with the following data:

\[
\frac{132kV}{\sqrt{3}} / \frac{120V}{\sqrt{3}}
\]

(Equation 1)

The following setting should be used: \( V_{\text{prim}}=132 \) (value in kV) \( V_{\text{sec}}=120 \) (value in V)

#### 4.2.4.2 Examples how to connect, configure and set VT inputs for most commonly used VT connections

Figure 27 defines the marking of voltage transformer terminals commonly used around the world.
Figure 27: Commonly used markings of VT terminals

Where:

a) is the symbol and terminal marking used in this document. Terminals marked with a square indicate the primary and secondary winding terminals with the same (positive) polarity

b) is the equivalent symbol and terminal marking used by IEC (ANSI) standard for phase-to-ground connected VTs

c) is the equivalent symbol and terminal marking used by IEC (ANSI) standard for open delta connected VTs

d) is the equivalent symbol and terminal marking used by IEC (ANSI) standard for phase-to-phase connected VTs

It shall be noted that depending on national standard and utility practices the rated secondary voltage of a VT has typically one of the following values:

- 100 V
- 110 V
- 115 V
- 120 V
- 230 V

The IED fully supports all of these values and most of them will be shown in the following examples.

4.2.4.3 Examples on how to connect a three phase-to-ground connected VT to the IED

Figure 28 gives an example on how to connect the three phase-to-ground connected VT to the IED. It gives an overview of required actions by the user in order to make this measurement available to the built-in protection and control functions within the IED.

For correct terminal designations, see the connection diagrams valid for the delivered IED.
Figure 28: A Three phase-to-ground connected VT

Figure 29: A two phase-to-earth connected VT
Where:

1) shows how to connect three secondary phase-to-ground voltages to three VT inputs on the IED

2) is the TRM where these three voltage inputs are located. For these three voltage inputs, the following setting values shall be entered:

\[ VT_{prim} = 110 \text{kV} \]
\[ VT_{sec} = 110 \text{V} \]

Inside the IED, only the ratio of these two parameters is used. It shall be noted that the ratio of the entered values exactly corresponds to ratio of one individual VT.

\[
\frac{66}{110} = \frac{66}{\sqrt{3}} \times \frac{\sqrt{3}}{\sqrt{3}}
\]

(Equation 2)

3) are three connections made in Signal Matrix Tool (SMT), which connect these three voltage inputs to first three input channels of the preprocessing function block 5). Depending on the type of functions which need this voltage information, more then one preprocessing block might be connected in parallel to these three VT inputs.

4) shows that in this example the fourth (that is, residual) input channel of the preprocessing block is not connected in SMT tool. Thus the preprocessing block will automatically calculate \(3V_o\) inside by vectorial sum from the three phase to ground voltages connected to the first three input channels of the same preprocessing block. Alternatively, the fourth input channel can be connected to open delta VT input, as shown in Figure 31.

5) is a Preprocessing block that has the task to digitally filter the connected analog inputs and calculate:

- fundamental frequency phasors for all input channels
- harmonic content for all input channels
- positive, negative and zero sequence quantities by using the fundamental frequency phasors for the first three input channels (channel one taken as reference for sequence quantities)

These calculated values are then available for all built-in protection and control functions within the IED, which are connected to this preprocessing function block in the configuration tool. For this application most of the preprocessing settings can be left to the default values. However the following settings shall be set as shown here:

- \(V_{Base}=66 \text{kV}\) (that is, rated Ph-Ph voltage)
- If frequency tracking and compensation is required (this feature is typically required only for IEDs installed in the generating stations) then the setting parameters \(DFTReference\) shall be set accordingly.

### 4.2.4.4 Example on how to connect a phase-to-phase connected VT to the IED

Figure 30 gives an example how to connect a phase-to-phase connected VT to the IED. It gives an overview of the required actions by the user in order to make this measurement available to the built-in protection and control functions within the IED. It shall be noted that this VT connection is only used on lower voltage levels (that is, rated primary voltage below 40 kV).
Figure 30: A Two phase-to-phase connected VT

Where:

1) shows how to connect the secondary side of a phase-to-phase VT to the VT inputs on the IED
2) is the TRM where these three voltage inputs are located. It shall be noted that for these three voltage inputs the following setting values shall be entered:
   - VTprim=13.8 kV
   - VTsec=120 V
   Please note that inside the IED only ratio of these two parameters is used.

Table continues on next page
3) are three connections made in the Signal Matrix tool (SMT), Application configuration tool (ACT), which connects these three voltage inputs to first three input channels of the preprocessing function block 5). Depending on the type of functions, which need this voltage information, more than one preprocessing block might be connected in parallel to these three VT inputs.

4) shows that in this example the fourth (that is, residual) input channel of the preprocessing block is not connected in SMT. Note. If the parameters $V_A, V_B, V_C, V_N$ should be used the open delta must be connected here.

5) Preprocessing block has a task to digitally filter the connected analog inputs and calculate:
   - fundamental frequency phasors for all four input channels
   - harmonic content for all four input channels
   - positive, negative and zero sequence quantities by using the fundamental frequency phasors for the first three input channels (channel one taken as reference for sequence quantities)

These calculated values are then available for all built-in protection and control functions within the IED, which are connected to this preprocessing function block in the configuration tool. For this application most of the preprocessing settings can be left to the default values. However the following settings shall be set as shown here:

- **ConnectionType**=Ph-Ph
- **VBase**=13.8 kV

If frequency tracking and compensation is required (this feature is typically required only for IEDs installed in the generating stations) then the setting parameters **DFTReference** shall be set accordingly.

### 4.2.4.5 Example on how to connect an open delta VT to the IED for high impedance grounded or ungrounded networks

Figure 31 gives an example about the wiring of an open delta VT to the IED for high impedance grounded or ungrounded power systems. It shall be noted that this type of VT connection presents a secondary voltage proportional to $3V_0$ to the IED.

In case of a solid ground fault close to the VT location the primary value of $3V_0$ will be equal to:

$$3V_0 = \sqrt{3} \cdot V_{Ph-Pe} = 3 \cdot V_{Ph-Gnd}$$

(Equation 3)

The primary rated voltage of an open Delta VT is always equal to $V_{Ph-Gnd}$. Three series connected VT secondary windings gives a secondary voltage equal to three times the individual VT secondary winding rating. Thus the secondary windings of open delta VTs quite often have a secondary rated voltage equal to one third of the rated phase-to-phase VT secondary voltage (110/3V in this particular example).

Figure 31 gives overview of required actions by the user in order to make this measurement available to the built-in protection and control functions within the IED as well.
**Figure 31:** Open delta connected VT in high impedance grounded power system
Where:

1) shows how to connect the secondary side of the open delta VT to one VT input on the IED.

+3Vo shall be connected to the IED

2) is the TRM where this voltage input is located. It shall be noted that for this voltage input the following setting values shall be entered:

\[ VT_{prim} = \sqrt{3} \times 6.6 = 11.43kV \]  

(Equation 4)

\[ VT_{sec} = 3 \times \frac{110}{3} = 110V \]  

(Equation 5)

Inside the IED, only the ratio of these two parameters is used. It shall be noted that the ratio of the entered values exactly corresponds to ratio of one individual open delta VT.

\[ \frac{\sqrt{3} \times 6.6}{110} = \frac{6.6/ \sqrt{3}}{110/3} \]  

(Equation 6)

3) shows that in this example the first three input channel of the preprocessing block is not connected in SMT tool or ACT tool.

4) shows the connection made in Signal Matrix Tool (SMT), Application configuration tool (ACT), which connect this voltage input to the fourth input channel of the preprocessing function block 5).

5) is a Preprocessing block that has the task to digitally filter the connected analog input and calculate:

- fundamental frequency phasors for all four input channels
- harmonic content for all four input channels
- positive, negative and zero sequence quantities by using the fundamental frequency phasors for the first three input channels (channel one taken as reference for sequence quantities)

These calculated values are then available for all built-in protection and control functions within the IED, which are connected to this preprocessing function block in the configuration tool. For this application most of the preprocessing settings can be left to the default values.

If frequency tracking and compensation is required (this feature is typically required only for IEDs installed in the generating stations) then the setting parameters DFTReference shall be set accordingly.

### 4.2.4.6 Example how to connect the open delta VT to the IED for low impedance grounded or solidly grounded power systems

Figure 32 gives an example about the connection of an open delta VT to the IED for low impedance grounded or solidly grounded power systems. It shall be noted that this type of VT connection presents secondary voltage proportional to 3V₀ to the IED.
In case of a solid ground fault close to the VT location the primary value of $3V_o$ will be equal to:

$$3V_o = \frac{V_{ph-ph}}{\sqrt{3}} = V_{ph-Gnd}$$

(Equation 7)

The primary rated voltage of such VT is always equal to VPh-Gnd. Therefore, three series connected VT secondary windings will give the secondary voltage equal only to one individual VT secondary winding rating. Thus the secondary windings of such open delta VTs quite often has a secondary rated voltage close to rated phase-to-phase VT secondary voltage, that is, 115V or 115/\sqrt{3}V as in this particular example. Figure 32 gives an overview of the actions which are needed to make this measurement available to the built-in protection and control functions within the IED.

**Figure 32: Open delta connected VT in low impedance or solidly grounded power system**
Where:

1) shows how to connect the secondary side of open delta VT to one VT input in the IED.

+3Vo shall be connected to the IED.

2) is TRM where this voltage input is located. It shall be noted that for this voltage input the following setting values shall be entered:

\[
VT_{prim} = \sqrt{3} \cdot \frac{138}{\sqrt{3}} = 138kV
\]

(Equation 8)

\[
VT_{sec} = \sqrt{3} \cdot \frac{115}{\sqrt{3}} = 115V
\]

(Equation 9)

Inside the IED, only the ratio of these two parameters is used. It shall be noted that the ratio of the entered values exactly corresponds to ratio of one individual open delta VT.

\[
\frac{138}{115} = \frac{138}{\sqrt{3}} \cdot \frac{115}{\sqrt{3}}
\]

(Equation 10)

3) shows that in this example the first three input channel of the preprocessing block is not connected in SMT tool.

4) shows the connection made in Signal Matrix Tool (SMT), which connect this voltage input to the fourth input channel of the preprocessing function block 4).

5) preprocessing block has a task to digitally filter the connected analog inputs and calculate:

- fundamental frequency phasors for all four input channels
- harmonic content for all four input channels
- positive, negative and zero sequence quantities by using the fundamental frequency phasors for the first three input channels (channel one taken as reference for sequence quantities)

These calculated values are then available for all built-in protection and control functions within the IED, which are connected to this preprocessing function block in the configuration tool. For this application most of the preprocessing settings can be left to the default values.

If frequency tracking and compensation is required (this feature is typically required only for IEDs installed in the generating stations) then the setting parameters \textit{DFTReference} shall be set accordingly.
Figure 33: Local human-machine interface

The LHMI of the IED contains the following elements

- Keypad
- Display (LCD)
- LED indicators
- Communication port for PCM600
The LHMI is used for setting, monitoring and controlling.

## 5.1 Display

The LHMI includes a graphical monochrome liquid crystal display (LCD) with a resolution of 320 x 240 pixels. The character size can vary.

The display view is divided into four basic areas.

*Figure 34: Display layout*

1. Path
2. Content
3. Status
4. Scroll bar (appears when needed)

The function key button panel shows on request what actions are possible with the function buttons. Each function button has a LED indication that can be used as a feedback signal for the function button control action. The LED is connected to the required signal with PCM600.
The indication LED panel shows on request the alarm text labels for the indication LEDs. Three indication LED pages are available.

Figure 36: Indication LED panel

The function button and indication LED panels are not visible at the same time. Each panel is shown by pressing one of the function buttons or the Multipage button. Pressing the ESC button clears the panel from the display. Both panels have a dynamic width that depends on the label string length.

5.2 LEDs

The LHMI includes three protection status LEDs above the display: Normal, Pickup and Trip.
There are 15 programmable indication LEDs on the front of the LHMI. Each LED can indicate three states with the colors: green, yellow and red. The texts related to each three-color LED are divided into three panels.

There are 3 separate panels of LEDs available. The 15 physical three-color LEDs in one LED group can indicate 45 different signals. Altogether, 135 signals can be indicated since there are three LED groups. The LEDs are lit according to priority, with red being the highest and green the lowest priority. For example, if on one panel there is an indication that requires the green LED to be lit, and on another panel there is an indication that requires the red LED to be lit, the red LED takes priority and is lit. The LEDs can be configured with PCM600 and the operation mode can be selected with the LHMI or PCM600.

Information panels for the indication LEDs are shown by pressing the Multipage button. Pressing that button cycles through the three pages. A lit or un-acknowledged LED is indicated with a highlight. Such lines can be selected by using the Up/Down arrow buttons. Pressing the Enter key shows details about the selected LED. Pressing the ESC button exits from information pop-ups as well as from the LED panel as such.

The Multipage button has a LED. This LED is lit whenever any LED on any panel is lit. If there are un-acknowledged indication LEDs, then the Multipage LED blinks. To acknowledge LEDs, press the Clear button to enter the Reset menu (refer to description of this menu for details).

There are two additional LEDs which are next to the control buttons and . These LEDs can indicate the status of two arbitrary binary signals by configuring the OPENCLOSE_LED function block. For instance, OPENCLOSE_LED can be connected to a circuit breaker to indicate the breaker open/close status on the LEDs.

![Image](IEC16000076-1-en.vsd)

**Figure 37:** OPENCLOSE_LED connected to SXCBR

### 5.3 Keypad

The LHMI keypad contains push-buttons which are used to navigate in different views or menus. The push-buttons are also used to acknowledge alarms, reset indications, provide help and switch between local and remote control mode.

The keypad also contains programmable push-buttons that can be configured either as menu shortcut or control buttons.
Figure 38: LHMI keypad with object control, navigation and command push-buttons and RJ-45 communication port

1...5 Function button
6 Close
7 Open
8 Escape
9 Left
10 Down
11 Up
12 Right
13 Key
14 Enter
15 Remote/Local
16 Uplink LED
17 Not in use
18 Multipage
19 Menu
20 Clear
21 Help
5.4 Local HMI functionality

5.4.1 Protection and alarm indication

Protection indicators

The protection indicator LEDs are Normal, Pickup and Trip.

Table 9: Normal LED (green)

<table>
<thead>
<tr>
<th>LED state</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>Auxiliary supply voltage is disconnected.</td>
</tr>
<tr>
<td>On</td>
<td>Normal operation.</td>
</tr>
<tr>
<td>Flashing</td>
<td>Internal fault has occurred.</td>
</tr>
</tbody>
</table>

Table 10: PickUp LED (yellow)

<table>
<thead>
<tr>
<th>LED state</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>Normal operation.</td>
</tr>
<tr>
<td>On</td>
<td>A protection function has picked up and an indication message is displayed. The pick up indication is latching and must be reset via communication, LHMI or binary input on the LEDGEN component. To open the reset menu on the LHMI, press [button].</td>
</tr>
<tr>
<td>Flashing</td>
<td>The IED is in test mode and protection functions are blocked, or the IEC61850 protocol is blocking one or more functions. The indication disappears when the IED is no longer in test mode and blocking is removed. The blocking of functions through the IEC61850 protocol can be reset in Main menu/Test/Reset IEC61850 Mod. The yellow LED changes to either On or Off state depending on the state of operation.</td>
</tr>
</tbody>
</table>

Table 11: Trip LED (red)

<table>
<thead>
<tr>
<th>LED state</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>Normal operation.</td>
</tr>
<tr>
<td>On</td>
<td>A protection function has tripped. An indication message is displayed if the auto-indication feature is enabled in the local HMI. The trip indication is latching and must be reset via communication, LHMI or binary input on the LEDGEN component. To open the reset menu on the LHMI, press [button].</td>
</tr>
<tr>
<td>Flashing</td>
<td>Configuration mode.</td>
</tr>
</tbody>
</table>
**Alarm indicators**

The 15 programmable three-color LEDs are used for alarm indication. An individual alarm/status signal, connected to any of the LED function blocks, can be assigned to one of the three LED colors when configuring the IED.

**Table 12: Alarm indications**

<table>
<thead>
<tr>
<th>LED state</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>Normal operation. All activation signals are off.</td>
</tr>
<tr>
<td>On</td>
<td>• Follow-S sequence: The activation signal is on.</td>
</tr>
<tr>
<td></td>
<td>• LatchedColl-S sequence: The activation signal is on, or it is off but the indication has not been acknowledged.</td>
</tr>
<tr>
<td></td>
<td>• LatchedAck-F-S sequence: The indication has been acknowledged, but the activation signal is still on.</td>
</tr>
<tr>
<td></td>
<td>• LatchedAck-S-F sequence: The activation signal is on, or it is off but the indication has not been acknowledged.</td>
</tr>
<tr>
<td></td>
<td>• LatchedReset-S sequence: The activation signal is on, or it is off but the indication has not been acknowledged.</td>
</tr>
<tr>
<td>Flashing</td>
<td>• Follow-F sequence: The activation signal is on.</td>
</tr>
<tr>
<td></td>
<td>• LatchedAck-F-S sequence: The activation signal is on, or it is off but the indication has not been acknowledged.</td>
</tr>
<tr>
<td></td>
<td>• LatchedAck-S-F sequence: The indication has been acknowledged, but the activation signal is still on.</td>
</tr>
</tbody>
</table>

**5.4.2 Parameter management**

The LHMI is used to access the relay parameters. Three types of parameters can be read and written.

- Numerical values
- String values
- Enumerated values

Numerical values are presented either in integer or in decimal format with minimum and maximum values. Character strings can be edited character by character. Enumerated values have a predefined set of selectable values.

**5.4.3 Front communication**

The RJ-45 port in the LHMI enables front communication.

- The green uplink LED on the left is lit when the cable is successfully connected to the port.
- The yellow LED is not used; it is always off.
Figure 39: RJ-45 communication port and green indicator LED

1 RJ-45 connector
2 Green indicator LED

The default IP address for the IED front port is 10.1.150.3 and the corresponding subnetwork mask is 255.255.254.0. It can be set through the local HMI path Main menu/Configuration/Communication/Ethernet configuration/Front port/AP_FRONT.

Ensure not to change the default IP address of the IED.

Do not connect the IED front port to a LAN. Connect only a single local PC with PCM600 to the front port. It is only intended for temporary use, such as commissioning and testing.
Section 6  Differential protection

6.1  Busbar differential protection

6.1.1  Identification

Two-zone busbar differential protections, four-or-eight three-phase CTs

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Busbar protection 2Z-3Ph-4/8CT, differential protection function</td>
<td>BZNTPDIF_x, (x = A, B)</td>
<td>3Id/I</td>
<td>87B</td>
</tr>
<tr>
<td>Busbar protection 2Z-3Ph-4/8CT, check zone protection function</td>
<td>BTCZPDIF</td>
<td>3Id/I</td>
<td>87B</td>
</tr>
<tr>
<td>Busbar protection 2Z-3Ph-4/8CT, bay function</td>
<td>BUTPTRC_Bx (1≤x≤4 or 1≤x≤8)</td>
<td>3Id/I</td>
<td>87B</td>
</tr>
<tr>
<td>Busbar protection 2Z-3Ph-4/8CT, zone interconnection function</td>
<td>BZITGGIO</td>
<td>3Id/I</td>
<td>87B</td>
</tr>
</tbody>
</table>
Two-zone busbar differential protections, twelve-or-twenty-four single-phase CTs

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Busbar protection 2Z-1Ph-12/24CT, differential protection function</td>
<td>BZNSPDIF_x, (x = A, B)</td>
<td>ld/I</td>
<td>87B</td>
</tr>
<tr>
<td>Busbar protection 2Z-1Ph-12/24CT, check zone protection function</td>
<td>BCZSPDF</td>
<td>ld/I</td>
<td>87B</td>
</tr>
<tr>
<td>Busbar protection 2Z-1Ph-12/24CT, bay function</td>
<td>BUSPTRC_Bx, (1≤x≤12 or 1≤x≤24)</td>
<td>ld/I</td>
<td>87B</td>
</tr>
<tr>
<td>Busbar protection 2Z-1Ph-12/24CT, zone interconnection function</td>
<td>BZISGGIO</td>
<td>ld/I</td>
<td>87B</td>
</tr>
</tbody>
</table>

Six-zone busbar differential protection, twenty-four, single-phase CT

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Busbar protection 6Z-1Ph-24CT, main function</td>
<td>BDZSGAPC</td>
<td>ld/I</td>
<td>87B</td>
</tr>
<tr>
<td>Busbar protection 6Z-1Ph-24CT, differential protection function</td>
<td>BZNPDIF_Zx, (1≤x≤6)</td>
<td>ld/I</td>
<td>87B</td>
</tr>
<tr>
<td>Busbar protection 6Z-1Ph-24CT, check zone protection function</td>
<td>BCZPDIF</td>
<td>ld/I</td>
<td>87B</td>
</tr>
<tr>
<td>Busbar protection 6Z-1Ph-24CT, feeder bay function</td>
<td>BFPTRC_Fx, (1≤x≤24)</td>
<td>ld/I</td>
<td>87B</td>
</tr>
<tr>
<td>Busbar protection 6Z-1Ph-24CT, bus interconnector bay function</td>
<td>BICPTRC_x, (1≤x≤5)</td>
<td>ld/I</td>
<td>87B</td>
</tr>
</tbody>
</table>

Status of primary switching object for busbar protection zone selection

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Status of primary switching object for Busbar protection zone selection</td>
<td>BDCGAPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
6.1.2 Basic applications

6.1.2.1 General

Basic types of applications for REB670 IED are shown and described in this chapter. For these applications usually three phase version of the IED, with two differential zones and four (or even eight) 3-phase CT inputs, is used.

6.1.2.2 Meshed corner application and T-connection application

The REB670 general differential function is suitable for application on mesh-corner arrangements. Mesh corners might have four or even up to six CT inputs and are basically simple single busbar arrangements. A similar application will occur when a T-protection is required for breaker-and-a-half or ring busbar arrangements.

![Image of meshed corner arrangement](ANSI11000237-1-en.vsd)

**Figure 40: Example of REB670 application on T-connection**

6.1.3 Busbar protection applications

A busbar protection scheme design depends very much on the substation arrangement. Complexity of the scheme can drastically vary from station to station. Typical applications problems, for the most common busbar protection schemes, are described in this chapter.

6.1.3.1 General

A busbar protection is a device which protects busbars against short-circuits and ground-faults. In the early development of electricity systems, no separate protection device was used for busbar protection. Remote end line protections were used as main protection for busbar faults. With the increased short-circuit power in the network separate differential IEDs for busbar protection have to be installed in order to limit the damage caused by the primary fault currents. At the same time, it is also a must to secure the network stability, as a delayed tripping for busbar faults can also lead to network instability, pole slip of near-by generators and even total system collapse.

For bus zone protection applications, it is extremely important to have good security since an unwanted operation might have severe consequences. The unwanted operation of the bus
differential IED will have the similar effect from the operational point of view as simultaneous faults on all power system elements connected to the bus. On the other hand, the IED has to be dependable as well. Failure to operate or even slow operation of the differential IED, in case of an actual internal fault, can have serious consequences. Human injuries, power system blackout, transient instability or considerable damage to the surrounding substation equipment and the close-by generators are some of the possible outcomes.

Therefore, Busbar differential protection must fulfill the following requirements:

1. Must be absolutely stable during all external faults. External faults are much more common than internal faults. The magnitude of external faults can be equal to the stations maximum short circuit capacity. Heavy CT-saturation due to high DC components and/or remanence at external faults must not lead to maloperation of the busbar differential protection. The security against misoperation must be extremely high due to the heavy impact on the overall network service.
2. Must have as short tripping time as possible in order to minimize the damage, minimize the danger and possible injury to the people who might be working in the station at the moment of internal fault, and secure the network stability.
3. Must be able to detect and securely operate for internal faults even with heavy CT saturation. The protection must also be sensitive enough to operate for minimum fault currents, which sometimes can be lower than the maximum load currents.
4. Must be able to selectively detect faults and trip only the faulty part of the busbar system.
5. Must be secure against maloperation due to auxiliary contact failure, possible human mistakes and faults in the secondary circuits and so on.

6.1.3.2 Differential protection

The basic concept for any differential IED is that the sum of all currents, which flow to and from the protection zone, must be equal to zero. If this is not the case, an internal fault has occurred. This is practically a direct use of well known Kirchhoff's first law. However, busbar differential IEDs do not measure directly the primary currents in the high voltage conductors, but the secondary currents of magnetic core current transformers (CTs), which are installed in all high-voltage bays connected to the busbar.

Therefore, the busbar differential IED is unique in this respect, that usually quite a few CTs, often with very different ratios and classes, are connected to the same differential protection zone. Because the magnetic core current transformers are non-linear measuring devices, under high current conditions in the primary CT circuits the individual secondary CT currents can be drastically different from the original primary currents. This is caused by CT saturation, a phenomenon that is well known to protection engineers. During the time when any of the current transformer connected to the differential IED is saturated, the sum of all CT secondary currents will not be equal to zero and the IED will measure false differential current. This phenomenon is especially predominant for busbar differential protection applications, since it has the strong tendency to cause unwanted operation of the differential IED.

Remanence in the magnetic core of a current transformer is an additional factor, which can influence the secondary CT current. It can improve or reduce the capability of the current transformer to properly transfer the primary current to the secondary side. However, the CT remanence is a random parameter and it is not possible in practice to precisely predict it.

Another, and maybe less known, transient phenomenon appears in the CT secondary circuit at the instant when a high primary current is interrupted. It is particularly dominant if the HV circuit breaker chops the primary current before its natural zero crossing. This phenomenon is manifested as an exponentially decaying DC current component in the CT secondary circuit. This
secondary DC current has no corresponding primary current in the power system. The phenomenon can be simply explained as a discharge of the magnetic energy stored in the magnetic core of the current transformer during the high primary current condition. Depending on the type and design of the current transformer this discharging current can have a time constant in the order of a hundred milliseconds.

Consequently, all these phenomena have to be considered during the design stage of a busbar differential IED in order to prevent the unwanted operation of the IED during external fault conditions.

The analog generation of the busbar differential IEDs (KA2, 87B, RADHA, RADSS, REB 103) generally solves all these problems caused by the CT non-linear characteristics by using the galvanic connection between the secondary circuits of all CTs connected to the protected zone. These IEDs are designed in such a way that the current distribution through the IED differential branch during all transient conditions caused by non-linearity of the CTs will not cause the unwanted operation of the differential IED. In order to obtain the required secondary CT current distribution, the resistive burden in the individual CT secondary circuits must be kept below the pre-calculated value in order to guaranty the stability of the IED.

In new numerical protection IEDs, all CT and VT inputs are galvanically separated from each other. All analog input quantities are sampled with a constant sampling rate and these discreet values are then transferred to corresponding numerical values (that is, AD conversion). After these conversions, only the numbers are used in the protection algorithms. Therefore, for the modern numerical differential IEDs the secondary CT circuit resistance might not be a decisive factor any more.

The important factor for the numerical differential IED is the time available to the IED to make the measurements before the CT saturation, which will enable the IED to take the necessary corrective actions. This practically means that the IED has to be able to make the measurement and the decision during the short period of time, within each power system cycle, when the CTs are not saturated. From the practical experience, obtained from heavy current testing, this time, even under extremely heavy CT saturation, is for practical CTs around two milliseconds. Because of this, it was decided to take this time as the design criterion in REB 670 IED, for the minimum acceptable time before saturation of a practical magnetic core CT. Thus, the CT requirements for REB 670 IED are kept to an absolute minimum. Refer to section "Rated equivalent secondary e.m.f. requirements" for more details.

However, if the necessary preventive action has to be taken for every single CT input connected to the differential IED, the IED algorithm would be quite complex. Thus, it was decided to re-use the ABB excellent experience from the analog percentage restrained differential protection IED (that is, RADSS and REB 103), and use only the following three quantities as the inputs into the differential algorithm in the numerical IED design:

1. incoming current (that is, sum of all currents which are entering the protection zone)
2. outgoing current (that is, sum of all currents which are leaving the protection zone)
3. differential current (that is, sum of all currents connected to the protection zone)

These three quantities can be easily calculated numerically from the raw sample values (that is, twenty times within each power system cycle in the IED) from all analog CT inputs connected to the differential zone. At the same time, they have extremely valuable physical meaning, which clearly describes the condition of the protected zone during all operating conditions.

By using the properties of only these three quantities, a new patented differential algorithm has been formed in the IED. This differential algorithm is completely stable for all external faults. All problems caused by the non-linearity of the CTs are solved in an innovative numerical way.
Meanwhile, very fast tripping time, typically 11 ms, can be commonly obtained for heavy internal faults.

Discriminating zones in the IED includes a sensitive operational level. This sensitive operational level is designed to be able to detect busbar ground faults in low impedance edgrounded power systems (that is, power systems where the ground-fault current is limited to a certain level, typically between 300 A and 2000 A by neutral point reactor or resistor) or for some other special applications where increased sensitivity is required. Operation and operating characteristic of the sensitive differential protection can be set independently from the operating characteristic of the main differential protection. The sensitive differential level is blocked as soon as the total incoming current exceeds the pre-set level. By appropriate setting then it can be insured that this sensitive level is blocked for external phase-to-phase or three-phase faults, which can cause CT saturation. Comparison between these two characteristics is shown in Figure 41.

![Differential protection operation characteristic](image)

**Figure 41:** Differential protection operation characteristic

where

- $I_{in}$ RMS value of the incoming current into the differential protection zone
- $I_d$ RMS value of the differential current from the differential protection zone
- $s = 0.53$ operating slope for the differential function (fixed in the algorithm).

Additionally the sensitive differential protection can be time delayed and it must be externally enabled by a binary signal (that is, from external open delta VT overvoltage relay or power transformer neutral point overcurrent relay).

Please refer to the technical reference manual for more details about the working principles of the differential function algorithm.
6.1.3.3 Check zone protection

An integrated overall differential zone, so-called check zone, is available for multiple busbar stations to secure stability of the busbar differential protection, in case of problem with disconnector auxiliary contacts (stuck contacts or wiring problems).

When CT-circuits are switched, depending on the position of the busbar disconnectors, there is a possibility that some of the CT secondary circuits can be open circuited by a mistake. At the same time this can cause unwanted operation of the differential protection scheme.

Therefore, an external check zone is often required for a traditional high impedance busbar protection scheme when switching in CT-circuit is done. The check zone is fixed and has no switching of CTs in any of the outgoing circuits and is not connected to busbar section and busbar coupler bays. The check zone, will detect faults anywhere in the substation but cannot distinguish in which part of the station the fault is located. When the check zone detects a fault it gives a release signal to the busbar protection relays in all individual, discriminating zones. The busbar protection discriminating zones will then trip the part of the substation that is faulty. However, this principle creates not only a high cost as separate CT cores are required, but also a need for extra cabling and a separate check zone differential relay.

In REB670, an internal built-in check zone in the IED is provided, therefore there is no need for such costly external check zone. This is possible, mainly due to the following facts:

- the CT switching is made only in software, and CT secondary current circuits do not include any auxiliary contacts, as shown in Figure 63.
- the IED is always supplied with a special zone and phase selective “Open CT Detection” algorithm, which can instantly block the differential function in case of an open CT secondary circuits caused by accidents or mistakes.

Consequently a very cost effective solution can be achieved producing extra savings during scheme engineering, installation, commissioning, service and maintenance.

Operating characteristics for the check zone can be set independently from the two discriminating zones. However, it is to be observed that the check zone has slightly different operating characteristic from the usual discriminating zones. For the check zone the outgoing current is used as stabilizing current instead of total incoming current in order to guarantee the check zone operation for all possible operating conditions in the station. The check zone operating characteristic is shown in Figure 42. Note that the check zone minimum differential operational level OperLevel shall be set equal to or less than the corresponding operating level of the usual discriminating zones.
Figure 42: Check zone operation characteristic

The built-in check zone feature has to be enabled (that is, setting parameter Operation shall be set to On) in order to fully enable the check zone. For substations where traditional “CT switching” is not required (that is, single busbar station or one-and-half breaker station), a check zone must not be used. For such applications, the check zone shall be disabled by the setting parameter Operation to Off.

The check zone measures only the currents from the bays which leaves the station. Exactly which CTs shall be measured by the check zone varies, depending on the version of the IED used:

- In two-zone busbar differential protections, it is freely settable by using the setting CheckZoneSel at each bay function block.
- In six-zone busbar differential protection, the currents of all active feeder bays are measured unconditionally by the check zone, if the check zone is able to operate. By an active feeder, we mean a feeder bay which is not deactivated, that is, neither its operMode is Off nor it is out of service.

And the check zone supervises differential protection zones. Exactly which differential protection zones shall be supervised by the operation of the check zone also varies, depending on the version of the IED used:

- In two-zone busbar differential protections, freely settable by using a setting CheckZoneSup at each zone function block,
- In six-zone busbar differential protection, all active protection zones are supervised unconditionally by the check zone, if the check zone is able to operate. By an active protection zone, we mean a zone which both is enabled and used/connected to at least one CT.

Moreover, there are some additional features only available for six-zone busbar differential protection, for example, the possibilities to externally block the check zone operation.

Please also refer to “Check zone protection” in Technical reference manual, for more details about the check zone.
6.1.3.4 Switch status monitoring

The so-called CT switching (that is, zone selection) is required in situations when one particular circuit (that is, bay) can be connected to different busbars by individual disconnectors. Typical example is a station with double busbars with or without transfer bus as shown in Figure 62, where any feeder bay can be connected to any of the two buses. In such cases the status of all busbar disconnectors and all transfer disconnectors shall be given to the busbar protection.

Traditionally, the CT switching has been done in CT secondary circuits. However, with REB670 this is not the case. All necessary zone selection (that is, CT switching) is done in software. Therefore, the CT secondary circuits are always intact and without any auxiliary relay contacts.

To provide proper zone selection (that is, busbar replica) the position information from all relevant primary switches (that is, disconnectors and/or circuit breakers) must be given to the IED. This is typically done by connecting two auxiliary contacts (that is, normally open and normally closed aux contacts) from each primary switch to the IED binary inputs (that is, optocouplers). In REB670 configuration one Switch Status function block shall be associated with each primary switching device. This block is then used internally to derive the primary object status and then pass this information to the busbar protection internal zone selection logic.

Auxiliary contact requirements for disconnectors and circuit breakers

The position of the primary switching object is typically obtained via two auxiliary contacts of the primary apparatus. The first auxiliary contact indicates that primary device is closed. In protection literature it is called by different names as stated below:

- Normally open auxiliary contact
- “a” contact (that is, 52a)
- “closed”

The second auxiliary contact indicates that primary device is open. In protection literature it is called by different names as stated below:

- Normally closed auxiliary contact
- “b” contact (that is, 52b)
- “open”

Typically both contacts are used to provide position indication and supervision for busbar protection.

Minimum contact requirements

The minimum requirement for the busbar replica is the record of the disconnector position by using just one auxiliary contact, either "normally open" or "normally closed" type. However recording a pair of auxiliary contacts, representing the “open” and “close” position, offer additional features which can improve the reliability of the bus replica including supervision possibilities.

Auxiliary contact evaluation logic

Two logic schemes can be found.
- **Scheme1_RADSS** "If not OPEN then CLOSED"

As the name of the scheme suggests, only when the auxiliary contacts signal clean open position ("normally open auxiliary (NO) contact input" = inactive and "normally closed auxiliary (NC) contact input" = active), the disconnector is taken to be open. For all other signal combinations the disconnector is considered to be closed. This scheme does not pose any special requirements to the auxiliary contact timing. Only the disconnector NC contact must open before the disconnector main contact is within arcing distance. The time during which the POSOPEN and POSCLOSE signal inputs disagree (that is, both binary inputs are active or both are inactive) is monitored by the isolator supervision function. The maximum time allowed before an alarm is given can be set according to the disconnector timing.

- **Scheme2_INX** "Closed or open if clear indication available otherwise last position saved"

As the name of the scheme suggests, only when the auxiliary contacts signal clean “open” or clean “closed” position, disconnector is considered to be open respectively closed. However this poses the stringent requirements on the auxiliary contacts that the PSCLOSED signal must become active a certain time (>150 ms) before current starts flowing for example, through arcing. Otherwise this current will not be taken into account in the busbar protection and this can result in a maloperation. Therefore, good timing of two auxiliary contacts is definitely required.

The time during which the POSOPEN and POSCLOSED signals disagree (that is, both binary inputs are active or both are inactive) is monitored by the isolator supervision function for both of the above two schemes. The maximum time allowed before an alarm is given can be set according to the disconnector timing.

Table 13 and the following two figures summarize the properties of these two schemes.

**Table 13:** Treatment of primary object auxiliary contact status within BBP in REB670

<table>
<thead>
<tr>
<th>Primary equipment</th>
<th>Status in busbar protection</th>
<th>Alarm facility</th>
<th>Information visible on local HMI</th>
</tr>
</thead>
<tbody>
<tr>
<td>Normally Open auxiliary contact status (&quot;closed&quot; or &quot;a&quot; contact)</td>
<td>Normally Closed auxiliary contact status (&quot;open&quot; or &quot;b&quot; contact) when &quot;Scheme 1 RADSS&quot; is selected</td>
<td>when &quot;Scheme 2 INX&quot; is selected</td>
<td>Alarm after settable time delay</td>
</tr>
<tr>
<td>open</td>
<td>open</td>
<td>closed</td>
<td>yes</td>
</tr>
<tr>
<td>open</td>
<td>closed</td>
<td>open</td>
<td>no</td>
</tr>
<tr>
<td>closed</td>
<td>open</td>
<td>closed</td>
<td>no</td>
</tr>
<tr>
<td>closed</td>
<td>closed</td>
<td>closed</td>
<td>yes</td>
</tr>
</tbody>
</table>
The circuit breaker position from a bay shall be given to the busbar protection when the position of this particular breaker can influence the busbar protection operation. Typical examples are Blind Spot protection in Bus-section and Bus-coupler bays or End Fault Protection in feeder bays. In both cases the measuring range of a busbar protection is limited by the CT location. By additionally recording the CB position of a feeder or a coupler the zone between the CT and the CB can be better protected while CB is open. However in such cases it is of utmost importance to connect the CB closing command to the busbar protection in order to include again the CT current to the busbar protection zones in time. It is as strongly recommended to always use Scheme1_RADSS for all CBs positions connected to the IED in order to minimize any risk of possible problems due to late inclusion of CT current to the relevant differential zones.
**Line disconnector replica**

The line disconnector position from a feeder bay might be required for busbar protection under certain circumstances. Typical example is when the line disconnector 989 and associated grounding switch QC1 are located between CT and protected busbar as indicated in Figure 45.

![Figure 45: Feeder bay layout when line disconnector position might be required for busbar protection](en00000086_ansi.vsd)

In order to avoid such problems for busbar protection the status of line disconnector can be monitored by busbar protection and CT measurement can be disconnected from both differential zones as soon line disconnector is open. Similar functionality can be obtained by instead monitoring the position of feeder breaker 152. In such case the breaker closing signal shall be connected to busbar protection as well.

**6.1.3.5 CT connection control**

The IED offers an extremely effective solution for stations where zone selection (that is, CT switching) is required. This is possible due to the software facility, which gives full and easy control over all CT inputs connected to the IED.

In applications where zone selection (that is, CT switching) is required (for example double or multiple busbar stations) all CTs will be permanently connected to the analogue input module(s), as shown in Figure 63. Therefore, all necessary switching of currents will be performed in internal software logic.

In general, there are dedicated binary inputs available for one CT at each bay, to control its current connection towards differential zones, based on operational status of related disconnectors. In addition, in two-zone busbar differential protections, a setting parameter is also introduced to determine, together with the binary inputs, how the bay CT current shall be connected to the differential zones.
The philosophy is to allow every CT input to be individually controlled by a setting parameter. This parameter called ZoneSel can be individually configured for every CT input. This parameter, for every bay, can be set to only one of the following five alternatives:

1. FIXEDtoZA
2. FIXEDtoZB
3. FIXEDtoZA&-ZB
4. CtrlIncludes
5. CtrlExcludes

If for a particular CT input setting parameter ZoneSel is set to FIXEDtoZA, then this CT input will be only included to the differential zone A. This setting is typically used for simple single zone application such as: single busbar stations, breaker-and-a-half stations or double breaker stations.

If for a particular CT input setting parameter ZoneSel is set to FIXEDtoZB, then this CT input will be only included to the differential zone B. This setting is typically used for applications such as: breaker-and-a-half stations or double breaker stations.

If for a particular CT input setting parameter ZoneSel is set to FIXEDtoZA&-ZB, then this CT input will be included to the differential zone A, but its inverted current value will be as well included to the differential zone B. This setting is typically used for bus coupler or bus section bays when only one current transformer is available see Figure 47.

If for a particular CT input setting parameter ZoneSel is set to Ctrl_Includes, then this CT input will be:

• included to the differential zone A when input signal CTRLZA on corresponding bay block is given logical value one and it will be excluded from the differential zone A when input signal CTRLZA on corresponding bay block is given logical value zero.
• included to the differential zone B when input signal CTRLZB on corresponding bay block is given logical value one and it will be excluded from the differential zone B when input signal CTRLZB on corresponding bay block is given logical value zero.

This setting is typically used for feeder bays in double busbar stations in order to form proper busbar disconnector replica. It is especially suitable both when normally open and normally closed (that is, "a" and "b" contacts) auxiliary contacts from the busbar disconnectors are available to the IED.

If for a particular CT input setting parameter ZoneSel is set to Ctrl_Excludes, then this CT input will be:

• excluded from the differential zone A when input signal CTRLZA on corresponding bay block is given logical value one and it will be included to the differential zone A when input signal CTRLZA on corresponding bay block is given logical value zero.
• excluded from the differential zone B when input signal CTRLZB on corresponding bay block is given logical value one and it will be included to the differential zone B when input signal CTRLZB on corresponding bay block is given logical value zero.

This setting is typically used for feeder bays in double busbar single breaker stations in order to form proper busbar disconnector replica. It is especially suitable when only normally closed (that is, “b” contact) auxiliary contact from the busbar disconnector(s) is available to the IED. For more information please refer to Figure 64.
6.1.3.6 CT disconnection for bus interconnector CT cores

For simplicity purpose, sometimes we also refer to a bus-section or bus-coupler bay as a bus interconnector bay. In practice there are three different solutions for bus interconnector bay layout. First solution is with two sets of main CTs, which are located on both sides of the circuit breaker, as shown in Figure 46.

Two differential zones are overlapping across the bus-section or bus-coupler circuit breaker. All faults in the overlapping zone will be instantly tripped by both zones irrespective of the section/coupler circuit breaker status. However with modern busbar protection it is possible to disconnect both CTs from the relevant zones when the bus-section or bus-coupler circuit breaker is open. This will insure that if internal fault happen, in the overlapping zone, while breaker is open, only the faulty zone will be tripped while other busbar section will remain in service. However, due to low probability of such fault happening, while the breaker is open, such special considerations are typically not included in the busbar protection scheme for this type of stations. In such applications, the bus section or bus coupler current transformers shall be wired just to two separate current inputs of the IED. Meanwhile these currents should be given to both related differential zones. To ensure this, in two-zone busbar protections, it requires to set the corresponding bays the parameter ZoneSel to FIXEDtoZA in one bay and FIXEDtoZB in another bay in the parameter setting tool (PST).

When live tank circuit breakers are used, often only one current transformer is available in bus-section or bus-coupler bay. The suggested solution in such applications is shown in Figure 47.
Figure 47: Example of station with just one main CT in the bus-section bay

For this type of solution just one main CT is located on only one side of the circuit breaker. Thus, there is no zone overlapping across the section/coupler circuit breaker as shown in Figure 46. A blind spot exists between the current transformer and the circuit breaker in the bus section or bus-coupler bay as shown in Figure 47.

As the example application in Figure 47 showed, for an internal fault in the blind spot, the differential zone ZA will unnecessarily operate and open the bus section breaker and all other feeder breakers associated with it. Nevertheless the fault will still exists on other busbar section, but it is outside the current transformer in the bus section bay and hence outside the zone ZB (that is, it is external fault for zone ZB). Similar problem will also exist if section/coupler circuit breaker was open before the internal fault in the blind zone. Therefore, the busbar protection scheme does not protect the complete busbar.

In order to improve the busbar protection scheme with this type of station layout, it is often required to disconnect the bus-section or bus-coupler CT from all affected differential zones as soon as the bus-section or bus-coupler circuit breaker is opened. This arrangement can be easily achieved within the IED. In such applications, the bus section or bus coupler current transformer shall be wired just to one current input of the IED. Meanwhile this current should be given to both related differential zones. To ensure this, in two-zone busbar protections, it requires to set the corresponding bay the parameter ZoneSel to FIXEDtoZA&-ZB in PST. In six-zone busbar protections, this single CT current should be connected to both CT inputs on the bus section or bus coupler function block, and then invert one of them in software by using the parameter setting OperMode in PST. This shall be done taking into account the actual location of the CT star point in this bay.

When the bus coupler/section breaker is open, to disconnect this current from both zones, additional configuration logic is required by using the binary input, for example ZEROCUR in two-zone differential protections. Figure 48 provides an example of such logic. Moreover, the following two binary inputs are at least necessary in order to guaranty proper operation of such logic:
• Normally closed contact of the bus section or bus coupler circuit breaker
• Signal from the bus section or bus coupler circuit breaker closing circuit that somebody wants
to close the breaker.

This solution does not depend on contact timing between the main contacts and auxiliary contact
of the circuit breaker. It directly follows the philosophy used for RADSS/REB 103 schemes used for
similar applications before. Principle connection between the bus-coupler CB normally closed
auxiliary contact ("b" contact), REB670 and internal configuration logic, as shown in Figure 48.

![Diagram](en06000155_ansi.vsd)

**Figure 48: Bus coupler bay with one CT and "b" aux. contact only from CB**

This scheme will disconnect the section/coupler CTs after a pre-set time length (≥80 ms
recommended) defined by the parameter setting tZeroCurrent in the relevant bay function block
from the moment of opening of the section/coupler CB (that is, from the moment when auxiliary
"b" contact operates). Nevertheless this time delay is absolutely necessary in order to prevent
racing between the opening of the main breaker contact and disconnection of the CT from the
differential zones. This scheme will as well disconnect the CT in case of the operation of any of the
two internal differential zones used in the scheme. This will secure the delayed (≥150 ms,
depending one the values of the setting tZeroCurrent) clearing and tripping of the internal fault
within the blind zone even in case of section/coupler circuit breaker failure during such fault. This
facility will improve the performance of the busbar protection scheme when one CT is located on
only one side of the bus-section / bus-coupler circuit breaker.

With GIS or live tank circuit breakers, owing to high cost of HV CT installations, sometimes no
current transformers are available in bus-section or bus-coupler bay. This is the third solution
shown in Figure 49.
Figure 49: Example of station without main CTs in the bus-section bay

In such case two separate zones can be maintained only while bus coupler breaker is open. As soon as bus coupler breaker is going to be closed the zone interconnection feature must be activated and complete busbars will be automatically protected with just one differential zone.

Since there are no current transformer in the bus coupler bay, there is no need to allocate internal bay function block for the bus coupler bay. However some additional configuration logic is required to obtain automatic zone interconnection activation when bus coupler breaker shall be closed. Example of such logic, is shown in Figure 50 for a two-zone differential protection application.

Figure 50: Configuration logic for bus coupler without main CTs
6.1.3.7 End fault protection

When Live tank CBs or GIS are involved, there is a physical separation between the CT and the CB. End Fault Protection is related to primary faults between main CT and CB in a feeder bay. Therefore, it is directly related to the position of the main CT in feeder bay. Three CT positions in feeder bays are typically used in power systems around the world, as shown in Figure 51.

![Figure 51: Typical CT locations in a feeder bay](en06000138_ansi.vsd)

Figure 51: Typical CT locations in a feeder bay

where:
A two CTs are available one on each side of the feeder circuit breaker
B one CT is available on the line side of the feeder circuit breaker
C one CT is available on the bus side of the feeder circuit breaker
1 End fault region

In Figure 51/A where two CTs are available in a feeder bay, the end fault protection is not an issue. The busbar and feeder protection zones overlap across feeder circuit breaker and all faults between these two CTs will be instantly detected and tripped by both protection schemes. As a consequence of such fault both busbar and feeder will be disconnected from the power system.

In Figure 51/B where one CT is available on the line side of the feeder circuit breaker, the primary fault between CT and CB will cause certain problems. Typically such fault will be detected and tripped by busbar protection. However to completely clear such fault the remote feeder end CB must be tripped as well. It shall be noted that for the feeder protection such fault will be either a reverse fault (that is, distance protection used for feeder protection) or external fault (that is, line/transformer differential protection used for feeder protection).

In Figure 51/C where one CT is available on the bus side of the feeder circuit breaker, the primary fault between CT and CB will cause problem as well. Typically such fault will be detected and tripped by feeder protection. However, to completely clear such fault the associated busbar section must be tripped as well. It shall be noted that the busbar differential protection will classify such fault as external and without any additional measures the busbar protection will remain stable.

For better understanding end fault protection applications within busbar protection, the Figure 52 is provided.
Figure 52: Busbar protection measuring and fault clearing boundaries

where:

1. Busbar Protection measuring boundary determined by feeder CT locations
2. Busbar Protection internal fault clearing boundary determined by feeder CB locations
3. End fault region for feeders as shown in Figure 51/B
4. End fault region for feeders as shown in Figure 51/C

Figure 52 shows a single busbar station, where two feeders on the left-hand side having CTs located on the line side of the breaker, meanwhile two feeders on the right-hand side of the busbar having CTs located on the busbar side of the breaker. It is assumed that busbar protection is connected to all four set of CTs in this station.

Due to CT locations in feeder bays, busbar protection will detect all primary faults located within measuring boundary determined by CT locations, see Figure 52. However its operation will only completely clear faults within clearing boundary determined by CB locations as shown in Figure 52. Obviously, the primary faults in-between these two boundaries do pose certain practical problems.

First of all it shall be noted that there is no ideal solution for faults within end zone region in a feeder bay, when the feeder breaker is closed. Such faults, within end fault region, will be then cleared with additional time delay either by operation of local backup protection (that is, feeder circuit breaker failure protection) or by operation of remote backup protection (that is, remote ends zone 2 distance protection).

However, the overall busbar protection behavior can be improved for primary faults within end fault regions, when feeder breaker is open. Under such circumstances the following actions can be taken:

- For feeders with CT on the line side of the circuit breaker (that is, the two feeders on the left-hand side in Figure 52), the current measurement can be disconnected from the busbar protection zone some time after feeder CB opening (for example, 400 ms for transformer and cable feeders or longest autoreclosing dead time +300 ms for overhead line feeders). At the same time, appropriately set and fast (that is, typically 40 ms time delayed) overcurrent protection shall be enabled to detect fault within end fault region. Any operation of this overcurrent protection shall only issue inter-trip command to the remote feeder end CB. Such
overcurrent protection is often called end fault protection in relay literature. It shall be noted that at the same time busbar protection will remain stable (that is, selective) for such fault.

- For feeders with CT on the bus side of the circuit breaker (that is, the two feeders on the right-hand side in Figure 52), the current measurement can be disconnected from the busbar protection zone some time after feeder CB opening (that is, after 400 ms). This measure will ensure fast busbar protection tripping for faults within end fault region in that feeder bay, while feeder CB is open.

However, it shall be noted that in order to utilize end fault protection feeder circuit breaker status and its closing command must be connected to the binary inputs of busbar protection scheme in order to be available for zone selection logic.

End fault protection logic can be easily done with the help of graphical configuration tool. One stage (that is, 4th stage) from optionally available overcurrent protection can be used as dedicated end fault protection for feeders with CT on the line side of the CB.

End fault protection is here explained for simple single busbar station. However the same principles are applicable to almost all other station layouts. Moreover, under certain circumstances, for stations with a transfer bus more extensive logic for end fault protection implementation might be required.

6.1.3.8 Zone interconnection (Load transfer)

In multiple busbar stations, with or without transfer bus, it is common requirement to use the possibility of zone interconnection loading current in any feeder bay from one busbar to the other(s). The sequence of operation during zone interconnection is normally as the following:

- bus coupler or bus section bay is closed (that is, CB and both disconnectors).
- feeder bay busbar disconnector to the busbar not already in service is then closed. The switchgear interlocking system shall allow this only when the bus coupler breaker is already closed. Depending on the thermal capacity of the feeder busbar disconnectors (189 and 289) the opening of the bus coupler circuit breaker is sometimes interlocked while both busbar disconnectors within one of the feeder bays are closed.
- opening of the feeder bay busbar disconnector originally closed. The load is now transferred from one to other bus.
- opening of bus coupler or bus section bay CB.

The zone interconnection has to be taken into consideration for the busbar differential protection scheme, as each two busbar zones are interconnected together via two disconnectors. The primary current split between busbars is then not known and the two separate measuring zones cannot be maintained.

In conventional, analog busbar protection systems the solutions have been to, by extensive zone switching IEDs. For example, in two-zone applications, it leads to disconnect one zone (normally zone B, and connect all feeders to other zone (normally zone A). At the same time the current from the bus-coupler bay, which just circulates between zones, must be disconnected from the measuring differential zone.

Similar situation regarding to busbar protection can occur between any busbar sections interconnected via sectionalizing disconnector, see Figure 55 for an example. When each sectionalizer is closed, two separate protection zones becomes the one and busbar protection must be able to dynamically handle this.
Due to the numerical design the IED can manage this situation in an elegant and simple way. Internal feature called Zone Interconnection will be used to handle both situations. This feature can be activated either externally via binary input or derived internally by built-in logic. Especially in two-zone busbar protections, to internally activate such feature, the setting ZoneSel shall be also considered. Consequently, the zone interconnection will be activated internally only when the following conditions are met:

- bays have parameter ZoneSel set to either CtrlInclude or CtrlExcludes
- internal zone selection logic concludes that this particular bay shall be simultaneously connected to multiple internal differential zones

This situation only means that for this particular bay its busbar disconnectors are closed and therefore zone interconnection switching is happening in the station.

When zone switching feature is activated inside the IED, each individual bay current will behave in the predetermined way. In six-zone busbar differential protection, it is internally determined how a bay CT should behave during zone interconnection, depending the bay is a feeder or a bus coupler or section bay. In two-zone busbar differential protections, it is determined by the end user with a settable setting ZoneSwitching. This parameter, for every bay, can be set to only one of the following three alternatives

- ForceOut
- ForceIn
- Conditionally

If for a particular CT input setting parameter ZoneSwitching is set to ForceOut, then this CT input will be disconnected from both the differential zones, regardless of any other set value or active binary input, while zone switching feature is active within the IED. This setting is typically used for bus coupler bay in double busbar stations.

If for a particular CT input setting parameter ZoneSwitching is set to ForceIn, then this CT input will be connected to both the differential zones, regardless of any other set value or active binary input, while zone switching feature is active within the IED. This setting is typically used for all feeders bay in a station with two single zone interconnected by a sectionalizing disconnector.

If for a particular CT input setting parameter ZoneSwitching is set to Conditionally, then this CT input will be connected to both the differential zones only if it was included to any of the two zones for 2ms before the zone switching feature was activated. This setting is typically used for all feeders bay in double busbar stations. With this setting all feeder bays, which were not connected to any of the two zones before the zone interconnection activation (that is, out for scheduled maintenance), will not either be included during zone interconnection.

This practically means that for double busbar station, when zone switching feature is active, all feeder bays will be connected to both differential zones, while bus coupler CT will be disconnected from both zones. In this way simple but effective solution is formed. It is as well important to notice that all necessary changes in the individual bay tripping arrangements will be automatically performed within the internal logic.

A dedicated binary signal will be immediately activated in the internal logic when zone interconnection feature is activated. If this feature is active longer than the pre-set time separate alarm binary signal is activated, in order to alarm the station personnel about such operating conditions. ZoneInterconnection feature can be disabled by a parameter setting for substation arrangements where it is not required that is, single busbar stations, breaker-and-a-half stations and so on.
6.1.3.9 Tripping arrangements

Tripping circuit arrangement

The contact outputs are of medium duty type. It is possible to use them to directly trip the individual bay circuit breakers. This solution is suitable for all types of station arrangements. The internal zone selection logic provides individual bay trip signals in the internal software and no external relay for this purpose are required. This arrangement insures correct trip signal distribution to all circuit breakers in case of busbar protection operation or individual bay breaker failure protection operation. Breaker failure protection can be internal or external to the IED.

By a parameter setting it is possible to provide self-rest or latched trip output contacts from the IED. However it shall be noted that the latching is electrical (that is, if DC supply to the IED is lost the output contacts will reset).

However, sometimes due to a large number of required trip output contacts (that is, single pole operated circuit breakers and/or main and backup trip coils), a separate trip repeat relay unit is applied for the tripping of the circuit breakers in the station. In that case the tripping arrangement can be done in different ways.

Trip arrangement with one-phase version

When one-phase version of the IED is used it is typically required to have three IEDs (that is, one per phase). Thus, when busbar protection in one IED operates the trip commands will be given to all bays but internal circuit breaker failure function will be started in the same phase only. In order to secure internal breaker failure picking-up in all three phases it is advisable to do the following. Connect a zone, say Zone A, trip signal from one IED to the external trip input of the Zone A in the other two IEDs. Thus all three IEDs will then issue trip in Zone A and pick-up internally circuit breaker failure protection in all three phases.

Note that:

• similar arrangements shall be done for the other zones
• this have to be done between all three IEDs (that is, three times)

Such a scheme can be arranged in one of the following ways:

• by wiring between three IEDs
• by using GOOSE messages when IEC 61850-8-1 is used
• by using LDCM communication module.

Note that in this case the external trip signal from other two IEDs shall be arranged via pulse timer in configuration in order to avoid locking of the trip signal between three IED. Such arrangement via GOOSE is given in Figure 53.
Centralized trip unit

Tripping is performed directly from the IED contacts, which then activate an auxiliary trip unit, which multiplies the number of required trip contacts. Separate potential free contacts are provided for each bay and are supplied by the bay auxiliary voltage and will activate the trip coil of each bay circuit breaker at operation. This tripping setup is suitable when no individual circuit breaker failure IEDs or lock-out of individual bay CB closing coils is required. A suitable external trip unit consists of a combination of RXMS1/RXMH 2 alternatively AR/MG6 when heavy duty contacts are required and only RXMS 1/AR relays when medium duty contacts are sufficient.

Decentralized trip arrangement

Tripping is performed directly from the IED contacts, which then activate dedicated auxiliary trip unit per bay. This individual auxiliary trip unit can be mounted either in the busbar protection cubicle or in the individual bay cubicles. This tripping setup is suitable when individual circuit breaker failure relays exist in all bays. A suitable external trip unit consists of a combination of RXMS1/RXMH 2 alternatively AR/MG6 when heavy duty contacts are required and only RXMS 1/AR relays when medium duty contacts are sufficient.

This solution is especially suitable for the station arrangements, which require the dynamic zone selection logic (that is, so called CT switching).

6.1.3.10 Mechanical lock-out function

It is sometimes required to use lock-out relays for busbar protection operation.
The IED has built-in feature to provide either self-reset or latched tripping in case of busbar protection operation. Which type of trip signal each zone will issue is determined by a parameter setting DiffTripOut which can be set either to SelfReset or Latched. When Latched is selected the trip output from the IED will only reset if:

1. Manual reset command is given to the IED
2. DC power auxiliary supply to the IED is interrupted (that is, switched-off)

However, if it is required to have mechanically latched tripping and lock-out in the circuit breaker closing circuit, then it is recommended to use one dedicated lockout IED for each bay. Such mechanical lock-out trip IEDs are available in the COMBIFLEX range (for example RXMVB2 or RXMVB4 bistable IEDs alternatively MG6 (hand reset contact) IEDs).

From the application point of view lock-out trip IEDs might have the following drawbacks:

- The trip contacts will remain closed. If the breaker would fail to open the tripping coil will be burnt and the DC supply short-circuited.
- The trip circuit supervision (TCS) IEDs will reset and give alarm for a failure in the trip circuit if the alarm is not opened by the lock-out IED or a double trip circuit supervision is recommended where the trip circuit is supervised with two alternatively, TCS IEDs.

### 6.1.3.11 Contact reinforcement with heavy duty relays

There is sometimes a request for heavy duty trip relays. Normally the circuit breaker trip coils, with a power consumption of 200 to 300 W, are provided with an auxiliary contact opening the trip circuit immediately at breaker tripping. Therefore, no heavy duty breaking capacity is required for the tripping relays. Nevertheless heavy duty trip relays are still often specified to ensure trip circuit opening also if the circuit breaker fails due to a mechanical failure or a lack of energy for operation. This can particularly occur during site testing. In this case it is recommended to use COMBIFLEX RXMH 2 or RXMVB 2 or MG6 heavy duty relays.

### 6.1.3.12 Trip circuit supervision

Trip circuit supervision is mostly required to supervise the trip circuit from the individual bay IED panel to the circuit breaker. It can be arranged also for the tripping circuits from the busbar protection.

However, it can be stated that the circuit from a busbar protection trip relay located in the busbar protection panel is not so essential to supervise as busbar faults are very rare compared to faults in bays, especially on overhead power lines. Also it is normally a small risk for faults in the tripping circuit and if there is a fault which affects only one bay and all other bays are thus correctly tripped meaning that the fault current disappears or is limited to a low value.

### 6.1.4 Two-zone busbar arrangements

#### 6.1.4.1 General

Busbar differential protection application principles for typical busbar arrangements, up to two differential zones, are shown and described in the following sections.
6.1.4.2 Single busbar arrangements

The simplest form of busbar protection is a one-zone protection for single busbar configuration, as shown in Figure 54. When different CT ratios exist in the bays compensation is done by setting the CT ratio individually for each bay.

The only requirement for busbar protection is that the protection scheme must have one differential zone. For any internal fault all circuit breakers must be tripped, which will cause loss of supply to all loads connected to the station.

![Diagram of single busbar section with six feeder bays](xx06000087_ansi.vsd)

Figure 54: Example of single busbar section with six feeder bays

This type of busbar arrangement can be very easily protected. The most common setups for this type of station are described in the following table.

Table 14: Typical solutions for single busbar arrangement

<table>
<thead>
<tr>
<th>Version of REB670 IED</th>
<th>Numbers of feeders per busbar</th>
<th>Number of REB670 IEDs required for the scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>3PH; 2-zones, 4-bays BBP (A20)</td>
<td>4</td>
<td>1</td>
</tr>
<tr>
<td>3PH; 2-zones, 8-bays BBP (A31)</td>
<td>8</td>
<td>1</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B20)</td>
<td>12</td>
<td>3</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B21)</td>
<td>12</td>
<td>3</td>
</tr>
<tr>
<td>1Ph; 2-zones, 24-bays BBP (B31)</td>
<td>24</td>
<td>3</td>
</tr>
</tbody>
</table>

Please note that the above Table 14 is given for the preconfigured versions of REB670 which do not contain any VT inputs.

6.1.4.3 Single busbar arrangements with sectionalizer

This arrangement is very similar to the single busbar arrangement. The sectionalizer allows the operator to split the station into two separate buses. However, switching of the sectionalizing disconnector has to be done without any load. This means that one of the two busbars has to be de-energized before any opening or closing of the sectionalizer.

For this case the protection scheme must have two differential zones, which can be either split to work independently from each other or switched to one overall differential zone when sectionalizing disconnector is closed. Nevertheless, when sectionalizer is closed, for internal fault on any of the two buses all feeder circuit breakers have to be tripped, which causes loss of supply to all loads connected to this station.
Figure 55: Example of two single busbar sections with bus-sectionalizing disconnector and eight feeder bays per each busbar section

The most common setups for this type of station are described in the following table.

Table 15: Typical solutions for stations with two single busbar sections with bus-sectionalizing disconnector

<table>
<thead>
<tr>
<th>Version of REB670 IED</th>
<th>Total Number of feeders in both busbar sections</th>
<th>Number of REB670 IEDs required for the scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>3PH; 2-zones, 4-bays BBP (A20)</td>
<td>4</td>
<td>1</td>
</tr>
<tr>
<td>3PH; 2-zones, 8-bays BBP (A31)</td>
<td>8</td>
<td>1</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B20)</td>
<td>12</td>
<td>3</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B21)</td>
<td>12</td>
<td>3</td>
</tr>
<tr>
<td>1Ph; 2-zones, 24-bays BBP (B31)</td>
<td>24</td>
<td>3</td>
</tr>
</tbody>
</table>

Please note that Table 15 is given for the preconfigured versions of REB670 which do not contain any VT inputs.

Two differential zones are available in the IED and the connecting of the two zones is simply controlled via zone interconnection logic, as described in Section “Zone interconnection (Load transfer)”. In practice, the closed position of the sectionalizer shall start the zone interconnection logic inside the IED. All other thinks (that is, tripping) will automatically be arranged.

6.1.4.4 Single busbar arrangements with bus-section breaker

This arrangement is very similar to the single busbar arrangement. The bus-section breaker allows the operator to split the station into two separate buses under full load. The requirement for busbar protection scheme is that the scheme must have two independent differential zones, one for each busbar section. If there is an internal fault on one of the two sections, bus-section circuit breaker and all feeder circuit breakers associated with this section have to be tripped, leaving the other busbar section in normal operation.
Figure 56: Example of two single busbar sections with bus-section circuit breaker and eight feeder bays per each busbar section

This type of busbar arrangement can be quite easily protected. The most common setups for this type of station are described in the following table.

Table 16: Typical solutions for single busbar arrangements with bus-section breaker

<table>
<thead>
<tr>
<th>Version of REB670 IED</th>
<th>Total number of feeders in both busbar sections</th>
<th>Number of REB670 IEDs required for the scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>3PH; 2-zones, 4-bays BBP (A20)</td>
<td>3*/6</td>
<td>1/2</td>
</tr>
<tr>
<td>3PH; 2-zones, 8-bays BBP (A31)</td>
<td>7*/14</td>
<td>1/2</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B20)</td>
<td>11*/22</td>
<td>3/6</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B21)</td>
<td>11*/22</td>
<td>3/6</td>
</tr>
<tr>
<td>1Ph; 2-zones, 24-bays BBP (B31)</td>
<td>23*/46</td>
<td>3/6</td>
</tr>
</tbody>
</table>

* with just one CT input from bus-section bay

Please note that Table 16 is given for the preconfigured versions of REB670 which do not contain any VT inputs.

For station with just one CT in the bus-section bay, it might be required, depending on the client requirements, to provide the special scheme for disconnection of bus-section CT when the bus-section CB is open. For more information, refer to Figure 48.

6.1.4.5 H-type busbar arrangements

The H-type stations are often used in transmission and sub-transmission networks as load-centre substations, as shown in Figure 57. These arrangements are very similar to the single busbar station with sectionalizer or bus-section breaker, but are characterized by very limited number of feeder bays connected to the station (normally only two OHL and two transformers).
Figure 57: Example of H-type station

The requirement for the busbar protection scheme for this type of station may differ from utility to utility. It is possible to apply just one overall differential zone, which protects both busbar sections. However, at an internal fault on any of the two buses all feeder circuit breakers have to be tripped, which will cause loss of supply to all loads connected to this station. Some utilities prefer to have two differential zones, one for each bus section.

The most common setups for this type of station are given in the following table.

Table 17: Typical solutions for H-type stations

<table>
<thead>
<tr>
<th>Version of REB670 IED</th>
<th>Number of differential zones/number of feeders per zone</th>
<th>Number of REB670 IEDs required for the scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>3PH; 2-zones, 4-bays BBP (A20)</td>
<td>1/4</td>
<td>1</td>
</tr>
<tr>
<td>3PH; 2-zones, 8-bays BBP (A31)</td>
<td>2/3</td>
<td>1</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B20)</td>
<td>NA</td>
<td>NA</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B21)</td>
<td>NA</td>
<td>NA</td>
</tr>
<tr>
<td>1Ph; 2-zones, 24-bays BBP (B31)</td>
<td>NA</td>
<td>NA</td>
</tr>
</tbody>
</table>

Please note that Table 17 is given for the preconfigured versions of REB670 which do not contain any VT inputs.
For station with double zone protection and just one set of CTs in the bus-section bay, it might be required, depending on the client requirements, to provide the special scheme for disconnection of bus-section CT when the bus-section CB is open. For more information, refer to Figure 48.

6.1.4.6 Double circuit breaker busbar arrangement

The circuit breaker, disconnectors and instrument transformers are duplicated for every feeder, as shown in Figure 58.

![Diagram of double breaker station](xx06000018_ansi.vsd)

**Figure 58: Example of double breaker station**

This is an extremely flexible solution. In normal service all breakers are closed. The requirement for busbar protection scheme is that the scheme must have two independent differential zones, one for each busbar. If there is an internal fault on one of the two buses all circuit breakers associated with the faulty busbar have to be tripped, but supply to any load will not be interrupted. The tripping logic for the circuit breaker failure protection must be carefully arranged.

The most common setups for this type of busbar arrangement are described in the following table.

<table>
<thead>
<tr>
<th>Version of REB670 IED</th>
<th>Numbers of feeders per station</th>
<th>Number of REB670 IEDs required for the scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>3PH; 2-zones, 4-bays BBP (A20)</td>
<td>4</td>
<td>2</td>
</tr>
<tr>
<td>3PH; 2-zones, 8-bays BBP (A31)</td>
<td>4/8</td>
<td>1/2</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B20)</td>
<td>6/12</td>
<td>3/6</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B21)</td>
<td>6/12</td>
<td>3/6</td>
</tr>
<tr>
<td>1Ph; 2-zones, 24-bays BBP (B31)</td>
<td>12/24</td>
<td>3/6</td>
</tr>
</tbody>
</table>

Please note that Table 18 is given for the preconfigured versions of REB670 which do not contain any VT inputs.

A principle overall drawing of how to use REB670 for this type of station is given in Figure 59.
Figure 59: Feeder bay in double bus – double breaker station

6.1.4.7 Breaker-and-a-half busbar arrangements

A fewer number of circuit breakers are needed for the same flexibility as for double circuit breaker busbar arrangement, as shown in Figure 60.
Figure 60: Example of breaker-and-a-half station

All breakers are normally closed. The requirement for the busbar protection scheme is that the scheme must have two independent differential zones, one for each busbar. In case of an internal fault on one of the two buses, all circuit breakers associated with the faulty busbar have to be tripped, but the supply to any load will not be interrupted. The breaker failure protection tripping logic also needs careful design.

This type of busbar arrangement can be very easily protected. The most common setups for this type of station are described in the following table.

<table>
<thead>
<tr>
<th>Version of REB670 IED</th>
<th>Number of diameters in the station</th>
<th>Number of REB670 IEDs required for the scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>3PH; 2-zones, 4-bays BBP (A20)</td>
<td>2/4</td>
<td>1/2</td>
</tr>
<tr>
<td>3PH; 2-zones, 8-bays BBP (A31)</td>
<td>4/8</td>
<td>1/2</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B20)</td>
<td>6/12</td>
<td>3/6</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B21)</td>
<td>6/12</td>
<td>3/6</td>
</tr>
<tr>
<td>1Ph; 2-zones, 24-bays BBP (B31)</td>
<td>12/24</td>
<td>3/6</td>
</tr>
</tbody>
</table>

Please note that Table 19 is given for the preconfigured versions of REB670 which do not contain any VT inputs.

A principle overall drawing of how to use REB670 for breaker-and-a-half station including internal CBF protection for middle breaker is given in Figure 61.
Figure 61: Diameter in breaker-and-a-half station with breaker failure protection for all three breakers inside REB670

6.1.4.8 Double busbar single breaker arrangement

This type of arrangement is shown in Figure 62.
Figure 62: Example of double busbar station

This type of busbar arrangement is very common. It is often preferred for larger installations. It provides good balance between maintenance work requirements and security of supply. If needed, two busbars can be split during normal service. The requirement for busbar protection scheme is that the scheme must have two independent differential zones, one for each busbar. In case of an internal fault on one of the two buses, bus-coupler circuit breaker and all feeder circuit breakers associated with the faulty bus have to be tripped, leaving other busbar still in normal operation. Provision for zone selection, disconnector replica and zone interconnection have to be included into the scheme design.

This type of busbar arrangement can be protected as described in the following table:

Table 20: Typical solutions for double busbar stations

<table>
<thead>
<tr>
<th>Version of REB670 IED</th>
<th>Number of feeders in the station (excluding bus-coupler bay)</th>
<th>Number of REB670 IED required for the scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>3PH; 2-zones, 4-bays BBP (A20)</td>
<td>3*)</td>
<td>1</td>
</tr>
<tr>
<td>3PH; 2-zones, 8-bays BBP (A31)</td>
<td>7*)</td>
<td>1</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B20)</td>
<td>NA</td>
<td>NA</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B21)</td>
<td>11*)</td>
<td>3</td>
</tr>
<tr>
<td>1Ph; 2-zones, 24-bays BBP (B31)</td>
<td>23*)</td>
<td>3</td>
</tr>
</tbody>
</table>

*) with just one CT input from bus-coupler bay

Please note that Table 20 is given for the preconfigured versions of REB670 which do not contain any VT inputs.

For station with just one CT in the bus-coupler bay, it might be required, depending on the client requirements, to provide the special scheme for disconnection of bus-coupler CT when the bus-coupler CB is open. For more info please refer to Figure 48.

Some principle overall drawings of how to use REB670 in this type of station are given in Figure 63 to Figure 67.
Figure 63: Feeder bay where a&b aux. contacts are used
Figure 64: Feeder bay where b aux. contacts are used
**Figure 65:** Bus coupler bay with two sets of CTs
Figure 66: Bus coupler bay with one CT and a&b aux. contact from CB
Figure 67: Bus coupler bay with one CT and b aux. contact only from CB

6.1.4.9 Double busbar arrangements with two bus-section breakers and two bus-coupler breakers

This type of station is commonly used for GIS installations. It offers high operational flexibility. For this type of stations, two schemes similar to the double busbar station scheme can be used.

Figure 68: Example of typical GIS station layout

With REB670 this type of arrangement can be protected as described in the following table.
Table 21: Possible solutions for a typical GIS station

<table>
<thead>
<tr>
<th>Version of REB670 IED</th>
<th>Number of feeders on each side of the station (excluding bus-coupler &amp; bus-section bays)</th>
<th>Number of REB670 IEDs required for the scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>3PH; 2-zones, 4-bays BBP (A20)</td>
<td>NA</td>
<td>NA</td>
</tr>
<tr>
<td>3PH; 2-zones, 8-bays BBP (A31)</td>
<td>5&lt;sup&gt;1)&lt;/sup&gt;</td>
<td>2</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B20)</td>
<td>NA</td>
<td>NA</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B21)</td>
<td>9&lt;sup&gt;1)&lt;/sup&gt;</td>
<td>6</td>
</tr>
<tr>
<td>1Ph; 2-zones, 24-bays BBP (B31)</td>
<td>21&lt;sup&gt;1)&lt;/sup&gt;</td>
<td>6</td>
</tr>
</tbody>
</table>

<sup>1)</sup> with just one CT input from bus-coupler bay

Please note that Table 21 is given for the preconfigured versions of REB670 which do not contain any VT inputs.

Provision for zone selection, disconnector replica and zone interconnection have to be included into the scheme design.

For station with just one CT in the bus-coupler or bus-section bays, it might be required, depending on the client requirements, to provide the special scheme for disconnection of bus-coupler or bus-section CT when the bus-coupler or bus-section CB is open. For more info please refer to Figure 48.

### 6.1.4.10 Combined busbar arrangements

There are stations which are practically a combination between two normal types of station arrangements, which are already previously described. Some typical examples will be shown here:
Figure 69: Combination between one-and-half and double breaker station layouts

This type of stations can be encountered very often in practice. Usually the station is arranged in such a way that double breaker bays can be, at a later stage, transformed into one-and-half breaker setup. For busbar protection this type of station can be protected in exactly the same way as one-and-half breaker stations described above. The same type of IEDs can be used, and same limitations regarding the number of diameters apply.

Figure 70: Combination between double breaker and double busbar station layouts

In this type of arrangement the double breaker bay has in the same time the role of the bus-coupler bay for normal double busbar single breaker stations. Therefore, zone interconnection, zone selection and disconnector replica facilities have to be provided for all double busbar bays. Because of the very specific requirements on zone interconnection feature, the following should be considered for this type of application:

- current inputs CT1 and CT2 shall be used for the first double breaker bay.
- current inputs CT3 and CT4 shall be used for the second double breaker bay.
- current inputs CT5 and CT6 shall be used for the third double breaker bay (only available in 1ph version).
Accordingly the following solutions are possible:

Table 22: Typical solutions for combination between double breaker and double busbar station layouts

<table>
<thead>
<tr>
<th>Version of REB670 IED</th>
<th>Number of double breaker feeders / Number of double busbar feeders in the station</th>
<th>Number of REB670 IEDs required for the scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>3PH; 2-zones, 4-bays BBP (A20)</td>
<td>NA</td>
<td>NA</td>
</tr>
<tr>
<td>3PH; 2-zones, 8-bays BBP (A31)</td>
<td>2/4</td>
<td>1</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B20)</td>
<td>NA</td>
<td>NA</td>
</tr>
<tr>
<td>1Ph; 2-zones, 12-bays BBP (B21)</td>
<td>3/6</td>
<td>3</td>
</tr>
<tr>
<td>1Ph; 2-zones, 24-bays BBP (B31)</td>
<td>3/18</td>
<td>3</td>
</tr>
</tbody>
</table>

Please note that Table 22 is given for the preconfigured versions of REB670 which do not contain any VT inputs.

Figure 71: Combination between one-and-half breaker and double busbar station layouts

For this type of busbar arrangement the double busbar bay is usually connected to the reactive power compensation equipment (that is, shunt reactor or shunt capacitor). The diameters in the one-and-half breaker part of the station have at the same time the role of the bus-coupler bay. Therefore, zone interconnection, zone selection and disconnector replica facilities have to be provided for all double busbar bays.
6.1.5 Six-zone busbar arrangements

6.1.5.1 General

Busbar differential protection application for some complex busbar arrangements with up to six differential zones are shown and described in the following sections.

6.1.5.2 Typical arrangement which can be covered

The six-zone busbar differential protection is intended for applications with complex switchgear layout. In general, there are the following restrictions:

1. Up to six independent protection zones
2. Up to twenty-four CT inputs (depends on selected IED hardware)
3. Up to twenty-four feeder bays
4. Up to five bus interconnector (i.e. bus-ties) bays
5. For very large installation the total number of binary inputs and outputs, which can be fitted in one IED, can also be a limiting factor. For such installations the possibility to share the disconnector status among the three IEDs using communication (e.g. GOOSE or LDCM) can be considered.

Five typical examples of busbar arrangements with up to six differential zones are presented in Figure 72 - Figure 76. In the next section, the engineering procedure is described for the example application shown in Figure 75 to illustrate the engineering principles.

Figure 72: Double busbar station with six protection zones
Figure 73: Double busbar station with breaker bypass facility and four protection zones

Figure 74: Double busbar station with transfer bus and five protection zones

Figure 75: Triple busbar station with two sections and six protection zones
6.1.5.3 Example engineering procedure

The procedure for how to engineer the triple busbar station with two sections and six protection zones (see Figure 75) will be presented here.

**Step 1: Allocation of Protection Zones**

In the single line diagram the individual bus-protection zones in the station shall be indicated and associated with the corresponding zones available within the IED. This has been already done for all five example applications, see the marked zones Z1, Z2 etc. in the figures above.

**Step 2: ACT Engineering of the Feeder Bays at Section 1**

The feeders located at the first section, namely Section 1, can be connected to either Z1 or Z2 or Z3, depending on the operational status of the three busbar disconnectors, in this particular station. More specifically, the following steps need to be done, for each feeder bay:

1. Connect the two auxiliary contacts (i.e. normally open and normally closed auxiliary contacts or also sometimes called “a & b contacts”) from each of the three busbar disconnectors to the IED. This can be done by wiring using binary inputs or via communication (e.g. via GOOSE or via LDCM).
2. Use three BDCGAPC function blocks (i.e. one per busbar disconnector) to derive open or closed status of the respective disconnector primary contacts.
3. Connect the close status output from each BDCGAPC function block to the respective zone control input on the feeder bay function block BFPTRC_Fxx. This will ensure proper dynamic inclusion of the feeder CT measurement towards the differential protection zones.
4. Connect the feeder current from the TRM via pre-processing block to the bay function block BFPTRC_Fxx.
5. Connect the trip output from BFPTRC_Fxx function block to the binary output contact (alternatively the tripping can also be arranged using GOOSE communication). This will ensure proper tripping of this feeder bay for any relevant operation of the BBP including BFP tripping.

ACT configuration example for Feeder 01 located at Section 1 is given in Figure 77. Any other feeders located at Section 1 shall be engineered in the similar way.

---

Figure 76: Triple busbar station with transfer bus and four protection zones
**Step 3: ACT engineering of the feeder bays at Section 2**

Identical steps to Step 2 shall be done for the feeders located at the second section, namely Section 2. The only difference is that in Section 2, the relevant zones are Z4, Z5 and Z6.

ACT configuration example for Feeder 17 located at Section 2 is given in Figure 78. Any other feeders located at Section 2 shall be engineered in the similar way.

**Figure 77: ACT configuration example for Feeder 01 in Section 1**

**Step 4: ACT engineering of the Bus Coupler bays at Section 1**

The bus-coupler bay is used to interconnect the differential zones via a circuit breaker within one section. In this particular station, the Bus-Coupler 01 is located at Section 1 and consequently it can be connected to either Z1 or Z2 or Z3. How it is connected depends on the status of the six busbar disconnectors (i.e. three on each side). More specifically, the following need to be done, for each bus coupler bay:

1. Connect the two auxiliary contacts (i.e. normally open and normally closed auxiliary contacts or also sometimes called “a & b contacts”) from each of the six busbar disconnectors to the

**Figure 78: ACT configuration example for Feeder 17 in Section 2**
IEC. This can be done by wiring using binary inputs or via communication (e.g. via GOOSE or via LDCM).

2. Use six BDCGAPC function blocks (i.e. one per busbar disconnector) to derive open or closed status of the respective disconnector primary contacts.

3. Connect the close status output from each BDCGAPC function block to the respective zone control input on the bus-interconnector function block BICPTRC_xx. This will ensure proper dynamic inclusion of the CT measurements from the two CTs located within the bus-section bay towards the differential protection zones.

4. Connect the two currents from the TRM via pre-processing blocks to the bus-interconnector function block BICPTRC_xx. Ensure overlapping of the two CTs. In case that only one CT is available, connect this CT to both CT inputs on the BICPTRC_xx function block and then invert one of the two inputs in software by using the parameter setting OperMode. This shall be done taking into account the actual location of the CT star point in this bay.

5. Connect the trip output from BICPTRC_xx function block to the binary output contact (alternatively the tripping can also be arranged using GOOSE communication). This will ensure proper tripping of the bus-coupler bay for any relevant operation of the BBP including BFP tripping.

ACT configuration example for Bus-coupler 01 located at Section 1 is given in Figure 79. Any other bus coupler bays located at Section 1 shall be engineered in the similar way. For the bus coupler bays located at Section 2, similar steps to Step 3 shall be done, except the relevant zones are then Z4, Z5 and Z6.

Figure 79: ACT configuration example for Bus-Coupler 01 in Section 1

Step 5: ACT engineering of the Bus Section bay

The bus-section bay is used to interconnect the two section via a circuit breaker. Similar steps to Step 4 can be done for bus section bay, except that the bus section bay can be connected to Z1 or Z2 or Z3 on the first section and to Z4 or Z5 or Z6 on the second section. How it is connected depends on the operational status of the six busbar disconnectors (i.e. three on each side). More specifically, the following steps need to be done:
1. Connect the two auxiliary contacts (i.e. normally open and normally closed auxiliary contacts or also sometimes called “a & b contacts”) from each of the six busbar disconnectors to the IED. This can be done by wiring using binary inputs or via communication (e.g. via GOOSE or via LDCM).

2. Use six BDCGAPC function blocks (i.e. one per busbar disconnector) to derive open or closed status of the respective disconnector primary contacts.

3. Connect the close status output from BDCGAPC function block to the respective zone control input on the bus-interconnector function block BICPTRC_xx. This will ensure proper dynamic inclusion of the CT measurements from the two CTs located within the bus-section bay towards the differential protection zones.

4. Connect the two currents from the TRM via pre-processing blocks to the BICPTRC_xx function block. Ensure overlapping of the two CTs. In case that only one CT is available, connect this CT to both CT inputs on the BICPTRC_xx function block and then invert one of the two in the software by using the parameter setting OperMode. This shall be done taking into account the actual location of the CT star point in this bay.

5. Connect the trip output from the BICPTRC_xx function block to the binary output contact (alternatively tripping can also be arranged using GOOSE communication). This will ensure proper tripping of the bus-section bay for any relevant operation of the BBP including BFP tripping.

ACT configuration example for Bus-section bay is given in Figure 80.

![ACT configuration example for Bus-section bay](image_url)

**Figure 80:** ACT configuration example for Bus-Section bay

**Step 6:** ACT engineering of the Bus Sectionalizing Disconnectors

Three bus-sectionalizing disconnectors are installed in this station. Each disconnector is used to interconnect the two zones located at the two sections. Ultimately when this type of disconnector
is closed, the two relevant zones shall be merged (i.e. switched into one common zone) in the IED software. In this particular station, the zones can be possibly linked in the following way: Z1&Z4, Z2&Z5 and Z3&Z6. When any linking shall occur, it depends on the operational status of the three bus-sectionalizing disconnectors. More specifically, the following steps need to be done:

1. Connect the two auxiliary contacts (i.e. normally open and normally closed auxiliary contacts or also sometimes called “a & b contacts”) from each of the three sectionalizing disconnectors to the IED. This can be done by wiring using binary inputs or via communication (e.g. via GOOSE or via LDCM).
2. Use three BDCGAPC function blocks (i.e. one per busbar disconnector) to derive open or closed status of the respective disconnector primary contacts.
3. Connect the close status output from each BDCGAPC function block to the respective zones merging input on the common BBP function block BDZSGAPC. This will ensure proper dynamic linking of the affected differential zones.

ACT configuration example for three bus-sectionalizing disconnectors presenting in this station is given in Figure 81.

![Configuration Diagram](image)

**Figure 81:** ACT configuration example for three bus-sectionalizing disconnectors

### 6.1.6 Summation principle

Summation principle is mainly intended for two-zone busbar protection applications.

#### 6.1.6.1 Introduction

A simplified bus differential protection for phase and ground faults can be obtained by using a single one-phase IED with external auxiliary summation current transformers for all busbar arrangements with up to two differential zones. By using this approach, more cost effective bus differential protection can be obtained. Such a solution makes it feasible to apply bus differential protection even to medium voltage substations. The principal differences between full, phase-segregated bus differential protection scheme and summation type bus differential protection scheme are shown in Figure 82.
Figure 82: Difference between phase segregated & summation type differential protection

In the full phase-segregated design, three one-phase REB670 IEDs (that is, one per phase) are used. However for the summation type only single one-phase REB670 IED plus one auxiliary summation CT per each main CT is required. These auxiliary summation CTs convert each main CT three-phase currents to a single-phase output current, which are all measured by one REB670 IED. The differential calculation is then made on a single-phase basis. By doing so, this more cost effective bus differential protection can be applied. Due to this characteristic, this summation type of bus differential protection can be applied for all types of stations arrangements up to two differential zones as shown in Section Two-zone busbar arrangements, for three one-phase IEDs.

As an example, the necessary equipment for the summation type, busbar differential protection for a single busbar station with up to 24 bays, is shown in Figure 83.
Figure 83: Principle CT connections for the complete station

This summation type bus differential protection still has the same main CT requirements as outlined in section "Rated equivalent secondary e.m.f. requirements". Some of these are:

- main CT ratio differences can be tolerated up to 10:1 (for example, 3000/5A CT can be balanced against CT"s as low as 300/5)
- different main CT ratios are compensated numerically by a parameter setting
- main CT shall not saturate quicker than 2 ms (refer to section "Rated equivalent secondary e.m.f. requirements" for detailed CT requirements regarding main CT knee-point voltage)

However, due to the summation principle this type of busbar protection scheme has the following limitations:
• Only one measuring circuit is utilized for all fault types (that is, no redundancy for multi-phase faults)
• Primary fault sensitivity varies depending on the type of fault and involved phase(s), see Table 24
• The load currents in the healthy phases might produce the stabilizing current when an internal single phase to ground fault occurs. However, there is no problem for solidly gounded systems with high ground-fault currents
• No indication of faulty phase(s) in case of an internal fault
• Not possible to fully utilize Open CT detection feature

6.1.6.2 Auxiliary summation CTs

Auxiliary Summation Current Transformer (that is, ASCT in further text) of the type SLCE 8 is used with the summation principle of the IED. The principle drawing of one such ASCT is shown in Figure 84.

![Auxiliary Summation CT type SLCE 8; X/1A](en03000118_ansi.vsd)

**Figure 84: Principle ASCT drawing**

The ASCT has three primary windings and one secondary winding. In further text, turn numbers of these windings will be marked with N1, N2, N3 & N4, respectively (see Figure 84 for more information).

There are three types of ASCT for REB670:

1. ASCT type with ratio 1/1A, for balanced 3-Ph current input, shall be used with all main current transformers with 1A rated secondary current (that is, 2000/1A)
2. ASCT type with ratio 5/1A, for balanced 3-Ph current input, shall be used with all main current transformers with 5A rated secondary current (that is, 3000/5A)
3. ASCT type with ratio 2/1A, for balanced 3-Ph current input, shall be used with all main current transformers with 2A rated secondary current (that is, 1000/2A)

Note the following:
- main CT rated primary current is not important for ASCT selection
- possible main CT ratio differences will be compensated by a parameter setting in the IED
- rated secondary current of ASCT is 1A for all types. That means that secondary ASCT winding should be always connected to the IED with 1A CT inputs, irrespective of the rated secondary current of the main CT

All of these features simplify the ordering of the ASCTs. Practically, in order to purchase ASCTs, the only required information is the main CT rated secondary current that is, (1A, 2A or 5A).

Table 23 summarizes the ASCT data:

<table>
<thead>
<tr>
<th>Type of fault</th>
<th>N1</th>
<th>N2</th>
<th>N3</th>
<th>N4</th>
<th>Vkp [V]</th>
<th>Burden [VA]</th>
</tr>
</thead>
<tbody>
<tr>
<td>ASCT SLCE 8; 1/1A</td>
<td>52</td>
<td>52</td>
<td>104</td>
<td>90</td>
<td>33</td>
<td>1.0</td>
</tr>
<tr>
<td>ASCT SLCE 8; 5/1A</td>
<td>12</td>
<td>12</td>
<td>24</td>
<td>104</td>
<td>38</td>
<td>1.0</td>
</tr>
<tr>
<td>ASCT SLCE 8; 2/1A</td>
<td>26</td>
<td>26</td>
<td>52</td>
<td>90</td>
<td>33</td>
<td>1.0</td>
</tr>
</tbody>
</table>

where:
- N1, N2, N3 & N4 are ASCT windings turn numbers (see Figure 84)
- Vkp is knee point voltage, at 1.6T, of the secondary winding with N4 turns
- Burden is the total 3Ph load of ASCT imposed to the main CT

Due to ASCT design, the ASCTs for summated bus differential protection, must always be mounted as close as possible to the IED (that is, in the same protection cubicle).

### 6.1.6.3 Possible ASCT connections for REB670

It is possible to connect the ASCTs for summated bus differential protection with REB670:

- at the end of the main CT circuit (for example, beyond the other protective relays, as shown in Figure 85
- in series with other secondary equipment when some other relay must be located at the end of the main CT circuit , as shown in Figure 86

End connection is the preferred arrangement as it gives greater sensitivity for summation type bus differential protection (as shown in Table 24 for more information).

However, it should be noted that these two connection types must not be mixed. This means that within one busbar installation all auxiliary summation CTs have to be either end-connected or series-connected.

Typical end-connection with ASCT is shown in Figure 85.
Figure 85:  End-connection with ASCT connected to CT3 input

It is important to notice that even in the case of 5A or 2A main CTs, secondary current of the summation CTs shall be connected to the IED with 1A CT inputs (as shown in Figure 85). The reason for this is that the rated secondary current of ASCT is always 1A irrespective of the rated secondary current of the main CT.

Refer to section "SLCE 8/ASCT characteristics for end-connection" for detailed ASCT current calculations for end-connection.

Typical series-connection with ASCT is shown in Figure 86.

Figure 86:  Series-connection with ASCT connected to CT3 input
Refer to Section "SLCE 8/ASCT characteristics for series-connection" for detailed ASCT current calculation for series-connection.

6.1.6.4 Main CT ratio mismatch correction

As stated before, three types of ASCTs for REB670 are available. The first type shall be used for main CTs with 1A rated secondary current. The second type shall be used for main CTs with 5A rated secondary current. The third type shall be used with 2A main CTs. However REB670 with 1A CT inputs is always used. Therefore main CT ratio shall always be set in such a way that the primary current is entered as for the main CT, but secondary current is always entered as 1A (that is, 3000/5 main CT will be entered as 3000/1 CT in REB670).

6.1.6.5 Primary pick-up levels for summation type differential protection

The minimal differential operating current level is entered directly in primary amperes. However, as stated previously, in case of the summated differential protection the primary fault sensitivity varies depending on the type of fault and involved phase(s). The entered value, for the minimal differential operating current level, will exactly correspond to the REB670 pickup value in the event of a 3-phase internal fault. For all other fault types this value must be multiplied by a coefficient shown in the Table 24 in order to calculate the actual primary pickup value.

Table 24: Pickup coefficients for Summated Differential Protection

<table>
<thead>
<tr>
<th>Type of fault</th>
<th>A-Gnd</th>
<th>B-Gnd</th>
<th>C-Gnd</th>
<th>A-B</th>
<th>BC</th>
<th>C-A</th>
<th>ABC</th>
</tr>
</thead>
<tbody>
<tr>
<td>ASCT end connected</td>
<td>0.434</td>
<td>0.578</td>
<td>0.867</td>
<td>1.732</td>
<td>1.732</td>
<td>0.867</td>
<td>1.0</td>
</tr>
<tr>
<td>ASCT series connected</td>
<td>1.732</td>
<td>0.867</td>
<td>0.578</td>
<td>1.732</td>
<td>1.732</td>
<td>0.867</td>
<td>1.0</td>
</tr>
</tbody>
</table>

The coefficients in Table 24 are only relevant for ideal internal faults (that is, load currents do not exist in the healthy phases).

Example 1:
The minimal differential operating current level in the IED is set to 1250A. All ASCTs are series connected. What is the theoretical primary pickup value in case of C-Gnd fault?

Answer 1:
According to Table 24, pickup coefficient for this type of ASCT connection and this type of fault is 0.578. Therefore:

\[ I_{\text{Pickup}}(L3 - \text{Gnd}) = 0.578 \cdot 1250 = 722.5A \]  
(Equation 11)

\[ I_{\text{Pickup}}(C - \text{Gnd}) = 0.578 \cdot 1250 = 722.5A \]  
(Equation 11)
This means that if 722.5 primary amperes is injected only in phase C of any of the connected main CTs, the IED shall display the differential current of 1250A (primary) and should be on the point of the pickup (that is, trip).

In addition to busbar protection differential zones, the IED can incorporate other additional functions and features. If and how they can be used together with summation busbar protection design is shown in Table 25:

Table 25: Functions

<table>
<thead>
<tr>
<th>Functions</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>Busbar Differential Protection</td>
<td>Differential Protection, Sensitive differential protection, OCT algorithm, Check Zone and Differential Supervision features will be connected to the summated bay currents. Therefore, they will have different level depending on the type of fault and involved phase(s). For more information, refer to Table 24. However, if all these limitations are acceptable it is still possible to use all these internal busbar protection features. Note that OCT operating logic will not work properly in case of opening or shorting the main CT secondary leads (that is, in-between main CT and ASCT). In case of opening or shorting the ASCT secondary leads (that is, in-between ASCT and the IED) the OCT logic will operate correctly.</td>
</tr>
<tr>
<td>Dynamic Zone Selection feature</td>
<td>Zone Selection feature in the IED can be used in the exactly same way as with phase segregated design. All built-in features, even including breaker failure protection, protection back-up trip command routing, EnFP logic can be used in the exactly same way as for phase segregated design.</td>
</tr>
<tr>
<td>CCRBRF/CCSRBRF function</td>
<td>Breaker Fail Protection function will be connected to the summated bay current. Therefore it will have different pickup level depending on the type of fault and involved phase(s). See Table 24 for more info. It will not be possible to have individual initiation per phase, but only three-phase initiation can be effectively used. However, if all these limitations are acceptable it is still possible to use internal CCRBRF/CCSRBRF functions.</td>
</tr>
<tr>
<td>OC4PTOC/PHS4PTOC function</td>
<td>Overcurrent Protection function will be connected to the summated bay current. Therefore it will have different pickup level depending on the type of fault and involved phase(s). See Table 24 for more info. Thus it will be very difficult to insure proper pickup and time grading with downstream overcurrent protection relays. Hence it will be quite difficult to use OC4PTOC/PHS4PTOC as backup feeder protection with summation design.</td>
</tr>
<tr>
<td>OC4PTOC/PHS4PTOC function</td>
<td>End Fault Protection feature will be connected to the summated bay current. Therefore it will have different pickup level depending on the type of fault and involved phase(s). See Table 24 for more info. However, OC4PTOC/PHS4PTOC do not need any pickup or time coordination with any other overcurrent protection. Thus if above limitations are acceptable OC4PTOC/PHS4PTOC can be used with summation design.</td>
</tr>
<tr>
<td>DRPRDRE function</td>
<td>Disturbance Recording feature will be connected to the individual summated bay current. Therefore recorded currents will not correspond to any actual primary currents. However such DRPRDRE records can still be used to evaluate internal busbar protection, CCRBRF/CCSRBRF and OC4PTOC/PHS4PTOC protections operation.</td>
</tr>
<tr>
<td>DRPRDRE function</td>
<td>Event List feature in the IED can be used in the exactly the same way as with phase segregated design.</td>
</tr>
</tbody>
</table>

Table continues on next page
### Functions

<table>
<thead>
<tr>
<th>Function</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>DRPRDRE function</td>
<td>Trip Value Recording feature will be connected to the individual summated bay current. Therefore recorded trip current values will not correspond to any actual primary currents. However such records can still be used to evaluate internal busbar protection, CCRBRF/CCSRBRF and OC4PTOC/PHS4PTOC protections operation.</td>
</tr>
<tr>
<td>Communication</td>
<td>All communication features in the IED can be used in the exactly the same way as with phase segregated design.</td>
</tr>
<tr>
<td>SMBRREC function</td>
<td>Autoreclosing function in the IED can be used in the exactly same way as with phase segregated design.</td>
</tr>
</tbody>
</table>

---

### 6.1.6.6 SLCE 8/ASCT characteristics for end-connection

Typical ASCT end-connection is shown in Figure 85. For this ASCT connection type, the ampere-turn balance equation has the form according to Equation 12:

\[
N_4 \cdot I_{SUMM} = N_1 \cdot I_A + N_2 \cdot (I_A + I_B) + N_3 \cdot (I_A + I_B + I_C)
\]

(Equation 12)

The relationships between number of turns for this SLCE 8, ASCT for REB670, is shown in Equation 13, Equation 14 and Equation 15:

\[
N_1 = N_2 = N;
\]

(Equation 13)

\[
N_3 = 2 \cdot N
\]

(Equation 14)

\[
N_4 = k \cdot \sqrt{3} \cdot N
\]

(Equation 15)

where:

- \(k\) a constant, which depends on the type of ASCT
  - (that is, \(k=1\) for 1/1A ASCT or \(k=5\) for 5/1A ASCT or \(k=2\) for 2/1A ASCT)

The well-known relationship, between positive, negative and zero sequence current components and individual phase current quantities is shown in Equation 16:
\[
\begin{bmatrix}
IA \\
IB \\
IC
\end{bmatrix} = \begin{bmatrix}
1 & 1 & 1 \\
a^2 & a & 1 \\
a & a^2 & 1
\end{bmatrix} \cdot \begin{bmatrix}
I_1 \\
I_2 \\
I_0
\end{bmatrix}
\]

where:
\[a\text{ complex constant (that is, } a=-0.5+j0.866)\].

By including Equation 13, Equation 14, Equation 15 and Equation 16 into the Equation 12 the equation for the end-connected, ASCT secondary current (that is, summated current) can be derived according to equation 17:

\[
I_{SUMM} = \frac{1}{k} \cdot (I_1 \cdot e^{j30^\circ} + I_2 \cdot e^{j30^\circ} + 3 \cdot \sqrt{3} \cdot I_0)
\]

From Equation 17 it is obvious that the ASCT rated ratio is declared for balanced three phase current system, when only positive sequence current component exist. For any unbalanced condition (that is, external or internal fault), both negative and zero sequence current components will give their own contribution to the summated current.

### 6.1.6.7 SLCE 8/ASCT characteristics for series-connection

Typical ASCT series-connection is shown in Figure 86. For this ASCT connection type, the ampere-turn balance equation has the form according to equation 18:

\[
N4 \cdot I_{SUMM} = N1 \cdot IA - N2 \cdot IC - N3(IA + IB + IC)
\]

The relationships between the number of turns for this SLCE 8 ASCT for REB670, is shown in Equation 19, Equation 20, Equation 21:

\[
N1 = N2 = N;
\]

\[
N3 = 2 \cdot N
\]
\[ N4 = k \cdot \sqrt{3} \cdot N \]  
(Equation 21)

where:

\( k \) is a constant, which depends on the type of ASCT  
(that is, \( k=1 \), for 1/1A ASCT or \( k=5 \) for 5/1A ASCT or \( k=2 \) for 2/1A ASCT).

The well-known relationship, between positive, negative and zero sequence current components and individual phase current quantities is shown in Equation 22:

\[
\begin{bmatrix}
IA \\
IB \\
IC
\end{bmatrix} = \begin{bmatrix}
1 & 1 & 1 \\
\alpha^2 & \alpha & 1 \\
\alpha & \alpha^2 & 1
\end{bmatrix} \begin{bmatrix}
I_1 \\
I_2 \\
I_0
\end{bmatrix}
\]

(Equation 22)

where:

\( \alpha \) is complex constant (that is, \( \alpha=-0.5+j0.866 \)).

By including Equation 19, Equation 20, Equation 21 and Equation 22 into the Equation 18 the equation for the series-connected, ASCT secondary current (that is, summated current) can be derived according to equation 23:

\[
I_{SUMM} = \frac{1}{k} \cdot (I_1 \cdot e^{j30^\circ} + I_2 \cdot e^{j30^\circ} + 2 \cdot \sqrt{3} \cdot I_0)
\]

(Equation 23)

From Equation 23 it is obvious that the ASCT rated ratio is declared for balanced three phase current system, when only positive sequence current component exist. For any unbalanced condition (that is, external or internal fault), both negative and zero sequence current components will give their own contribution to the summated current.
Section 7  Current protection

7.1  Directional phase overcurrent protection, four steps OC4PTOC(51_67)

7.1.1  Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Directional phase overcurrent protection, four steps</td>
<td>OC4PTOC</td>
<td></td>
<td>51_67</td>
</tr>
</tbody>
</table>

7.1.2  Application

Directional phase overcurrent protection, four steps OC4PTOC (51_67) is used in several applications in the power system. Some applications are:

- Short circuit protection of feeders in distribution and subtransmission systems. Normally these feeders have a radial structure.
- Back-up short circuit protection of transmission lines.
- Back-up short circuit protection of power transformers.
- Short circuit protection of different kinds of equipment connected to the power system such as; shunt capacitor banks, shunt reactors, motors and others.
- Back-up short circuit protection of power generators.

In many applications several steps with different current pickup levels and time delays are needed. OC4PTOC (51_67) can have up to four different, individually settable steps. The following options are possible:

Non-directional / Directional function: In most applications the non-directional functionality is used. This is mostly the case when no fault current can be fed from the protected object itself. In order to achieve both selectivity and fast fault clearance, the directional function can be necessary.

If VT inputs are not available or not connected, the setting parameter DirModeSelx (x = step 1, 2, 3 or 4) shall be left to the default value Non-directional.

Choice of time delay characteristics: There are several types of time delay characteristics available such as definite time delay and different types of inverse time delay characteristics. The selectivity between different overcurrent protections is normally enabled by co-ordination between the
function time delays of the different protections. To enable optimal co-ordination between all overcurrent protections, they should have the same time delay characteristic. Therefore, a wide range of standardized inverse time characteristics are available for IEC and ANSI. It is also possible to tailor make the inverse time characteristic.

Normally, it is required that the phase overcurrent protection shall reset as fast as possible when the current level gets lower than the operation level. In some cases some sort of delayed reset is required. Therefore, different kinds of reset characteristics can be used.

For some protection applications, there can be a need to change the current pick-up level for some time. A typical case is when the protection will measure the current to a large motor. At the start up sequence of a motor the start current can be significantly larger than the rated current of the motor. Therefore, there is a possibility to give a setting of a multiplication factor to the current pick-up level. This multiplication factor is activated from a binary input signal to the function.

Power transformers can have a large inrush current, when being energized. This phenomenon is due to saturation of the transformer magnetic core during parts of the period. There is a risk that inrush current will reach levels above the pick-up current of the phase overcurrent protection. The inrush current has a large 2\textsuperscript{nd} harmonic content. This can be used to avoid unwanted operation of the protection function. Therefore, OC4PTOC (51/67) has a possibility of 2\textsuperscript{nd} harmonic restrain if the level of 2\textsuperscript{nd} harmonic current reaches a value above a set percent of the fundamental current.

The phase overcurrent protection is often used as a protection for two and three phase short circuits. In some cases, it is not wanted to detect single-phase ground faults by the phase overcurrent protection. This fault type is detected and cleared after operation of ground fault protection. Therefore, it is possible to make a choice how many phases, at minimum, that have to have current above the pick-up level, to enable operation. If set 1 of 3 it is sufficient to have high current in one phase only. If set 2 of 3 or 3 of 3 single-phase ground faults are not detected.

### 7.1.3 Setting guidelines

When inverse time overcurrent characteristic is selected, the trip time of the stage will be the sum of the inverse time delay and the set definite time delay. Thus, if only the inverse time delay is required, it is important to set the definite time delay for that stage to zero.

The parameters for the directional phase overcurrent protection, four steps OC4PTOC (51/67) are set via the local HMI or PCM600.

The following settings can be done for OC4PTOC (51/67).

Common base IED values for the primary current (\(I_{\text{Base}}\)), primary voltage (\(V_{\text{Base}}\)) and primary power (\(S_{\text{Base}}\)) are set in global base values for settings function GBASVAL.

\(\text{GlobalBaseSel}:\) Selects the global base value group used by the function to define \(I_{\text{Base}}, V_{\text{Base}}\) and \(S_{\text{Base}}\). Note that this function will only use \(I_{\text{Base}}\) value.

\(\text{MeasType}:\) Selection of discrete Fourier filtered (DFT) or true RMS filtered (RMS) signals. RMS is used when the harmonic contents are to be considered, for example in applications with shunt capacitors.

\(\text{Operation}:\) The protection can be set to \textit{Enabled} or \textit{Disabled}. 


*AngleRCA*: Protection characteristic angle set in degrees. If the angle of the fault loop current has the angle RCA, the direction to the fault is forward.

*AngleROA*: Angle value, given in degrees, to define the angle sector of the directional function, shown in Figure 87.

*NumPhSel*: Number of phases, with high current, required for operation. The setting possibilities are: 1 out of 3, 2 out of 3 and 3 out of 3. The default setting is 1 out of 3.

*PUMinOpPhSel*: Minimum current setting level for releasing the directional start signals in % of $IB$. This setting should be less than the lowest step setting. The default setting is 7% of $IB$.

*2ndHarmStab*: Operate level of 2nd harmonic current restrain set in % of the fundamental current. The setting range is 5 - 100% in steps of 1%. The default setting is 20%.

*Figure 87*: Directional function characteristic

1. RCA = Relay characteristic angle
2. ROA = Relay operating angle
3. Reverse
4. Forward
7.1.3.1 Settings for each step

$x$ means step 1, 2, 3 and 4.

**DirModeSel**: The directional mode of step $x$. Possible settings are *Disabled/ Non-directional/ Forward/ Reverse*.

**Characteristics**: Selection of time characteristic for step $x$. Definite time delay and different types of inverse time characteristics are available according to Table 26.

**Table 26: Inverse time characteristics**

<table>
<thead>
<tr>
<th>Curve name</th>
</tr>
</thead>
<tbody>
<tr>
<td>ANSI Extremely Inverse</td>
</tr>
<tr>
<td>ANSI Very Inverse</td>
</tr>
<tr>
<td>ANSI Normal Inverse</td>
</tr>
<tr>
<td>ANSI Moderately Inverse</td>
</tr>
<tr>
<td>ANSI/IEEE Definite time</td>
</tr>
<tr>
<td>ANSI Long Time Extremely Inverse</td>
</tr>
<tr>
<td>ANSI Long Time Very Inverse</td>
</tr>
<tr>
<td>ANSI Long Time Inverse</td>
</tr>
<tr>
<td>IEC Normal Inverse</td>
</tr>
<tr>
<td>IEC Very Inverse</td>
</tr>
<tr>
<td>IEC Inverse</td>
</tr>
<tr>
<td>IEC Extremely Inverse</td>
</tr>
<tr>
<td>IEC Short Time Inverse</td>
</tr>
<tr>
<td>IEC Long Time Inverse</td>
</tr>
<tr>
<td>IEC Definite Time</td>
</tr>
<tr>
<td>User Programmable</td>
</tr>
<tr>
<td>ASEA RI</td>
</tr>
<tr>
<td>RXIDG or RD (logarithmic)</td>
</tr>
</tbody>
</table>

The different characteristics are described in *Technical manual*.

**Pickup**: Operate phase current level for step $x$ given in % of $I_B$.

$Ix>Max$ and $Ix>Min$ should only be changed if remote setting of operation current level, $Ix>$, is used. The limits are used for decreasing the used range of the $Ix>$ setting. If $Ix>$ is set outside $Ix>Max$ and $Ix>Min$, the closest of the limits to $Ix>$ is used by the function. If $Ix>Max$ is smaller than $Ix>Min$, the limits are swapped.

**$tx$**: Definite time delay for step $x$. The definite time $tx$ is added to the inverse time when inverse time characteristic is selected. Note that the value set is the time between activation of the start and the trip outputs.

**$TDx$**: Time multiplier for inverse time delay for step $x$. 
IMinx: Minimum operate current in % of IBase for all inverse time characteristics, below which no operation takes place.

IMinx: Minimum pickup current for step x in % of IBase. Set IMinx below Pickupx for every step to achieve ANSI reset characteristic according to standard. If IMinx is set above Pickupx for any step the ANSI reset works as if current is zero when current drops below IMinx.

txmin: Minimum trip time for all inverse time characteristics. At high currents the inverse time characteristic might give a very short operation time. By setting this parameter the operation time of the step can never be shorter than the setting. Setting range: 0.000 - 60.000s in steps of 0.001s.

MultPUx: Multiplier for scaling of the current setting value. If a binary input signal ENMULTx (enableMultiplier) is activated the current operation level is increased by this setting constant. Setting range: 1.0-10.0

Figure 88: Minimum pickup current and trip time for inverse time characteristics
In order to fully comply with the definition of the curve, the setting parameter txMin shall be set to a value equal to the operating time of the selected inverse curve for twenty times the set current pickup value. Note that the operate time is dependent on the selected time multiplier setting kx.

ResetTypeCrvx: The reset of the delay timer can be made as shown in Table 27.

Table 27: Reset possibilities

<table>
<thead>
<tr>
<th>Curve name</th>
<th>Curve index no.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Instantaneous</td>
<td>1</td>
</tr>
<tr>
<td>IEC Reset (constant time)</td>
<td>2</td>
</tr>
<tr>
<td>ANSI Reset (inverse time)</td>
<td>3</td>
</tr>
</tbody>
</table>
The delay characteristics are described in *Technical manual*. There are some restrictions regarding the choice of the reset delay.

For the definite time delay characteristics, the possible delay time setting instantaneous (1) and IEC (2 = set constant time reset).

For ANSI inverse time characteristics, all three types of reset time characteristics are available: instantaneous (1), IEC (2 = set constant time reset) and ANSI (3 = current dependent reset time).

For IEC inverse time characteristics, the possible delay time settings are instantaneous (1) and IEC (2 = set constant time reset).

For the customer tailor-made inverse time delay characteristics (type 17), all three types of reset time characteristics are available: instantaneous (1), IEC (2 = set constant time reset) and ANSI (3 = current dependent reset time). If the current-dependent type is used, settings $pr$, $tr$ and $cr$ must be given.

$tResetx$: Constant reset time delay in seconds for step $x$.

t$PCrvx$, t$ACrvx$, t$BCrvx$, t$CCrvx$: These parameters are used by the customer to create the inverse time characteristic curve. See equation 24 for the time characteristic equation. For more information, refer to *Technical manual*.

\[
t[s] = \left( \frac{A}{\left( \frac{i}{in>} \right)^n} + B \right) \cdot \text{MultPU}x
\]

(Equation 24)

t$PRCrvx$, t$TRCrvx$, t$CRCrvx$: These parameters are used by the customer to create the inverse reset time characteristic curve. For more information, refer to *Technical manual*.

HarmRestrainx: Enables the block of step $x$ from the harmonic restrain function (2nd harmonic). This function should be used when there is a risk of an unwanted trip caused by power transformer inrush currents. It can be set to Disabled/Enabled.

### 7.1.3.2 Setting example

Directional phase overcurrent protection, four steps can be used in different ways, depending on the application where the protection is used. A general description is given below.

The pickup current setting of the inverse time protection, or the lowest current step of the definite time protection, must be defined so that the highest possible load current does not cause protection operation. The protection reset current must also be considered so that a short peak of overcurrent does not cause the operation of a protection even when the overcurrent has ceased. This phenomenon is described in Figure 89.
Figure 89: Pickup and reset current for an overcurrent protection

The lowest setting value can be written according to Equation 25.

\[
I_{pu} \geq 1.2 \cdot \frac{I_{max}}{k}
\]  

(Equation 25)

where:
- 1.2 is a safety factor
- \( k \) is the reset ratio of the protection
- \( I_{max} \) is the maximum load current

The load current up to the present situation can be found from operation statistics. The current setting must remain valid for several years. In most cases, the setting values are updated once every five years or less often. Investigate the maximum load current that the equipment on the line can withstand. Study components, such as line conductors, current transformers, circuit breakers, and disconnectors. The manufacturer of the equipment normally gives the maximum thermal load current of the equipment.

The maximum load current on the line has to be estimated. There is also a demand that all faults within the zone that the protection shall cover must be detected by the phase overcurrent protection. The minimum fault current \( I_{scmin} \) to be detected by the protection must be calculated. Taking this value as a base, the highest pickup current setting can be written according to Equation 26.
Ipu ≤ 0.7 · Isc min

(Equation 26)

where:

0.7 is a safety factor

Isc min is the smallest fault current to be detected by the overcurrent protection.

As a summary, the pickup current shall be chosen within the interval stated in Equation 27.

\[
1.2 \cdot \frac{I_{\text{max}}}{k} \leq \text{Ipu} \leq 0.7 \cdot \text{Isc min}
\]

(Equation 27)

The high current function of the overcurrent protection, which only has a short-delay trip time, must be given a current setting so that the protection is selective to other protection functions in the power system. It is desirable to have rapid tripping of faults within a large part of the power system to be protected by the protection (primary protected zone). A fault current calculation gives the largest current of faults, Iscmax, at the most remote part of the primary protected zone. The risk of transient overreach must be considered, due to a possible DC component of the short circuit current. The lowest current setting of the fastest stage can be written according to

\[
I_{\text{high}} \geq 1.2 \cdot k_t \cdot I_{\text{sc max}}
\]

(Equation 28)

where:

1.2 is a safety factor

k_t is a factor that takes care of the transient overreach due to the DC component of the fault current and can be considered to be less than 1.05

Isc max is the largest fault current at a fault at the most remote point of the primary protection zone.

The operate time of the phase overcurrent protection has to be chosen so that the fault time is short enough that the protected equipment will not be destroyed due to thermal overload while, at the same time, selectivity is assured. For overcurrent protection in a radial fed network, the time setting can be chosen in a graphical way. This is mostly used in the case of inverse time overcurrent protection. Figure 90 shows how the time-versus-current curves are plotted in a diagram. The time setting is chosen to get the shortest fault time with maintained selectivity. Selectivity is assured if the time difference between the curves is larger than a critical time difference.
The operation time can be set individually for each overcurrent protection.

To assure selectivity between different protection functions in the radial network, there has to be a minimum time difference $\Delta t$ between the time delays of two protections. To determine the shortest possible time difference, the operation time of the protection, the breaker opening time and the protection resetting time must be known. These time delays can vary significantly between different protective equipment. The following time delays can be estimated:

- Protection operation time: 15-60 ms
- Protection resetting time: 15-60 ms
- Breaker opening time: 20-120 ms

**Example for time coordination**

Assume two substations A and B directly connected to each other via one line, as shown in the Figure 91. Consider a fault located at another line from the station B. The fault current to the overcurrent protection of IED B1 has a magnitude so that the overcurrent protection will start and subsequently trip, and the overcurrent protection of IED A1 must have a delayed operation in order to avoid maloperation. The sequence of events during the fault can be described using a time axis shown in Figure 91.
The fault occurs
Protection B1 trips
Breaker at B1 opens
Protection A1 resets

**Figure 91: Sequence of events during fault**

where:
\[ t=0 \] is when the fault occurs
\[ t=t_1 \] is when protection IED B1 and protection IED A1 start
\[ t=t_2 \] is when the trip signal from the overcurrent protection at IED B1 is sent to the circuit breaker.
\[ t=t_3 \] is when the circuit breaker at IED B1 opens. The circuit breaker opening time is \( t_3 - t_2 \)
\[ t=t_4 \] is when the overcurrent protection at IED A1 resets. The protection resetting time is \( t_4 - t_3 \).

To ensure that the overcurrent protection at IED A1 is selective to the overcurrent protection at IED B1, the minimum time difference must be larger than the time \( t_3 \). There are uncertainties in the values of protection operation time, breaker opening time and protection resetting time. Therefore a safety margin has to be included. With normal values the needed time difference can be calculated according to Equation 29.

\[ \Delta t \geq 40 \text{ms} + 100 \text{ms} + 40 \text{ms} + 40 \text{ms} = 220 \text{ms} \]

(Equation 29)

where it is considered that:
the operate time of overcurrent protection B1 is 40 ms
the breaker open time is 100 ms
the resetting time of protection A1 is 40 ms and
the additional margin is 40 ms
7.2 Four step single phase overcurrent protection
PH4SPTOC (51)

7.2.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Four step single phase overcurrent protection</td>
<td>PH4SPTOC</td>
<td></td>
<td>51</td>
</tr>
</tbody>
</table>

7.2.2 Application

The Four step single phase overcurrent protection (PH4SPTOC, 51) function is used in several applications in the power system. Some applications are:

- Short circuit protection of feeders in distribution and subtransmission systems. Normally these feeders have radial structure.
- Back-up short circuit protection of transmission lines.
- Back-up short circuit protection of power transformers
- Short circuit protection of different kinds of equipment connected to the power system such as; shunt capacitor banks, shunt reactors, motors and others.
- Back-up short circuit protection of power generators.

The single phase overcurrent protection is used in IEDs having only input from one phase, for example busbar protection for large busbars (with many bays).

In many applications several steps with different current pick up levels and time delays are needed. PH4SPTOC(51) can have up to four different, individual settable, steps. The flexibility of each step of PH4SPTOC(51) function is great. The following options are possible:

Choice of delay time characteristics: There are several types of time delay characteristics available such as definite time delay and different types of inverse time delay characteristics. The selectivity between different overcurrent protections is normally enabled by co-ordination between the function time delays of the different protections. To enable optimal co-ordination all overcurrent IEDs, to be co-ordinated against each other, should have the same time delay characteristic. Therefore a wide range of standardised inverse time characteristics are available: IEC and ANSI. It is also possible to programme a user defined inverse time characteristic.

Normally it is required that the phase overcurrent function shall reset as fast as possible when the current level gets lower than the operation level. In some cases some sort of time delayed reset is required. Therefore three different kinds of reset time characteristics can be used.

For some protection applications there can be a need to change the current pick-up level for some time. A typical case is when the protection will measure the current to a large motor. At the start up sequence of a motor the start current can be significantly larger then the rated current of the
motor. Therefore there is a possibility to give a setting of a multiplication factor to the current pick-up level. This multiplication factor is activated from a binary input signal to the function.

Power transformers can have a large inrush current, when being energized. This phenomenon is due to saturation of the transformer magnetic core during parts of the period. There is a risk that inrush current will reach levels above the pick-up current of the phase overcurrent protection. The inrush current has a large second harmonic content. This can be used to avoid unwanted operation of the protection. Therefore the Four step phase overcurrent protection (OC4PTOC, 5L/6T) function have a possibility of second harmonic restrain if the level of this harmonic current reaches a value above a set percentage of the fundamental current.

### 7.2.3 Setting guidelines

A typical starting time delay of 24ms is subtracted from the set trip time delay, so that the resulting trip time will take the internal IED start time into consideration.

The parameters for the four step phase overcurrent protection function (OC) are set via the local HMI or Protection and Control IED Manager (PCM 600).

Common base IED values for the primary current ($I_{Base}$), primary voltage ($V_{Base}$) and primary power ($S_{Base}$) are set in global base values for settings function GBASVAL.

$GlobalBaseSel$: Selects the global base value group used by the function to define $I_{Base}$, $V_{Base}$ and $S_{Base}$. Note that this function will only use $I_{Base}$ value.

The following settings can be done for the four step phase overcurrent protection.

**Operation**: Off/On

**$I_{Base}$**: Base current in primary A. This current is used as reference for current setting. It can be suitable to set this parameter to the rated primary current of the protected object.

**2ndHarmStab**: Operate level of 2$^{nd}$ harmonic current restrain set in % of the fundamental current. The setting range is 5-100% of $I_{Base}$ in steps of 1%. Default setting is 20%.

**HarmRestrainx**: Disabled/Enabled, enables blocking from harmonic restrain.

### 7.2.3.1 Settings for each step (x = 1-4)

**Characteristx**: Selection of time delay characteristic for step x. Definite time delay and different types of inverse time delay characteristics are available according to table 28.

<table>
<thead>
<tr>
<th>Curve name</th>
</tr>
</thead>
<tbody>
<tr>
<td>ANSI Extremely Inverse</td>
</tr>
<tr>
<td>ANSI Very Inverse</td>
</tr>
<tr>
<td>ANSI Normal Inverse</td>
</tr>
<tr>
<td>ANSI Moderately Inverse</td>
</tr>
<tr>
<td>ANSI/IEEE Definite time</td>
</tr>
</tbody>
</table>

Table 28: **Inverse time delay characteristics**

Table continues on next page
The different characteristics are described in the “Technical reference manual”.  

\( I_x \): Operation phase current level for step \( x \) given in \% of \( I_{\text{Base}} \).

\( t_x \): Definite time delay for step \( x \). Used if definite time characteristic is chosen. Setting range: 0.000-60.000 s in step of 0.001 s. Note that the value set is the time between activation of the start and the trip outputs.

\( k_x \): Time multiplier for the dependent (inverse) characteristic.

\( I_{\text{nxMult}} \): Multiplier for scaling of the current setting value. If a binary input signal (enableMultiplier) is activated the current operation level is increase by this setting constant. Setting range: 1.0-10.0

\( t_{\text{Min}} \): Minimum operation time for IEC inverse time characteristics. At high currents the inverse time characteristic might give a very short operation time. By setting this parameter the operation time of the step can never be shorter than the setting. Setting range: 0.000-60.000 s in step of 0.001 s.

\( \text{ResetTypeCrux} \): The reset of the delay timer can be made in different ways. By choosing setting the possibilities are according to table 29.

**Table 29: Reset possibilities**

<table>
<thead>
<tr>
<th>Curve name</th>
<th>Curve index no.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Instantaneous</td>
<td>1</td>
</tr>
<tr>
<td>IEC Reset (constant time)</td>
<td>2</td>
</tr>
<tr>
<td>ANSI Reset (inverse time)</td>
<td>3</td>
</tr>
</tbody>
</table>

The delay characteristics are described in the “Technical reference manual”. There are some restrictions regarding the choice of reset delay.

For the independent time delay characteristics the possible delay time settings are instantaneous (1) and IEC (2 = set constant time reset).
For ANSI inverse time delay characteristics all three types of reset time characteristics are available; instantaneous (1), IEC (2 = set constant time reset) and ANSI (3 = current dependent reset time).

For IEC inverse time delay characteristics the possible delay time settings are instantaneous (1) and IEC (2 = set constant time reset).

For the customer tailor made inverse time delay characteristics (type 17) all three types of reset time characteristics are available; instantaneous (1), IEC (2 = set constant time reset) and ANSI (3 = current dependent reset time). If the current dependent type is used then settings pr, tr and cr must be given.

\[ t[s] = \left( \frac{A}{i^{2}} + B \right) \cdot IxMult \]

\[ \text{Equation 30} \]

HarmRestrainx: Enable block of step x from the harmonic restrain function (2nd harmonic). This function should be used when there is a risk if power transformer inrush currents might cause unwanted trip. Can be set Off/On.

tPCrvx, tACrvx, tBCrvx, tCCrvx: Parameters for customer creation of inverse time characteristic curve (Curve type = 17). See equation 30 for the time characteristic equation.

7.2.3.2 Second harmonic restrain

If a power transformer is energized there is a risk that the transformer core will saturate during part of the period, resulting in an inrush transformer current. This will give a declining residual current in the network, as the inrush current is deviating between the phases. There is a risk that the phase overcurrent function will give an unwanted trip. The inrush current has a relatively large ratio of 2nd harmonic component. This component can be used to create a restrain signal to prevent this unwanted function.

The settings for the 2nd harmonic restrain are described below.

2ndHarmStab: The rate of second harmonic current content for activation of the 2nd harmonic restrain signal, to block chosen steps. The setting is given in % of the fundamental frequency residual current. The setting range is 5-100% in step of 1%. The default setting is 20%.

HarmRestrainx: This parameter can be set Off/On, disable or enable the 2nd harmonic restrain.

The four-step phase overcurrent protection can be used in different ways, depending on the application where the protection is used. A general description is given below.

The pick up current setting inverse time protection or the lowest current step constant inverse time protection must be given a current setting so that the highest possible load current does not
cause protection operation. Here consideration also has to be taken to the protection reset current, so that a short peak of overcurrent does not cause operation of the protection even when the overcurrent has ceased. This phenomenon is described in figure 92.

![Diagram of current protection](IEC05000203-en-2.vsd)

**Figure 92:** Pick up and reset current for an overcurrent protection

The lowest setting value can be written according to equation 31.

\[
I_{pu} \geq 1.2 \cdot \frac{I_{max}}{k}
\]

(Equation 31)

where:
1.2 is a safety factor,
k is the resetting ratio of the protection, and
\(I_{max}\) is the maximum load current.

The maximum load current on the line has to be estimated. From operation statistics the load current up to the present situation can be found. The current setting must be valid also for some years ahead. It is, in most cases, realistic that the setting values are updated not more often than once every five years. In many cases this time interval is still longer. Investigate the maximum load current that different equipment on the line can withstand. Study components such as line conductors, current transformers, circuit breakers, and disconnectors. The manufacturer of the equipment normally gives the maximum thermal load current of the equipment.
There is also a demand that all faults, within the zone that the protection shall cover, must be detected by the phase overcurrent protection. The minimum fault current $I_{scmin}$ to be detected by the protection, must be calculated. Taking this value as a base, the highest pick up current setting can be written according to equation 32.

$$I_{pu} \leq 0.7 \cdot I_{scmin}$$  
(Equation 32)

where:
- 0.7 is a safety factor and
- $I_{scmin}$ is the smallest fault current to be detected by the overcurrent protection.

As a summary the pick up current shall be chosen within the interval stated in equation 33.

$$1.2 \cdot \frac{I_{max}}{k} \leq I_{pu} \leq 0.7 \cdot I_{scmin}$$  
(Equation 33)

The high current function of the overcurrent protection, which only has a short delay of the operation, must be given a current setting so that the protection is selective to other protection in the power system. It is desirable to have a rapid tripping of faults within as large portion as possible of the part of the power system to be protected by the protection (primary protected zone). A fault current calculation gives the largest current of faults, $I_{scmax}$, at the most remote part of the primary protected zone. Considerations have to be made to the risk of transient overreach, due to a possible DC component of the short circuit current. The lowest current setting of the most rapid stage, of the phase overcurrent protection, can be written according to

$$I_{high} \geq 1.2 \cdot k_t \cdot I_{scmax}$$  
(Equation 34)

where:
- 1.2 is a safety factor,
- $k_t$ is a factor that takes care of the transient overreach due to the DC component of the fault current and can be considered to be less than 1.1
- $I_{scmax}$ is the largest fault current at a fault at the most remote point of the primary protection zone.

The operate times of the phase overcurrent protection has to be chosen so that the fault time is so short so that equipment will not be destroyed due to thermal overload, at the same time as selectivity is assured. For overcurrent protection, in a radial fed network, the time setting can be chosen in a graphical way. This is mostly used in the case of inverse time overcurrent protection. In the figure below is shown how the time-versus-current curves are plotted in a diagram. The time setting is chosen to get the shortest fault time with maintained selectivity. Selectivity is assured if the time difference between the curves is larger than a critical time difference.
The operation time can be set individually for each overcurrent protection. To assure selectivity between different protective protections, in the radial network, there have to be a minimum time difference $\Delta t$ between the time delays of two protections. The minimum time difference can be determined for different cases. To determine the shortest possible time difference between we must have knowledge about operation time of protections, breaker opening time and protection resetting time. These time delays can vary significantly between different pieces of equipment. The following time delays can be estimated:

- Protection operation time: 15-60 ms
- Protection resetting time: 15-60 ms
- Breaker opening time: 20-120 ms

**Example**

Assume two substations A and B directly connected to each other via one line, as shown in the figure below. We study a fault located at another line from the station B. The fault current to the overcurrent protection of IED B1 has a magnitude so that the protection will have instantaneous function. The overcurrent protection of IED A1 must have a delayed function. The sequence of events during the fault can be described using a time axis, see figure 94.
Figure 94: Sequence of events during fault

where:
- $t=0$ is the fault occurs,
- $t=t_1$ is the trip signal from the overcurrent protection at IED B1 is sent. Operation time of this protection is $t_1$,
- $t=t_2$ is the circuit breaker at IED B1 opens. The circuit breaker opening time is $t_2 - t_1$ and
- $t=t_3$ is the overcurrent protection at IED A1 resets. The protection resetting time is $t_3 - t_2$.

To ensure that the overcurrent protection at IED A1, is selective to the overcurrent protection at IED B1, the minimum time difference must be larger than the time $t_3$. There are uncertainties in the values of protection operation time, breaker opening time and protection resetting time. Therefore a safety margin has to be included. With normal values the needed time difference can be calculated according to equation 35.

$$\Delta t \geq 40\, ms + 100\, ms + 40\, ms + 40\, ms = 220\, ms$$

(Equation 35)

where it is considered that:
- the operation time of overcurrent protection B1 is 40 ms
- the breaker open time is 100 ms
- the resetting time of protection A1 is 40 ms and
- the additional margin is 40 ms
7.3 Directional residual overcurrent protection, four steps EF4PTOC (51N/67N)

7.3.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Directional residual overcurrent protection, four steps</td>
<td>EF4PTOC</td>
<td></td>
<td>51N_67N</td>
</tr>
</tbody>
</table>

7.3.2 Setting guidelines

When inverse time overcurrent characteristic is selected, the trip time of the stage will be the sum of the inverse time delay and the set definite time delay. Thus, if only the inverse time delay is required, it is important to set the definite time delay for that stage to zero.

The parameters for the four step residual overcurrent protection are set via the local HMI or PCM600. The following settings can be done for the function.

Common base IED values for the primary current (IBase), primary voltage (VBase) and primary power (SBase) are set in global base values for settings function GBASVAL.

GlobalBaseSel: Selects the global base value group used by the function to define IBase, VBase and SBase. Note that this function will only use IBase value.

SeqTypeUPol: This is used to select the type of voltage polarising quantity i.e. Zero seq or Neg seq for direction detection.

SeqTypeIPol: This is used to select the type of current polarising quantity i.e. Zero seq or Neg seq for direction detection.

SeqTypeIDir: This is used to select the type of operating current quantity i.e. Zero seq or Neg seq for direction detection.

7.3.2.1 Common settings for all steps

AngleRCA: Relay characteristic angle given in degree. This angle is defined as shown in Figure 95. The angle is defined positive when the residual current lags the reference voltage (Vpol = 3V₀ or V₂)
**Figure 95:** Relay characteristic angle given in degree

In a normal transmission network a normal value of RCA is about 65°. The setting range is -180° to +180°.

**polMethod:** Defines if the directional polarization is from

- **Voltage** (3V₀ or V₂)
- **Current** (3I₀ · ZNpol or 3I₂ · ZNpol where ZNpol is RNpol + jXNpol), or
- both currents and voltage, **Dual** (dual polarizing, (3V₀ + 3I₀ · ZNpol) or (V₂ + I₂ · ZNpol)).

Normally voltage polarizing from the internally calculated residual sum or an external open delta is used.

Current polarizing is useful when the local source is strong and a high sensitivity is required. In such cases the polarizing voltage (3V₀) can be below 1% and it is then necessary to use current polarizing or dual polarizing. Multiply the required set current (primary) with the minimum impedance (ZNpol) and check that the percentage of the phase-to-ground voltage is definitely higher than 1% (minimum 3V₀ > VPolMin setting) as a verification.

**RNPol, XNPol:** The zero-sequence source is set in primary ohms as base for the current polarizing. The polarizing voltage is then achieved as 3I₀ · ZNpol. The ZNpol can be defined as (ZS₁-ZS₀)/3, that is the ground return impedance of the source behind the protection. The maximum ground-fault current at the local source can be used to calculate the value of ZN as V/(√3 · 3I₀) Typically, the minimum ZNPol (3 · zero sequence source) is set. The setting is in primary ohms.

When the dual polarizing method is used, it is important that the setting **Pickupx** or the product 3I₀ · ZNpol is not greater than 3V₀. If so, there is a risk for incorrect operation for faults in the reverse direction.

**IPolMin:** is the minimum ground-fault current accepted for directional evaluation. For smaller currents than this value, the operation will be blocked. A typical setting is 5-10% of IB.
**VPolMin**: Minimum polarization (reference) polarizing voltage for the directional function, given in % of VBase/√3.

**IDirPU**: Operate residual current release level in % of IB for directional comparison scheme. The setting is given in % of IB and must be set below the lowest INx> setting, set for the directional measurement. The output signals, PUFW and PUREV can be used in a teleprotection scheme. The appropriate signal should be configured to the communication scheme block.

### 7.3.2.2 2nd harmonic restrain

If a power transformer is energized there is a risk that the current transformer core will saturate during part of the period, resulting in a transformer inrush current. This will give a declining residual current in the network, as the inrush current is deviating between the phases. There is a risk that the residual overcurrent function will give an unwanted trip. The inrush current has a relatively large ratio of 2nd harmonic component. This component can be used to create a restrain signal to prevent this unwanted function.

At current transformer saturation a false residual current can be measured by the protection. Here the 2nd harmonic restrain can prevent unwanted operation as well.

**2ndHarmStab**: The rate of 2nd harmonic current content for activation of the 2nd harmonic restrain signal. The setting is given in % of the fundamental frequency residual current.

### 7.3.2.3 Parallel transformer inrush current logic

In case of parallel transformers there is a risk of sympathetic inrush current. If one of the transformers is in operation, and the parallel transformer is switched in, the asymmetric inrush current of the switched-in transformer will cause partial saturation of the transformer already in service. This is called transferred saturation. The 2nd harmonic of the inrush currents of the two transformers will be in phase opposition. The summation of the two currents will thus give a small 2nd harmonic current. The residual fundamental current will however be significant. The inrush current of the transformer in service before the parallel transformer energizing, will be a little delayed compared to the first transformer. Therefore, we will have high 2nd harmonic current initially. After a short period this current will however be small and the normal 2nd harmonic blocking will reset.

![Figure 96: Application for parallel transformer inrush current logic](en05000136_ansi.vsd)

If the BlkParTransf function is activated, the 2nd harmonic restraint signal will latch as long as the residual current measured by the relay is larger than a selected step current level. Assume that step 4 is chosen to be the most sensitive step of the four step residual overcurrent protection
function EF4PTOC (51N_67N). The harmonic restrain blocking is enabled for this step. Also the same current setting as this step is chosen for the blocking at parallel transformer energizing.

The settings for the parallel transformer logic are described below.

**BlkParTransf:** This is used to *Enable* blocking at energising of parallel transformers.

**Use_PUValue:** Gives which current level should be used for the activation of the blocking signal. This is given as one of the settings of the steps: Step 1/2/3/4. Normally, the step having the lowest operation current level should be set.

### 7.3.2.4 Switch onto fault logic

In case of energizing a faulty object there is a risk of having a long fault clearance time, if the fault current is too small to give fast operation of the protection. The switch on to fault function can be activated from auxiliary signals from the circuit breaker, either the close command or the open/close position (change of position).

This logic can be used to issue a fast trip if one breaker pole does not close properly at a manual or automatic closing.

SOTF and under time are similar functions to achieve fast clearance at asymmetrical closing based on requirements from different utilities.

The function is divided into two parts. The SOTF function will give operation from step 2 or 3 during a set time after change in the position of the circuit breaker. The SOTF function has a set time delay. The under time function, which has 2nd harmonic restrain blocking, will give operation from step 4. The 2nd harmonic restrain will prevent unwanted function in case of transformer inrush current. The under time function has a set time delay.

Below the settings for switch on to fault logics are described.

**SOTF:** This parameter can be set: *Disabled/SOTF/Under Time/SOTF+Under Time.*

**SOTFSel:** This setting will select the signal to activate SOTF function; *CB position open/CB position closed/CB close command.*

**StepForSOTF:** If this parameter is set on step 3, the step 3 pickup signal will be used as current set level. If set on step 2, the step 2 pickup signal will be used as current set level.

**HarmBlkSOTF:** This is used to *On/Off* harmonic restrain during SOTF conditions.

**tSOTF:** Time delay for operation of the SOTF function. The setting range is 0.000 - 60.000 s in step of 0.001 s. The default setting is 0.100 s

**t4U:** Time interval when the SOTF function is active after breaker closing. The setting range is 0.000 - 60.000 s in step of 0.001 s. The default setting is 1.000 s.

**ActUndrTimeSel:** Describes the mode to activate the sensitive undertime function. The function can be activated by Circuit breaker position (change) or Circuit breaker command.

**tUnderTime:** Time delay for operation of the sensitive undertime function. The setting range is 0.000 - 60.000 s in step of 0.001 s. The default setting is 0.300 s
### Settings for each step (x = 1, 2, 3 and 4)

*Operation:* Sets the protection to *Enabled* or *Disabled*.

*DirModeSelx:* The directional mode of step x. Possible settings are *Disabled/ Non-directional/ Forward/ Reverse*.

*Characteristx:* Selection of time characteristic for step x. Definite time delay and different types of inverse time characteristics are available.

Inverse time characteristic enables fast fault clearance of high current faults at the same time as selectivity to other inverse time phase overcurrent protections can be assured. This is mainly used in radial fed networks but can also be used in meshed networks. In meshed networks, the settings must be based on network fault calculations.

To assure selectivity between different protections, in the radial network, there has to be a minimum time difference $\Delta t$ between the time delays of two protections. To determine the shortest possible time difference, the operation time of protections, breaker opening time and protection resetting time must be known. These time delays can vary significantly between different protective equipment. The following time delays can be estimated:

<table>
<thead>
<tr>
<th>Time Delay</th>
<th>Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>Protection trip time</td>
<td>15-60 ms</td>
</tr>
<tr>
<td>Protection resetting time</td>
<td>15-60 ms</td>
</tr>
<tr>
<td>Breaker opening time</td>
<td>20-120 ms</td>
</tr>
</tbody>
</table>

The different characteristics are described in the technical reference manual.

*tx:* Definite time delay for step x. The definite time $tx$ is added to the inverse time when inverse time characteristic is selected. Note that the value set is the time between activation of the start and the trip outputs.

*Pickup"x":* Operate residual current level for step x given in % of $IB$.

$INx>Max$ and $INx>Min$ should only be changed if remote setting of operation current level, $INx>$, is used. The limits are used for decreasing the used range of the $INx>$ setting. If $INx>$ is set outside $INx>Max$ and $INx>Min$, the closest of the limits to $INx>$ is used by the function. If $INx>Max$ is smaller than $INx>Min$, the limits are swapped.

*kx:* Time multiplier for the dependent (inverse) characteristic for step x.

*IMinx:* Minimum pickup current for step x in % of $IB$. Set $IMinx$ below $Pickupx$ for every step to achieve ANSI reset characteristic according to standard. If $IMinx$ is set above for any step, signal will reset at current equals to zero.

*txMin:* Minimum operating time for inverse time characteristics. At high currents, the inverse time characteristic might give a very short operation time. By setting this parameter, the operation time of the step can never be shorter than the setting.
In order to fully comply with the curves definition, the setting parameter $txMin$ shall be set to the value which is equal to the operate time of the selected IEC inverse curve for measured current of twenty times the set current pickup value. Note that the operate time value is dependent on the selected setting value for time multiplier $kx$.

$INxMult$: Multiplier for scaling of the current setting value. If a binary input signal (MULTPUx) is activated, the current operation level is increased by this setting constant.

$ResetTypeCrvx$: The reset of the delay timer can be made in different ways. The possibilities are described in the technical reference manual.

$tResetx$: Constant reset time delay in s for step $x$.

$HarmBlockx$: This is used to enable block of step $x$ from $2^{nd}$ harmonic restrain function.

$tPCrvx$, $tACr vx$, $tBCr vx$, $tCCr vx$: Parameters for user programmable of inverse time characteristic curve. The time characteristic equation is according to equation 36:

$$
\begin{align*}
t[s] &= \left(\frac{A}{\frac{i}{ipickup}^p} + B\right) \cdot TD \\
\end{align*}
$$

(Equation 36)

Further description can be found in the technical reference manual.

$tPRCr vx$, $tTRCr vx$, $tCRCr vx$: Parameters for user programmable of inverse reset time characteristic curve. Further description can be found in the technical reference manual.
7.4 Four step directional negative phase sequence overcurrent protection NS4PTOC (46I2)

7.4.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Four step negative sequence overcurrent protection</td>
<td>NS4PTOC</td>
<td></td>
<td>46I2</td>
</tr>
</tbody>
</table>

7.4.2 Application

Four step negative sequence overcurrent protection NS4PTOC (46I2) is used in several applications in the power system. Some applications are:

- Ground-fault and phase-phase short circuit protection of feeders in effectively grounded distribution and subtransmission systems. Normally these feeders have radial structure.
- Back-up ground-fault and phase-phase short circuit protection of transmission lines.
- Sensitive ground-fault protection of transmission lines. NS4PTOC (46I2) can have better sensitivity to detect resistive phase-to-ground-faults compared to distance protection.
- Back-up ground-fault and phase-phase short circuit protection of power transformers.
- Ground-fault and phase-phase short circuit protection of different kinds of equipment connected to the power system such as shunt capacitor banks, shunt reactors and others.

In many applications several steps with different current pickup levels and time delays are needed. NS4PTOC (46I2) can have up to four, individual settable steps. The flexibility of each step of NS4PTOC (46I2) function is great. The following options are possible:

Non-directional/Directional function: In some applications the non-directional functionality is used. This is mostly the case when no fault current can be fed from the protected object itself. In order to achieve both selectivity and fast fault clearance, the directional function can be necessary. This can be the case for unsymmetrical fault protection in meshed and effectively grounded transmission systems. The directional negative sequence overcurrent protection is also well suited to operate in teleprotection communication schemes, which enables fast clearance of unsymmetrical faults on transmission lines. The directional function uses the voltage polarizing quantity.

Choice of time characteristics: There are several types of time characteristics available such as definite time delay and different types of inverse time characteristics. The selectivity between different overcurrent protections is normally enabled by co-ordination between the operating time of the different protections. To enable optimal co-ordination all overcurrent relays, to be co-ordinated against each other, should have the same time characteristic. Therefore a wide range of standardized inverse time characteristics are available: IEC and ANSI.
Table 30: Inverse time characteristics

<table>
<thead>
<tr>
<th>Curve name</th>
</tr>
</thead>
<tbody>
<tr>
<td>ANSI Extremely Inverse</td>
</tr>
<tr>
<td>ANSI Very Inverse</td>
</tr>
<tr>
<td>ANSI Normal Inverse</td>
</tr>
<tr>
<td>ANSI Moderately Inverse</td>
</tr>
<tr>
<td>ANSI/IEEE Definite time</td>
</tr>
<tr>
<td>ANSI Long Time Extremely Inverse</td>
</tr>
<tr>
<td>ANSI Long Time Very Inverse</td>
</tr>
<tr>
<td>ANSI Long Time Inverse</td>
</tr>
<tr>
<td>IEC Normal Inverse</td>
</tr>
<tr>
<td>IEC Very Inverse</td>
</tr>
<tr>
<td>IEC Inverse</td>
</tr>
<tr>
<td>IEC Extremely Inverse</td>
</tr>
<tr>
<td>IEC Short Time Inverse</td>
</tr>
<tr>
<td>IEC Long Time Inverse</td>
</tr>
<tr>
<td>IEC Definite Time</td>
</tr>
<tr>
<td>User Programmable</td>
</tr>
<tr>
<td>ASEA RI</td>
</tr>
<tr>
<td>RXIDG (logarithmic)</td>
</tr>
</tbody>
</table>

There is also a user programmable inverse time characteristic.

Normally it is required that the negative sequence overcurrent function shall reset as fast as possible when the current level gets lower than the operation level. In some cases some sort of delayed reset is required. Therefore different kinds of reset characteristics can be used.

For some protection applications there can be a need to change the current pickup level for some time. Therefore there is a possibility to give a setting of a multiplication factor $MultPUx$ to the negative sequence current pick-up level. This multiplication factor is activated from a binary input signal $MULTPUx$ to the function.

### 7.4.3 Setting guidelines

The parameters for Four step negative sequence overcurrent protection NS4PTOC (46I2) are set via the local HMI or Protection and Control Manager (PCM600).

The following settings can be done for the four step negative sequence overcurrent protection:

*Operation*: Sets the protection to *Enabled* or *Disabled*.

Common base IED values for the primary current ($I_{Base}$), primary voltage ($V_{Base}$) and primary power ($S_{Base}$) are set in global base values for settings function $GBASVAL$.

*GlobalBaseSel*: Selects the global base value group used by the function to define $I_{Base}$, $V_{Base}$ and $S_{Base}$. Note that this function will only use $I_{Base}$ value.
When inverse time overcurrent characteristic is selected, the trip time of the stage will be the sum of the inverse time delay and the set definite time delay. Thus, if only the inverse time delay is required, it is important to set the definite time delay for that stage to zero.

### 7.4.3.1 Settings for each step

- **x** means step 1, 2, 3 and 4.

- **DirModeSel**: The directional mode of step x. Possible settings are off/nondirectional/forward/reverse.

- **Characteris**: Selection of time characteristic for step x. Definite time delay and different types of inverse time characteristics are available.

#### Table 31: Inverse time characteristics

<table>
<thead>
<tr>
<th>Curve name</th>
</tr>
</thead>
<tbody>
<tr>
<td>ANSI Extremely Inverse</td>
</tr>
<tr>
<td>ANSI Very Inverse</td>
</tr>
<tr>
<td>ANSI Normal Inverse</td>
</tr>
<tr>
<td>ANSI Moderately Inverse</td>
</tr>
<tr>
<td>ANSI/IEEE Definite time</td>
</tr>
<tr>
<td>ANSI Long Time Extremely Inverse</td>
</tr>
<tr>
<td>ANSI Long Time Very Inverse</td>
</tr>
<tr>
<td>ANSI Long Time Inverse</td>
</tr>
<tr>
<td>IEC Normal Inverse</td>
</tr>
<tr>
<td>IEC Very Inverse</td>
</tr>
<tr>
<td>IEC Inverse</td>
</tr>
<tr>
<td>IEC Extremely Inverse</td>
</tr>
<tr>
<td>IEC Short Time Inverse</td>
</tr>
<tr>
<td>IEC Long Time Inverse</td>
</tr>
<tr>
<td>IEC Definite Time</td>
</tr>
<tr>
<td>User Programmable</td>
</tr>
<tr>
<td>ASEA RI</td>
</tr>
<tr>
<td>RXIDG (logarithmic)</td>
</tr>
</tbody>
</table>

The different characteristics are described in the Technical Reference Manual (TRM).

- **Pickup**: Operation negative sequence current level for step x given in % of IBase.
tx: Definite time delay for step x. The definite time tx is added to the inverse time when inverse time characteristic is selected. Note that the value set is the time between activation of the start and the trip outputs.

TDx: Time multiplier for the dependent (inverse) characteristic.

IMinx: Minimum pickup current for step x in % of IBase. Set IMinx below Pickupx for every step to achieve ANSI reset characteristic according to standard. If IMinx is set above Pickupx for any step the ANSI reset works as if current is zero when current drops below IMinx.

MultPUx: Multiplier for scaling of the current setting value. If a binary input signal (ENMULTx) is activated the current operation level is multiplied by this setting constant.

txMin: Minimum operation time for inverse time characteristics. At high currents the inverse time characteristic might give a very short operation time. By setting this parameter the operation time of the step can never be shorter than the setting.

Figure 98: Minimum operate current and operation time for inverse time characteristics

ResetTypeCrvx: The reset of the delay timer can be made in different ways. By choosing setting there are the following possibilities:

<table>
<thead>
<tr>
<th>Curve name</th>
</tr>
</thead>
<tbody>
<tr>
<td>Instantaneous</td>
</tr>
<tr>
<td>IEC Reset (constant time)</td>
</tr>
<tr>
<td>ANSI Reset (inverse time)</td>
</tr>
</tbody>
</table>

The different reset characteristics are described in the Technical Reference Manual (TRM). There are some restrictions regarding the choice of reset delay.

For the independent time delay characteristics the possible delay time settings are instantaneous (1) and IEC (2 = set constant time reset).
For ANSI inverse time delay characteristics all three types of reset time characteristics are available; instantaneous (1), IEC (2 = set constant time reset) and ANSI (3 = current dependent reset time).

For IEC inverse time delay characteristics the possible delay time settings are instantaneous (1) and IEC (2 = set constant time reset).

For the programmable inverse time delay characteristics all three types of reset time characteristics are available; instantaneous (1), IEC (2 = set constant time reset) and ANSI (3 = current dependent reset time). If the current dependent type is used settings \( pr \), \( tr \) and \( cr \) must be given.

\( t_{PCrvx}, t_{ACrvx}, t_{BCrvx}, t_{CCrvx} \): Parameters for programmable inverse time characteristic curve. The time characteristic equation is according to equation 37:

\[
t[s] = \frac{A}{\left(\frac{i}{ipickup}\right)^p - C} + B \cdot TD
\]

(Equation 37)

Further description can be found in the Technical reference manual (TRM).

\( t_{PRCrvx}, t_{TRCrvx}, t_{CRCrvx} \): Parameters for customer creation of inverse reset time characteristic curve. Further description can be found in the Technical Reference Manual.

### 7.4.3.2 Common settings for all steps

\( x \) means step 1, 2, 3 and 4.

**AngleRCA**: Relay characteristic angle given in degrees. This angle is defined as shown in figure 99. The angle is defined positive when the residual current lags the reference voltage (\( V_{pol} = - \))
Figure 99: Relay characteristic angle given in degree

In a transmission network a normal value of RCA is about 80°.

\[ V_{PolMin} \]: Minimum polarization (reference) voltage % of \( V_{Base} \).

\( I_{Dir} \): Operate residual current level for directional comparison scheme. The setting is given in % of \( I_{Base} \). The pickup forward or pickup reverse signals can be used in a communication scheme. The appropriate signal must be configured to the communication scheme block.

### 7.5 Thermal overload protection, two time constants

**TRPTTR (49)**

#### 7.5.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Thermal overload protection, two time constants</td>
<td>TRPTTR</td>
<td>49</td>
<td></td>
</tr>
</tbody>
</table>
7.5.2 Application

Transformers in the power system are designed for a certain maximum load current (power) level. If the current exceeds this level the losses will be higher than expected. As a consequence the temperature of the transformer will increase. If the temperature of the transformer reaches too high a value, the equipment might be damaged;

- The insulation within the transformer experiences forced ageing. As a consequence of this, the risk of internal phase-to-phase or phase-to-ground faults increases.
- There might be hot spots within the transformer, which degrades the paper insulation. It might also cause bubbling in the transformer oil.

In stressed situations in the power system it can be required to overload transformers for a limited time. This should be done without the above mentioned risks. The thermal overload protection provides information and makes temporary overloading of transformers possible.

The permissible load level of a power transformer is highly dependent on the cooling system of the transformer. There are two main principles:

- OA: The air is naturally circulated to the coolers without fans and the oil is naturally circulated without pumps.
- FOA: The coolers have fans to force air for cooling and pumps to force the circulation of the transformer oil.

The protection can have two sets of parameters, one for non-forced cooling and one for forced cooling. Both the permissive steady state loading level as well as the thermal time constant is influenced by the cooling system of the transformer. The two parameters sets can be activated by the binary input signal COOLING. This can be used for transformers where forced cooling can be taken out of operation, for example at fan or pump faults.

The thermal overload protection estimates the internal heat content of the transformer (temperature) continuously. This estimation is made by using a thermal model of the transformer which is based on current measurement.

If the heat content of the protected transformer reaches a set alarm level a signal can be given to the operator. Two alarm levels are available. This enables preventive actions in the power system to be taken before dangerous temperatures are reached. If the temperature continues to increase to the trip value, the protection initiates a trip of the protected transformer.

After tripping by the thermal overload protection, the transformer will cool down over time. There will be a time gap before the heat content (temperature) reaches such a level so that the transformer can be taken into service again. Therefore, the function will continue to estimate the heat content using a set cooling time constant. Energizing of the transformer can be blocked until the heat content has reached a set level.

7.5.3 Setting guideline

The parameters for the thermal overload protection, two time constants (TRPTTR, 49) are set via the local HMI or Protection and Control IED Manager (PCM600).

The following settings can be done for the thermal overload protection:

*Operation: Disabled/ Enabled*
**Operation**: Sets the mode of operation. *Disabled* switches off the complete function.

**GlobalBaseSel**: Selects the global base value group used by the function to define `IBase`, `VBase` and `SBase`. Note that this function will only use `IBase` value.

**IRef**: Reference level of the current given in %. When the current is equal to `IRef` the final (steady state) heat content is equal to 1. It is suggested to give a setting corresponding to the rated current of the transformer winding.

**IRefMult**: If a binary input `ENMULT` is activated the reference current value can be multiplied by the factor `IRefMult`. The activation could be used in case of deviating ambient temperature from the reference value. In the standard for loading of a transformer an ambient temperature of 20°C is used. For lower ambient temperatures the load ability is increased and vice versa. `IRefMult` can be set within a range: 0.01 - 10.00.

**IBase1**: Base current for setting given as percentage of `IBase`. This setting shall be related to the status with no `COOLING` input. It is suggested to give a setting corresponding to the rated current of the transformer with natural cooling (OA).

**IBase2**: Base current for setting given as percentage of `IBase`. This setting shall be related to the status with activated `COOLING` input. It is suggested to give a setting corresponding to the rated current of the transformer with forced cooling (FOA). If the transformer has no forced cooling `IBase2` can be set equal to `IBase1`.

**Tau1**: The thermal time constant of the protected transformer, related to `IBase1` (no cooling) given in minutes.

**Tau2**: The thermal time constant of the protected transformer, related to `IBase2` (with cooling) given in minutes.

The thermal time constant should be obtained from the transformer manufacturers manuals. The thermal time constant is dependent on the cooling and the amount of oil. Normal time constants for medium and large transformers (according to IEC 60076-7) are about 2.5 hours for naturally cooled transformers and 1.5 hours for forced cooled transformers.

The time constant can be estimated from measurements of the oil temperature during a cooling sequence (described in IEC 60076-7). It is assumed that the transformer is operated at a certain load level with a constant oil temperature (steady state operation). The oil temperature above the ambient temperature is `ΔΘo0`. Then the transformer is disconnected from the grid (no load). After a time `t` of at least 30 minutes the temperature of the oil is measured again. Now the oil temperature above the ambient temperature is `ΔΘot`. The thermal time constant can now be estimated as:

\[
\tau = \frac{t}{\ln \Delta \Theta_{o0} - \ln \Delta \Theta_{ot}}
\]  

(Equation 38)

If the transformer has forced cooling (FOA) the measurement should be made both with and without the forced cooling in operation, giving `Tau2` and `Tau1`.

The time constants can be changed if the current is higher than a set value or lower than a set value. If the current is high it is assumed that the forced cooling is activated while it is deactivated at low current. The setting of the parameters below enables automatic adjustment of the time constant.
**Tau1High**: Multiplication factor to adjust the time constant $\tau_1$ if the current is higher than the set value $I_{HighTau1}$. $I_{HighTau1}$ is set in % of $I_{Base1}$.

**Tau1Low**: Multiplication factor to adjust the time constant $\tau_1$ if the current is lower than the set value $I_{LowTau1}$. $I_{LowTau1}$ is set in % of $I_{Base1}$.

**Tau2High**: Multiplication factor to adjust the time constant $\tau_2$ if the current is higher than the set value $I_{HighTau2}$. $I_{HighTau2}$ is set in % of $I_{Base2}$.

**Tau2Low**: Multiplication factor to adjust the time constant $\tau_2$ if the current is lower than the set value $I_{LowTau2}$. $I_{LowTau2}$ is set in % of $I_{Base2}$.

The possibility to change time constant with the current value as the base can be useful in different applications. Below some examples are given:

- In case a total interruption (low current) of the protected transformer all cooling possibilities will be inactive. This can result in a changed value of the time constant.
- If other components (motors) are included in the thermal protection, there is a risk of overheating of that equipment in case of very high current. The thermal time constant is often smaller for a motor than for the transformer.

**ITrip**: The steady state current that the transformer can withstand. The setting is given in % of $I_{Base1}$ or $I_{Base2}$.

**Alarm1**: Heat content level for activation of the signal ALARM1. ALARM1 is set in % of the trip heat content level.

**Alarm2**: Heat content level for activation of the output signal ALARM2. ALARM2 is set in % of the trip heat content level.

**LockoutReset**: Lockout release level of heat content to release the lockout signal. When the thermal overload protection trips a lock-out signal is activated. This signal is intended to block switching on of the protected circuit transformer as long as the transformer temperature is high. The signal is released when the estimated heat content is below the set value. This temperature value should be chosen below the alarm temperature. **LockoutReset** is set in % of the trip heat content level.

**ThetaInit**: Heat content before activation of the function. This setting can be set a little below the alarm level. If the transformer is loaded before the activation of the protection function, its temperature can be higher than the ambient temperature. The start point given in the setting will prevent risk of no trip at overtemperature during the first moments after activation. **ThetaInit** is set in % of the trip heat content level.

**Warning**: If the calculated time to trip factor is below the setting **Warning** a warning signal is activated. The setting is given in minutes.

### 7.5.3.1 Setting example

Calculation of the operate time with the available current is performed only if the calculated final temperature is greater than the operate level temperature.
where:

\[ t_{\text{operate}} \]

is the time to operate

\[ \tau \]

is the time constant

\[ \theta_{\text{final}} = \left( \frac{l}{I_{\text{ref}}} \right)^2 \]

is the steady state heat content

\[ l \]

is the largest phase load current

\[ I_{\text{ref}} \]

is the given reference load current

\[ \theta_{\text{operate}} = \left( I_{\text{bx}} \times I_{\text{ref}} \times I_{\text{trip}} \right)^2 \]

is the operate level heat content

\[ I_{\text{bx}} \]

is the selected base current based cooling system ON/OFF

\[ I_{\text{trip}} \]

is the operate level load current

\[ \theta_n \]

is the current heat content

Consider that the given system has \( I_{\text{Base}} \) of 1000 A and the cooling system is ON. The following settings are used to calculate the operate time:

\[ I_{\text{Ref}} \]

110%

\[ I_{\text{Base}1} \]

110% of IB

\[ I_{\text{Base}2} \]

120% of IB

\[ \tau_{1} \]

150 min

\[ \tau_{2} \]

90 min

\[ I_{\text{High}1} \]

110% of IB1

\[ \tau_{\text{High}1} \]

125% of tC1

\[ I_{\text{Low}1} \]

90% of IB1

\[ \tau_{\text{Low}1} \]

75% of tC1

\[ I_{\text{High}2} \]

110% of IB2

\[ \tau_{\text{High}2} \]

115% of tC2

\[ I_{\text{Low}2} \]

90% of IB2

\[ \tau_{\text{Low}2} \]

85% of tC2

\[ I_{\text{Trip}} \]

120% of IBx

\[ \theta_{\text{Init}} \]

50%

\[ \text{ResLo} \]

60% of Itr

As the cooling system is ON, \( I_{\text{Base}2} \) is selected as the base current and \( \tau_{2} \) setting is selected as the time constant.

For example, the largest phase load current is taken as 1800 A, then:
\[ \theta_{\text{final}} = \left( \frac{1800}{1.1} \right)^2 = 2677685.95 \]

\[ \theta_{\text{operate}} = (1.2 \times 1000 \times 1.1 \times 1.2)^2 = 2509056 \]

Here \[ \Theta_{\text{final}} > \Theta_n \]

At \( t=0 \)

\[ \theta_n = \theta_{\text{init}} = \text{ThetaInit} \times \theta_{\text{operate}} = 0.5 \times 2509056 = 1254528 \]

At next execution, \( \theta_{n-1}=1254528 \) and \( \theta_n=1254555.04 \).

Therefore, \( t_{\text{operate}} = -90 \times \ln((2677685.95 - 2509056) / (2677685.95 - 1254555.04)) = 192 \text{ min} \)

After the trip, a lockout is released to inhibit reconnecting the tripped circuit. The output lockout signal LOCKOUT is activated when the temperature of the object is greater than the set lockout release temperature setting \( \text{ResLo} \).

\[ t_{\text{lockout release}} = -\tau \cdot \ln \left( \frac{\Theta_{\text{final}} - \Theta_{\text{lockout release}}}{\Theta_{\text{final}} - \Theta_n} \right) \]

(Equation 40)

where:

- \( t_{\text{lockout release}} \) is the time to lockout release
- \( \Theta_{\text{lockout release}} \) is the lockout release level heat content = \( \text{ResLo} \times \theta_{\text{operate}} \)

Consider that the current heat content \( \theta_n \) is 2700000 and the cooling system is still \( \text{ON} \), then:

\[ \theta_{\text{lockout release}} = 0.6 \times 2509056 = 1505433.6 \]

\[ t_{\text{lockout release}} = -90 \times \ln((2677685.95 - 1505433.6) / (2677685.95 - 2700000)) = 244 \text{ min} \]

7.6 Breaker failure protection CCRBRF(50BF)
### 7.6.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Breaker failure protection, 3-phase activation and output</td>
<td>CCRBRF</td>
<td></td>
<td>50BF</td>
</tr>
</tbody>
</table>

### 7.6.2 Application

In the design of the fault clearance system the N-1 criterion is often used. This means that a fault needs to be cleared even if any component in the fault clearance system is faulty. One necessary component in the fault clearance system is the circuit breaker.

It is from practical and economical reason not feasible to duplicate the circuit breaker for the protected object. Instead a breaker failure protection is used.

Breaker failure protection CCRBRF (50BF) will issue a backup trip command to adjacent circuit breakers in case of failure to trip of the “normal” circuit breaker for the protected object. The detection of failure to break the current through the breaker is made either by means of current measurement or as detection of closed status using auxiliary contact.

CCRBFR (50BF) can also give a retrip command. This means that a second trip signal is sent to the protected object circuit breaker. The retrip function can be used to increase the probability of operation of the breaker, or it can be used to avoid backup trip of many breakers in case of mistakes during relay maintenance and testing.

### 7.6.3 Setting guidelines

The parameters for Breaker failure protection CCRBRF (50BF) are set via the local HMI or PCM600.

The following settings can be done for the breaker failure protection.

- **GlobalBaseSel**: Selects the global base value group used by the function to define $I_{\text{Base}}, V_{\text{Base}}$ and $S_{\text{Base}}$. Note that this function will only use $I_{\text{Base}}$ value.

- **Operation**: Disabled/Enabled to enable/disable the complete function.

- **FunctionMode**: It defines the way the detection of failure of the breaker is performed. In the *Current mode*, the current measurement is used for the detection. In the *CB Pos mode*, the CB auxiliary contact status is used as an indicator of the failure of the breaker. The mode *Current* or *CB Pos* means that both ways of detections can be activated. The *CB Pos mode* is used in applications where the fault current through the circuit breaker is small. This can be the case for some generator protection application (for example, reverse power protection) or in the case of line ends with weak end infeed.

- **StartMode**: By this setting it is possible to select how t1 and t2 timers are run and consequently how output commands are given from the function:
  - **Option 1 - LatchedStart**: “By external start signals which is internally latched”.
When function is once started by external START signal, the timers t1 and t2 will always elapse and then measurement criterion defined by parameter FunctionMode will be always checked in order to verify if the appropriate command shall be given out from the function. Timers cannot be stopped by removing the external START signal. Function can be started again only when all of the following three timers t1, t2 and fixed timer of 150ms in function internal design has expired and the measurement criterion defined by parameter FunctionMode has deactivated, see Figure 100. Note that this option corresponds to the function behavior in previous versions of the 670 Series from version 1.0 up to and including version 2.1.

- **Option 2 - Follow_start**: “Follow the external start signal only”. The timers t1 and t2 will run while external START signal is present. If they elapse then measurement criterion defined by parameter FunctionMode will be checked in order to verify if the appropriate command shall be given out from the function. Timers can be always stopped by resetting the external START signal, see Figure 101.

- **Option 3 - Follow_start&Mode**: “Follow external start signal and selected FunctionMode”. The timers t1 and t2 will run while external START signal is present and in the same time the measurement criterion defined by parameter FunctionMode is active. If they elapse then the appropriate command will be given out from the function. Timers can be stopped by resetting the external START signal or if the measurement criterion de-activates, see Figure 102.

When one of the two “follow modes” is used, there is a settable timer tStartTimeout which will block the external START input signal when it times-out. This will automatically also reset the t1 and t2 timers and consequently prevent any backup trip command. At the same time the STALARM output from the function will have logical value one. To reset this signal external START signal shall be removed. This is done in order to prevent unwanted operation of the breaker failure function for cases where a permanent START signal is given by mistake (e.g. due to a fault in the station battery system). Note that any backup trip command will inhibit running of tStartTimeout timer.

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**Figure 100**: Simplified overall logic for LatchedStart

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Busbar protection REB670
Application manual 187
Figure 101: Simplified overall logic for FollowStart

Figure 102: Simplified overall logic for FollowStart&Mode

RetripMode: This setting defines how the re-trip function shall operate. Refer to Table 32 for more details.

<table>
<thead>
<tr>
<th>RetripMode</th>
<th>FunctionMode</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>N/A</td>
<td>The re-trip function is disabled</td>
</tr>
<tr>
<td>UseFunctionMode</td>
<td>Current</td>
<td>A phase current should be larger than the set operate level to allow re-trip once the t1 timer elapses</td>
</tr>
<tr>
<td></td>
<td>CB Pos</td>
<td>Re-trip is done when the breaker position indicates that breaker is still closed after re-trip time has elapsed</td>
</tr>
<tr>
<td></td>
<td>Current or CB Pos</td>
<td>Both the methods are used</td>
</tr>
<tr>
<td>Always</td>
<td>N/A</td>
<td>Re-trip is always given when t1 elapses without any further checks</td>
</tr>
</tbody>
</table>

BuTripMode: Defines how many current criterias to be fulfilled in order to detect failure of the breaker. For Current operation 2 out of 4 means that at least two currents, of the three-phase currents and the residual current, shall be high to indicate breaker failure. 1 out of 3 means that at least one current of the three-phase currents shall be high to indicate breaker failure. 1 out of 4 means that at least one current of the three phase currents or the residual current shall be high to
indicate breaker failure. In most applications 1 out of 3 is sufficient. For CB Pos operation 1 out of 3 is always used.

*Pickup PH*: Current level for detection of breaker failure, set in % of *IBase*. This parameter should be set so that faults with small fault current can be detected. The setting can be chosen in accordance with the most sensitive protection function to start the breaker failure protection. Default setting is 10% of *IBase*. Note that this setting shall not be set lower than 4% of *Ir*, where *Ir* is rated current of the IED CT input where the function is connected. In principle *Ir* is either 1A or 5A depending on the ordered IED.

*Pickup>BlkCBPos*: If the *FunctionMode* is set to *Current* or *CB pos* breaker failure for high current faults are safely detected by the current measurement function. To increase security for low currents the contact based function will be enabled only if the current at the moment of starting is below this set level. The setting can be given within the range 5 – 200% of *IBase*. It is strongly recommended to set this level above IPPU set level.

*Pickup N*: Residual current level for detection of breaker failure set in % of *IBase*. In high impedance earthed systems the residual current at phase-to-ground faults are normally much smaller than the short circuit currents. In order to detect breaker failure at single phase-to-ground faults in such systems it is necessary to measure the residual current separately. The *BuTripMode* shall be set 1 out of 4 in such systems. The current setting should be chosen in accordance to the setting of the sensitive ground-fault protection. The setting can be given within the range 2 – 200% of *IBase*.

*t1*: Time delay of the retrip. The setting can be given within the range 0 – 60s in steps of 0.001 s. Typical setting is within range 0 – 50ms.

*t2*: Time delay of the backup trip. The choice of this setting is made as short as possible at the same time as unwanted operation must be avoided. Typical setting is within range 90 – 200ms (also dependent of retrip timer).

Timer *t2* is used when function is started in one phase only (i.e. for single-phase to ground fault on an OHL (Over Head Lines) when single-pole auto-reclosing is used).

The minimum time delay for the backup trip can be estimated as:

\[ t_2 \geq t_1 + t_{CB\_open} + t_{BFP\_reset} + t_{margin} \]

(Equation 41)

where:

- \( t_{CB\_open} \) is the maximum opening time for the circuit breaker
- \( t_{BFP\_reset} \) is the maximum time for breaker failure protection to detect correct breaker function (the current criteria reset)
- \( t_{margin} \) is a safety margin

It is often required that the total fault clearance time shall be less than a given critical time. This time is often dependent of the ability of the power system to maintain transient stability in case of a fault close to a power plant.
Figure 103: Time sequence

t2MPh: Time delay of the backup trip at multi-phase initiate. The critical fault clearance time is often shorter in case of multi-phase faults, compared to single phase-to-ground faults. Therefore there is a possibility to reduce the backup trip delay for multi-phase faults. Typical setting is 90 – 150 ms.

Note that for a protected object which are always tripped three-phase (e.g. transformers, generators, reactors, cables, etc.) this timer shall always be set to the same value as t2 timer.

t3: Additional time delay to t2 for a second backup trip TRBU2. In some applications there might be a requirement to have separated backup trip functions, tripping different backup circuit breakers.

tCBAalarm: Time delay for alarm in case of indication of faulty circuit breaker. There is a binary input 52FAIL from the circuit breaker. This signal is activated when internal supervision in the circuit breaker detect that the circuit breaker is unable to clear fault. This could be the case when gas pressure is low in a SF6 circuit breaker, of others. After the set time an alarm is given, so that actions can be done to repair the circuit breaker. Note that the time delay for backup trip t2 is bypassed when the 52FAIL is active. Typical setting is 2.0 seconds..

tPulse: Trip pulse duration. This setting must be larger than the opening time of circuit breakers to be tripped from the breaker failure protection. Typical setting is 200 ms.

tStartTimeout: When one of the two “Follow Modes” is used, there is a settable timer tStartTimeout which will block the external START input signal when it times-out. This will automatically also reset the t1 and t2 timers and consequently prevent any backup trip command. At the same time the STALARM output from the function will have logical value one. To reset that condition external START signal shall be removed. This is done in order to prevent unwanted operation of the breaker failure function for cases where a permanent START signal is given by mistake (e.g. due to a fault in the station battery system). Note that any backup trip command will inhibit running of tStartTimeout timer.
### Table 33: Setting summary for FunctionMode, StartMode, RetripMode and BuTripMode

<table>
<thead>
<tr>
<th>No.</th>
<th>StartMode</th>
<th>RetripMode</th>
<th>t1 and t2 initiated with</th>
<th>When t1 has elapsed, TRRET will</th>
<th>When t2 or t2MPh has elapsed, TRBU will be given if</th>
<th>t1 and t2 and t2MPh will be stopped (reset) if</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>LatchedStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>current is above set level *)</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level *)</td>
</tr>
<tr>
<td>2</td>
<td>LatchedStart</td>
<td>UseFunctionMode</td>
<td>external START</td>
<td>be given if current is above set level of IPh&gt;</td>
<td>current is above set level *)</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level *)</td>
</tr>
<tr>
<td>3</td>
<td>LatchedStart</td>
<td>Always</td>
<td>external START</td>
<td>always be given</td>
<td>current is above set level *)</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level *)</td>
</tr>
<tr>
<td>4</td>
<td>FollowStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>current is above set level *)</td>
<td>external START disappears</td>
</tr>
<tr>
<td>5</td>
<td>FollowStart</td>
<td>UseFunctionMode</td>
<td>external START</td>
<td>be given if current is above set level of IPh&gt;</td>
<td>current is above set level *)</td>
<td>external START disappears</td>
</tr>
<tr>
<td>6</td>
<td>FollowStart</td>
<td>Always</td>
<td>external START</td>
<td>be given if external START is present</td>
<td>current is above set level *)</td>
<td>external START disappears</td>
</tr>
<tr>
<td>7</td>
<td>FollowStart&amp; Mode</td>
<td>Off</td>
<td>external START and current above set level</td>
<td>never be given</td>
<td>current is above set level *) and external START present</td>
<td>current is below set level *) or external START disappears</td>
</tr>
<tr>
<td>8</td>
<td>FollowStart&amp; Mode</td>
<td>UseFunctionMode</td>
<td>external START and current above set level</td>
<td>be given if current is above set level of IPh&gt; and external START is present</td>
<td>current is above set level *) and external START present</td>
<td>current is below set level *) or external START disappears</td>
</tr>
<tr>
<td>9</td>
<td>FollowStart&amp; Mode</td>
<td>Always</td>
<td>external START and current above set level</td>
<td>be given if external START is present</td>
<td>current is above set level *) and external START present</td>
<td>current is below set level *) or external START disappears</td>
</tr>
</tbody>
</table>

*) Set level depends on selected BuTripMode, that is, set level can be either IPh> or IN> or both.

---

<table>
<thead>
<tr>
<th>No.</th>
<th>StartMode</th>
<th>RetripMode</th>
<th>t1 and t2 initiated with</th>
<th>When t1 has elapsed, TRRET will</th>
<th>When t2 or t2MPh has elapsed, TRBU will be given if</th>
<th>t1 and t2 and t2MPh will be stopped (reset) if</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>LatchedStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>CBCLDLx input has logical value one</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and CBCLDLx input has logical value zero</td>
</tr>
<tr>
<td>11</td>
<td>LatchedStart</td>
<td>UseFunctionMode</td>
<td>external START</td>
<td>be given if CBCLDLx input has logical value one</td>
<td>CBCLDLx input has logical value one</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and CBCLDLx input has logical value zero</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>No.</th>
<th>StartMode</th>
<th>RetripMode</th>
<th>t1 and t2 initiated with</th>
<th>When t1 has elapsed, TRRET will</th>
<th>When t2 or t2MPh has elapsed, TRBU will be given if</th>
<th>t1 and t2 and t2MPh will be stopped (reset) if</th>
</tr>
</thead>
<tbody>
<tr>
<td>12</td>
<td>LatchedStart</td>
<td>Always</td>
<td>external START</td>
<td>always be given</td>
<td>CBCLDLx input has logical value one</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and CBCLDLx input has logical value zero</td>
</tr>
<tr>
<td>13</td>
<td>FollowStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>CBCLDLx input has logical value one</td>
<td>external START disappears</td>
</tr>
<tr>
<td>14</td>
<td>FollowStart</td>
<td>UseFunctionMode</td>
<td>external START</td>
<td>be given if CBCLDLx input has logical value one</td>
<td>CBCLDLx input has logical value one</td>
<td>external START disappears</td>
</tr>
<tr>
<td>15</td>
<td>FollowStart</td>
<td>Always</td>
<td>external START</td>
<td>if external START is present</td>
<td>CBCLDLx input has logical value one</td>
<td>external START disappears</td>
</tr>
<tr>
<td>16</td>
<td>FollowStart&amp;Mode</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>be given if CBCLDLx input has logical value one and external START is present</td>
<td>CBCLDLx input has logical value zero or external START disappears</td>
</tr>
<tr>
<td>17</td>
<td>FollowStart&amp;Mode</td>
<td>UseFunctionMode</td>
<td>external START</td>
<td>be given if CBCLDLx input has logical value one and external START is present</td>
<td>be given if CBCLDLx input has logical value one and external START is present</td>
<td>CBCLDLx input has logical value zero or external START disappears</td>
</tr>
<tr>
<td>18</td>
<td>FollowStart&amp;Mode</td>
<td>Always</td>
<td>external START</td>
<td>be given if external START is present</td>
<td>be given if CBCLDLx input has logical value one and external START is present</td>
<td>CBCLDLx input has logical value zero or external START disappears</td>
</tr>
</tbody>
</table>

FunctionMode = Current or CB Pos

<table>
<thead>
<tr>
<th>No.</th>
<th>StartMode</th>
<th>RetripMode</th>
<th>t1 and t2 initiated with</th>
<th>When t1 has elapsed, TRRET will</th>
<th>When t2 or t2MPh has elapsed, TRBU will be given if</th>
<th>t1 and t2 and t2MPh will be stopped (reset) if</th>
</tr>
</thead>
<tbody>
<tr>
<td>19</td>
<td>LatchedStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>current is above set level *) and higher than I&gt;BikCBPos or CBCLDLx input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level *) or CBCLDLx input has logical value zero</td>
</tr>
<tr>
<td>20</td>
<td>LatchedStart</td>
<td>UseFunctionMode</td>
<td>external START</td>
<td>be given if current is above set level of IPh&gt; and higher than I&gt;BikCBPos or CBCLDLx input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>current is above set level *) and higher than I&gt;BikCBPos or CBCLDLx input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level *) or CBCLDLx input has logical value zero</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
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<tr>
<th>No.</th>
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<tbody>
<tr>
<td>21</td>
<td>LatchedStart</td>
<td>Always</td>
<td>external START</td>
<td>always be given</td>
<td>current is above set level *) and higher than I&gt;BikCBPos or CBCLDLx input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level *) or CBCLDLx input has logical value zero</td>
</tr>
<tr>
<td>22</td>
<td>FollowStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>current is above set level *) and higher than I&gt;BikCBPos or CBCLDLx input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>external START disappears</td>
</tr>
<tr>
<td>23</td>
<td>FollowStart</td>
<td>UseFunctionMode</td>
<td>external START</td>
<td>be given if current is above set level of IPh&gt; and higher than I&gt;BikCBPos or CBCLDLx input has logical value one when current is smaller than I&gt;BikCBPos</td>
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<td>external START disappears</td>
</tr>
<tr>
<td>24</td>
<td>FollowStart</td>
<td>Always</td>
<td>external START</td>
<td>be given if external START is present</td>
<td>current is above set level *) and higher than I&gt;BikCBPos or CBCLDLx input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>external START disappears</td>
</tr>
<tr>
<td>25</td>
<td>FollowStart&amp;Mode</td>
<td>Off</td>
<td>external START and current above set level</td>
<td>never be given</td>
<td>current is above set level *) and higher than I&gt;BikCBPos or CBCLDLx input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>current is below set level *) or external START disappears</td>
</tr>
</tbody>
</table>

Table continues on next page
7.7 Breaker failure protection, single phase version CCSRBRF (50BF)

7.7.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
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<tbody>
<tr>
<td>Breaker failure protection, single phase version</td>
<td>CCSRBRF</td>
<td>I&gt;BF</td>
<td>50BF</td>
</tr>
</tbody>
</table>

7.7.2 Application

In the design of the fault clearance system the N-1 criterion is often used. This means that a fault needs to be cleared even if any component in the fault clearance system is faulty. One necessary component in the fault clearance system is the circuit breaker.

It is from practical and economical reason not feasible to duplicate the circuit breaker for the protected object. Instead a breaker failure protection is used.

Breaker failure protection, single phase version (CCSRBRF) (50BF) will issue a backup trip command to adjacent circuit breakers in case of failure to trip of the “normal” circuit breaker for the protected object. The detection of failure to break the current through the breaker is made either by means of current measurement or as detection of closed status using auxiliary contact.

CCCSRBRF (50BF) can also give a retrip command. This means that a second trip signal is sent to the protected object circuit breaker. The retrip function can be used to increase the probability of
operation of the breaker, or it can be used to avoid backup trip of many breakers in case of mistakes during relay maintenance and test.

### 7.7.3 Setting guidelines

The parameters for Breaker failure protection, single phase version (CCSRBRF,50BF) are set via the local HMI or PCM600.

The following settings can be done for the breaker failure protection.

- **GlobalBaseSel**: Selects the global base value group used by the function to define IBase, VBase and SBase. Note that this function will only use IBase value.

- **Operation**: Disabled or Enabled to enable/disable the complete function.

- **FunctionMode**: It defines the way the detection of failure of the breaker is performed. In the mode Current the current measurement is used for the detection. In the mode CB Pos, the CB auxiliary contact status is used as indicator of failure of the breaker. The mode Current or CB Pos means that both ways of detections can be activated. CB Pos mode can be usable in applications where the fault current through the circuit breaker is small. This can be the case for some generator protection application (for example reverse power protection) or in case of line ends with weak end infeed.

- **By setting the StartMode**, it is possible to select how $t_1$ and $t_2$ timers are run and consequently how output commands are given from the function:
  - **Option 1 - LatchedStart**: “By external start signal which is internally latched”. When function is once started by external BFI_3P signal, the timers $t_1$ and $t_2$ will always elapse and then measurement criterion defined by parameter FunctionMode will be always checked in order to verify if the appropriate command shall be given out from the function. Timers cannot be stopped by removing the external BFI_3P signal. Function can be started again only when all of the following three timers $t_1$, $t_2$, and fixed timer of 150ms in function internal design has expired and the measurement criterion defined by parameter FunctionMode has deactivated, see Figure 104. Note that this option corresponds to the function behavior in previous versions of the 670 Series from version 1.0 up to and including version 2.1
  - **Option 2 - FollowStart**: “Follow the external start signal only”. The timers $t_1$ and $t_2$ will run while external BFI_3P signal is present. If they elapse then measurement criterion defined by parameter FunctionMode will be checked in order to verify if the appropriate command shall be given out from the function. Timers can be always stopped by resetting the external BFI_3P signal, see Figure 105.
  - **Option 3 - FollowStart&Mode**: “Follow external start signal and selected FunctionMode”. The timers $t_1$ and $t_2$ will run while external BFI_3P signal is present and in the same time the measurement criterion defined by parameter FunctionMode is active. If they elapse then the appropriate command will be given out from the function. Timers can be stopped by resetting the external BFI_3P signal or if the measurement criterion de-activates, see Figure 106.

When one of the two “follow modes” is used, there is a settable timer $t_{StartTimeout}$ which will block the external BFI_3P input signal when it times-out. This will automatically also reset the $t_1$ and $t_2$ timers and consequently prevent any backup trip command. At the same time the STALARM output from the function will have logical value one. To reset this signal external BFI_3P signal shall be removed. This is done in order to prevent unwanted operation of the breaker failure function for cases where a permanent BFI_3P signal is given by mistake (e.g. due to a fault in the station battery system). Note that any backup trip command will inhibit running of $t_{StartTimeout}$ timer.
RetripMode: This setting defines how the retrip function shall operate. Refer to Table 34 for more details.
Table 34: Dependencies between RetripMode and FunctionMode

<table>
<thead>
<tr>
<th>RetripMode</th>
<th>FunctionMode</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>N/A</td>
<td>the re-trip function is disabled</td>
</tr>
<tr>
<td>UseFunctionMode</td>
<td>Current</td>
<td>a phase current must be larger than the set operate level to allow re-trip</td>
</tr>
<tr>
<td></td>
<td></td>
<td>once the t1 timer elapses</td>
</tr>
<tr>
<td></td>
<td>CB Pos</td>
<td>Re-trip is done when breaker position indicates that breaker is still closed</td>
</tr>
<tr>
<td></td>
<td></td>
<td>after re-trip time has elapsed</td>
</tr>
<tr>
<td></td>
<td>Current or CB Pos</td>
<td>both methods are used</td>
</tr>
<tr>
<td>Always</td>
<td>N/A</td>
<td>Re-trip is always given when t1 elapses without any further checks</td>
</tr>
</tbody>
</table>

Current level for detection of breaker failure, set in % of IBase. This parameter should be set so that faults with small fault current can be detected. The setting can be chosen in accordance with the most sensitive protection function to start the breaker failure protection. Default setting is 10% of IBase. Note that this setting shall not be set lower than 4% of Ir, where Ir is rated current of the IED CT input where function is connected. In principle Ir is either 1A or 5A depending on the ordered IED.

PU_BlkCont: If the FunctionMode is set to Current or CB pos breaker failure for high current faults are safely detected by the current measurement function. To increase security for low currents the contact based function will be enabled only if the current at the moment of starting is below this set level. The setting can be given within the range 5 – 200% of IBase. It is strongly recommended to set this level above IPPU set level.

t1: Time delay of the retrip. The setting can be given within the range 0 – 60s in steps of 0.001 s. Typical setting is within range 0 – 50ms.

t2: Time delay of the backup trip. The choice of this setting is made as short as possible at the same time as unwanted operation must be avoided. Typical setting is 90 – 200ms (also dependent of retrip timer).

The minimum time delay for the backup trip can be estimated as:

\[ t_2 \geq t_1 + t_{CB\_open} + t_{BFP\_reset} + t_{margin} \]

(Equation 42)

where:
- \( t_{CB\_open} \) is the maximum opening time for the circuit breaker
- \( t_{BFP\_reset} \) is the maximum time for breaker failure protection to detect correct breaker function (the current criteria reset)
- \( t_{margin} \) is a safety margin
It is often required that the total fault clearance time shall be less than a given critical time. This time is often dependent on the ability to maintain transient stability in case of a fault close to a power plant.

**Figure 107: Time sequence**

- **t3**: Additional time delay to t2 for a second backup trip TRBU2. In some applications there might be a requirement to have separated backup trip functions, tripping different backup circuit breakers.

- **tCBAalarm**: Time delay for alarm in case of indication of faulty circuit breaker. There is a binary input 52FAIL from the circuit breaker. This signal is activated when internal supervision in the circuit breaker detect that the circuit breaker is unable to clear fault. This could be the case when gas pressure is low in a SF6 circuit breaker. After the set time an alarm is given, so that action can be done to repair the circuit breaker. The time delay for backup trip is bypassed when the 52FAIL is active. Typical setting is 2.0 seconds.

- **tPulse**: Trip pulse duration. This setting must be larger than the critical impulse time of circuit breakers to be tripped from the breaker failure protection. Typical setting is 200 ms.

- **tStartTimeout**: When one of the two “Follow Modes” is used, there is a settable timer tStartTimeout which will block the external BFI_3P input signal when it times-out. This will automatically also reset the t1 and t2 timers and consequently prevent any backup trip command. At the same time the STALARM output from the function will have logical value one. To reset that condition external BFI_3P signal shall be removed. This is done in order to prevent unwanted operation of the breaker failure function for cases where a permanent BFI_3P signal is given by mistake (e.g. due to a fault in the station battery system). Note that any backup trip command will inhibit running of tStartTimeout timer.
### Table 35: Setting summary for FunctionMode, StartMode and RetripMode

<table>
<thead>
<tr>
<th>No.</th>
<th>StartMode</th>
<th>RetripMode</th>
<th>t1 and t2 initiated with</th>
<th>When t1 has elapsed, TRRET will</th>
<th>When t2 has elapsed, TRBU will be given if</th>
<th>t1 and t2 will be stopped (reset) if</th>
</tr>
</thead>
<tbody>
<tr>
<td>FunctionMode = Current</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>LatchedStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>current is above set level of IPh&gt;</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level of IPh&gt;</td>
</tr>
<tr>
<td>2</td>
<td>LatchedStart</td>
<td>UseFunction Mode</td>
<td>external START</td>
<td>be given if current is above set level of IPh&gt;</td>
<td>current is above set level of IPh&gt;</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level of IPh&gt;</td>
</tr>
<tr>
<td>3</td>
<td>LatchedStart</td>
<td>Always</td>
<td>external START</td>
<td>always be given</td>
<td>current is above set level of IPh&gt;</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level of IPh&gt;</td>
</tr>
<tr>
<td>4</td>
<td>FollowStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>current is above set level of IPh&gt;</td>
<td>external START disappears</td>
</tr>
<tr>
<td>5</td>
<td>FollowStart</td>
<td>UseFunction Mode</td>
<td>external START</td>
<td>be given if current is above set level of IPh&gt;</td>
<td>current is above set level of IPh&gt;</td>
<td>external START disappears</td>
</tr>
<tr>
<td>6</td>
<td>FollowStart</td>
<td>Always</td>
<td>external START</td>
<td>be given if external START is present</td>
<td>current is above set level of IPh&gt;</td>
<td>external START disappears</td>
</tr>
<tr>
<td>7</td>
<td>FollowStart&amp; Mode</td>
<td>Off</td>
<td>external START and current above set level</td>
<td>never be given</td>
<td>current is above set level of IPh&gt; and external START present</td>
<td>current is below set level of IPh&gt; or external START disappears</td>
</tr>
<tr>
<td>8</td>
<td>FollowStart&amp; Mode</td>
<td>UseFunction Mode</td>
<td>external START and current above set level</td>
<td>be given if current is above set level of IPh&gt; and external START is present</td>
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<td>current is below set level of IPh&gt; or external START disappears</td>
</tr>
<tr>
<td>9</td>
<td>FollowStart&amp; Mode</td>
<td>Always</td>
<td>external START and current above set level</td>
<td>be given if external START is present</td>
<td>current is above set level of IPh&gt; and external START present</td>
<td>current is below set level of IPh&gt; or external START disappears</td>
</tr>
<tr>
<td>FunctionMode = CB Pos</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>10</td>
<td>LatchedStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>CBCLD input has logical value one</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and CBCLD input has logical value zero</td>
</tr>
<tr>
<td>11</td>
<td>LatchedStart</td>
<td>UseFunction Mode</td>
<td>external START</td>
<td>be given if CBCLD input has logical value one</td>
<td>CBCLD input has logical value one</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and CBCLD input has logical value zero</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>No.</th>
<th>StartMode</th>
<th>RetripMode</th>
<th>t1 and t2 initiated with</th>
<th>When t1 has elapsed, TRRET will</th>
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<th>t1 and t2 will be stopped (reset) if</th>
</tr>
</thead>
<tbody>
<tr>
<td>12</td>
<td>LatchedStart</td>
<td>Always</td>
<td>external START</td>
<td>always be given</td>
<td>CBCLD input has logical value one</td>
<td>t1 and (t2 or t2MPh) expires and CBCLD input has logical value zero</td>
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<tr>
<td>13</td>
<td>FollowStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
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</tr>
<tr>
<td>14</td>
<td>FollowStart</td>
<td>UseFunction Mode</td>
<td>external START</td>
<td>be given if CBCLD input has logical value one</td>
<td>CBCLD input has logical value one</td>
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</tr>
<tr>
<td>15</td>
<td>FollowStart</td>
<td>Always</td>
<td>external START</td>
<td>if external START is present</td>
<td>CBCLD input has logical value one</td>
<td>external START disappears</td>
</tr>
<tr>
<td>16</td>
<td>FollowStart&amp; Mode</td>
<td>Off</td>
<td>external START and CBCLD input has logical value one</td>
<td>never be given</td>
<td>be given if CBCLD input has logical value one and external START is present</td>
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<td>17</td>
<td>FollowStart&amp; Mode</td>
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<td>external START and CBCLD input has logical value one</td>
<td>be given if CBCLD input has logical value one and external START is present</td>
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</tr>
<tr>
<td>18</td>
<td>FollowStart&amp; Mode</td>
<td>Always</td>
<td>external START and CBCLD input has logical value one</td>
<td>be given if external START is present</td>
<td>be given if CBCLD input has logical value one and external START is present</td>
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**FunctionMode = Current or CB Pos**

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<td>external START</td>
<td>never be given</td>
<td>current is above set level of IPH&gt; and higher than I&gt;BikCBPos or CBCLD input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level of IPH&gt; or CBCLD input has logical value zero</td>
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<td>20</td>
<td>LatchedStart</td>
<td>UseFunction Mode</td>
<td>external START</td>
<td>be given if current is above set level of IPH&gt; and higher than I&gt;BikCBPos or CBCLD input has logical value one when current is smaller than I&gt;BikCBPos</td>
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<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level of IPH&gt; or CBCLD input has logical value zero</td>
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<tr>
<th>No.</th>
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<th>t1 and t2 initiated with</th>
<th>When t1 has elapsed, TRRET will be given if</th>
<th>When t2 has elapsed, TRBU will be given if</th>
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<tbody>
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<td>21</td>
<td>LatchedStart</td>
<td>Always</td>
<td>external START</td>
<td>always be given</td>
<td>current is above set level of IPh&gt; and higher than I&gt;BlkCBPos or CBCLD input has logical value one when current is smaller than I&gt;BlkCBPos</td>
<td>t1 and (t2 or t2MPh) and 150ms expires and current is below set level of IPh&gt; or CBCLD input has logical value zero</td>
</tr>
<tr>
<td>22</td>
<td>FollowStart</td>
<td>Off</td>
<td>external START</td>
<td>never be given</td>
<td>current is above set level of IPh&gt; and higher than I&gt;BlkCBPos or CBCLD input has logical value one when current is smaller than I&gt;BlkCBPos</td>
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</tr>
<tr>
<td>23</td>
<td>FollowStart</td>
<td>UseFunction Mode</td>
<td>external START</td>
<td>be given if current is above set level of IPh&gt; and higher than I&gt;BlkCBPos or CBCLD input has logical value one when current is smaller than I&gt;BlkCBPos</td>
<td>current is above set level of IPh&gt; and higher than I&gt;BlkCBPos or CBCLD input has logical value one when current is smaller than I&gt;BlkCBPos</td>
<td>external START disappears</td>
</tr>
<tr>
<td>24</td>
<td>FollowStart</td>
<td>Always</td>
<td>external START</td>
<td>be given if external START is present</td>
<td>current is above set level of IPh&gt; and higher than I&gt;BlkCBPos or CBCLD input has logical value one when current is smaller than I&gt;BlkCBPos</td>
<td>external START disappears</td>
</tr>
</tbody>
</table>

Table continues on next page
### 7.8 Directional underpower protection GUPPDUP (37)

#### 7.8.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Directional underpower protection</td>
<td>GUPPDUP</td>
<td></td>
<td>37</td>
</tr>
</tbody>
</table>

#### 7.8.2 Application

The task of a generator in a power plant is to convert mechanical energy available as a torque on a rotating shaft to electric energy.

Sometimes, the mechanical power from a prime mover may decrease so much that it does not cover bearing losses and ventilation losses. Then, the synchronous generator becomes a synchronous motor and starts to take electric power from the rest of the power system. This operating state, where individual synchronous machines operate as motors, implies no risk for the

<table>
<thead>
<tr>
<th>No.</th>
<th>StartMode</th>
<th>RetripMode</th>
<th>t1 and t2 initiated with</th>
<th>When t1 has elapsed, TRRET will</th>
<th>When t2 has elapsed, TRBU will be given if</th>
<th>t1 and t2 will be stopped (reset) if</th>
</tr>
</thead>
<tbody>
<tr>
<td>25</td>
<td>FollowStart&amp; Mode</td>
<td>Off</td>
<td>external START and current above set level</td>
<td>never be given</td>
<td>current is above set level of Iph&gt; and higher than I&gt;BikCBPos or CBCLD input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>current is below set level of Iph&gt; or external START disappears</td>
</tr>
<tr>
<td>26</td>
<td>FollowStart&amp; Mode</td>
<td>UseFunction Mode</td>
<td>external START and current above set level</td>
<td>be given if current is above set level of Iph&gt; and higher than I&gt;BikCBPos or CBCLD input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>current is above set level of Iph&gt; and higher than I&gt;BikCBPos or CBCLD input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>current is below set level of Iph&gt; or external START disappears</td>
</tr>
<tr>
<td>27</td>
<td>FollowStart&amp; Mode</td>
<td>Always</td>
<td>external START and current above set level</td>
<td>be given if external START is present</td>
<td>current is above set level of Iph&gt; and higher than I&gt;BikCBPos or CBCLD input has logical value one when current is smaller than I&gt;BikCBPos</td>
<td>current is below set level of Iph&gt; or external START disappears</td>
</tr>
</tbody>
</table>
machine itself. If the generator under consideration is very large and if it consumes lots of electric power, it may be desirable to disconnect it to ease the task for the rest of the power system.

Often, the motoring condition may imply that the turbine is in a very dangerous state. The task of the reverse power protection is to protect the turbine and not to protect the generator itself.

Steam turbines easily become overheated if the steam flow becomes too low or if the steam ceases to flow through the turbine. Therefore, turbo-generators should have reverse power protection. There are several contingencies that may cause reverse power: break of a main steam pipe, damage to one or more blades in the steam turbine or inadvertent closing of the main stop valves. In the last case, it is highly desirable to have a reliable reverse power protection. It may prevent damage to an otherwise undamaged plant.

During the routine shutdown of many thermal power units, the reverse power protection gives the tripping impulse to the generator breaker (the unit breaker). By doing so, one prevents the disconnection of the unit before the mechanical power has become zero. Earlier disconnection would cause an acceleration of the turbine generator at all routine shutdowns. This should have caused overspeed and high centrifugal stresses.

When the steam ceases to flow through a turbine, the cooling of the turbine blades will disappear. Now, it is not possible to remove all heat generated by the windage losses. Instead, the heat will increase the temperature in the steam turbine and especially of the blades. When a steam turbine rotates without steam supply, the electric power consumption will be about 2% of rated power. Even if the turbine rotates in vacuum, it will soon become overheated and damaged. The turbine overheats within minutes if the turbine loses the vacuum.

The critical time to overheating a steam turbine varies from about 0.5 to 30 minutes depending on the type of turbine. A high-pressure turbine with small and thin blades will become overheated more easily than a low-pressure turbine with long and heavy blades. The conditions vary from turbine to turbine and it is necessary to ask the turbine manufacturer in each case.

Power to the power plant auxiliaries may come from a station service transformer connected to the secondary side of the step-up transformer. Power may also come from a start-up service transformer connected to the external network. One has to design the reverse power protection so that it can detect reverse power independent of the flow of power to the power plant auxiliaries.

Hydro turbines tolerate reverse power much better than steam turbines do. Only Kaplan turbine and bulb turbines may suffer from reverse power. There is a risk that the turbine runner moves axially and touches stationary parts. They are not always strong enough to withstand the associated stresses.

Ice and snow may block the intake when the outdoor temperature falls far below zero. Branches and leaves may also block the trash gates. A complete blockage of the intake may cause cavitations. The risk for damages to hydro turbines can justify reverse power protection in unattended plants.

A hydro turbine that rotates in water with closed wicket gates will draw electric power from the rest of the power system. This power will be about 10% of the rated power. If there is only air in the hydro turbine, the power demand will fall to about 3%.

Diesel engines should have reverse power protection. The generator will take about 15% of its rated power or more from the system. A stiff engine may require perhaps 25% of the rated power to motor it. An engine that is good run in might need no more than 5%. It is necessary to obtain information from the engine manufacturer and to measure the reverse power during commissioning.
Gas turbines usually do not require reverse power protection.

Figure 108 illustrates the reverse power protection with underpower protection and with overpower protection. The underpower protection gives a higher margin and should provide better dependability. On the other hand, the risk for unwanted operation immediately after synchronization may be higher. One should set the underpower protection (reference angle set to 0) to trip if the active power from the generator is less than about 2%. One should set the overpower protection (reference angle set to 180) to trip if the power flow from the network to the generator is higher than 1%.

![Diagram of reverse power protection with underpower and overpower protection]

**Figure 108: Reverse power protection with underpower or overpower protection**

### 7.8.3 Setting guidelines

**GlobalBaseSel**: Selects the global base value group used by the function to define \( I_{Base} \), \( V_{Base} \) and \( S_{Base} \). Note that this function will only use \( I_{Base} \) value.

**Operation**: With the parameter Operation the function can be set Enabled/Disabled.

**Mode**: The voltage and current used for the power measurement. The setting possibilities are shown in table 36.

#### Table 36: Complex power calculation

<table>
<thead>
<tr>
<th>Set value Mode</th>
<th>Formula used for complex power calculation</th>
</tr>
</thead>
<tbody>
<tr>
<td>A, B, C</td>
<td>( \vec{S} = \vec{V}_A \cdot \vec{I}_A^* + \vec{V}_B \cdot \vec{I}_B^* + \vec{V}_C \cdot \vec{I}_C^* )</td>
</tr>
<tr>
<td>Arone</td>
<td>( \vec{S} = \vec{V}_{AB} \cdot \vec{I}<em>A^* - \vec{V}</em>{BC} \cdot \vec{I}_C^* )</td>
</tr>
<tr>
<td>PosSeq</td>
<td>( \vec{S} = 3 \cdot \vec{V}<em>{PosSeq} \cdot \vec{I}</em>{PosSeq}^* )</td>
</tr>
</tbody>
</table>

Table continues on next page
The function has two stages that can be set independently.

With the parameter OpMode1(2) the function can be set Enabled/Disabled.

The function gives trip if the power component in the direction defined by the setting Angle1(2) is smaller than the set pick up power value Power1(2)
Figure 109: Underpower mode

The setting \textit{Power1(2)} gives the power component pick up value in the \textit{Angle1(2)} direction. The setting is given in p.u. of the generator rated power, see equation 53.

Minimum recommended setting is 0.2\% of \( S_N \) when metering class CT inputs into the IED are used.

\[
S_N = \sqrt{3} \cdot V_{\text{Base}} \cdot I_{\text{Base}}
\]

(Equation 53)

The setting \textit{Angle1(2)} gives the characteristic angle giving maximum sensitivity of the power protection function. The setting is given in degrees. For active power the set angle should be 0° or 180°. 0° should be used for generator low forward active power protection.
For low forward power the set angle should be 0° in the underpower function. TripDelay1(2) is set in seconds to give the time delay for trip of the stage after pick up. Hysteresis1(2) is given in p.u. of generator rated power according to equation 54.

\[ S_N = \sqrt{3} \cdot V_{\text{Base}} \cdot I_{\text{Base}} \]  

(Equation 54)

The drop out power will be \( \text{Power1}(2) + \text{Hysteresis1}(2) \).

The possibility to have low pass filtering of the measured power can be made as shown in the formula:

\[ S = TD \cdot S_{\text{old}} + (1 - TD) \cdot S_{\text{calculated}} \]  

(Equation 55)

Where

- \( S \) is a new measured value to be used for the protection function
- \( S_{\text{old}} \) is the measured value given from the function in previous execution cycle
- \( S_{\text{calculated}} \) is the new calculated value in the present execution cycle
- \( TD \) is settable parameter

The value of \( k=0.92 \) is recommended in generator applications as the trip delay is normally quite long.
The calibration factors for current and voltage measurement errors are set as a percentage of rated current/voltage:

- \( I_{\text{MagComp}5}, I_{\text{MagComp}30}, I_{\text{MagComp}100} \)
- \( V_{\text{MagComp}5}, V_{\text{MagComp}30}, V_{\text{MagComp}100} \)
- \( I_{\text{MagComp}5}, I_{\text{MagComp}30}, I_{\text{MagComp}100} \)

The angle compensation is given as the difference between current and voltage angle errors.

The values are given for operating points 5, 30, and 100% of rated current/voltage. The values should be available from instrument transformer test protocols.

### 7.9 Directional overpower protection GOPPDOP (32)

#### 7.9.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Directional overpower protection</td>
<td>GOPPDOP</td>
<td></td>
<td>32</td>
</tr>
</tbody>
</table>

#### 7.9.2 Application

The task of a generator in a power plant is to convert mechanical energy available as a torque on a rotating shaft to electric energy.

Sometimes, the mechanical power from a prime mover may decrease so much that it does not cover bearing losses and ventilation losses. Then, the synchronous generator becomes a synchronous motor and starts to take electric power from the rest of the power system. This operating state, where individual synchronous machines operate as motors, implies no risk for the machine itself. If the generator under consideration is very large and if it consumes lots of electric power, it may be desirable to disconnect it to ease the task for the rest of the power system.

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Diesel engines should have reverse power protection. The generator will take about 15% of its rated power or more from the system. A stiff engine may require perhaps 25% of the rated power to motor it. An engine that is well run in might need no more than 5%. It is necessary to obtain information from the engine manufacturer and to measure the reverse power during commissioning.

Gas turbines usually do not require reverse power protection.

Figure 111 illustrates the reverse power protection with underpower IED and with overpower IED. The underpower IED gives a higher margin and should provide better dependability. On the other hand, the risk for unwanted operation immediately after synchronization may be higher. One should set the underpower IED to trip if the active power from the generator is less than about 2%. One should set the overpower IED to trip if the power flow from the network to the generator is higher than 1%.
Figure 11: Reverse power protection with underpower IED and overpower IED

7.9.3 Setting guidelines

*GlobalBaseSel:* Selects the global base value group used by the function to define *IBase, VBase and SBase.* Note that this function will only use *IBase* value.

*Operation:* With the parameter *Operation* the function can be set *Enabled/Disabled.*

*Mode:* The voltage and current used for the power measurement. The setting possibilities are shown in table 37.

<table>
<thead>
<tr>
<th>Set value Mode</th>
<th>Formula used for complex power calculation</th>
</tr>
</thead>
<tbody>
<tr>
<td>A,B,C</td>
<td>$$\bar{S} = \bar{V}_A \cdot \bar{I}_A^* + \bar{V}_B \cdot \bar{I}_B^* + \bar{V}_C \cdot \bar{I}_C^*$$</td>
</tr>
<tr>
<td>Arone</td>
<td>$$\bar{S} = \bar{V}_{AB} \cdot \bar{I}<em>A^* - \bar{V}</em>{BC} \cdot \bar{I}_C^*$$</td>
</tr>
<tr>
<td>PosSeq</td>
<td>$$\bar{S} = 3 \cdot \bar{V}<em>{PosSeq} \cdot \bar{I}</em>{PosSeq}^*$$</td>
</tr>
<tr>
<td>A,B</td>
<td>$$\bar{S} = \bar{V}_{AB} \cdot (\bar{I}_A^* - \bar{I}_B^*)$$</td>
</tr>
<tr>
<td>B,C</td>
<td>$$\bar{S} = \bar{V}_{BC} \cdot (\bar{I}_B^* - \bar{I}_C^*)$$</td>
</tr>
<tr>
<td>C,A</td>
<td>$$\bar{S} = \bar{V}_{CA} \cdot (\bar{I}_C^* - \bar{I}_A^*)$$</td>
</tr>
</tbody>
</table>

Table continues on next page
### Set value Mode

<table>
<thead>
<tr>
<th>Mode</th>
<th>Formula used for complex power calculation</th>
</tr>
</thead>
<tbody>
<tr>
<td>A</td>
<td>$\tilde{S} = 3 \cdot \tilde{V}_A \cdot \tilde{I}_A^*$</td>
</tr>
<tr>
<td>B</td>
<td>$\tilde{S} = 3 \cdot \tilde{V}_B \cdot \tilde{I}_B^*$</td>
</tr>
<tr>
<td>C</td>
<td>$\tilde{S} = 3 \cdot \tilde{V}_C \cdot \tilde{I}_C^*$</td>
</tr>
</tbody>
</table>

The function has two stages that can be set independently.

With the parameter `OpMode1(2)` the function can be set `Enabled/Disabled`.

The function gives trip if the power component in the direction defined by the setting `Angle1(2)` is larger than the set pick up power value `Power1(2)`

![Diagram of Overpower mode](en06000440.vsd)

**Figure 112: Overpower mode**

The setting `Power1(2)` gives the power component pick up value in the `Angle1(2)` direction. The setting is given in p.u. of the generator rated power, see equation 66.

Minimum recommended setting is 0.2% of $S_n$ when metering class CT inputs into the IED are used.
The setting $\text{Angle1(2)}$ gives the characteristic angle giving maximum sensitivity of the power protection function. The setting is given in degrees. For active power the set angle should be 0° or 180°. 180° should be used for generator reverse power protection.

$S_N = \sqrt{3} \cdot V_{\text{Base}} \cdot I_{\text{Base}}$

(Equation 66)

Figure 113: For reverse power the set angle should be 180° in the overpower function

$\text{TripDelay1(2)}$ is set in seconds to give the time delay for trip of the stage after pick up.

$\text{Hysteresis1(2)}$ is given in p.u. of generator rated power according to equation 67.

$S_N = \sqrt{3} \cdot V_{\text{Base}} \cdot I_{\text{Base}}$

(Equation 67)

The drop out power will be $\text{Power1(2)} - \text{Hysteresis1(2)}$.

The possibility to have low pass filtering of the measured power can be made as shown in the formula:
\[ S = TD \cdot S_{\text{Old}} + (1 - TD) \cdot S_{\text{Calculated}} \]

(Equation 68)

Where

- \( S \) is a new measured value to be used for the protection function
- \( S_{\text{Old}} \) is the measured value given from the function in previous execution cycle
- \( S_{\text{Calculated}} \) is the new calculated value in the present execution cycle
- \( TD \) is settable parameter

The value of \( TD = 0.92 \) is recommended in generator applications as the trip delay is normally quite long.

The calibration factors for current and voltage measurement errors are set % of rated current/voltage:

- \( IMag\text{Comp}5, IMag\text{Comp}30, IMag\text{Comp}100 \)
- \( VMag\text{Comp}5, VMag\text{Comp}30, VMag\text{Comp}100 \)
- \( I\text{Ang}\text{Comp}5, I\text{Ang}\text{Comp}30, I\text{Ang}\text{Comp}100 \)

The angle compensation is given as difference between current and voltage angle errors.

The values are given for operating points 5, 30 and 100% of rated current/voltage. The values should be available from instrument transformer test protocols.

### 7.10 Capacitor bank protection CBPGAPC

#### 7.10.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitor bank protection</td>
<td>CBPGAPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

#### 7.10.2 Application

Shunt capacitor banks (SCBs) are somewhat specific and different from other power system elements. These specific features of SCB are briefly summarized in this section.

A capacitor unit is the building block used for SCB construction. The capacitor unit is made up of individual capacitor elements, arranged in parallel or series connections. Capacitor elements normally consist of aluminum foil, paper, or film-insulated cells immersed in a biodegradable insulating fluid and are sealed in a metallic container. The internal discharge resistor is also integrated within the capacitor unit in order to reduce trapped residual voltage after disconnection of the SCB from the power system. Units are available in a variety of voltage ratings.
(240V to 25kV) and sizes (2.5kVAR to about 1000kVAR). Capacitor unit can be designed with one or two bushings.

The high-voltage SCB is normally constructed using individual capacitor units connected in series and/or parallel to obtain the required voltage and MVAR rating. Typically the neighboring capacitor units are mounted in racks. Each rack must be insulated from the other by insulators because the can casing within each rack are at a certain potential. Refer figure 114 for an example:

![Capacitor Unit (Can)](image1)

**Figure 114: Replacement of a faulty capacitor unit within SCB**

There are four types of the capacitor unit fusing designs which are used for construction of SCBs:

- **Externally fused** where an individual fuse, externally mounted, protects each capacitor unit.
- **Internally fused** where each capacitor element is fused inside the capacitor unit.
- **Fuseless** where SCB is built from series connections of the individual capacitor units (that is, strings) and without any fuses.
- **Unfused** where, in contrary to the fuseless configuration, a series or parallel connection of the capacitor units is used to form SCB, still without any fuses.
Which type of fusing is used may depend on can manufacturer or utility preference and previous experience.

Because the SCBs are built from the individual capacitor units the overall connections may vary. Typically used SCB configurations are:

1. Delta-connected banks (generally used only at distribution voltages)
2. Single wye-connected banks
3. Double wye-connected banks
4. H-configuration, where each phase is connected in a bridge

Additionally, the SCB star point, when available, can be either grounded, grounded via impedance or isolated from ground. Which type of SCB grounding is used depends on voltage level, used circuit breaker, utility preference and previous experience. Many utilities have standard system grounding principle to ground neutrals of SCB above 100 kV.

Switching of SCB will produce transients in power system. The transient inrush current during SCB energizing typically has high frequency components and can reach peak current values, which are multiples of SCB rating. Opening of capacitor bank circuit breaker may produce step recovery voltages across open CB contact, which can consequently cause restrikes upon the first interruption of capacitive current. In modern power system the synchronized CB closing/opening may be utilized in such a manner that transients caused by SCB switching are avoided.

### 7.10.2.1 SCB protection

IED protection of shunt capacitor banks requires an understanding of the capabilities and limitations of the individual capacitor units and associated electrical equipment. Different types of shunt capacitor bank fusing, configuration or grounding may affect the IED selection for the protection scheme. Availability and placement of CTs and VTs can be additional limiting factor during protection scheme design.

SCB protection schemes are provided in order to detect and clear faults within the capacitor bank itself or in the connected leads to the substation busbar. Bank protection may include items such as a means to disconnect a faulted capacitor unit or capacitor element(s), a means to initiate a shutdown of the bank in case of faults that may lead to a catastrophic failure and alarms to indicate unbalance within the bank.

Capacitor bank outages and failures are often caused by accidental contact by animals. Vermin, monkeys, birds, may use the SCB as a resting place or a landing site. When the animal touches the HV live parts this can result in a flash-over, can rupture or a cascading failures that might cause extensive damages, fire or even total destruction of the whole SCB, unless the bank is sufficiently fitted with protection IEDs.

In addition, to fault conditions SCB can be exposed to different types of abnormal operating conditions. In accordance with IEC and ANSI standards capacitors shall be capable of continuous operation under contingency system and bank conditions, provided the following limitations are not exceeded:

1. Capacitor units should be capable of continuous operation including harmonics, but excluding transients, to 110% of rated IED root-mean-square (RMS) voltage and a crest voltage not exceeding of rated RMS voltage. The capacitor should also be able to carry 135% of nominal
current. The voltage capability of any series element of a capacitor unit shall be considered to be its share of the total capacitor unit voltage capability.

2. Capacitor units should not give less than 100% nor more than 110% of rated reactive power at rated sinusoidal voltage and frequency, measured at a uniform case and internal temperature of 25°C.

3. Capacitor units mounted in multiple rows and tiers should be designed for continuous operation for a 24h average temperature of 40 °C during the hottest day, or −40 °C during the coldest day expected at the location.

4. Capacitor units should be suitable for continuous operation at up to 135% of rated reactive power caused by the combined effects of:
   - Voltage in excess of the nameplate rating at fundamental frequency, but not over 110% of rated RMS voltage
   - Harmonic voltages superimposed on the fundamental frequency
   - Reactive power manufacturing tolerance of up to 115% of rated reactive power

5. Capacitor units rated above 600 V shall have an internal discharge device to reduce the residual voltage to 50 V or less in 5 or 10 minutes (depending on national standard).

Note that capacitor units designed for special applications can exceed these ratings.

Thus, as a general rule, the minimum number of capacitor units connected in parallel within a SCB is such that isolation of one capacitor unit in a group should not cause a voltage unbalance sufficient to place more than 110% of rated voltage on the remaining capacitors of that parallel group. Equally, the minimum number of series connected groups within a SCB is such that complete bypass of one group should not pause voltage higher than 110% of the rated voltage on the remaining capacitors of that serial group. The value of 110% is the maximum continuous overvoltage capability of capacitor units as per IEEE Std 18-1992.

The SCB typically requires the following types of IED protection:

1. Short circuit protection for SCB and connecting leads (can be provided by using PHPIOC, OC4PTOC, CVGAPC, T2WPDIFF/T3WPDIFF or HZPDIF functions)
2. Ground-fault protection for SCB and connecting leads (can be provided by using EFPIOC, EF4PTOC, CVGAPC, T2WPDIFF/T3WPDIFF or HZPDIF functions)
3. Current or Voltage based unbalance protection for SCB (can be provided by using EF4PTOC, OC4PTOC, CVGAPC or VDCPTOV functions)
4. Overload protection for SCB
5. Undercurrent protection for SCB
6. Reconnection inhibit protection for SCB
7. Restrike condition detection

CBPGAPC function can be used to provide the last four types of protection mentioned in the above list.

7.10.3 Setting guidelines

This setting example will be done for application as shown in figure 115:
From figure 115 it is possible to calculate the following rated fundamental frequency current for this SCB:

\[ I_r = \frac{1000 \cdot 200\,[MVAr]}{\sqrt{3} \cdot 400\,[kV]} = 289\,A \]

(Equation 69)

or on the secondary CT side:

\[ I_{r, \text{sec}} = \frac{289\,A}{500/1} = 0.578\,A \]

(Equation 70)

Note that the SCB rated current on the secondary CT side is important for secondary injection of the function.

The parameters for the Capacitor bank protection function CBPGAPC are set via the local HMI or PCM600. The following settings are done for this function:

**General Settings:**

- **Operation** = **Enabled**, to enable the function
- **IBase** = **289A**, Fundamental frequency SCB rated current in primary amperes. This value is used as a base value for pickup settings of all other features integrated in this function.

**Reconnection inhibit feature:**

- **OperationRecIn** = **Enabled**, to enable this feature
$I_{RecInhibit} = 10\% \text{ (of IBase)}$; Current level under which function will detect that SCB is disconnected from the power system

$t_{ReconnInhibit} = 300s$; Time period under which SCB shall discharge remaining residual voltage to less than 5%.

**Overcurrent feature:**

Operation $OC = Enabled$; to enable this feature

$PU_{51} = 135\% \text{ (of IBase)}$; Current level for overcurrent pickup. Selected value gives pickup recommended by international standards.

$t_{OC} = 30s$; Time delay for overcurrent trip

**Undercurrent feature:**

Operation $37 = Enabled$; to enable this feature

$PU_{37} = 70\% \text{ (of IBase)}$; Current level for undercurrent pickup

$t_{UC} = 5s$; Time delay for undercurrent trip

Undercurrent feature is blocked by operation of Reconnection inhibit feature.

**Reactive power overload feature:**

Operation $QOL = Enabled$; to enable this feature

$UP_{QOL} = 130\% \text{ (of SCB MVAr rating)}$; Reactive power level required for pickup. Selected value gives pickup recommended by international standards.

$t_{QOL} = 60s$; Time delay for reactive power overload trip

**Harmonic voltage overload feature:**

Operation $HOL = Enabled$; to enable this feature

Settings for definite time delay step

$HOL_{DT\_V} = 200\% \text{ (of SCB voltage rating)}$; Voltage level required for pickup

$t_{HOL\_DT} = 10s$; Definite time delay for harmonic overload trip

Settings for IDMT delay step

$PU_{HOL\_DT\_V} = 110\% \text{ (of SCB voltage rating)}$; Voltage level required for pickup of IDMT stage. Selected value gives pickup recommended by international standards.

$k_{HOL\_IDMT} = 1.0$; Time multiplier for IDMT stage. Selected value gives operate time in accordance with international standards

$t_{Max\_HOL\_IDMT} = 2000s$; Maximum time delay for IDMT stage for very low level of harmonic overload
\[ t_{\text{Min HOL IDMT}} = 0.1s; \] Minimum time delay for IDMT stage. Selected value gives operate time in accordance with international standards

### 7.10.3.1 Restrike detection

Opening of SCBs can be quite problematic for certain types of circuit breakers (CBs). Typically such problems are manifested as CB restrikes.

In simple words this means that the CB is not breaking the current at the first zero crossing after separation of the CB contacts. Instead current is re-ignited and only braked at consecutive current zero crossings. This condition is manifested as high current pulses at the moment of current re-ignition.

To detect this CB condition, the built in overcurrent feature can be used. Simply, any start of the overcurrent feature during breaker normal opening means a restrike. Therefore simple logic can be created in the Application Configuration tool to detect such CB behavior. Such CB condition can be just alarmed, and if required, the built in disturbance recorder can also be triggered.

To create this logic, a binary signal that the CB is going to be opened (but not trip command) shall be made available to the IED.
Section 8  Voltage protection

8.1  Two step undervoltage protection UV2PTUV (27)

8.1.1  Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Two step undervoltage protection</td>
<td>UV2PTUV</td>
<td>3U&lt;</td>
<td>27</td>
</tr>
</tbody>
</table>

8.1.2  Setting guidelines

All the voltage conditions in the system where UV2PTUV (27) performs its functions should be considered. The same also applies to the associated equipment, its voltage and time characteristic.

There is a very wide application area where general undervoltage functions are used. All voltage-related settings are made as a percentage of the global base value \( V_{\text{Base}} \), which normally is set to the primary rated voltage level (phase-to-phase) of the power system or the high voltage equipment under consideration.

The trip time setting for UV2PTUV (27) is normally not critical, since there must be enough time available for the main protection to clear short circuits and ground faults.

Some applications and related setting guidelines for the voltage level are described in the following sections.

8.1.2.1  Equipment protection, such as for motors and generators

The setting must be below the lowest occurring "normal" voltage and above the lowest acceptable voltage for the equipment.

8.1.2.2  Disconnected equipment detection

The setting must be below the lowest occurring "normal" voltage and above the highest occurring voltage, caused by inductive or capacitive coupling, when the equipment is disconnected.
8.1.2.3 **Power supply quality**

The setting must be below the lowest occurring "normal" voltage and above the lowest acceptable voltage, due to regulation, good practice or other agreements.

8.1.2.4 **Voltage instability mitigation**

This setting is very much dependent on the power system characteristics, and thorough studies have to be made to find the suitable levels.

8.1.2.5 **Backup protection for power system faults**

The setting must be below the lowest occurring "normal" voltage and above the highest occurring voltage during the fault conditions under consideration.

8.1.2.6 **Settings for two step undervoltage protection**

The following settings can be done for Two step undervoltage protection UV2PTUV (27):

*ConnType*: Sets whether the measurement shall be phase-to-ground fundamental value, phase-to-phase fundamental value, phase-to-ground RMS value or phase-to-phase RMS value.

*Operation*: *Disabled* or *Enabled*.

*VBase* (given in *GlobalBaseSel*): Base voltage phase-to-phase in primary kV. This voltage is used as reference for voltage setting. UV2PTUV (27) will operate if the voltage becomes lower than the set percentage of *VBase*. This setting is used when *ConnType* is set to *PhPh DFT* or *PhPh RMS*. Therefore, always set *VBase* as rated primary phase-to-phase voltage of the protected object. For more information, refer to the *Technical manual*.

The setting parameters described below are identical for the two steps (*n = 1 or 2*). Therefore, the setting parameters are described only once.

*Characteristic*: This parameter gives the type of time delay to be used. The setting can be *Definite time, Inverse Curve A, Inverse Curve B, Prog. inv. curve*. The selection is dependent on the protection application.

*OpModen*: This parameter describes how many of the three measured voltages should be below the set level to give operation for step *n*. The setting can be *1 out of 3, 2 out of 3 or 3 out of 3*. In most applications, it is sufficient that one phase voltage is low to give operation. If UV2PTUV (27) shall be insensitive for single phase-to-ground faults, *2 out of 3* can be chosen. In subtransmission and transmission networks the undervoltage function is mainly a system supervision function and *3 out of 3* is selected.

*Pickupn*: Set pickup undervoltage operation value for step *n*, given as % of the parameter *VBase*. The setting is highly dependent on the protection application. It is essential to consider the minimum voltage at non-faulted situations. Normally, this non-faulted voltage is larger than 90% of the nominal voltage.

*tn*: time delay of step *n*, given in s. This setting is dependent on the protection application. In many applications the protection function shall not directly trip when there is a short circuit or ground faults in the system. The time delay must be coordinated to the other short circuit protections.

*tResetn*: Reset time for step *n* if definite time delay is used, given in s. The default value is 25 ms.
**tnMin:** Minimum operation time for inverse time characteristic for step \( n \), given in s. When using inverse time characteristic for the undervoltage function during very low voltages can give a short operation time. This might lead to unselective tripping. By setting \( t1Min \) longer than the operation time for other protections, such unselective tripping can be avoided.

**ResetTypeCrvn:** This parameter for inverse time characteristic can be set to *Instantaneous, Frozen time, Linearly decreased*. The default setting is *Instantaneous*.

**tIResetn:** Reset time for step \( n \) if inverse time delay is used, given in s. The default value is 25 ms.

**TDn:** Time multiplier for inverse time characteristic. This parameter is used for coordination between different inverse time delayed undervoltage protections.

**ACrvn, BCrvn, CCrvn, DCrvn, PCrvn:** Parameters to create a programmable under voltage inverse time characteristic. Description of this can be found in the *Technical manual*.

**CrvSatn:** Tuning parameter that is used to compensate for the undesired discontinuity created when the denominator in the equation for the customer programmable curve is equal to zero. For more information, see the *Technical manual*.

**IntBlkSeln:** This parameter can be set to *Disabled, Block of trip, Block all*. In case of a low voltage the undervoltage function can be blocked. This function can be used to prevent function when the protected object is switched off. If the parameter is set *Block of trip or Block all* unwanted trip is prevented.

**IntBlkStValn:** Voltage level under which the blocking is activated set in % of \( V_{Base} \). This setting must be lower than the setting \( Pickupn \). As switch of shall be detected the setting can be very low, that is, about 10%.

**tBlkUVn:** Time delay to block the undervoltage step \( n \) when the voltage level is below \( IntBlkStValn \), given in s. It is important that this delay is shorter than the trip time delay of the undervoltage protection step.

### 8.2 Two step overvoltage protection OV2PTOV (59)

#### 8.2.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Two step overvoltage protection</td>
<td>OV2PTOV</td>
<td></td>
<td>59</td>
</tr>
</tbody>
</table>

#### 8.2.2 Application

Two step overvoltage protection OV2PTOV (59) is applicable in all situations, where reliable detection of high voltage is necessary. OV2PTOV (59) is used for supervision and detection of
abnormal conditions, which, in combination with other protection functions, increase the security of a complete protection system.

High overvoltage conditions are caused by abnormal situations in the power system. OV2PTOV (59) is applied to power system elements, such as generators, transformers, motors and power lines in order to detect high voltage conditions. OV2PTOV (59) is used in combination with low current signals, to identify a transmission line, open in the remote end. In addition to that, OV2PTOV (59) is also used to initiate voltage correction measures, like insertion of shunt reactors, to compensate for low load, and thereby decreasing the voltage. The function has a high measuring accuracy and hysteresis setting to allow applications to control reactive load.

OV2PTOV (59) is used to disconnect apparatuses, like electric motors, which will be damaged when subject to service under high voltage conditions. It deals with high voltage conditions at power system frequency, which can be caused by:

1. Different kinds of faults, where a too high voltage appears in a certain power system, like metallic connection to a higher voltage level (broken conductor falling down to a crossing overhead line, transformer flash over fault from the high voltage winding to the low voltage winding and so on).
2. Malfunctioning of a voltage regulator or wrong settings under manual control (symmetrical voltage decrease).
3. Low load compared to the reactive power generation (symmetrical voltage decrease).
4. Ground-faults in high impedance grounded systems causes, beside the high voltage in the neutral, high voltages in the two non-faulted phases, (unsymmetrical voltage increase).

OV2PTOV (59) prevents sensitive equipment from running under conditions that could cause their overheating or stress of insulation material, and, thus, shorten their life time expectancy. In many cases, it is a useful function in circuits for local or remote automation processes in the power system.

### 8.2.3 Setting guidelines

The parameters for Two step overvoltage protection (OV2PTOV, 59) are set via the local HMI or PCM600.

All the voltage conditions in the system where OV2PTOV (59) performs its functions should be considered. The same also applies to the associated equipment, its voltage and time characteristic.

There are wide applications where general overvoltage functions are used. All voltage related settings are made as a percentage of a settable base primary voltage, which is normally set to the nominal voltage level (phase-to-phase) of the power system or the high voltage equipment under consideration.

The time delay for the OV2PTOV (59) can sometimes be critical and related to the size of the overvoltage - a power system or a high voltage component can withstand smaller overvoltages for some time, but in case of large overvoltages the related equipment should be disconnected more rapidly.

Some applications and related setting guidelines for the voltage level are given below:

The hysteresis is for overvoltage functions very important to prevent that a transient voltage over set level is not “sealed-in” due to a high hysteresis. Typical values should be ≤ 0.5%.
8.2.3.1 Equipment protection, such as for motors, generators, reactors and transformers

High voltage will cause overexcitation of the core and deteriorate the winding insulation. The setting has to be well above the highest occurring "normal" voltage and well below the highest acceptable voltage for the equipment.

8.2.3.2 Equipment protection, capacitors

High voltage will deteriorate the dielectricum and the insulation. The setting has to be well above the highest occurring "normal" voltage and well below the highest acceptable voltage for the capacitor.

8.2.3.3 Power supply quality

The setting has to be well above the highest occurring "normal" voltage and below the highest acceptable voltage, due to regulation, good practice or other agreements.

8.2.3.4 High impedance grounded systems

In high impedance grounded systems, ground-faults cause a voltage increase in the non-faulty phases. Two step overvoltage protection (OV2PTOV, 59) is used to detect such faults. The setting must be above the highest occurring "normal" voltage and below the lowest occurring voltage during faults. A metallic single-phase ground-fault causes the non-faulted phase voltages to increase a factor of \(\sqrt{3}\).

8.2.3.5 The following settings can be done for the two step overvoltage protection

ConnType: Sets whether the measurement shall be phase-to-ground fundamental value, phase-to-phase fundamental value, phase-to-ground RMS value or phase-to-phase RMS value.

Operation: Disabled/Enabled.

\(V_{\text{Base}}\) (given in GlobalBaseSel): Base voltage phase to phase in primary kV. This voltage is used as reference for voltage setting. OV2PTOV (59) measures selectively phase-to-ground voltages, or phase-to-phase voltage chosen by the setting ConnType. The function will operate if the voltage gets lower than the set percentage of \(V_{\text{Base}}\). When ConnType is set to PhN DFT or PhN RMS then the IED automatically divides set value for \(V_{\text{Base}}\) by \(\sqrt{3}\). When ConnType is set to PhPh DFT or PhPh RMS then set value for \(V_{\text{Base}}\) is used. Therefore, always set \(V_{\text{Base}}\) as rated primary phase-to-phase ground voltage of the protected object. If phase to neutral (PhN) measurement is selected as setting, the operation of phase-to-ground over voltage is automatically divided by \(\sqrt{3}\). This means operation for phase-to-ground voltage over:

\[
V > \left(\%\right) \cdot V_{\text{Base}}(kV)/\sqrt{3}
\]

(Equation 71)

and operation for phase-to-phase voltage over:

\[
V_{\text{pickup}} > \left(\%\right) \cdot V_{\text{Base}}(kV)
\]

(Equation 72)
The below described setting parameters are identical for the two steps \((n = 1 \text{ or } 2)\). Therefore the setting parameters are described only once.

**Characteristic:** This parameter gives the type of time delay to be used. The setting can be Definite time, Inverse Curve A, Inverse Curve B, Inverse Curve C or I/Prog. inv. curve. The choice is highly dependent of the protection application.

**OpMode:** This parameter describes how many of the three measured voltages that should be above the set level to give operation. The setting can be 1 out of 3, 2 out of 3, 3 out of 3. In most applications it is sufficient that one phase voltage is high to give operation. If the function shall be insensitive for single phase-to-ground faults 1 out of 3 can be chosen, because the voltage will normally rise in the non-faulted phases at single phase-to-ground faults. In subtransmission and transmission networks the UV function is mainly a system supervision function and 3 out of 3 is selected.

**Pickup:** Set pickup overvoltage operation value for step \(n\), given as \(\% \text{ of } V_{\text{Base}}\). The setting is highly dependent of the protection application. Here it is essential to consider the maximum voltage at non-faulted situations. Normally this voltage is less than 110\% of nominal voltage.

**\(t_n\):** time delay of step \(n\), given in \(s\). The setting is highly dependent of the protection application. In many applications the protection function is used to prevent damages to the protected object. The speed might be important for example in case of protection of transformer that might be overexcited. The time delay must be co-ordinated with other automated actions in the system.

**\(t_{\text{Reset}n}\):** Reset time for step \(n\) if definite time delay is used, given in \(s\). The default value is 25 ms.

**\(t_{\text{nMin}}\):** Minimum operation time for inverse time characteristic for step \(n\), given in \(s\). For very high voltages the overvoltage function, using inverse time characteristic, can give very short operation time. This might lead to unselective trip. By setting \(t_{\text{1Min}}\) longer than the operation time for other protections such unselective tripping can be avoided.

**ResetTypeCrv:** This parameter for inverse time characteristic can be set: Instantaneous, Frozen time, Linearly decreased. The default setting is Instantaneous.

**\(t_{\text{IReset}n}\):** Reset time for step \(n\) if inverse time delay is used, given in \(s\). The default value is 25 ms.

**\(T_{\text{Dn}}\):** Time multiplier for inverse time characteristic. This parameter is used for co-ordination between different inverse time delayed undervoltage protections.

**\(ACrvn, BCrvn, CCrvn, DCrvn, PCrvn\):** Parameters to set to create programmable under voltage inverse time characteristic. Description of this can be found in the technical reference manual.

**\(CrvSatn\):** When the denominator in the expression of the programmable curve is equal to zero the time delay will be infinity. There will be an undesired discontinuity. Therefore a tuning parameter \(CrvSatn\) is set to compensate for this phenomenon. In the voltage interval \(\text{Pickup} > (1.0 + \frac{CrvSatn}{100})\) the used voltage will be: \(\text{Pickup} \cdot (1.0 + \frac{CrvSatn}{100})\). If the programmable curve is used, this parameter must be calculated so that:

\[
B \cdot \frac{CrvSatn}{100} - C > 0
\]

(Equation 73)

**HystAbs:** Absolute hysteresis set in \(\% \text{ of } V_{\text{Base}}\). The setting of this parameter is highly dependent of the application. If the function is used as control for automatic switching of reactive
compensation devices the hysteresis must be set smaller than the voltage change after switching of the compensation device.

8.3 Two step residual overvoltage protection ROV2PTOV (59N)

8.3.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
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<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Two step residual overvoltage protection</td>
<td>ROV2PTOV</td>
<td>2(U0&gt;)</td>
<td>59N</td>
</tr>
</tbody>
</table>

8.3.2 Application

Two step residual overvoltage protection ROV2PTOV (59N) is primarily used in high impedance grounded distribution networks, mainly as a backup for the primary ground fault protection of the feeders and the transformer. To increase the security for different ground fault related functions, the residual overvoltage signal can be used as a release signal. The residual voltage can be measured either at the transformer neutral or from a voltage transformer open delta connection. The residual voltage can also be calculated internally, based on the measurement of the three phase-to-ground voltages.

In high impedance grounded systems the residual voltage will increase in case of any fault connected to ground. Depending on the type of fault and fault resistance the residual voltage will reach different values. The highest residual voltage, equal to three times the phase-to-ground voltage, is achieved for a single phase-to-ground fault. The residual voltage increases approximately to the same level in the whole system and does not provide any guidance in finding the faulted component. Therefore, ROV2PTOV (59N) is often used as a backup protection or as a release signal for the feeder ground fault protection.

8.3.3 Setting guidelines

All the voltage conditions in the system where ROV2PTOV (59N) performs its functions should be considered. The same also applies to the associated equipment, its voltage withstand capability and time characteristic.

All voltage-related settings are made as a percentage of a settable base voltage, which shall be set to the primary nominal voltage (phase-phase) level of the power system or the high-voltage equipment under consideration.

The time delay for ROV2PTOV (59N) is seldom critical, since residual voltage is related to ground faults in a high-impedance grounded system, and enough time must normally be given for the primary protection to clear the fault. In some more specific situations, where the residual overvoltage protection is used to protect some specific equipment, the time delay is shorter.

Some applications and related setting guidelines for the residual voltage level are given below.
8.3.3.1 Equipment protection, such as for motors, generators, reactors and transformers

Equipment protection for transformers

High residual voltage indicates ground fault in the system, perhaps in the component to which two step residual overvoltage protection (ROV2PTOV, 59N) is connected. For selectivity reasons to the primary protection for the faulted device, ROV2PTOV (59N) must trip the component with some time delay. The setting must be above the highest occurring "normal" residual voltage and below the highest acceptable residual voltage for the equipment.

8.3.3.2 Equipment protection, capacitors

High voltage will deteriorate the dielectric and the insulation. Two step residual overvoltage protection (ROV2PTOV, 59N) has to be connected to a neutral or open delta winding. The setting must be above the highest occurring "normal" residual voltage and below the highest acceptable residual voltage for the capacitor.

8.3.3.3 Power supply quality

The setting must be above the highest occurring "normal" residual voltage and below the highest acceptable residual voltage, due to regulation, good practice or other agreements.

8.3.3.4 High impedance grounded systems

In high impedance grounded systems, ground faults cause a neutral voltage in the feeding transformer neutral. Two step residual overvoltage protection ROV2PTOV (59N) is used to trip the transformer, as a backup protection for the feeder ground fault protection, and as a backup for the transformer primary ground fault protection. The setting must be above the highest occurring "normal" residual voltage, and below the lowest occurring residual voltage during the faults under consideration. A metallic single-phase ground fault causes a transformer neutral to reach a voltage equal to the nominal phase-to-ground voltage.

The voltage transformers measuring the phase-to-ground voltages measure zero voltage in the faulty phase. The two healthy phases will measure full phase-to-phase voltage, as the faulty phase will be connected to ground. The residual overvoltage will be three times the phase-to-ground voltage. See figure 116.
8.3.3.5 **Direct grounded system**

In direct grounded systems, an ground fault on one phase is indicated by voltage collapse in that phase. The other healthy phase will still have normal phase-to-ground voltage. The residual sum will have the same value as the remaining phase-to-ground voltage, which is shown in Figure 117.

**Figure 117: Ground fault in Direct grounded system**

8.3.3.6 **Settings for two step residual overvoltage protection**

*Operation: Disabled or Enabled*
VBase (given in GlobalBaseSel) is used as voltage reference for the set pickup values. The voltage can be fed to the IED in different ways:

1. The IED is fed from a normal voltage transformer group where the residual voltage is calculated internally from the phase-to-ground voltages within the protection. The setting of the analogue input is given as VBase=Vph-ph.
2. The IED is fed from a broken delta connection normal voltage transformer group. In an open delta connection the protection is fed by the voltage 3V0 (single input). Section Analog inputs in the Application manual explains how the analog input needs to be set.
3. The IED is fed from a single voltage transformer connected to the neutral point of a power transformer in the power system. In this connection the protection is fed by the voltage VN=V0 (single input). Section Analog inputs in the Application manual explains how the analog input needs to be set.

ROV2PTOV (59N) will measure the residual voltage corresponding to the nominal phase-to-ground voltage for a high-impedance grounded system. The measurement will be based on the neutral voltage displacement.

The setting parameters described below are identical for the two steps (n = step 1 and 2). Therefore the setting parameters are described only once.

**OperationStepn**: This is to enable/disable operation of step n.

**Characteristicn**: Selected inverse time characteristic for step n. This parameter gives the type of time delay to be used. The setting can be, *Definite time* or *Inverse curve A* or *Inverse curve B* or *Inverse curve C* or *Prog. inv. curve*. The choice is highly dependent of the protection application.

**Pickupn**: Set operate overvoltage operation value for step n, given as % of residual voltage corresponding to VBase:

\[
V > \left( \% \right) \cdot V_{\text{Base}}(kV) / \sqrt{3}
\]

(Equation 74)

The setting depends on the required sensitivity of the protection and the type of system grounding. In non-effectively grounded systems, the residual voltage cannot be higher than three times the rated phase-to-ground voltage, which should correspond to 100%.

In effectively grounded systems, this value depends on the ratio Z0/Z1. The required setting to detect high resistive ground faults must be based on network calculations.

**tn**: time delay of step n, given in s. The setting is highly dependent on the protection application. In many applications, the protection function has the task to prevent damage to the protected object. The speed might be important, for example, in the case of the protection of a transformer that might be overexcited. The time delay must be co-ordinated with other automated actions in the system.

**tResetn**: Reset time for step n if definite time delay is used, given in s. The default value is 25 ms.

**tnMin**: Minimum operation time for inverse time characteristic for step n, given in s. For very high voltages the overvoltage function, using inverse time characteristic, can give very short operation time. This might lead to unselective trip. By setting tMin longer than the operation time for other protections such unselective tripping can be avoided.

**ResetTypeCrvn**: Set reset type curve for step n. This parameter can be set: *Instantaneous*, *Frozen time*, *Linearly decreased*. The default setting is *Instantaneous*. 
**tlResetn**: Reset time for step $n$ if inverse time delay is used, given in s. The default value is 25 ms.

**TDn**: Time multiplier for inverse time characteristic. This parameter is used for co-ordination between different inverse time delayed undervoltage protections.

**ACrvn, BCrvn, CCrvn, DCrvn, PCrvn**: Parameters for step $n$, to set to create programmable undervoltage inverse time characteristic. Description of this can be found in the technical reference manual.

**CrvSatn**: Tuning parameter for step $n$. When the denominator in the expression of the programmable curve is equal to zero, the time delay will be infinite. There will be an undesired discontinuity. Therefore, a tuning parameter $CrvSatn$ is set to compensate for this phenomenon. In the voltage interval $Pickup > up to Pickup > (1.0 + CrvSatn/100)$ the used voltage will be: $Pickup > (1.0 + CrvSatn/100)$. If the programmable curve is used this parameter must be calculated so that:

$$B \cdot \frac{CrvSatn}{100} - C > 0$$

(Equation 75)

**HystAbsn**: Absolute hysteresis for step $n$, set in % of $V_{Base}$. The setting of this parameter is highly dependent of the application. The hysteresis is used to avoid oscillations of the PICKUP output signal. This signal resets when the measured voltage drops below the setting level and leaves the hysteresis area. Make sure that the set value for parameter $HystAbsn$ is somewhat smaller than the set pickup value. Otherwise there is a risk that step $n$ will not reset properly.

### 8.4 Voltage differential protection VDCPTOV (60)

#### 8.4.1 Identification

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<thead>
<tr>
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<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voltage differential protection</td>
<td>VDCPTOV</td>
<td>-</td>
<td>60</td>
</tr>
</tbody>
</table>

#### 8.4.2 Application

The Voltage differential protection VDCPTOV (60) functions can be used in some different applications.

- Voltage unbalance protection for capacitor banks. The voltage on the bus is supervised with the voltage in the capacitor bank, phase- by phase. Difference indicates a fault, either short-circuited or open element in the capacitor bank. It is mainly used on elements with external fuses but can also be used on elements with internal fuses instead of a current unbalance protection measuring the current between the neutrals of two half’s of the capacitor bank. The function requires voltage transformers in all phases of the capacitor bank. Figure 118 shows some different alternative connections of this function.
VDCPTOV (60) function has a block input (BLOCK) where a fuse failure supervision (or MCB tripped) can be connected to prevent problems if one fuse in the capacitor bank voltage transformer set has opened and not the other (capacitor voltage is connected to input V2). It will also ensure that a fuse failure alarm is given instead of a Undervoltage or Differential voltage alarm and/or tripping.

**8.4.3 Setting guidelines**

The parameters for the voltage differential function are set via the local HMI or PCM600.

The following settings are done for the voltage differential function.

*Operation*: Off/On

*GlobalBaseSel*: Selects the global base value group used by the function to define *I*<sub>Base</sub>, *V*<sub>Base</sub> and *S*<sub>Base</sub>. Note that this function will only use *I*<sub>Base</sub> value.

*BlkDiffAtVLow*: The setting is to block the function when the voltages in the phases are low.

*RFLx*: Is the setting of the voltage ratio compensation factor where possible differences between the voltages is compensated for. The differences can be due to different voltage transformer ratios, different voltage levels e.g. the voltage measurement inside the capacitor bank can have a different voltage level but the difference can also e.g. be used by voltage drop in the secondary circuits. The setting is normally done at site by evaluating the differential voltage achieved as a
service value for each phase. The factor is defined as \( V2 \cdot RFLx \) and shall be equal to the \( V1 \) voltage. Each phase has its own ratio factor.

\( VDTrip \): The voltage differential level required for tripping is set with this parameter. For application on capacitor banks the setting will depend of the capacitor bank voltage and the number of elements per phase in series and parallel. Capacitor banks must be tripped before excessive voltage occurs on the healthy capacitor elements. The setting values required are normally given by the capacitor bank supplier. For other applications it has to be decided case by case. For fuse supervision normally only the alarm level is used.

\( tTrip \): The time delay for tripping is set by this parameter. Normally, the delay does not need to be so short in capacitor bank applications as there is no fault requiring urgent tripping.

\( tReset \): The time delay for reset of tripping level element is set by this parameter. Normally, it can be set to a short delay as faults are permanent when they occur.

For the advanced users following parameters are also available for setting. Default values are here expected to be acceptable.

\( V1Low \): The setting of the undervoltage level for the first voltage input is decided by this parameter. The proposed default setting is 70%.

\( V2Low \): The setting of the undervoltage level for the second voltage input is decided by this parameter. The proposed default setting is 70%.

\( tBlock \): The time delay for blocking of the function at detected undervoltages is set by this parameter.

\( VDAAlarm \): The voltage differential level required for alarm is set with this parameter. For application on capacitor banks the setting will depend of the capacitor bank voltage and the number of elements per phase in series and parallel. Normally values required are given by capacitor bank supplier.

For fuse supervision normally only this alarm level is used and a suitable voltage level is 3-5% if the ratio correction factor has been properly evaluated during commissioning.

For other applications it has to be decided case by case.

\( tAlarm \): The time delay for alarm is set by this parameter. Normally, few seconds delay can be used on capacitor banks alarm. For fuse failure supervision (SDDRFUF) the alarm delay can be set to zero.

### 8.5 Loss of voltage check LOVPTUV (27)

#### 8.5.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Loss of voltage check</td>
<td>LOVPTUV</td>
<td>-</td>
<td>27</td>
</tr>
</tbody>
</table>
8.5.2 Application

The trip of the circuit breaker at a prolonged loss of voltage at all the three phases is normally used in automatic restoration systems to facilitate the system restoration after a major blackout. Loss of voltage check (LOVPTUV, 27) generates a TRIP signal only if the voltage in all the three phases is low for more than the set time. If the trip to the circuit breaker is not required, LOVPTUV (27) is used for signalization only through an output contact or through the event recording function.

8.5.3 Setting guidelines

Loss of voltage check (LOVPTUV, 27) is in principle independent of the protection functions. It requires to be set to open the circuit breaker in order to allow a simple system restoration following a main voltage loss of a big part of the network and only when the voltage is lost with breakers still closed.

All settings are in primary values or per unit. Set operate level per phase to typically 70% of the global parameter $V_{Base}$ level. Set the time delay $t_{Trip}$=5-20 seconds.

8.5.3.1 Advanced users settings

For advanced users the following parameters need also to be set. Set the length of the trip pulse to typical $t_{Pulse}$=0.15 sec. Set the blocking time $t_{Block}$ to block Loss of voltage check (LOVPTUV, 27), if some but not all voltage are low, to typical 5.0 seconds and set the time delay for enabling the function after restoration $t_{Restore}$ to 3 - 40 seconds.
Section 9  
**Frequency protection**

### 9.1 Underfrequency protection SAPTUF (81)

#### 9.1.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Underfrequency protection</td>
<td>SAPTUF</td>
<td></td>
<td>81</td>
</tr>
</tbody>
</table>

#### 9.1.2 Application

Underfrequency protection SAPTUF (81) is applicable in all situations, where reliable detection of low fundamental power system frequency is needed. The power system frequency, and the rate of change of frequency, is a measure of the unbalance between the actual generation and the load demand. Low fundamental frequency in a power system indicates that the available generation is too low to fully supply the power demanded by the load connected to the power grid. SAPTUF (81) detects such situations and provides an output signal, suitable for load shedding, generator boosting, HVDC-set-point change, gas turbine start up and so on. Sometimes shunt reactors are automatically switched in due to low frequency, in order to reduce the power system voltage and hence also reduce the voltage dependent part of the load.

SAPTUF (81) is very sensitive and accurate and is used to alert operators that frequency has slightly deviated from the set-point, and that manual actions might be enough. The underfrequency signal is also used for overexcitation detection. This is especially important for generator step-up transformers, which might be connected to the generator but disconnected from the grid, during a roll-out sequence. If the generator is still energized, the system will experience overexcitation, due to the low frequency.

#### 9.1.3 Setting guidelines

All the frequency and voltage magnitude conditions in the system where SAPTUF (81) performs its functions should be considered. The same also applies to the associated equipment, its frequency and time characteristic.

There are two specific application areas for SAPTUF (81):

1. to protect equipment against damage due to low frequency, such as generators, transformers, and motors. Overexcitation is also related to low frequency
2. to protect a power system, or a part of a power system, against breakdown, by shedding load, in generation deficit situations.
The under frequency pickup value is set in Hz. All voltage magnitude related settings are made as a percentage of a global base voltage parameter. The UBase value should be set as a primary phase-to-phase value.

Some applications and related setting guidelines for the frequency level are given below:

**Equipment protection, such as for motors and generators**

The setting has to be well below the lowest occurring "normal" frequency and well above the lowest acceptable frequency for the equipment.

**Power system protection, by load shedding**

The setting has to be well below the lowest occurring "normal" frequency and well above the lowest acceptable frequency for power stations, or sensitive loads. The setting level, the number of levels and the distance between two levels (in time and/or in frequency) depends very much on the characteristics of the power system under consideration. The size of the "largest loss of production" compared to "the size of the power system" is a critical parameter. In large systems, the load shedding can be set at a fairly high frequency level, and the time delay is normally not critical. In smaller systems the frequency pickup level has to be set at a lower value, and the time delay must be rather short.

The voltage related time delay is used for load shedding. The settings of SAPTUF (81) could be the same all over the power system. The load shedding is then performed firstly in areas with low voltage magnitude, which normally are the most problematic areas, where the load shedding also is most efficient.

### 9.2 Overfrequency protection SAPTOF (81)

#### 9.2.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Overfrequency protection</td>
<td>SAPTOF</td>
<td></td>
<td>81</td>
</tr>
</tbody>
</table>

#### 9.2.2 Application

Overfrequency protection function SAPTOF (81) is applicable in all situations, where reliable detection of high fundamental power system frequency is needed. The power system frequency, and rate of change of frequency, is a measure of the unbalance between the actual generation and the load demand. High fundamental frequency in a power system indicates that the available generation is too large compared to the power demanded by the load connected to the power grid. SAPTOF (81) detects such situations and provides an output signal, suitable for generator shedding, HVDC-set-point change and so on. SAPTOF (81) is very sensitive and accurate and can also be used to alert operators that frequency has slightly deviated from the set-point, and that manual actions might be enough.
9.2.3 Setting guidelines

All the frequency and voltage magnitude conditions in the system where SAPTOF (81) performs its functions must be considered. The same also applies to the associated equipment, its frequency and time characteristic.

There are two application areas for SAPTOF (81):

1. to protect equipment against damage due to high frequency, such as generators, and motors
2. to protect a power system, or a part of a power system, against breakdown, by shedding generation, in over production situations.

The overfrequency pickup value is set in Hz. All voltage magnitude related settings are made as a percentage of a settable global base voltage parameter $V_{\text{Base}}$. The $U_{\text{Base}}$ value should be set as a primary phase-to-phase value.

Some applications and related setting guidelines for the frequency level are given below:

**Equipment protection, such as for motors and generators**

The setting has to be well above the highest occurring “normal” frequency and well below the highest acceptable frequency for the equipment.

**Power system protection, by generator shedding**

The setting must be above the highest occurring "normal" frequency and below the highest acceptable frequency for power stations, or sensitive loads. The setting level, the number of levels and the distance between two levels (in time and/or in frequency) depend very much on the characteristics of the power system under consideration. The size of the "largest loss of load" compared to "the size of the power system" is a critical parameter. In large systems, the generator shedding can be set at a fairly low frequency level, and the time delay is normally not critical. In smaller systems the frequency PICKUP level has to be set at a higher value, and the time delay must be rather short.

9.3 Rate-of-change of frequency protection SAPFRC (81)

9.3.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rate-of-change of frequency protection</td>
<td>SAPFRC</td>
<td>$\frac{df}{dt}$</td>
<td>81</td>
</tr>
</tbody>
</table>

9.3.2 Application

Rate-of-change of frequency protection (SAPFRC, 81) is applicable in all situations, where reliable detection of change of the fundamental power system voltage frequency is needed. SAPFRC (81)
can be used both for increasing frequency and for decreasing frequency. SAPFRC (81) provides an output signal, suitable for load shedding or generator shedding, generator boosting, HVDC-set-point change, gas turbine start up and so on. Very often SAPFRC (81) is used in combination with a low frequency signal, especially in smaller power systems, where loss of a fairly large generator will require quick remedial actions to secure the power system integrity. In such situations load shedding actions are required at a rather high frequency level, but in combination with a large negative rate-of-change of frequency the underfrequency protection can be used at a rather high setting.

9.3.3 Setting guidelines

The parameters for Rate-of-change frequency protection SAPFRC (81) are set via the local HMI or through the Protection and Control Manager (PCM600).

All the frequency and voltage magnitude conditions in the system where SAPFRC (81) performs its functions should be considered. The same also applies to the associated equipment, its frequency and time characteristic.

There are two application areas for SAPFRC (81):

1. to protect equipment against damage due to high or too low frequency, such as generators, transformers, and motors
2. to protect a power system, or a part of a power system, against breakdown by shedding load or generation, in situations where load and generation are not in balance.

SAPFRC (81) is normally used together with an overfrequency or underfrequency function, in small power systems, where a single event can cause a large imbalance between load and generation. In such situations load or generation shedding has to take place very quickly, and there might not be enough time to wait until the frequency signal has reached an abnormal value. Actions are therefore taken at a frequency level closer to the primary nominal level, if the rate-of-change frequency is large (with respect to sign).

The pickup value for SAPFRC (81) is set in Hz/s. All voltage magnitude related settings are made as a percentage of a settable base voltage, which normally is set to the primary nominal voltage level (phase-phase) of the power system or the high voltage equipment under consideration.

SAPFRC (81) is not instantaneous, since the function needs some time to supply a stable value. It is recommended to have a time delay long enough to take care of signal noise. However, the time, rate-of-change frequency and frequency steps between different actions might be critical, and sometimes a rather short operation time is required, for example, down to 70 ms.

Smaller industrial systems might experience rate-of-change frequency as large as 5 Hz/s, due to a single event. Even large power systems may form small islands with a large imbalance between load and generation, when severe faults (or combinations of faults) are cleared - up to 3 Hz/s has been experienced when a small island was isolated from a large system. For more "normal" severe disturbances in large power systems, rate-of-change of frequency is much less, most often just a fraction of 1.0 Hz/s.
Section 10    Multipurpose protection

10.1    General current and voltage protection CVGAPC

10.1.1    Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>General current and voltage protection</td>
<td>CVGAPC</td>
<td>2(I&gt;/U&lt;)</td>
<td>-</td>
</tr>
</tbody>
</table>

10.1.2    Application

A breakdown of the insulation between phase conductors or a phase conductor and ground results in a short circuit or a ground fault respectively. Such faults can result in large fault currents and may cause severe damage to the power system primary equipment. Depending on the magnitude and type of the fault different overcurrent protections, based on measurement of phase, ground or sequence current components can be used to detect these faults. Additionally it is sometimes required that these overcurrent protections shall be directional and/or voltage controlled/restrained.

The over/under voltage protection is applied on power system elements, such as generators, transformers, motors and power lines in order to detect abnormal voltage conditions. Depending on the type of voltage deviation and type of power system abnormal condition different over/under voltage protections based on measurement of phase-to-ground, phase-to-phase, residual- or sequence- voltage components can be used to detect and operate for such incident.

The IED can be provided with multiple General current and voltage protection (CVGAPC) protection modules. The function is always connected to three-phase current and three-phase voltage input in the configuration tool, but it will always measure only one current and one voltage quantity selected by the end user in the setting tool.

Each CVGAPC function module has got four independent protection elements built into it.

1. Two overcurrent steps with the following built-in features:
- Definite time delay or Inverse Time Overcurrent (TOC/IDMT) delay for both steps
- Second harmonic supervision is available in order to only allow operation of the overcurrent stage(s) if the content of the second harmonic in the measured current is lower than pre-set level
- Directional supervision is available in order to only allow operation of the overcurrent stage(s) if the fault location is in the pre-set direction (Forward or Reverse). Its behavior during low-level polarizing voltage is settable (Non-Directional, Block, Memory)
- Voltage restrained/controlled feature is available in order to modify the pick-up level of the overcurrent stage(s) in proportion to the magnitude of the measured voltage
- Current restrained feature is available in order to only allow operation of the overcurrent stage(s) if the measured current quantity is bigger than the set percentage of the current restrain quantity.

2. Two undercurrent steps with the following built-in features:
   - Definite time delay for both steps

3. Two overvoltage steps with the following built-in features
   - Definite time delay or Inverse Time Overcurrent TOC/IDMT delay for both steps

4. Two undervoltage steps with the following built-in features
   - Definite time delay or Inverse Time Overcurrent TOC/IDMT delay for both steps

All these four protection elements within one general protection function works independently from each other and they can be individually enabled or disabled. However, note that all these four protection elements measure one selected current quantity and one selected voltage quantity (see table 38 and table 39). It is possible to simultaneously use all four protection elements and their individual stages. Sometimes, it is necessary to provide interaction between two or more protection elements/stages within one CVGAPC function by appropriate IED configuration to obtain desired application functionality.

### 10.1.2.1 Current and voltage selection for CVGAPC function

CVGAPC function is always connected to three-phase current and three-phase voltage input in the configuration tool, but it will always measure only the single current and the single voltage quantity selected by the end user in the setting tool.

The user can select a current input, by a setting parameter CurrentInput, to measure one of the current quantities shown in table 38.

#### Table 38: Available selection for current quantity within CVGAPC function

<table>
<thead>
<tr>
<th>Set value for parameter &quot;CurrentInput&quot;</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 PhaseA</td>
<td>CVGAPC function will measure the phase A current phasor</td>
</tr>
<tr>
<td>2 PhaseB</td>
<td>CVGAPC function will measure the phase B current phasor</td>
</tr>
<tr>
<td>3 PhaseC</td>
<td>CVGAPC function will measure the phase C current phasor</td>
</tr>
<tr>
<td>4 PosSeq</td>
<td>CVGAPC function will measure internally calculated positive sequence current phasor</td>
</tr>
<tr>
<td>5 NegSeq</td>
<td>CVGAPC function will measure internally calculated negative sequence current phasor</td>
</tr>
<tr>
<td>6 3 · ZeroSeq</td>
<td>CVGAPC function will measure internally calculated zero sequence current phasor multiplied by factor 3</td>
</tr>
</tbody>
</table>

Table continues on next page
Set value for parameter “CurrentInput” | Comment
---|---
7  MaxPh | CVGAPC function will measure current phasor of the phase with maximum magnitude
8  MinPh | CVGAPC function will measure current phasor of the phase with minimum magnitude
9  UnbalancePh | CVGAPC function will measure magnitude of unbalance current, which is internally calculated as the algebraic magnitude difference between the current phasor of the phase with maximum magnitude and current phasor of the phase with minimum magnitude. Phase angle will be set to 0° all the time
10 PhaseA-PhaseB | CVGAPC function will measure the current phasor internally calculated as the vector difference between the phase A current phasor and phase B current phasor (VA-VB)
11 PhaseB-PhaseC | CVGAPC function will measure the current phasor internally calculated as the vector difference between the phase B current phasor and phase C current phasor (VB-VC)
12 PhaseC-PhaseA | CVGAPC function will measure the current phasor internally calculated as the vector difference between the phase C current phasor and phase A current phasor (VC-VA)
13 MaxPh-Ph | CVGAPC function will measure ph-ph current phasor with the maximum magnitude
14 MinPh-Ph | CVGAPC function will measure ph-ph current phasor with the minimum magnitude
15 UnbalancePh-Ph | CVGAPC function will measure magnitude of unbalance current, which is internally calculated as the algebraic magnitude difference between the ph-ph current phasor with maximum magnitude and ph-ph current phasor with minimum magnitude. Phase angle will be set to 0° all the time

The user can select a voltage input, by a setting parameter VoltageInput, to measure one of the voltage quantities shown in table 39.

Table 39: Available selection for voltage quantity within CVGAPC function

<table>
<thead>
<tr>
<th>Set value for parameter “VoltageInput”</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 PhaseA</td>
<td>CVGAPC function will measure the phase A voltage phasor</td>
</tr>
<tr>
<td>2 PhaseB</td>
<td>CVGAPC function will measure the phase B voltage phasor</td>
</tr>
<tr>
<td>3 PhaseC</td>
<td>CVGAPC function will measure the phase C voltage phasor</td>
</tr>
<tr>
<td>4 PosSeq</td>
<td>CVGAPC function will measure internally calculated positive sequence voltage phasor</td>
</tr>
<tr>
<td>5 -NegSeq</td>
<td>CVGAPC function will measure internally calculated negative sequence voltage phasor. This voltage phasor will be intentionally rotated for 180° in order to enable easier settings for the directional feature when used.</td>
</tr>
<tr>
<td>6 -3*ZeroSeq</td>
<td>CVGAPC function will measure internally calculated zero sequence voltage phasor multiplied by factor 3. This voltage phasor will be intentionally rotated for 180° in order to enable easier settings for the directional feature when used.</td>
</tr>
<tr>
<td>7 MaxPh</td>
<td>CVGAPC function will measure voltage phasor of the phase with maximum magnitude</td>
</tr>
<tr>
<td>8 MinPh</td>
<td>CVGAPC function will measure voltage phasor of the phase with minimum magnitude</td>
</tr>
</tbody>
</table>

Table continues on next page
Set value for parameter "VoltageInput" | Comment
---|---
9 UnbalancePh | CVGAPC function will measure magnitude of unbalance voltage, which is internally calculated as the algebraic magnitude difference between the voltage phasor of the phase with maximum magnitude and voltage phasor of the phase with minimum magnitude. Phase angle will be set to 0° all the time
10 PhaseA-PhaseB | CVGAPC function will measure the voltage phasor internally calculated as the vector difference between the phase A voltage phasor and phase B voltage phasor (VA-VB)
11 PhaseB-PhaseC | CVGAPC function will measure the voltage phasor internally calculated as the vector difference between the phase B voltage phasor and phase C voltage phasor (VB-VC)
12 PhaseC-PhaseC | CVGAPC function will measure the voltage phasor internally calculated as the vector difference between the phase C voltage phasor and phase A voltage phasor (VC-VA)
13 MaxPh-Ph | CVGAPC function will measure ph-ph voltage phasor with the maximum magnitude
14 MinPh-Ph | CVGAPC function will measure ph-ph voltage phasor with the minimum magnitude
15 UnbalancePh-Ph | CVGAPC function will measure magnitude of unbalance voltage, which is internally calculated as the algebraic magnitude difference between the ph-ph voltage phasor with maximum magnitude and ph-ph voltage phasor with minimum magnitude. Phase angle will be set to 0° all the time

Note that the voltage selection from table 39 is always applicable regardless the actual external VT connections. The three-phase VT inputs can be connected to IED as either three phase-to-ground voltages, VA, VB and VC or three phase-to-phase voltages $V_{AB}$, $V_{BC}$ and $V_{CA}$. This information about actual VT connection is entered as a setting parameter for the pre-processing block, which will then be taken care automatically.

### 10.1.2.2 Base quantities for CVGAPC function

The parameter settings for the base quantities, which represent the base (100%) for pickup levels of all measuring stages shall be entered as setting parameters for every CVGAPC function.

Base current shall be entered as:

1. rated phase-to-earth current of the protected object in primary amperes, when the measured Current Quantity is selected from 1 to 9, as shown in table 38.
2. rated phase current of the protected object in primary amperes multiplied by $\sqrt{3}$ (1.732 x Iphase), when the measured Current Quantity is selected from 10 to 15, as shown in table 38.

Base voltage shall be entered as:

1. rated phase-to-ground voltage of the protected object in primary kV, when the measured Voltage Quantity is selected from 1 to 9, as shown in table 39.
2. rated phase-to-phase voltage of the protected object in primary kV, when the measured Voltage Quantity is selected from 10 to 15, as shown in table 39.
10.1.2.3 Application possibilities

Due to its flexibility the general current and voltage protection (CVGAPC) function can be used, with appropriate settings and configuration in many different applications. Some of possible examples are given below:

1. Transformer and line applications:
   - Underimpedance protection (circular, non-directional characteristic) (21)
   - Underimpedance protection (circular mho characteristic) (21)
   - Voltage Controlled/Restrained Overcurrent protection (51C, 51V)
   - Phase or Negative/Positive/Zero Sequence (Non-Directional or Directional) Overcurrent protection (50, 51, 46, 67, 67N, 67Q)
   - Phase or phase-to-phase or Negative/Positive/Zero Sequence over/under voltage protection (27, 59, 47)
   - Thermal overload protection (49)
   - Open Phase protection
   - Unbalance protection

2. Generator protection
   - 80-95% Stator earth fault protection (measured or calculated 3Vo) (59GN)
   - Rotor earth fault protection (with external COMBIFLEX RXTTE4 injection unit) (64F)
   - Underimpedance protection (21)
   - Voltage Controlled/Restrained Overcurrent protection (51C, 51V)
   - Turn-to-Turn & Differential Backup protection (directional Negative Sequence. Overcurrent protection connected to generator HV terminal CTs looking into generator) (67Q)
   - Stator Overload protection (49S)
   - Rotor Overload protection (49R)
   - Loss of Excitation protection (directional positive sequence OC protection) (40)
   - Reverse power/Low forward power protection (directional positive sequence OC protection, 2% sensitivity) (32)
   - Dead-Machine/Inadvertent-Energizing protection (51/27)
   - Breaker head flashover protection
   - Improper synchronizing detection
   - Sensitive negative sequence generator over current protection and alarm (46)
   - Phase or phase-to-phase or Negative/Positive/Zero Sequence over/under voltage protection (27x, 59x, 47)
   - Generator out-of-step detection (based on directional positive sequence OC) (78)
   - Inadvertent generator energizing

10.1.2.4 Inadvertent generator energization

When the generator is taken out of service, and stand-still, there is a risk that the generator circuit breaker is closed by mistake.

Three-phase energizing of a generator, which is at standstill or on turning gear, causes it to behave and accelerate similarly to an induction motor. The machine, at this point, essentially represents the subtransient reactance to the system and it can be expected to draw from one to four per unit current, depending on the equivalent system impedance. Machine terminal voltage can range from 20% to 70% of rated voltage, again, depending on the system equivalent impedance (including the block transformer). Higher quantities of machine current and voltage (3 to 4 per unit current and 50% to 70% rated voltage) can be expected if the generator is connected
to a strong system. Lower current and voltage values (1 to 2 per unit current and 20% to 40% rated voltage) are representative of weaker systems.

Since a generator behaves similarly to an induction motor, high currents will develop in the rotor during the period it is accelerating. Although the rotor may be thermally damaged from excessive high currents, the time to damage will be on the order of a few seconds. Of more critical concern, however, is the bearing, which can be damaged in a fraction of a second due to low oil pressure. Therefore, it is essential that high speed tripping is provided. This tripping should be almost instantaneous (< 100 ms).

There is a risk that the current into the generator at inadvertent energization will be limited so that the “normal” overcurrent or underimpedance protection will not detect the dangerous situation. The delay of these protection functions might be too long. The reverse power protection might detect the situation but the operation time of this protection is normally too long.

For big and important machines, fast protection against inadvertent energizing should, therefore, be included in the protective scheme.

The protection against inadvertent energization can be made by a combination of undervoltage, overvoltage and overcurrent protection functions. The undervoltage function will, with a delay for example 10 s, detect the situation when the generator is not connected to the grid (standstill) and activate the overcurrent function. The overvoltage function will detect the situation when the generator is taken into operation and will disable the overcurrent function. The overcurrent function will have a pick-up value about 50% of the rated current of the generator. The trip delay will be about 50 ms.

10.1.3 Setting guidelines

When inverse time overcurrent characteristic is selected, the trip time of the stage will be the sum of the inverse time delay and the set definite time delay. Thus, if only the inverse time delay is required, it is important to set the definite time delay for that stage to zero.

The parameters for the general current and voltage protection function (CVMAPC) are set via the local HMI or Protection and Control Manager (PCM600).

Common base IED values for the primary current (IBase), primary voltage (VBase) and primary power (SBase) are set in global base values for settings function GBASVAL.

GlobalBaseSel: Selects the global base value group used by the function to define IBase, VBase and SBase. Note that this function will only use IBase value.

The overcurrent steps has a IMinx (x=1 or 2 depending on step) setting to set the minimum pickup current. Set IMinx below PickupCurr_OCx for every step to achieve ANSI reset characteristic according to standard. If IMinx is set above PickupCurr_OCx for any step the ANSI reset works as if current is zero when current drops below IMinx.
10.1.3.1 Directional negative sequence overcurrent protection

Directional negative sequence overcurrent protection is typically used as sensitive ground-fault protection of power lines where incorrect zero sequence polarization may result from mutual induction between two or more parallel lines. Additionally, it can be used in applications on underground cables where zero-sequence impedance depends on the fault current return paths, but the cable negative-sequence impedance is practically constant. It shall be noted that directional negative sequence OC element offers protection against all unbalance faults (phase-to-phase faults as well). Care shall be taken that the minimum pickup of such protection function shall be set above natural system unbalance level.

An example will be given, how sensitive-ground-fault protection for power lines can be achieved by using negative-sequence directional overcurrent protection elements within a CVGAPC function.

This functionality can be achieved by using one CVGAPC function. The following shall be done to ensure proper operation of the function:

1. Connect three-phase power line currents and three-phase power line voltages to one CVGAPC instance (for example, GF04)
2. Set CurrentInput to NegSeq (please note that CVGAPC function measures I2 current and NOT 3I2 current; this is essential for proper OC pickup level setting)
3. Set VoltageInput to -NegSeq (please note that the negative sequence voltage phasor is intentionally inverted in order to simplify directionality
4. Set base current IBase value equal to the rated primary current of power line CTs
5. Set base voltage UBase value equal to the rated power line phase-to-phase voltage in kV
6. Set RCADir to value +65 degrees (NegSeq current typically lags the inverted NegSeq voltage for this angle during the fault)
7. Set ROADir to value 90 degree
8. Set LowVolt_VM to value 2% (NegSeq voltage level above which the directional element will be enabled)
9. Enable one overcurrent stage (for example, OC1)
10. By parameter CurveType_OC1 select appropriate TOC/IDMT or definite time delayed curve in accordance with your network protection philosophy
11. Set PickupCurr_OC1 to value between 3-10% (typical values)
12. Set tDef_OC1 or parameter "TD" when TOC/IDMT curves are used to insure proper time coordination with other ground-fault protections installed in the vicinity of this power line
13. Set DirMode_OC1 to Forward
14. Set DirPrinc_OC1 to IcosPhi&U
15. Set ActLowVolt_VM to Block
   • In order to insure proper restraining of this element for CT saturations during three-phase faults it is possible to use current restraint feature and enable this element to operate only when NegSeq current is bigger than a certain percentage (10% is typical value) of measured PosSeq current in the power line. To do this the following settings within the same function shall be done:
16. Set EnRestrainCurr to On
17. Set RestrCurrInput to PosSeq
18. Set RestrCurrCoeff to value 0.1

If required, this CVGAPC function can be used in directional comparison protection scheme for the power line protection if communication channels to the remote end of this power line are available. In that case typically two NegSeq overcurrent steps are required. One for forward and one for reverse direction. As explained before the OC1 stage can be used to detect faults in forward direction. The built-in OC2 stage can be used to detect faults in reverse direction.
However the following shall be noted for such application:

- the set values for $RCADir$ and $ROADir$ settings will be as well applicable for OC2 stage
- setting $DirMode_{OC2}$ shall be set to $Reverse$
- setting parameter $pickupCurr_{OC2}$ shall be made more sensitive than pickup value of forward OC1 element (that is, typically 60% of OC1 set pickup level) in order to insure proper operation of the directional comparison scheme during current reversal situations
- pickup signals from OC1 and OC2 elements shall be used to send forward and reverse signals to the remote end of the power line
- the available scheme communications function block within IED shall be used between multipurpose protection function and the communication equipment in order to insure proper conditioning of the above two pickup signals

Furthermore the other built-in UC, OV and UV protection elements can be used for other protection and alarming purposes.

### 10.1.3.2 Negative sequence overcurrent protection

Example will be given how to use one CVGAPC function to provide negative sequence inverse time overcurrent protection for a generator with capability constant of 20s, and maximum continuous negative sequence rating of 7% of the generator rated current.

The capability curve for a generator negative sequence overcurrent protection, often used worldwide, is defined by the ANSI standard in accordance with the following formula:

\[
\text{t}_{\text{op}} = \frac{\text{TD}}{\left(\frac{I_{\text{NS}}}{I_{r}}\right)^2}
\]

(Equation 76)

where:
- $t_{\text{op}}$ is the operating time in seconds of the negative sequence overcurrent IED
- $TD$ is the generator capability constant in seconds
- $I_{\text{NS}}$ is the measured negative sequence current
- $I_{r}$ is the generator rated current

By defining parameter $x$ equal to maximum continuous negative sequence rating of the generator in accordance with the following formula

\[
x = 7\% = 0.07 \text{ pu}
\]

(Equation 77)

Equation 76 can be re-written in the following way without changing the value for the operate time of the negative sequence inverse overcurrent IED:
In order to achieve such protection functionality with one CVGAPC function the following must be done:

1. Connect three-phase generator currents to one CVGAPC instance (for example, GF01)
2. Set parameter *CurrentInput* to value *NegSeq*
3. Set base current value to the rated generator current in primary amperes
4. Enable one overcurrent step (for example, OC1)
5. Select parameter *CurveType_OC1* to value *Programmable*

\[
t_{op} = \frac{TD \cdot \frac{1}{x^2}}{\left( \frac{I_{NS}}{x \cdot I_1} \right)^2}
\]

(Equation 78)

where:
- \( t_{op} \) is the operating time in seconds of the Inverse Time Overcurrent TOC/IDMT algorithm
- \( TD \) is time multiplier (parameter setting)
- \( M \) is ratio between measured current magnitude and set pickup current level
- \( A, B, C \) and \( P \) are user settable coefficients which determine the curve used for Inverse Time Overcurrent TOC/IDMT calculation

When the equation 76 is compared with the equation 78 for the inverse time characteristic of the OC1 it is obvious that if the following rules are followed:

1. set \( TD \) equal to the generator negative sequence capability value
2. set \( A_{OC1} \) equal to the value \( 1/x^2 \)
3. set \( B_{OC1} = 0.0, C_{OC1}=0.0 \) and \( P_{OC1}=2.0 \)
4. set \( PickupCurr_{OC1} \) equal to the value \( x \)

then the OC1 step of the CVGAPC function can be used for generator negative sequence inverse overcurrent protection.

For this particular example the following settings shall be entered to insure proper function operation:

1. select negative sequence current as measuring quantity for this CVGAPC function
2. make sure that the base current value for the CVGAPC function is equal to the generator rated current
3. set \( TD_{OC1} = 20 \)
4. set \( A_{OC1}= 1/0.07^2 = 204.0816 \)
5. set \( B_{OC1} = 0.0, C_{OC1} = 0.0 \) and \( P_{OC1}=2.0 \)
6. set \( PickupCurr_{OC1} = 7\% \)
Proper timing of the CVGAPC function made in this way can easily be verified by secondary injection. All other settings can be left at the default values. If required delayed time reset for OC1 step can be set in order to ensure proper function operation in case of repetitive unbalance conditions.

Furthermore the other built-in protection elements can be used for other protection and alarming purposes (for example, use OC2 for negative sequence overcurrent alarm and OV1 for negative sequence overvoltage alarm).

**10.1.3.3 Generator stator overload protection in accordance with IEC or ANSI standards**

Example will be given how to use one CVGAPC function to provide generator stator overload protection in accordance with IEC or ANSI standard if minimum-operating current shall be set to 116% of generator rating.

The generator stator overload protection is defined by IEC or ANSI standard for turbo generators in accordance with the following formula:

\[
\text{EQUATION1743-ANSI V1 EN-US} \\
\text{(Equation 80)}
\]

\[
\text{EQUATION1755-ANSI V1 EN-US} \\
\text{(Equation 81)}
\]

\[
t_{op} = \frac{\text{TD}}{\left(\frac{I_m}{I_r}\right)^2 - 1}
\]

where:

- \(t_{op}\) is the operating time of the generator stator overload IED
- \(\text{TD}\) is the generator capability constant in accordance with the relevant standard (TD = 37.5 for the IEC standard or TD = 41.4 for the ANSI standard)
- \(I_m\) is the magnitude of the measured current
- \(I_r\) is the generator rated current

This formula is applicable only when measured current (for example, positive sequence current) exceeds a pre-set value (typically in the range from 105 to 125% of the generator rated current).

By defining parameter \(x\) equal to the per unit value for the desired pickup for the overload IED in accordance with the following formula:

\[
x = 116\% = 1.16 \text{ pu}
\]

\[
\text{EQUATION3.5-ANSI V1 EN-US} \\
\text{(Equation 81)}
\]

formula 3.5 can be re-written in the following way without changing the value for the operate time of the generator stator overload IED:
In order to achieve such protection functionality with one CVGAPC function, the following must be done:

1. Connect three-phase generator currents to one CVGAPC instance (for example, GF01).
2. Set parameter `CurrentInput` to value `PosSeq`.
3. Set base current value to the rated generator current in primary amperes.
4. Enable one overcurrent step (for example OC1).
5. Select parameter `CurveType_OC1` to value `Programmable`.

\[
t_{\text{op}} = \frac{\text{TD} \cdot \frac{1}{X^2}}{\left( \frac{I_{\text{m}}}{X \cdot I_r} \right)^2 - \frac{1}{X^2}}
\]

(Equation 82)

where:
- \(t_{\text{op}}\) is the operating time in seconds of the Inverse Time Overcurrent TOC/IDMT algorithm.
- TD is time multiplier (parameter setting).
- M is ratio between measured current magnitude and set pickup current level.
- A, B, C and P are user settable coefficients which determine the curve used for Inverse Time Overcurrent TOC/IDMT calculation.

When the equation 82 is compared with the equation 83 for the inverse time characteristic of the OC1 step in it is obvious that if the following rules are followed:

1. set TD equal to the IEC or ANSI standard generator capability value.
2. set parameter `A_OC1` equal to the value 1/x².
3. set parameter `C_OC1` equal to the value 1/x².
4. set parameters `B_OC1 = 0.0` and `P_OC1 = 2.0`.
5. set `PickupCurr_OC1` equal to the value `x`.

then the OC1 step of the CVGAPC function can be used for generator negative sequence inverse overcurrent protection.

1. select positive sequence current as measuring quantity for this CVGAPC function.
2. make sure that the base current value for CVGAPC function is equal to the generator rated current.
3. set TD = 37.5 for the IEC standard or TD = 41.4 for the ANSI standard.
4. set `A_OC1` = 1/1.162 = 0.7432.
5. set `C_OC1` = 1/1.162 = 0.7432.
6. set `B_OC1` = 0.0 and `P_OC1` = 2.0.
7. set `PickupCurr_OC1` = 116%.
Proper timing of CVGAPC function made in this way can easily be verified by secondary injection. All other settings can be left at the default values. If required delayed time reset for OC1 step can be set in order to insure proper function operation in case of repetitive overload conditions.

Furthermore the other built-in protection elements can be used for other protection and alarming purposes.

In the similar way rotor overload protection in accordance with ANSI standard can be achieved.

### 10.1.3.4 Open phase protection for transformer, lines or generators and circuit breaker head flashover protection for generators

Example will be given how to use one CVGAPC function to provide open phase protection. This can be achieved by using one CVGAPC function by comparing the unbalance current with a pre-set level. In order to make such a function more secure it is possible to restrain it by requiring that at the same time the measured unbalance current must be bigger than 97% of the maximum phase current. By doing this it will be insured that function can only pickup if one of the phases is open circuited. Such an arrangement is easy to obtain in CVGAPC function by enabling the current restraint feature. The following shall be done in order to insure proper operation of the function:

1. Connect three-phase currents from the protected object to one CVGAPC instance (for example, GF03)
2. Set CurrentInput to value UnbalancePh
3. Set EnRestrainCurr to On
4. Set RestrCurrInput to MaxPh
5. Set RestrCurrCoeff to value 0.97
6. Set base current value to the rated current of the protected object in primary amperes
7. Enable one overcurrent step (for example, OC1)
8. Select parameter CurveType_OC1 to value IEC Def. Time
9. Set parameter PickupCurr_OC1 to value 5%
10. Set parameter tDef_OC1 to desired time delay (for example, 2.0s)

Proper operation of CVGAPC function made in this way can easily be verified by secondary injection. All other settings can be left at the default values. However it shall be noted that set values for restrain current and its coefficient will as well be applicable for OC2 step as soon as it is enabled.

Furthermore the other built-in protection elements can be used for other protection and alarming purposes. For example, in case of generator application by enabling OC2 step with set pickup to 200% and time delay to 0.1s simple but effective protection against circuit breaker head flashover protection is achieved.

### 10.1.3.5 Voltage restrained overcurrent protection for generator and step-up transformer

Example will be given how to use one CVGAPC function to provide voltage restrained overcurrent protection for a generator. Let us assume that the time coordination study gives the following required settings:

- Inverse Time Over Current TOC/IDMT curve: ANSI very inverse
- Pickup current of 185% of generator rated current at rated generator voltage
- Pickup current 25% of the original pickup current value for generator voltages below 25% of rated voltage
This functionality can be achieved by using one CVGAPC function. The following shall be done in order to ensure proper operation of the function:

1. Connect three-phase generator currents and voltages to one CVGAPC instance (for example, GF05)
2. Set `CurrentInput` to value `MaxPh`
3. Set `VoltageInput` to value `MinPh-Ph` (it is assumed that minimum phase-to-phase voltage shall be used for restraining. Alternatively, positive sequence voltage can be used for restraining by selecting `PosSeq` for this setting parameter)
4. Set base current value to the rated generator current primary amperes
5. Set base voltage value to the rated generator phase-to-phase voltage in kV
6. Enable one overcurrent step (for example, OC1)
7. Select `CurveType_OC1` to value `ANSI Very inv`
8. If required set minimum operating time for this curve by using parameter `tMin_OC1` (default value 0.05s)
9. Set `PickupCurr_OC1` to value 185%
10. Set `VCntrlMode_OC1` to `On`
11. Set `VDepMode_OC1` to `Slope`
12. Set `VDepFact_OC1` to value 0.25
13. Set `VHighLimit_OC1` to value 100%
14. Set `VLowLimit_OC1` to value 25%

Proper operation of the CVGAPC function made in this way can easily be verified by secondary injection. All other settings can be left at the default values. Furthermore the other built-in protection elements can be used for other protection and alarming purposes.

### 10.1.3.6 Loss of excitation protection for a generator

Example will be given how by using positive sequence directional overcurrent protection element within a CVGAPC function, loss of excitation protection for a generator can be achieved. Let us assume that from rated generator data the following values are calculated:

- Maximum generator capability to contentiously absorb reactive power at zero active loading 38% of the generator MVA rating
- Generator pull-out angle 84 degrees

This functionality can be achieved by using one CVGAPC function. The following shall be done in order to insure proper operation of the function:

1. Connect three-phase generator currents and three-phase generator voltages to one CVGAPC instance (for example, GF02)
2. Set parameter `CurrentInput` to `PosSeq`
3. Set parameter `VoltageInput` to `PosSeq`
4. Set base current value to the rated generator current primary amperes
5. Set base voltage value to the rated generator phase-to-phase voltage in kV
6. Set parameter `RCADir` to value -84 degree (that is, current lead voltage for this angle)
7. Set parameter `ROADir` to value 90 degree
8. Set parameter `LowVolt_VM` to value 90 degree
9. Set parameter `tDef_OC1` to value 2.0s (typical setting)
13. Set parameter \textit{DirMode\_OC1} to \textit{Forward}
14. Set parameter \textit{DirPrinc\_OC1} to \textit{IcosPhi\&V}
15. Set parameter \textit{ActLowVolt1\_VM} to \textit{Block}

Proper operation of the CVGAPC function made in this way can easily be verified by secondary injection. All other settings can be left at the default values. However it shall be noted that set values for RCA & ROA angles will be applicable for OC2 step if directional feature is enabled for this step as well. Figure 119 shows overall protection characteristic.

Furthermore the other build-in protection elements can be used for other protection and alarming purposes.

\textit{Figure 119: Loss of excitation}
Section 11  Secondary system supervision

11.1  Fuse failure supervision FUFSVPC

11.1.1  Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fuse failure supervision</td>
<td>FUFSVPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

11.1.2  Application

Different protection functions within the protection IED, operates on the basis of the measured voltage in the relay point. Examples are:

- impedance protection functions
- undervoltage function
- energizing check function and voltage check for the weak infeed logic

These functions can operate unintentionally if a fault occurs in the secondary circuits between the voltage instrument transformers and the IED.

It is possible to use different measures to prevent such unwanted operations. Miniature circuit breakers in the voltage measuring circuits should be located as close as possible to the voltage instrument transformers, and shall be equipped with auxiliary contacts that are wired to the IEDs. Separate fuse-failure monitoring IEDs or elements within the protection and monitoring devices are another possibilities. These solutions are combined to get the best possible effect in the fuse failure supervision function (FUFSVPC).

FUFSVPC function built into the IED products can operate on the basis of external binary signals from the miniature circuit breaker or from the line disconnector. The first case influences the operation of all voltage-dependent functions while the second one does not affect the impedance measuring functions.

The negative sequence detection algorithm, based on the negative-sequence measuring quantities is recommended for use in isolated or high-impedance grounded networks: a high value of voltage $3V_2$ without the presence of the negative-sequence current $3I_2$ is a condition that is related to a fuse failure event.

The zero sequence detection algorithm, based on the zero sequence measuring quantities is recommended for use in directly or low impedance grounded networks: a high value of voltage $3V_0$ without the presence of the residual current $3I_0$ is a condition that is related to a fuse failure event. In cases where the line can have a weak-infeed of zero sequence current this function shall be avoided.
A criterion based on delta current and delta voltage measurements can be added to the fuse failure supervision function in order to detect a three phase fuse failure. This is beneficial for example during three phase transformer switching.

### 11.1.3 Setting guidelines

#### 11.1.3.1 General

The negative and zero sequence voltages and currents always exist due to different non-symmetries in the primary system and differences in the current and voltage instrument transformers. The minimum value for the operation of the current and voltage measuring elements must always be set with a safety margin of 10 to 20%, depending on the system operating conditions.

Pay special attention to the dissymmetry of the measuring quantities when the function is used on long untransposed lines, on multicircuit lines and so on.

The settings of negative sequence, zero sequence and delta algorithm are in percent of the base voltage and base current for the function. Common base IED values for primary current (I_base), primary voltage (V_base) and primary power (S_base) are set in Global Base Values GBASVAL. The setting GlobalBaseSel is used to select a particular GBASVAL and used its base values.

#### 11.1.3.2 Setting of common parameters

Set the operation mode selector Operation to Enabled to release the fuse failure function.

The voltage threshold VPPU is used to identify low voltage condition in the system. Set VPPU below the minimum operating voltage that might occur during emergency conditions. We propose a setting of approximately 70% of V_base.

The drop off time of 200 ms for dead phase detection makes it recommended to always set SealIn to Enabled since this will secure a fuse failure indication at persistent fuse fail when closing the local breaker when the line is already energized from the other end. When the remote breaker closes the voltage will return except in the phase that has a persistent fuse fail. Since the local breaker is open there is no current and the dead phase indication will persist in the phase with the blown fuse. When the local breaker closes the current will start to flow and the function detects the fuse failure situation. But due to the 200 ms drop off timer the output BLKZ will not be activated until after 200 ms. This means that distance functions are not blocked and due to the “no voltage but current” situation might issue a trip.

The operation mode selector OpModeSel has been introduced for better adaptation to system requirements. The mode selector enables selecting interactions between the negative sequence and zero sequence algorithm. In normal applications, the OpModeSel is set to either V2I2 for selecting negative sequence algorithm or V0I0 for zero sequence based algorithm. If system studies or field experiences shows that there is a risk that the fuse failure function will not be activated due to the system conditions, the dependability of the fuse failure function can be increased if the OpModeSel is set to V0I0 OR V2I2 or OptimZsNs. In mode V0I0 OR V2I2 both negative and zero sequence based algorithms are activated and working in an OR-condition. Also in mode OptimZsNs both negative and zero sequence algorithms are activated and the one that has the highest magnitude of measured negative or zero sequence current will operate. If there is a requirement to increase the security of the fuse failure function OpModeSel can be selected to V0I0 AND V2I2 which gives that both negative and zero sequence algorithms are activated and
working in an AND-condition, that is, both algorithms must give condition for block in order to activate the output signals BLKV or BLKZ.

### 11.1.3.3 Negative sequence based

The relay setting value $V_{2PU}$ is given in percentage of the base voltage $V_{Base}$ and should not be set lower than the value that is calculated according to equation (84).

$$V_{2PU} = \frac{V_2^{\text{max}}}{V_{Base}} \times 100$$

(Equation 84)

where:
- $V_{2PU}$ is the maximal negative sequence voltage during normal operation conditions, plus a margin of 10...20%
- $V_{Base}$ is the base voltage for the function according to the setting $\text{GlobalBaseSel}$

The setting of the current limit $I_{2PU}$ is in percentage of parameter $I_{Base}$. The setting of $I_{2PU}$ must be higher than the normal unbalance current that might exist in the system and can be calculated according to equation (85).

$$I_{2PU} = \frac{I_2^{\text{max}}}{I_{Base}} \times 100$$

(Equation 85)

where:
- $I_2$ is the maximal negative sequence current during normal operating conditions, plus a margin of 10...20%
- $I_{Base}$ is the base current for the function according to the setting $\text{GlobalBaseSel}$

### 11.1.3.4 Zero sequence based

The IED setting value $V_{0PU}$ is given in percentage of the base voltage $V_{Base}$. The setting of $V_{0PU}$ should not be set lower than the value that is calculated according to equation (86).

$$V_{0PU} = \frac{V_0^{\text{max}}}{V_{Base}} \times 100$$

(Equation 86)

where:
- $V_0$ is the maximal zero sequence voltage during normal operation conditions, plus a margin of 10...20%
- $V_{Base}$ is the base voltage for the function according to the setting $\text{GlobalBaseSel}$

The setting of the current limit $I_{0PU}$ is done in percentage of $I_{Base}$. The setting of pickup must be higher than the normal unbalance current that might exist in the system. The setting can be calculated according to equation (87).
$3I_{0PU} = \frac{3I_0}{IBase} \cdot 100$  

(Equation 87)

where:
- $3I_{0PU}$ is the maximal zero sequence current during normal operating conditions, plus a margin of 10...20%
- $IBase$ is the base current for the function according to the setting GlobalBaseSel

11.1.3.5 **Delta V and delta I**

Set the operation mode selector OpDVDI to Enabled if the delta function shall be in operation.

The setting of $DVPU$ should be set high (approximately 60% of $VBase$) and the current threshold $DIPU$ low (approximately 10% of $IBase$) to avoid unwanted operation due to normal switching conditions in the network. The delta current and delta voltage function shall always be used together with either the negative or zero sequence algorithm. If $V_{Set_{prim}}$ is the primary voltage for operation of $dU/dt$ and $I_{Set_{prim}}$ the primary current for operation of $dI/dt$, the setting of $DVPU$ and $DIPU$ will be given according to equation 88 and equation 89.

$$DVPU = \frac{V_{Set_{prim}}}{VBase} \cdot 100$$  

(Equation 88)

$$DIPU = \frac{I_{Set_{prim}}}{IBase} \cdot 100$$  

(Equation 89)

The voltage thresholds $VPPU$ is used to identify low voltage condition in the system. Set $VPPU$ below the minimum operating voltage that might occur during emergency conditions. A setting of approximately 70% of $VBase$ is recommended.

The current threshold $5OP$ shall be set lower than the $IMinOp$ for the distance protection function. A 5...10% lower value is recommended.

11.1.3.6 **Dead line detection**

The condition for operation of the dead line detection is set by the parameters $IDLDPU$ for the current threshold and $UDLD<$ for the voltage threshold.

Set the $IDLDPU$ with a sufficient margin below the minimum expected load current. A safety margin of at least 15-20% is recommended. The operate value must however exceed the maximum charging current of an overhead line, when only one phase is disconnected (mutual coupling to the other phases).

Set the $VDLDPU$ with a sufficient margin below the minimum expected operating voltage. A safety margin of at least 15% is recommended.
11.2  Fuse failure supervision VDSPVC (60)

11.2.1  Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fuse failure supervision</td>
<td>VDSPVC</td>
<td>VTS</td>
<td>60</td>
</tr>
</tbody>
</table>

11.2.2  Application

Some protection functions operate on the basis of measured voltage at the relay point. Examples of such protection functions are distance protection function, undervoltage function and energisation-check function. These functions might mal-operate if there is an incorrect measured voltage due to fuse failure or other kind of faults in voltage measurement circuit.

VDSPVC is designed to detect fuse failures or faults in voltage measurement circuit based on comparison of the voltages of the main and pilot fused circuits phase wise. VDSPVC output can be configured to block voltage dependent protection functions such as high-speed distance protection, undervoltage relays, underimpedance relays and so on.
11.2.3 Setting guidelines

The parameters for Fuse failure supervision VDSPVC are set via the local HMI or PCM600.

The voltage input type (phase-to-phase or phase-to-neutral) is selected using ConTypeMain and ConTypePilot parameters, for main and pilot fuse groups respectively.

The connection type for the main and the pilot fuse groups must be consistent.

The settings Vdif Main block, Vdif Pilot alarm and VSealIn are in percentage of the base voltage, VBase. Set VBase to the primary rated phase-to-phase voltage of the potential voltage transformer. VBase is available in the Global Base Value groups; the particular Global Base Value group, that is used by VDSPVC (60), is set by the setting parameter GlobalBaseSel.
The settings *Vdif Main block* and *Vdif Pilot alarm* should be set low (approximately 30% of *VBase*) so that they are sensitive to the fault on the voltage measurement circuit, since the voltage on both sides are equal in the healthy condition. If \( V_{\text{SetPrim}} \) is the desired pick up primary phase-to-phase voltage of measured fuse group, the setting of *Vdif Main block* and *Vdif Pilot alarm* will be given according to equation 90.

\[
V_{\text{dif Main block or Vdif Pilot alarm}} = \frac{V_{\text{SetPrim}}}{V_{\text{Base}}} \cdot 100
\]

*(Equation 90)*

\( V_{\text{SetPrim}} \) is defined as phase to neutral or phase to phase voltage dependent of the selected *ConTypeMain* and *ConTypePilot*. If *ConTypeMain* and *ConTypePilot* are set to *Ph-N* than the function performs internally the rescaling of \( V_{\text{SetPrim}} \).

When *SealIn* is set to *On* and the fuse failure has last for more than 5 seconds, the blocked protection functions will remain blocked until normal voltage conditions are restored above the \( V_{\text{SealIn}} \) setting. The fuse failure outputs are deactivated when the normal voltage conditions are restored.

### 11.3 Voltage based delta supervision DELVSPVC(78V)

#### 11.3.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
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</thead>
<tbody>
<tr>
<td>Voltage based delta supervision</td>
<td>DELVSPVC</td>
<td>–</td>
<td>7V_78V</td>
</tr>
</tbody>
</table>

#### 11.3.2 Application

In a weak grid networks, fault detection and operation of other protection functions is reliably done by delta supervision functionality. In this type of network, a delta based release criteria is used to release the trip signal. The measurement of delta differs from country to country between magnitude, vector or sample based detection.

In this function, a voltage based delta supervision is implemented in a phase segregated design. The delta function has the following features:

- Instantaneous sample based delta detection
- True RMS value based delta detection
- DFT magnitude based delta detection
- Vector shift protection

The Delta detection mode is selected on the basis of application requirements. For example, instantaneous sample based delta supervision is very fast; the delta is detected in less than a cycle typically. Hence, instantaneous sample based delta supervision can be used for functions that are used as protection enablers or fault detectors.

All the other supervision modes like RMS/DFT Mag or Angle requires minimum one cycle for delta detection and can be used for time delay functions.
**Angle shift mode**

Use of distributed generation (DG) units is increasing due to liberalized markets (deregulation) and the global trend to use more renewable sources of energy. They generate power in the range of 10 kW to 10 MW and most of them are interconnected to the distribution network. They can supply power into the network as well as to the local loads. It is not common to connect generators directly to the distribution networks and thus the distributed generation can cause some challenges for the protection of distribution networks. From the protection point of view, one of the most challenging issue is islanding.

Islanding is defined as a condition in which a distributed generation unit continues to supply power to a certain part of the distribution network when power from the larger utility main grid is no longer available after opening of a circuit-breaker.

Islanding is also referred as Loss of Mains (LOM) or Loss of Grid (LOG). When LOM occurs, neither the voltage nor the frequency is controlled by the utility supply. Also, these distributed generators are not equipped with voltage and frequency control; therefore, the voltage magnitude of an islanded network may not be kept within the desired limits resulting into undefined voltage magnitudes during islanding situations and frequency instability. Further, uncontrolled frequency represents a high risk for drives and other machines.

Islanding can occur as a consequence of:

- a fault in the network
- circuit-breaker maloperation
- circuit-breaker opening during maintenance

If the distributed generator continues its operation after the utility supply is disconnected, faults do not clear under certain conditions as the arc is charged by the distributed generators. Moreover, the distributed generators are incompatible with the current reclosing practices. During the reclosing sequence dead time, the generators in the network usually tend to drift out of synchronism with the grid and, reconnecting them without synchronizing may damage the generators introducing high currents and voltages in the neighbouring network.

Due to the technical difficulties mentioned above, protection should be provided, which disconnects the distributed generation once it is electrically isolated from the main grid supply. Various techniques are used for detecting Loss of Mains. However, the present feature of voltage supervision focuses on voltage vector shift.

For islanding based on vector shift protection, the logic shown in Figure 121 should be used to trip the breaker. With this logic, reliable tripping can be ensured as angle shift has been detected in all the three phase voltages.
Figure 121: DELVSPVC connection diagram

The vector shift detection guarantees fast and reliable detection of mains failure in almost all operational conditions when a distributed generation unit is running in parallel with the mains supply, but in certain cases this may fail.

If the active and reactive power generated by the distributed generation units is nearly balanced (for example, if the power mismatch or unbalance is less than 5...10%) with the active and reactive power consumed by loads, a large enough voltage phase shift may not occur which can be detected by the vector shift algorithm. This means that the vector shift algorithm has a small non-detection zone (NDZ) which is also dependent on the type of generators, loads, network and start or operate value of the vector shift algorithm.

Other network events like capacitor switching, switching of very large loads in weak network or connection of parallel transformer at HV/MV substation, in which the voltage magnitude is not changed considerably (unlike in faults) can potentially cause maloperation of vector shift algorithm, if very sensitive settings are used.

The vector shift detection also protects synchronous generators from damaging due to islanding or loss-of-mains.

11.3.3 Setting guidelines

Operation: This setting is used to enable/disable the delta supervision function.

Umin: The minimum start level setting should be set as % of UBase. This setting enables the function to start detecting delta. Typical setting is 10% of UBase. If the MeasMode setting is set as phase to ground, this setting is taken as 50% of the set value.

MeasMode: This setting is used to detect the mode of measurement; phase to phase or phase to ground.

OpMode: This setting is used to select the mode of operation. For protection applications, this should be set to Instantaneous 1 cycle old. Load supervision can be done using vector shift mode or DFT mag mode.

DelU>: This setting is used to detect the start value for instantaneous sample, RMS, DFT mag based delta detection. Set a typical value of 50% of UBase to use this function as fault detection.
**DelUang**: This setting is used for angle based delta detection. This setting could be used to detect islanding condition. A typical setting of 8-10 deg. is good to detect a major islanding condition.

**DeltaT**: This setting defines the number of old cycles data to be used for delta calculation in RMS/DFT Mag and angle mode. Typical value is 2 cycles. This value is not used if OpMode is chosen as instantaneous 1 cycle or instantaneous 2 cycle.

**tHold**: This setting defines the pulse length for supervision start signal. Typical value is 100 ms.

## 11.4 Current based delta supervision DELISPVC(7I)

### 11.4.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Current based delta supervision</td>
<td>DELISPVC</td>
<td>–</td>
<td>7I &lt; &gt;</td>
</tr>
</tbody>
</table>

### 11.4.2 Application

In power system networks, fault detection and operation of other protection functions is reliably done by delta supervision functionality. Single phase networks are an important application of delta supervision. In this type of network, a delta based release criteria is used to release the protection function. The measurement of delta differs from country to country between magnitude, vector or sample based detection.

In this function, a current based delta supervision is implemented in a phase segregated design. The delta function has the following features:

- Instantaneous sample based delta detection (vectorial delta)
- True RMS value based delta detection
- DFT magnitude based delta detection
- 2\textsuperscript{nd} harmonic blocking of delta function
- 3\textsuperscript{rd} harmonic based adaption of starting value

Instantaneous sample based delta supervision is very fast; the delta is detected in less than a cycle typically. This mode can be used for high impedance earth fault detection. All the other supervision modes like RMS/DFT Mag requires minimum one cycle for delta detection.

Therefore, the choice of delta detection mode should be based on the application requirement. Instantaneous sample based delta supervision can be used for functions that are used as protection enabler or fault detector. For time delayed functions, other modes can be used. Current based function can be used for load supervision also in DFT Mag based delta mode.

### 11.4.3 Setting guidelines

**Operation**: This setting is used to enable/disable the delta supervision function.
**Imin:** The minimum pickup level setting should be set as % of \(I_{Base}\). This setting enables the function to start detecting delta. Typical setting is 10% of \(I_{Base}\).

**MeasMode:** This setting is used to detect the mode of measurement; *phase to phase* or *phase to ground*.

**OpMode:** This setting is used to select the mode of operation. For protection applications, this should be set to *Instantaneous 1 cycle old*. Load supervision can be done using DFT mag mode.

**DelI:** This setting is used to detect the pickup value for instantaneous sample, RMS, DFT mag based delta detection. Set a typical value of 200% of \(I_{Base}\) to use this function as fault detection.

**DeltaT:** This setting defines the number of old cycles data to be used for delta calculation in RMS/DFT Mag mode. Typical value is 2 cycles.

**tHold:** This setting defines the pulse length for supervision pickup signal. Typical value is 100 ms.

**EnaHarm2Blk:** This setting should be set to ON to enable blocking for heavy inrush currents or other sources of 2\(^{nd}\) harmonic injections.

**Harm2BlkLev:** This is the blocking level of 2\(^{nd}\) harmonic with respect to the fundamental signal. Typical setting is 15% of fundamental signal.

**EnStValAdap:** This setting should be set to ENABLE in special networks where settings in the network are adapted with respect to 3\(^{rd}\) harmonic level.

**Harm3Level:** This is the set level of 3\(^{rd}\) harmonic with respect to fundamental signal at which the \(DelI\) should be modified. Typical setting is 15% of fundamental signal.

**StValGrad:** This setting is used to modify the \(DelI\) based on 3\(^{rd}\) harmonic level. Typical setting is 10% to modify the \(DelI\).

## 11.5 Delta supervision of real input DELSPVC

### 11.5.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Delta supervision of real input</td>
<td>DELSPVC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

### 11.5.2 Application

Delta supervision of real input DELSPVC is a general processed input delta supervision. It is used to configure any processed inputs such as:

- Power (S)
- Active power (P)
- Reactive power (P)
- Thermal heat content (\(\phi\))
- Energy
The change over time of these quantities with respect to the old value can be supervised with this function.

### 11.5.3 Setting guidelines

*Operation:* This setting is used to enable/disable the delta supervision function.

*MinStVal:* The minimum start level of the function. If the input is below this level, the function will be blocked. It should be set depending on the input connected.

*DelSt>:* This setting is used to set the start value for delta detection.

*DeltaT:* This setting defines the number of execution cycles of old data to be used for delta calculation. That is, if *DeltaT* setting is set as 6 for a 3 ms function, an 18 ms old value will be used to compare the change against.

*tHold:* This setting defines the pulse length for the start signal. A typical value of this setting is 100 ms.
Section 12 Control

12.1 Synchronism check, energizing check, and synchronizing SESRSYN (25)

12.1.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Synchrocheck, energizing check, and synchronizing</td>
<td>SESRSYN</td>
<td></td>
<td>25</td>
</tr>
</tbody>
</table>

12.1.2 Application

12.1.2.1 Synchronizing

To allow closing of breakers between asynchronous networks, a synchronizing feature is provided. The breaker close command is issued at the optimum time when conditions across the breaker are satisfied in order to avoid stress on the network and its components.

The systems are defined as asynchronous when the frequency difference between bus and line is larger than an adjustable parameter. If the frequency difference is less than this threshold value the system is defined to have a parallel circuit and the synchronism check function is used.

The synchronizing function measures the difference between the V-Line and the V-Bus. It operates and enables a closing command to the circuit breaker when the calculated closing angle is equal to the measured phase angle and the following conditions are simultaneously fulfilled:

- The voltages V-Line and V-Bus are higher than the set values for $V_{HighBusSynch}$ and $V_{HighLineSynch}$ of the respective base voltages $GblBaseSelBus$ and $GblBaseSelLine$.
- The difference in the voltage is smaller than the set value of $VDiffSynch$.
- The difference in frequency is less than the set value of $FreqDiffMax$ and larger than the set value of $FreqDiffMin$. If the frequency is less than $FreqDiffMin$ the synchronism check is used and the value of $FreqDiffMin$ must thus be identical to the value $FreqDiffM$ resp $FreqDiffA$ for synchronism check function. The bus and line frequencies must also be within a range of ±5 Hz from the rated frequency. When the synchronizing option is included also for autoreclose there is no reason to have different frequency setting for the manual and automatic reclosing and the frequency difference values for synchronism check should be kept low.
- The frequency rate of change is less than set value for both V-Bus and V-Line.
- The difference in the phase angle is smaller than the set value of $CloseAngleMax$.
- The closing angle is decided by the calculation of slip frequency and required pre-closing time.
The synchronizing function compensates for the measured slip frequency as well as the circuit breaker closing delay. The phase angle advance is calculated continuously. The calculation of the operation pulse sent in advance is using the measured \textit{SlipFrequency} and the set \textit{tBreaker} time. To prevent incorrect closing pulses, a maximum closing angle between bus and line is set with \textit{CloseAngleMax}. Table 40 below shows the maximum settable value for \textit{tBreaker} when \textit{CloseAngleMax} is set to 15 or 30 degrees, at different allowed slip frequencies for synchronizing. To minimize the moment stress when synchronizing near a power station, a narrower limit for the \textit{CloseAngleMax} needs to be used.

\begin{table}[h]
\centering
\begin{tabular}{|c|c|c|}
\hline
\textbf{SlipFrequency [Hz] (BusFrequency - LineFrequency)} & \textbf{CloseAngleMax = 15 degrees [default value]} & \textbf{CloseAngleMax = 30 degrees [max value]} \\
\hline
0.040 & 0.080 & 1.000 \\
0.050 & 0.100 & 0.800 \\
0.080 & 0.160 & 0.500 \\
0.200 & 0.400 & 0.200 \\
0.400 & 0.810 & 0.100 \\
& 1.000 & 0.080 \\
0.800 & & 0.050 \\
1.000 & & 0.040 \\
\hline
\end{tabular}
\caption{Dependencies between \textit{tBreaker} and \textit{SlipFrequency} with different \textit{CloseAngleMax} values}
\end{table}

The reference voltage can be phase-neutral A, B, C or phase-phase A-B, B-C, C-A or positive sequence (Require a three phase voltage, that is VA, VB and VC). By setting the phases used for SESRSYN, with the settings \textit{SelPhaseBus1}, \textit{SelPhaseBus2}, \textit{SelPhaseLine2} and \textit{SelPhaseLine2}, a compensation is made automatically for the voltage amplitude difference and the phase angle difference caused if different setting values are selected for the two sides of the breaker. If needed an additional phase angle adjustment can be done for selected line voltage with the \textit{PhaseShift} setting.

\subsection{12.1.2.2 Synchronism check}

The main purpose of the synchronism check function is to provide control over the closing of circuit breakers in power networks in order to prevent closing if conditions for synchronism are not detected. It is also used to prevent the re-connection of two systems, which are divided after islanding and after a three pole reclosing.

Single pole auto-reclosing does not require any synchronism check since the system is tied together by two phases.

SESRSYN (25) function block includes both the synchronism check function and the energizing function to allow closing when one side of the breaker is dead. SESRSYN (25) function also includes a built in voltage selection scheme which allows adoption to various busbar arrangements.
Figure 122: Two interconnected power systems

Figure 122 shows two interconnected power systems. The cloud means that the interconnection can be further away, that is, a weak connection through other stations. The need for a check of synchronization increases if the meshed system decreases since the risk of the two networks being out of synchronization at manual or automatic closing is greater.

The synchronism check function measures the conditions across the circuit breaker and compares them to set limits. Output is generated only when all measured conditions are within their set limits simultaneously. The check consists of:

- Live line and live bus.
- Voltage level difference.
- Frequency difference (slip). The bus and line frequency must also be within a range of ±5 Hz from rated frequency.
- Phase angle difference.

A time delay is available to ensure that the conditions are fulfilled for a minimum period of time.

In very stable power systems the frequency difference is insignificant or zero for manually initiated closing or closing by automatic restoration. In steady conditions a bigger phase angle difference can be allowed as this is sometimes the case in a long and loaded parallel power line. For this application we accept a synchronism check with a long operation time and high sensitivity regarding the frequency difference. The phase angle difference setting can be set for steady state conditions.

Another example is the operation of a power network that is disturbed by a fault event: after the fault clearance a highspeed auto-reclosing takes place. This can cause a power swing in the net and the phase angle difference may begin to oscillate. Generally, the frequency difference is the time derivative of the phase angle difference and will, typically oscillate between positive and negative values. When the circuit breaker needs to be closed by auto-reclosing after fault-clearance some frequency difference should be tolerated, to a greater extent than in the steady condition mentioned in the case above. But if a big phase angle difference is allowed at the same time, there is some risk that auto-reclosing will take place when the phase angle difference is big and increasing. In this case it should be safer to close when the phase angle difference is smaller.

To fulfill the above requirements the synchronism check function is provided with duplicate settings, one for steady (Manual) conditions and one for operation under disturbed conditions (Auto).
12.1.2.3 Energizing check

The main purpose of the energizing check function is to facilitate the controlled re-connection of disconnected lines and buses to energized buses and lines.

The energizing check function measures the bus and line voltages and compares them to both high and low threshold values. The output is given only when the actual measured conditions match the set conditions. Figure 124 shows two substations, where one (1) is energized and the other (2) is not energized. The line between CB A and CB B is energized (DLLB) from substation 1 via the circuit breaker A and energization of station 2 is done by CB B energization check device for that breaker DBLL. (or Both).
The energizing operation can operate in the dead line live bus (DLLB) direction, dead bus live line (DBLL) direction, or in both directions over the circuit breaker. Energizing from different directions can be different for automatic reclosing and manual closing of the circuit breaker. For manual closing it is also possible to allow closing when both sides of the breaker are dead, Dead Bus Dead Line (DBDL).

The equipment is considered energized (Live) if the voltage is above the set value for $V_{LiveBusEnerg}$ or $V_{LiveLineEnerg}$ of the base voltages $G_{b}l_{b}a_{s}e_{s}f_{s}Bus$ and $V_{G_{b}l_{b}a_{s}e_{s}f_{s}SelLine}$, which are defined in the Global Base Value groups, according to the setting of $G_{b}l_{b}a_{s}e_{s}f_{s}Bus$ and $G_{b}l_{b}a_{s}e_{s}f_{s}SelLine$. In a similar way, the equipment is considered non-energized (Dead) if the voltage is below the set value for $V_{DeadBusEnerg}$ or $V_{DeadLineEnerg}$ of the respective Global Base Value groups. A disconnected line can have a considerable potential due to factors such as induction from a line running in parallel, or feeding via extinguishing capacitors in the circuit breakers. This voltage can be as high as 50% or more of the base voltage of the line. Normally, for breakers with single breaking elements (<330 kV) the level is well below 30%.

When the energizing direction corresponds to the settings, the situation has to remain constant for a certain period of time before the close signal is permitted. The purpose of the delayed operate time is to ensure that the dead side remains de-energized and that the condition is not due to temporary interference.

### 12.1.2.4 Voltage selection

The voltage selection function is used for the connection of appropriate voltages to the synchronism check, synchronizing and energizing check functions. For example, when the IED is used in a double bus arrangement, the voltage that should be selected depends on the status of the breakers and/or disconnectors. By checking the status of the disconnectors auxiliary contacts, the right voltages for the synchronism check and energizing check functions can be selected.

Available voltage selection types are for single circuit breaker with double busbars and the breaker-and-a-half arrangement. A double circuit breaker arrangement and single circuit breaker with a single busbar do not need any voltage selection function. Neither does a single circuit breaker with double busbars using external voltage selection need any internal voltage selection.

Manual energization of a completely open diameter in breaker-and-a-half switchgear is allowed by internal logic.

The voltages from busbars and lines must be physically connected to the voltage inputs in the IED and connected, using the PCM software, to each of the SESRSYN (25) functions available in the IED.

### 12.1.2.5 External fuse failure

Either external fuse-failure signals or signals from a tripped fuse (or miniature circuit breaker) are connected to HW binary inputs of the IED; these signals are connected to inputs of SESRSYN function in the application configuration tool of PCM600. The internal fuse failure supervision function can also be used if a three phase voltage is present. The signal BLKV, from the internal fuse failure supervision function, is then used and connected to the fuse supervision inputs of the SESRSYN function block. In case of a fuse failure, the SESRSYN energizing (25) function is blocked.

The VB1OK/VB2OK and VB1FF/VB2FF inputs are related to the busbar voltage and the VL1OK/VL2OK and VL1FF/VL2FF inputs are related to the line voltage.
External selection of energizing direction

The energizing can be selected by use of the available logic function blocks. Below is an example where the choice of mode is done from a symbol, created in the Graphical Design Editor (GDE) tool on the local HMI, through selector switch function block, but alternatively there can for example, be a physical selector switch on the front of the panel which is connected to a binary to integer function block (B16I).

If the PSTO input is used, connected to the Local-Remote switch on the local HMI, the choice can also be from the station HMI system, typically ABB Microscada through IEC 61850–8–1 communication.

The connection example for selection of the manual energizing mode is shown in figure 125. Selected names are just examples but note that the symbol on the local HMI can only show the active position of the virtual selector.

Figure 125: Selection of the energizing direction from a local HMI symbol through a selector switch function block.

12.1.3 Application examples

The synchronism check function block can also be used in some switchyard arrangements, but with different parameter settings. Below are some examples of how different arrangements are connected to the IED analog inputs and to the function block SESRSYN, 25. One function block is used per circuit breaker.

The input used below in example are typical and can be changed by use of configuration and signal matrix tools.

The SESRSYN and connected SMAI function block instances must have the same cycle time in the application configuration.
12.1.3.1 Single circuit breaker with single busbar

Figure 126: Connection of SESRSYN (25) function block in a single busbar arrangement

Figure 126 illustrates connection principles for a single busbar. For the SESRSYN (25) function there is one voltage transformer on each side of the circuit breaker. The voltage transformer circuit connections are straightforward; no special voltage selection is necessary.

The voltage from busbar VT is connected to V3PB1 and the voltage from the line VT is connected to V3PL1. The conditions of the VT fuses shall also be connected as shown above. The voltage selection parameter CBCConfig is set to No voltage sel.
12.1.3.2 Single circuit breaker with double busbar, external voltage selection

Figure 127: Connection of SESRSYN (25) function block in a single breaker, double busbar arrangement with external voltage selection

In this type of arrangement no internal voltage selection is required. The voltage selection is made by external relays typically connected according to figure 127. Suitable voltage and VT fuse failure supervision from the two busbars are selected based on the position of the busbar disconnectors. This means that the connections to the function block will be the same as for the single busbar arrangement. The voltage selection parameter CBConfig is set to No voltage sel.
12.1.3.3 Single circuit breaker with double busbar, internal voltage selection

![Diagram of connection of the SESRSYN function block in a single breaker, double busbar arrangement with internal voltage selection.](image)

**Figure 128: Connection of the SESRSYN function block in a single breaker, double busbar arrangement with internal voltage selection**

When internal voltage selection is needed, the voltage transformer circuit connections are made according to figure 128. The voltage from the busbar 1 VT is connected to V3PB1 and the voltage from busbar 2 is connected to V3PB2. The voltage from the line VT is connected to V3PL1. The positions of the disconnectors and VT fuses shall be connected as shown in figure 128. The voltage selection parameter *CBConfig* is set to *Double bus*. 

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Figure 129: Connections of the SESRSYN (25) function block in a double breaker arrangement

A double breaker arrangement requires two function blocks, one for breaker WA1_QA1 and one for breaker WA2_QA1. No voltage selection is necessary, because the voltage from busbar 1 VT is connected to V3PB1 on SESRSYN for WA1_QA1 and the voltage from busbar 2 VT is connected to V3PB1 on SESRSYN for WA2_QA1. The voltage from the line VT is connected to V3PL1 on both function blocks. The condition of VT fuses shall also be connected as shown in figure 128. The voltage selection parameter \( CBConfig \) is set to No voltage sel. for both function blocks.
12.1.3.5 Breaker-and-a-half

Figure 130 describes a breaker-and-a-half arrangement with three SESRSYN functions in the same IED, each of them handling voltage selection for WA1_QA1, TIE_QA1 and WA2_QA1 breakers respectively. The voltage from busbar 1 VT is connected to V3PB1 on all three function blocks and the voltage from busbar 2 VT is connected to V3PB2 on all three function blocks. The voltage from line1 VT is connected to V3PL1 on all three function blocks and the voltage from line2 VT is connected to V3PL2 on all three function blocks. The positions of the disconnectors and VT fuses shall be connected as shown in Figure 130.
**Figure 130: Connections of the SESRSYN (25) function block in a breaker-and-a-half arrangement with internal voltage selection**

The connections are similar in all SESRSYN functions, apart from the breaker position indications. The physical analog connections of voltages and the connection to the IED and SESRSYN (25) function blocks must be carefully checked in PCM600. In all SESRSYN functions the connections...
and configurations must abide by the following rules: Normally apparatus position is connected with contacts showing both open (b-type) and closed positions (a-type).

**WA1_QA1:**

- BUS1_OP/CL = Position of TIE_QA1 breaker and belonging disconnectors
- BUS2_OP/CL = Position of WA2_QA1 breaker and belonging disconnectors
- LINE1_OP/CL = Position of LINE1_QB9 disconnector
- LINE2_OP/CL = Position of LINE2_QB9 disconnector
- VB1OK/FF = Supervision of WA1_MCB fuse
- VB2OK/FF = Supervision of WA2_MCB fuse
- VL1OK/FF = Supervision of LINE1_MCB fuse
- VL2OK/FF = Supervision of LINE2_MCB fuse
- Setting CBConfig = 1 1/2 bus CB

**TIE_QA1:**

- BUS1_OP/CL = Position of WA1_QA1 breaker and belonging disconnectors
- BUS2_OP/CL = Position of WA2_QA1 breaker and belonging disconnectors
- LINE1_OP/CL = Position of LINE1_QB9 disconnector
- LINE2_OP/CL = Position of LINE2_QB9 disconnector
- VB1OK/FF = Supervision of WA1_MCB fuse
- VB2OK/FF = Supervision of WA2_MCB fuse
- VL1OK/FF = Supervision of LINE1_MCB fuse
- VL2OK/FF = Supervision of LINE2_MCB fuse
- Setting CBConfig = Tie CB

**WA2_QA1:**

- BUS1_OP/CL = Position of WA1_QA1 breaker and belonging disconnectors
- BUS2_OP/CL = Position of TIE_QA1 breaker and belonging disconnectors
- LINE1_OP/CL = Position of LINE1_QB9 disconnector
- LINE2_OP/CL = Position of LINE2_QB9 disconnector
- VB1OK/FF = Supervision of WA1_MCB fuse
- VB2OK/FF = Supervision of WA2_MCB fuse
- VL1OK/FF = Supervision of LINE1_MCB fuse
- VL2OK/FF = Supervision of LINE2_MCB fuse
- Setting CBConfig = 1 1/2 bus alt. CB

If only two SESRSYN functions are provided in the same IED, the connections and settings are according to the SESRSYN functions for WA1_QA1 and TIE_QA1.

### 12.1.4 Setting guidelines

The setting parameters for the Synchronizing, synchronism check and energizing check function SESRSYN (25) are set via the local HMI (LHMI) or PCM600.

This setting guidelines describes the settings of the SESRSYN (25) function via the LHMI.

Common base IED value for primary voltage (VBase) is set in a Global base value function, GBASVAL, found under Main menu/Configuration/Power system/GlobalBaseValue/GBASVAL_X/VBase. The SESRSYN (25) function has one setting for the bus reference voltage (GblBaseSelBus) and one setting for the line reference voltage (GblBaseSelLine) which independently of each other can be set to select one of the twelve GBASVAL functions used for
reference of base values. This means that the reference voltage of bus and line can be set to different values. The settings for the SESRSYN (25) function are found under Main menu/Settings/IED Settings/Control/Synchronizing(25,SC/VC)/SESRSYN(25,SC/VC):X has been divided into four different setting groups: General, Synchronizing, Synchrocheck and Energizingcheck.

**General settings**

*Operation:* The operation mode can be set Enabled or Disabled. The setting Disabled disables the whole function.

*GblBaseSelBus* and *GblBaseSelLine*

These configuration settings are used for selecting one of twelve GBASVAL functions, which then is used as base value reference voltage, for bus and line respectively.

*SelPhaseBus1* and *SelPhaseBus2*

Configuration parameters for selecting the measuring phase of the voltage for busbar 1 and 2 respectively, which can be a single-phase (phase-neutral), two-phase (phase-phase) or a positive sequence voltage.

*SelPhaseLine1* and *SelPhaseLine2*

Configuration parameters for selecting the measuring phase of the voltage for line 1 and 2 respectively, which can be a single-phase (phase-neutral), two-phase (phase-phase) or a positive sequence voltage.

*CBConfig*

This configuration setting is used to define type of voltage selection. Type of voltage selection can be selected as:

- no voltage selection, *No voltage sel.*
- single circuit breaker with double bus, *Double bus*
- breaker-and-a-half arrangement with the breaker connected to busbar 1, *1 1/2 bus CB*
- breaker-and-a-half arrangement with the breaker connected to busbar 2, *1 1/2 bus alt. CB*
- breaker-and-a-half arrangement with the breaker connected to line 1 and 2, *Tie CB*

*PhaseShift*

This setting is used to compensate the phase shift between the measured bus voltage and line voltage when:

- different phase-neutral voltages are selected (for example UL1 for bus and UL2 for line);
- one available voltage is phase-phase and the other one is phase-neutral (for example UL1L2 for bus and UL1 for line).

The set value is added to the measured line phase angle. The bus voltage is reference voltage.

**Synchronizing settings**

*OperationSynch*

The setting *Off* disables the Synchronizing function. With the setting *On*, the function is in the service mode and the output signal depends on the input conditions.
VHighBusSynch and VHighLineSynch

The voltage level settings shall be chosen in relation to the bus/line network voltage. The threshold voltages VHighBusSynch and VHighLineSynch have to be set lower than the value where the network is expected to be synchronized. A typical value is 80% of the rated voltage.

VDiffSynch

Setting of the voltage difference between the line voltage and the bus voltage. The difference is set depending on the network configuration and expected voltages in the two networks running asynchronously. A normal setting is 0.10-0.15 p.u.

FreqDiffMin

The setting FreqDiffMin is the minimum frequency difference where the systems are defined to be asynchronous. For frequency differences lower than this value, the systems are considered to be in parallel. A typical value for FreqDiffMin is 10 mHz. Generally, the value should be low if both synchronizing and synchrocheck functions are provided, and it is better to let the synchronizing function close, as it will close at exactly the right instance if the networks run with a frequency difference.

To avoid overlapping of the synchronizing function and the synchrocheck function the setting FreqDiffMin must be set to a higher value than used setting FreqDiffM, respective FreqDiffA used for synchrocheck.

FreqDiffMax

The setting FreqDiffMax is the maximum slip frequency at which synchronizing is accepted. 1/FreqDiffMax shows the time for the vector to move 360 degrees, one turn on the synchronoscope, and is called Beat time. A typical value for FreqDiffMax is 200-250 mHz, which gives beat times on 4-5 seconds. Higher values should be avoided as the two networks normally are regulated to nominal frequency independent of each other, so the frequency difference shall be small.

FreqRateChange

The maximum allowed rate of change for the frequency.

CloseAngleMax

The setting CloseAngleMax is the maximum closing angle between bus and line at which synchronizing is accepted. To minimize the moment stress when synchronizing near a power station, a narrower limit should be used. A typical value is 15 degrees.

tBreaker

The tBreaker shall be set to match the closing time for the circuit breaker and should also include the possible auxiliary relays in the closing circuit. It is important to check that no slow logic components are used in the configuration of the IED as there then can be big variations in closing time due to those components. Typical setting is 80-150 ms depending on the breaker closing time.

tClosePulse

The setting for the duration of the breaker close pulse.
The setting $t_{MaxSynch}$ is set to reset the operation of the synchronizing function if the operation does not take place within this time. The setting must allow for the setting of $FreqDiffMin$, which will decide how long it will take maximum to reach phase equality. At the setting of 10 mHz, the beat time is 100 seconds and the setting would thus need to be at least $t_{MinSynch}$ plus 100 seconds. If the network frequencies are expected to be outside the limits from the start, a margin needs to be added. A typical setting is 600 seconds.

$t_{MinSynch}$

The setting $t_{MinSynch}$ is set to limit the minimum time at which the synchronizing closing attempt is given. The synchronizing function will not give a closing command within this time, from when the synchronizing is started, even if a synchronizing condition is fulfilled. A typical setting is 200 ms.

**Synchrocheck settings**

*OperationSC*

The **OperationSC** setting Off disables the synchrocheck function and sets the outputs AUTOSYOK, MANSYOK, TSTAUTSY and TSTMANSY to low. With the setting On, the function is in the service mode and the output signal depends on the input conditions.

*VHighBusSC and VHighLineSC*

The voltage level settings must be chosen in relation to the bus or line network voltage. The threshold voltages $V_{HighBusSC}$ and $V_{HighLineSC}$ have to be set lower than the value at which the breaker is expected to close with the synchronism check. A typical value can be 80% of the base voltages.

*VDiffSC*

The setting for voltage difference between line and bus in p.u. This setting in p.u. is defined as $(V_{Bus}/GblBaseSelBus) - (V_{Line}/GblBaseSelLine)$. A normal setting is 0.10-0.15 p.u.

*FreqDiffM* and *FreqDiffA*

The frequency difference level settings, $FreqDiffM$ and $FreqDiffA$, shall be chosen depending on the condition in the network. At steady conditions a low frequency difference setting is needed, where the $FreqDiffM$ setting is used. For autoreclosing a bigger frequency difference setting is preferable, where the $FreqDiffA$ setting is used. A typical value for $FreqDiffM$ can be 10 mHz, and a typical value for $FreqDiffA$ can be 100-200 mHz.

*PhaseDiffM* and *PhaseDiffA*

The phase angle difference level settings, $PhaseDiffM$ and $PhaseDiffA$, shall also be chosen depending on conditions in the network. The phase angle setting must be chosen to allow closing under maximum load condition. A typical maximum value in heavy-loaded networks can be 45 degrees, whereas in most networks the maximum occurring angle is below 25 degrees. The $PhaseDiffM$ setting is a limitation to $PhaseDiffA$ setting. Fluctuations occurring at high speed autoreclosing limit $PhaseDiffA$ setting.

$t_{SCM}$ and $t_{SCA}$

The purpose of the timer delay settings, $t_{SCM}$ and $t_{SCA}$, is to ensure that the synchrocheck conditions remains constant and that the situation is not due to a temporary interference. Should
the conditions not persist for the specified time, the delay timer is reset and the procedure is restarted when the conditions are fulfilled again. Circuit breaker closing is thus not permitted until the synchrocheck situation has remained constant throughout the set delay setting time. Manual closing is normally under more stable conditions and a longer operation time delay setting is needed, where the $t_{SCM}$ setting is used. During auto-reclosing, a shorter operation time delay setting is preferable, where the $t_{SCA}$ setting is used. A typical value for $t_{SCM}$ can be 1 second and a typical value for $t_{SCA}$ can be 0.1 seconds.

**Energizingcheck settings**

*AutoEnerg* and *ManEnerg*

Two different settings can be used for automatic and manual closing of the circuit breaker. The settings for each of them are:

- **Disabled**, the energizing function is disabled.
- **DLLB**, Dead Line Live Bus, the line voltage is below set value of $V_{DeadLineEnerg}$ and the bus voltage is above set value of $V_{LiveBusEnerg}$.
- **DBLL**, Dead Bus Live Line, the bus voltage is below set value of $V_{DeadBusEnerg}$ and the line voltage is above set value of $V_{LiveLineEnerg}$.
- **Both**, energizing can be done in both directions, DLLB or DBLL.

*ManEnergDBDL*

If the parameter is set to *Enabled*, manual closing is also enabled when both line voltage and bus voltage are below $V_{DeadLineEnerg}$ and $V_{DeadBusEnerg}$ respectively, and *ManEnerg* is set to DLLB, DBLL or Both.

*VLiveBusEnerg* and *VLiveLineEnerg*

The voltage level settings must be chosen in relation to the bus or line network voltage. The threshold voltages $V_{LiveBusEnerg}$ and $V_{LiveLineEnerg}$ have to be set lower than the value at which the network is considered to be energized. A typical value can be 80% of the base voltages.

*VDeadBusEnerg* and *VDeadLineEnerg*

The threshold voltages $V_{DeadBusEnerg}$ and $V_{DeadLineEnerg}$, have to be set to a value greater than the value where the network is considered not to be energized. A typical value can be 40% of the base voltages.

A disconnected line can have a considerable potential due to, for instance, induction from a line running in parallel, or by being fed via the extinguishing capacitors in the circuit breakers. This voltage can be as high as 30% or more of the base line voltage.

Because the setting ranges of the threshold voltages $V_{LiveBusEnerg}$/$V_{LiveLineEnerg}$ and $V_{DeadBusEnerg}$/$V_{DeadLineEnerg}$ partly overlap each other, the setting conditions may be such that the setting of the non-energized threshold value is higher than that of the energized threshold value. The parameters must therefore be set carefully to avoid overlapping.

*VMaxEnerg*

This setting is used to block the closing when the voltage on the live side is above the set value of $V_{MaxEnerg}$. 
1. The purpose of the timer delay settings, $t_{AutoEnerg}$ and $t_{ManEnerg}$, is to ensure that the dead side remains de-energized and that the condition is not due to a temporary interference. Should the conditions not persist for the specified time, the delay timer is reset and the procedure is restarted when the conditions are fulfilled again. Circuit breaker closing is thus not permitted until the energizing condition has remained constant throughout the set delay setting time.

### 12.2 Autorecloser for 1 phase, 2 phase and/or 3 phase operation SMBRREC (79)

#### 12.2.1 Identification

<table>
<thead>
<tr>
<th>Function Description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
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<tr>
<td>Autorecloser for 1 phase, 2 phase and/or 3 phase</td>
<td>SMBRREC</td>
<td></td>
<td>79</td>
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</table>

<table>
<thead>
<tr>
<th>5(0 --&gt;1)</th>
</tr>
</thead>
</table>

#### 12.2.2 Application

In certain countries it is standard practice to provide delayed restoration after busbar protection operation for internal fault, reason being that many busbar faults are of the transient natures that is, animals, birds, storm, flying objects, etc. In such applications, typically one pre-selected feeder is automatically closed with certain time delay in order to try to re-energize the faulty bus. Typically, the longest overhead line is selected in order to limit the fault current in case of permanent busbar fault. If the first feeder is successfully closed, all other feeder which have been connected to the same busbar, are automatically put back into service.

Sensitive differential protection level available in REB670 can be used during such operation, if increased sensitivity from busbar protection is required. Such busbar restoration logic can be implemented by using optionally available auto reclosers and built-in logical gates. Two auto reclosers are available.

Automatic reclosing is a well-established method for the restoration of service in a power system after a transient line fault. The majority of line faults are flashovers, which are transient by nature. When the power line is switched off by the operation of line protection and line breakers, the arc de-ionizes and recovers its ability to withstand voltage at a somewhat variable rate. Thus, a certain dead time with a de-energized line is necessary. Line service can then be resumed by automatic reclosing of the line breakers. The dead time selected should be long enough to ensure a high probability of arc de-ionization and successful reclosing.

For individual line breakers, auto reclosing equipment, the required circuit breaker dead time is used to determine the “dead time” setting value. When simultaneous tripping and reclosing at the two line ends occurs, line dead time is approximately equal to the auto recloser “dead time”. If the auto reclosing dead time and line “dead time” differ then, the line will be energized until the breakers at both ends have opened.
Figure 131: Single-shot automatic reclosing at a permanent fault

Single-pole tripping and single-phase automatic reclosing is a way of limiting the effect of a single-phase line fault on power system operation. Especially at higher voltage levels, the majority of faults are of single-phase type (around 90%). To maintain system stability in power systems with limited meshing or parallel routing single-phase auto reclosing is of particular value. During the single-phase dead time the system is still capable of transmitting load on the two healthy phases and the system is still synchronized. It requires that each circuit breaker pole can be operated individually, which is usually the case for higher transmission voltages.

A somewhat longer dead time may be required for single-phase reclosing compared to high-speed three-phase reclosing. This is due to the influence on the fault arc from the voltage and the current in the non-faulted phases.

To maximize the availability of the power system it is possible to choose single-phase tripping and automatic reclosing during single-phase faults and three-phase tripping and automatic reclosing during multi-phase faults. Three-phase automatic reclosing can be performed with or without the use of synchrocheck.

During the single-phase dead time there is an equivalent “series”-fault in the system resulting in a flow of zero sequence current. It is therefore necessary to coordinate the residual current protections (ground fault protection) with the single-phase tripping and the auto reclosing function. Attention shall also be paid to “pole discrepancy” that arises when circuit breakers are provided with single-phase operating devices. These breakers need pole discrepancy protection. They must also be coordinated with the single-phase auto recloser and blocked during the dead time when a normal discrepancy occurs. Alternatively, they should use a trip time longer than the set single-phase dead time.
For the individual line breakers and auto reclosing equipment, the auto reclosing dead time expression is used. This is the dead time setting for the auto recloser. During simultaneous tripping and reclosing at the two line ends, auto reclosing dead time is approximately equal to the line dead time. Otherwise these two times may differ as one line end might have a slower trip than the other end which means that the line will not be dead until both ends have opened.

If the fault is permanent, the line protection will trip again when reclosing is attempted in order to clear the fault.

It is common to use one automatic reclosing function per line circuit breaker (CB). When one CB per line end is used, then there is one auto-recloser per line end. If auto reclosers are included in duplicated line protection, which means two auto reclosers per CB, one should take measures to avoid uncoordinated reclosing commands. In breaker-and-a-half, double-breaker and ring bus arrangements, two CBs per line end are operated. One auto recloser per CB is recommended. Arranged in such a way, that sequential reclosing of the two CBs can be arranged with a priority circuit available in the auto recloser. In case of a permanent fault and unsuccessful reclosing of the first CB, reclosing of the second CB is cancelled and thus the stress on the power system is limited.

The auto recloser can be selected to perform single-phase and/or three-phase automatic reclosing from several single-shot to multiple-shot reclosing programs. The three-phase auto reclosing dead time can be set to give either High-Speed Automatic Reclosing (HSAR) or Delayed Automatic Reclosing (DAR). These expressions, HSAR and DAR, are mostly used for three-phase auto reclosing as single-phase auto reclosing is always high speed to avoid maintaining the unsymmetrical condition. HSAR usually means a dead time of less than 1 second.

In power transmission systems it is common practice to apply single- and/or three-phase, single-shot auto reclosing. In sub-transmission and distribution systems tripping and auto reclosing are usually three-phase. The mode of automatic reclosing varies however. Single-shot and multi-shot are in use. The first shot can have a short delay, HSAR, or a longer delay, DAR. The second and following reclosing shots have a rather long delay. When multiple shots are used the dead time must harmonize with the breaker duty-cycle capacity.

Automatic reclosing is usually started by the line protection and in particular by instantaneous tripping of such protection. The auto recloser can be inhibited (blocked) when certain protection functions detecting permanent faults, such as shunt reactor, cable or busbar protection are in operation. Back-up protection zones indicating faults outside the own line are typically connected to inhibit the auto recloser.

Automatic reclosing should not be attempted when closing a CB and energizing a line onto a fault (SOTF), except when multiple-shots are used where shots 2 etc. will be started at SOTF. Likewise a CB in a multi-breaker busbar arrangement which was not closed when a fault occurred should not be closed by operation of the auto recloser. Auto reclosing is often combined with a release condition from synchronism check and dead line or dead busbar check. In order to limit the stress on turbo generator sets from auto reclosing onto a permanent fault, one can arrange to combine auto reclosing with a synchronism check on line terminals close to such power stations and attempt energizing from the side furthest away from the power station and perform the synchronism check at the local end if the energizing was successful.

Transmission protection systems are usually sub-divided and provided with two redundant protection IEDs. In such systems it is common to provide auto reclosing in only one of the sub-systems as the requirement is for fault clearance and a failure to reclose because of the auto recloser being out of service is not considered a major disturbance. If two auto reclosers are provided on the same breaker, the application must be carefully checked and normally one must be the master and be connected to inhibit the other auto recloser if it has started. This inhibit can, for example, be done from an auto recloser for 3-phase operation in progress signal.
When Single and/or three phase auto reclosing is considered, there are a number of cases where the tripping shall be three phase anyway. For example:

- Evolving fault where the fault during the dead-time spreads to another phase. The other two phases must then be tripped and a three phase dead-time and auto reclose initiated
- Permanent fault
- Fault during three-phase dead time
- Auto recloser out of service or circuit breaker not ready for an auto reclosing cycle

“Prepare three-pole tripping” is then used to switch the tripping to three-pole. This signal is generated by the auto-recloser and connected to the trip function block and also connected outside the IED through IO when a common auto-recloser is provided for two sub-systems. An alternative signal “Prepare 1 Pole tripping” is also provided and can be used as an alternative when the autorecloser is shared with another subsystem. This provides a fail safe connection so that even a failure in the IED with the auto-recloser will mean that the other sub-system will start a three-pole trip.

A permanent fault will cause the line protection to trip again when it recloses in an attempt to energize the line.

The auto reclosing function allows a number of parameters to be adjusted.

Examples:

- number of auto reclosing shots
- auto reclosing program
- auto reclosing dead times for each shot

### 12.2.2.1 Auto reclosing operation Off and On

Operation of the automatic recloser can be set to Off and On by a setting parameter or by external control. The setting parameter *Operation = Disabled,* or *Enabled* sets the function to Off or On. With the settings *Operation = Enabled* and *ExternalCtrl = Enabled,* the control is made by input signal pulses to the inputs On and Off, for example, from a control system or by a control switch.

When the auto recloser is set On, the SETON output is set, and the auto recloser becomes operative if other conditions such as circuit breaker is closed and circuit breaker is ready are also fulfilled, the READY output is activated (high). Then the auto recloser is ready to accept a start.

### 12.2.2.2 Initiate auto reclosing and conditions for start of a reclosing cycle

The usual way to start an auto reclosing cycle, or sequence, is to start it at selective tripping by line protection by applying a signal to the RI input. Starting signals can be either, general trip signals or, only the conditions for differential, distance protection zone 1 and distance protection aided trip. In some cases also directional ground fault protection aided trip can be connected to start an auto reclose attempt. If general trip is used to start the auto recloser it is important to block it from other functions that should not start an auto reclosing sequence.

In cases where one wants to differentiate three-phase auto reclosing dead time, for different power system configuration or at tripping by different protection stages, one can also use the *RI_HS* input (initiate high-speed reclosing). When initiating *RI_HS,* the auto reclosing dead time for three-phase shot 1, *t1_3PhHS* is used and the closing is done without checking the synchrocheck condition.
A number of conditions need to be fulfilled for the start to be accepted and a new auto reclosing cycle to be started. They are linked to dedicated inputs. The inputs are:

- **CBREADY**, circuit breaker ready for a reclosing cycle, for example, charged operating gear.
- **CBCLOSED** to ensure that the circuit breaker was closed when the line fault occurred and start was applied.
- No signal at **INHIBIT** input that is, no blocking or inhibit signal present. After the start has been accepted, it is latched in and an internal signal “start” is set. It can be interrupted by certain events, like an “inhibit” signal.

### 12.2.2.3 Initiate auto reclosing from circuit breaker open information

If a user wants to initiate auto reclosing from the circuit breaker open position instead of from protection trip signals, the function offers such a possibility. This starting mode is selected with the setting parameter **StartByCBOpen=Enabled**. Typically a circuit breaker auxiliary contact of type NO (normally open) is connected to **CBCLOSED** and **RI**. When the signal changes from circuit breaker closed to circuit breaker open an auto reclosing start pulse is generated and latched in the function, subject to the usual checks. The auto reclosing sequence continues then as usual. Signals from manual tripping and other functions, which shall prevent auto reclosing, need to be connected to the **INHIBIT** input.

### 12.2.2.4 Blocking of the auto recloser

Auto reclose attempts are expected to take place only for faults on the own line. The auto recloser must be blocked by activating the **INHIBIT** input for the following conditions:

- Tripping from delayed distance protection zones
- Tripping from back-up protection functions
- Tripping from breaker failure function
- Intertrip received from remote end circuit breaker failure function
- Busbar protection tripping

Depending of the starting principle (general trip or only instantaneous trip) adopted above the delayed and back-up zones might not be required. Breaker failure trip local and remote must however always be connected.

### 12.2.2.5 Control of the auto reclosing dead time for shot 1

Up to four different time settings can be used for the first shot, and one extension time. There are separate settings for single-, two- and three-phase auto reclosing dead time, $t_{1\text{1Ph}}$, $t_{1\text{2Ph}}$, $t_{1\text{3Ph}}$. If no particular input signal is applied, and an auto reclosing program with single-phase auto reclosing is selected, the auto reclosing dead time $t_{1\text{1Ph}}$ will be used. If one of the **TR2P** or **TR3P** inputs is activated in connection with the start, the auto reclosing dead time for two-phase or three-phase auto reclosing is used. There is also a separate time setting facility for three-phase high-speed auto reclosing without synchrocheck, $t_{1\text{3PhHS}}$, available for use when required. It is activated by the **RI_HS** input.

A time extension delay, $t_{\text{Extended } t1}$, can be added to the dead time delay for the first shot. It is intended to come into use if the communication channel for permissive line protection is lost. In a case like this there can be a significant time difference in fault clearance at the two line ends, where a longer auto reclosing dead time can be useful. This time extension is controlled by the setting **Extended t1 = On** and the **PLCLOST** input. If this functionality is used the auto recloser
start must also be allowed from distance protection zone 2 time delayed trip. Time extension delay is not possible to add to the three-phase high-speed auto reclosing dead time, t1 3PhHS.

12.2.2.6 Long trip signal

In normal circumstances the auto recloser is started with a protection trip command which resets quickly due to fault clearing. The user can set a maximum start pulse duration tLongStartInh. This start pulse duration time is controlled by setting LongStartInhib.

When start pulse duration signal is longer than set maximum start pulse duration, the auto reclosing sequence interrupts in the same way as for a signal to the INHIBIT input.

12.2.2.7 Maximum number of reclosing shots

The maximum number of auto reclosing shots in an auto reclosing cycle is selected by the setting NoOfShots. A maximum of five shots can be done. The type of auto reclosing used at the first auto reclosing shot is set by the setting ARMode. The first alternative is three-phase auto reclosing. The other alternatives include some single-phase or two-phase auto reclosing. Usually there is no two-pole tripping arranged, and then there will be no two-phase auto reclosing.

The decision for single- and three-phase trip is also made in the tripping logic (SMPTTRC) function block where the setting 3 phase, 1ph/3Ph (or 1ph/2ph/3Ph) is selected.

12.2.2.8 ARMode = 3ph, (normal setting for a three-phase shot)

Three-phase auto reclosing, one to five shots according to the NoOfShots setting. The prepare three-pole trip PREP3P output is always set (high). A trip operation is made as a three-pole trip for all type of faults. The auto reclosing is as a three-phase auto reclosing as in mode 1/2/3ph described below. All signals, blockings, inhibits, timers, requirements and so on, are the same as in the example described below.

12.2.2.9 ARMode = 1/2/3ph

Single-phase, two-phase or three-phase auto reclosing first shot, followed by 3-phase auto reclosing shots, if selected. Here, the auto recloser is assumed to be "On" and "Ready". The circuit breaker is closed and the operation gear ready (operating energy stored). START input (or STARTHS) is received and sealed-in. The READY output is reset (set to false). ACTIVE output is set.

- If TR2P and TR3P inputs are low (i.e. single-phase trip): The timer for single-phase auto reclosing dead time is started and the 1PT1 output (single-phase reclosing in progress) is activated. It can be used to suppress pole disagreement and earth-fault protection trip during the single-phase dead time interval.
- If TR2P input is high and TR3P input is low (i.e. two-phase trip): The timer for two-phase auto reclosing dead time is started and the 2PT1 output (two-phase reclosing in progress) is activated.
- If TR3P input is high (i.e. three-phase trip): The timer for three-phase auto reclosing dead time, t1 3Ph or t1 3PhHS, is started depending on if START or STARTHS input has been activated and 3PT1 output (three-phase reclosing shot 1 in progress) is set.

While any of the auto reclosing dead time timers are running, the INPROGR output is activated. When the dead time runs out, the respective internal signal is transmitted to the output module for further checks and to issue a breaker closing command.
When a circuit breaker closing command is issued, the prepare three-pole output trip is set. When issuing a circuit breaker closing command the `Reclaim` timer is started. If no tripping takes place during that time, the auto recloser resets to the “Ready” state and the `ACTIVE` output resets. If the first reclosing shot fails, a three-phase trip will be initiated and three-phase reclosing can follow, if selected.

### 12.2.2.10 ARMode = 1/2ph, 1-phase or 2-phase reclosing in the first shot

At single-pole or two-pole tripping, the operation is as in the example described above, program mode 1/2/3ph. If the first reclosing shot fails, a three-pole trip will be issued and three-pole auto reclosing can follow, if selected. In the event of a three-pole trip, `TR3P` input high, the auto recloser will be inhibited and no auto reclosing takes place.

### 12.2.2.11 ARMode = 1ph+1*2ph, 1-phase or 2-phase reclosing in the first shot

At single-pole tripping, the operation is as in the above described example, program mode 1/2/3ph. The single-pole auto reclosing attempt can be followed by three-pole auto reclosing, if selected. At two-pole trip, a failure of a two-pole auto reclosing attempt will inhibit the auto recloser. No more shots are attempted. If the first trip is a three-pole trip, the auto-reclosing will be inhibited. No more shots are attempted. The expression “1*2ph” should be understood as “Only one shot at two-phase auto reclosing”.

### 12.2.2.12 ARMode = 1/2ph + 1*3ph, 1-phase, 2-phase or 3-phase reclosing in the first shot

At single-pole or two-pole tripping, the operation is as in the example described above, program mode 1/2/3ph. If the first reclosing shot fails, a three-pole trip will be issued and three-pole reclosing will follow, if selected. At three-pole trip, a failure of a three-pole auto reclosing attempt will inhibit the auto recloser. No more shots are attempted. At two-pole or three-pole trip a failure of a two-pole or three-pole auto reclosing attempt will inhibit the auto recloser. No more shots are attempted. The expression “1*3ph” should be understood as “Only one shot at three-pole auto reclosing”.

### 12.2.2.13 ARMode = 1ph + 1*2/3ph, 1-phase, 2-phase or 3-phase reclosing in the first shot

At single-pole or two-pole tripping, the operation is as in the above described example, program mode 1/2/3ph. If the first reclosing shot fails, a three-pole trip will be issued and three-pole reclosing will follow, if selected. At two-pole or three-pole trip a failure of a two-pole or three-pole auto reclosing attempt will inhibit the auto recloser. No more shots are attempted. The expression “1*2/3ph” should be understood as “Only one shot at two-pole or three-pole auto reclosing”.

<table>
<thead>
<tr>
<th>MODEINT (integer)</th>
<th>ARMode</th>
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<td>1</td>
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<td></td>
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</tr>
</tbody>
</table>

Table continues on next page
A start of a new auto reclosing cycle during the set “reset time” is blocked when the set number of reclosing shots have been reached.

### 12.2.2.14 External selection of auto reclosing mode

The auto reclosing mode can be selected by use of available logic function blocks. Below is an example where the choice of mode, \textit{ARMode}=3ph or ARMode=1/2/3ph, is done from a hardware function key at the front of the IED, but alternatively there can for example, be a physical selector switch on the front of the panel which is connected to a binary to integer function block (BTIGAPC).

The connection example for selection of the auto reclosing mode is shown in Figure 132.

![Figure 132: Selection of the auto-reclose mode from a hardware functional key in front of the IED](ANSI09000168_1_en.vsd)

### 12.2.2.15 Auto reclosing reset timer

The \textit{tReset} timer defines the time it takes from issue of the breaker closing command, until the auto recloser resets. Should a new trip occur during this time, it is treated as a continuation of the first fault. The reclaim timer is started when the circuit breaker closing command is given.
**12.2.16** **Pulsing of the circuit breaker closing command and counter**

The circuit breaker closing command, CLOSECB is given as a pulse with a duration set by the *tpulse* setting. For circuit breakers without an anti-pumping function, close pulse cutting can be used. It is selected by the *CutPulse* setting. In case of a new start pulse (trip), the breaker closing command pulse is then cut (interrupted). The minimum breaker closing command pulse length is always 50ms. At the issue of the breaker closing command, the appropriate auto recloser operation counter is incremented. There is a counter for each type of auto reclosing command and one for the total number of auto reclosing commands.

**12.2.17** **Transient fault**

After the breaker closing command the reclaim timer keeps running for the set *tReclaim* time. If no start (trip) occurs within this time, the auto recloser will reset. The circuit breaker remains closed and the operating gear recharges. The CLOSED and CBREADY input signals will be set.

**12.2.18** **Permanent fault and reclosing unsuccessful signal**

If a new start occurs, and the number of auto reclosing shots is set to 1, and a new START or TRSOTF input signal appears, after the circuit breaker closing command, the UNSUCCL output (unsuccessful reclosing) is set high. The timer for the first shot can no longer be started. Depending on the set number of auto reclosing shots further shots may be made or the auto reclosing sequence is ended. After reclaim timer time-out the auto recloser resets, but the circuit breaker remains open. The circuit breaker closed information through the CBCLOSED input is missing. Thus, the auto recloser is not ready for a new auto reclosing cycle. Normally, the UNSUCCL output appears when a new start is received after the last auto reclosing shot has been made and the auto recloser is inhibited. The output signal resets after reclaim time. The “unsuccessful” signal can also be made to depend on the circuit breaker position input. The *UnsucClByCBChk* setting should then be set to *CBCheck*, and the *tUnsuccCl* timer should be set too. If the circuit breaker does not respond to the breaker closing command and does not close, but remains open, the UNSUCCL output is set high after the set *tUnsuccCl* time. The UNSUCCL output can for example, be used in multi-breaker arrangement to cancel the auto reclosing for the second circuit breaker, if the first circuit breaker closed onto a persistent fault. It can also be used to generate a lock-out of manual circuit breaker closing until the operator has reset the lock-out, see separate section.

**12.2.19** **Lock-out initiation**

In many cases there is a requirement that a lock-out is generated when the auto reclosing attempt fails. This is done with logic connected to the in- and outputs of the auto recloser and connected to binary I/O as required. Many alternative ways of performing the logic exist depending on whether manual circuit breaker closing is interlocked in the IED, whether an external physical lock-out relay exists and whether the reset is hardwired, or carried out by means of communication. There are also different alternatives regarding what shall generate lock-out. Examples of questions are:

- shall back-up time delayed trip give lock-out (normally yes)
- shall lock-out be generated when closing onto a fault (mostly)
- shall lock-out be generated when the auto recloser is Off at the fault or for example, in single-phase auto recloser mode and the fault was multi-phase (normally not as no closing attempt has been given)
- shall lock-out be generated if the circuit breaker did not have sufficient operating power for an auto reclosing sequence (normally not as no auto closing attempt has been given)
In Figures 133 and 134 the logic shows how a closing lock-out logic can be designed with the lock-out relay as an external relay alternatively with the lock-out created internally with the manual closing going through the synchrocheck function. An example of lock-out logic.

![Lock-out System Block Diagram](image1.png)

**Figure 133:** Lock-out arranged with an external lock-out relay

![Lock-out System Block Diagram](image2.png)

**Figure 134:** Lock-out arranged with internal logic with manual closing going through in IED

### 12.2.2.20 Evolving fault

An evolving fault starts as a single-phase fault which leads to single-pole tripping and then the fault spreads to another phase. The second fault is then cleared by three-pole tripping.

The auto recloser will first receive a start signal (START) without any three-phase signal (TR3P). The auto recloser will start a single-phase auto reclosing sequence, if programmed to do so. At the evolving fault clearance there will be a new START signal and three-phase trip information, TR3P. The single-phase auto reclosing sequence will then be stopped, and instead the timer, t1 3Ph, for three-pole auto reclosing will be started from zero. The sequence will continue as a three-pole auto reclosing sequence, if it is a selected alternative reclosing mode. The second fault which can be single-phase is tripped three-pole because the trip function (SMPPTRC) in the IED has an evolving fault timer which ensures that second fault is always tripped three-pole. For other types
of relays where the relays do not include this function, the PREP3PH output (or the inverted PERMIT1PH output) is used to prepare the other sub-system for three-phase tripping. This signal will, for evolving fault situations, be activated a short time after the first trip has reset and will thus ensure that new starts (trips) will be three phase.

12.2.2.21 Automatic continuation of the auto reclosing sequence

The auto recloser can be programmed to proceed to the next auto reclosing shots (if multiple shots are selected) even if start signals are not received from protection functions, but the circuit breaker is still not closed. This is done by setting AutoCont = Enabled and tAutoContWait to the required delay for the function to proceed without a new start.

12.2.2.22 Thermal overload protection holding the auto recloser back

If the THOLHOLD input (thermal overload protection holding auto reclosing back) is activated, it will keep the auto recloser on a hold until it is reset. There may thus be a considerable delay between start of the auto recloser and the breaker closing command to the circuit breaker. An external logic limiting the time and sending an inhibit to the INHIBIT input can be used. The input can also be used to set the auto recloser on hold for a longer or shorter period.

12.2.3 Setting guidelines

12.2.3.1 Configuration

Use the PCM600 configuration tool to configure signals.

Auto recloser function parameters are set via the local HMI or Parameter Setting Tool (PST). Parameter Setting Tool is a part of PCM600.

Recommendations for input signals
Please see Figure 135, Figure 136 and Figure 137 and default factory configuration for examples.

BLKOFF
Used to unblock the auto recloser when it has been blocked due to activating BLKON input or by an unsuccessful auto reclosing attempt if the BlockByUnsuccCl setting is set to On.

BLKON
Used to block the auto recloser, for example, when certain special service conditions arise. When used, blocking must be reset with BLKOFF.

CBCLOSED and CBREADY

These binary inputs should pick-up information from the circuit breaker. At three operating gears in the circuit breaker (single pole operated circuit breakers) the connection should be “All poles closed” (series connection of the NO contacts) or “At least one pole open” (parallel connection of NC contacts). The CBREADY is a signal meaning that the circuit breaker is ready for an auto reclosing operation, either Close-Open (CO), or Open-Close-Open (OCO). If the available signal is of type “circuit breaker not charged” or “not ready”, an inverter can be inserted in front of the CBREADY input.
**INHIBIT**

To this input shall be connected signals that interrupt an auto reclosing cycle or prevent a start from being accepted. Such signals can come from protection for a line connected shunt reactor, from transfer trip receive, from back-up protection functions, busbar protection trip or from breaker failure protection. When the circuit breaker open position is set to start the auto recloser, then manual opening must also be connected here. The inhibit is often a combination of signals from external IEDs via the I/O and internal functions. An OR-gate is then used for the combination.

**MODEINT**

The auto reclosing mode is selected with the $ARMode$ setting. As an alternative to the setting, the mode can be selected by connecting an integer, for example from function block B16I, to the $MODEINT$ input. The six possible modes are described in table 6 with their corresponding $MODEINT$ integer value. When a valid integer is connected to the input $MODEINT$ the selected $ARMode$ setting will be invalid and the $MODEINT$ input value will be used instead. The selected mode is reported as an integer on the $MODE$ output.

**ON and OFF**

These inputs can be connected to binary inputs or to a communication interface block for external control.

**PLCLOST**

This is intended for line protection permissive signal channel lost (fail) for example, PLC= Power Line Carrier failure. It can be connected, when it is required to prolong the auto reclosing dead time when communication is not working, that is, one line end might trip with a zone2 delay. If this is used the auto recloser must also be started from zone2 time delayed trip.

**RESET**

Used to reset the auto recloser to start conditions. Possible hold by thermal overload protection will be reset. Circuit breaker position will be checked and time settings will be restarted with their set times.

**RSTCOUNT**

There is a counter for each type of auto reclosing and one for the total number of circuit breaker close commands issued. All counters are reset with the $RSTCOUNT$ input or by an IEC 61850 command.

**SKIPHS**

The high-speed auto reclosing sequence can be skipped and be replaced by normal auto reclosing sequence by activating $SKIPHS$ input before the $STARTHS$ high-speed start input is activated. The replacement is done for the 1st shot.

**RI**

The $START$ input should be connected to the trip function (SMPPTRC) output, which starts the auto recloser for 1/2/3-phase operation. It can also be connected to a binary input for start from an external contact. A logical OR-gate can be used to combine the number of start sources.

If $StartByCBOpen$ is used, the circuit breaker open condition shall also be connected to the $RI$ input.
**RI_HS, Initiate high-speed auto reclosing**

It may be used when one wants to use two different dead times in different protection trip operations. This input starts the dead time \( t_1 \text{ 3PhHS} \). High-speed reclosing shot 1 started by this input is without a synchronization check.

**SYNC**

This input is connected to the internal synchronization check function when required or to an external device for synchronization. If neither internal nor external synchronization or energizing check is required, it can be connected to a permanently high source, TRUE. The signal is required for three-phase shots 1-5 to proceed (Note! Not the high-speed step).

**THOLHOLD**

Signal “Thermal overload protection holding back auto reclosing”. It can be connected to a thermal overload protection trip signal which resets only when the thermal content has fallen to an acceptable level, for example, 70%. As long as the signal is high, indicating that the line is hot, the auto reclosing is held back. When the signal resets, a reclosing cycle will continue. Observe that this have a considerable delay. Input can also be used for other purposes if for some reason the auto reclosing shot needs to be halted.

**TR2P and TR3P**

They are usually connected to the corresponding output of the trip function block. They control the choice of dead time and the auto reclosing cycle according to the selected program. Signal TR2P needs to be connected only if the trip function block has been selected to give 1/2/3 pole trip and an auto reclosing cycle with two phase reclosing is foreseen.

**TRSOTF**

This is the signal “Trip by Switch Onto Fault”. It is usually connected to the “switch onto fault” output of line protection if multi-shot auto reclosing attempts are used. The input will start the shots two to five.

**WAIT**

Used to hold back reclosing of the “low priority unit” during sequential auto reclosing. See “Recommendation for multi-breaker arrangement” below. The signal is activated from output \( \text{WFMASTER} \) on the second breaker auto recloser in multi-breaker arrangements.

**ZONESTEP**

The \( \text{ZONESTEP} \) input is used when coordination between local auto reclosers and down stream auto reclosers is needed. When this input is activated the auto recloser increases its actual shot number by one and enters “reclaim time” status. If a start is received during this reclaim time the auto recloser is proceeding as usual but with the dead time for the increased shot number. Every new increase of the shot number needs a new activation of the \( \text{ZONESTEP} \) input. This functionality is controlled by the setting \( \text{ZoneSeqCoord} \).

**Recommendations for output signals**

Please see Figure 135, Figure 136 and Figure 137 and default factory configuration for examples.

**1PT1 and 2PT1**

Indicates that single-pole or two-pole auto reclosing is in progress. It is used to temporarily block an ground-fault and/or pole disagreement function during the single-pole or two-pole open interval.
3PT1, 3PT2, 3PT3, 3PT4 and 3PT5

Indicates that three-pole auto reclosing shots one to five are in progress. The signals can be used as an indication of progress or for own logic.

ABORTED

The ABORTED output indicates that the auto recloser is inhibited while it is in one of the following internal states:

- inProgress: auto recloser is started and dead time is in progress
- reclaimTimeStarted: the circuit breaker closing command has started the reclaim timer
- wait: an auto recloser, acting as slave, is waiting for a release from the master to proceed with its own reclosing sequence

ACTIVE

Indicates that the auto recloser is active, from start until end of reset time.

BLOCKED

Indicates that auto recloser is temporarily or permanently blocked.

CLOSECMD

Connect to a binary output for circuit breaker closing command.

COUNT1P, COUNT2P, COUNT3P1, COUNT3P2, COUNT3P3, COUNT3P4 and COUNT3P5

Indicates the number of auto reclosing shots made for respective shot.

COUNTAR

Indicates the total number of auto reclosing shots made.

INHIBOUT

If the INHIBIT input is activated it is reported on the INHIBOUT output.

INPROGR

Indicates that an auto recloser sequence is in progress, from start until circuit breaker close command.

MODE

When a valid integer is connected to the MODEINT input, the selected ARMode setting will be invalid and the MODEINT input value will be used instead. The selected mode is reported as an integer on the MODE output. The six possible modes are described in Table 41 with their corresponding MODEINT integer value.

PERMIT1P

Permit single-pole trip is the inverse of PREP3P. It can be connected to a binary output relay for connection to external protection or trip relays. In case of a total loss of auxiliary power, the output relay drops and does not allow single-pole trip.
**PREP3P**
Prepare three-pole trip is usually connected to the trip block to force a coming trip to be a three-pole one. If the auto recloser cannot make a single-pole or two-pole auto reclosing, the tripping should be three-pole.

**READY**
Indicates that the auto recloser is ready for a new and complete auto reclosing sequence. It can be connected to the zone extension if a line protection should have extended zone reach before auto reclosing.

**SETON**
Indicates that auto recloser is switched on and operative.

**SUCCCL**
If the circuit breaker closing command is given and the circuit breaker is closed within the set time interval \( t_{UnsucCl} \), the SUCCCL output is activated after the set time interval \( t_{Successful} \).

**SYNCFAIL**
The SYNCFAIL output indicates that the auto recloser is inhibited because the synchrocheck or energizing check condition has not been fulfilled within the set time interval, \( t_{Sync} \). Also ABORTED output will be activated.

**UNSUCCCL**
Indicates unsuccessful reclosing.

**WFMASTER**
Wait from master is used in high priority units to hold back auto reclosing of the low priority unit during sequential auto reclosing. Refer to the recommendation for multi-breaker arrangements in Figure 137.

**Connection and setting examples**
Figure 135 is showing an example of how to connect the auto recloser when used for three-pole auto reclosing and Figure 136 is showing an example of how to connect the auto recloser when used for single-pole, two-pole or three-pole auto reclosing.
Figure 135: Example of I/O-signal connections at a three-phase auto reclosing sequence
Setting recommendations for multi-breaker arrangements

Sequential reclosing in multi-breaker arrangements, like breaker-and-a-half, double breaker and ring bus, is achieved by giving the two line breakers different priorities. Refer to figure 137. In a single breaker arrangement the setting is Priority = None. In a multi-breaker arrangement the setting for the first circuit breaker, the master, is Priority = High and for the other circuit breaker Priority = Low.

While the auto reclosing of the master is in progress, it issues the WFMASTER output. After an unsuccessful reclosing the WFMASTER output is also maintained by the UNSUCL output. When activating the WAIT input, in the auto recloser set as slave, every dead timer is changed to the value of setting tSlaveDeadTime and holds back the auto reclosing operation. When the WAIT input is reset at the time of a successful reclosing of the first circuit breaker, the slave is released to continue the reclosing sequence after the set tSlaveDeadTime. The reason for shortening the time, for the normal dead timers with the value of tSlaveDeadTime, is to give the slave permission to react almost immediately when the WAIT input resets. The minimum settable time for tSlaveDeadTime is 0.1sec because both master and slave should not send the breaker closing command at the same time. The slave should take the duration of the breaker closing time of the master into consideration before sending the breaker closing command. A setting tWaitForMaster sets a maximum wait time for the WAIT input to reset. If the wait time expires, the reclosing cycle of the slave is inhibited. If auto reclosing of the first breaker is unsuccessful, the UNSUCL output connected to the INHIBIT input of the slave unit interrupts the auto reclosing sequence of the latter.
The signals can be cross-connected to allow simple changing of the priority by just setting the High and the Low priorities without changing the configuration. The input 52a for each circuit breaker is important in multi-breaker arrangements to ensure that the circuit breaker was closed at the beginning of the cycle. If the High priority circuit breaker is not closed the High priority moves to the low priority circuit breaker.

Figure 137: Additional input and output signals at multi-breaker arrangement. The connections can be made "symmetrical" to make it possible to control the priority by the settings, Priority: High/Low

12.2.3.2 Auto recloser settings

The settings for the auto recloser are set using the local HMI (LHMI) or PCM600.

This setting guideline describes the settings of the auto recloser using the LHMI.
The settings for the auto recloser are found under **Main menu/Settings/IED Settings/Control/AutoRecloser(79,5(0->1))/SMBRREC(79,5(0->)):X** and have been divided into four different setting groups: General, CircuitBreaker, DeadTime and MasterSlave.

**General settings**

*Operation*: The operation of the auto recloser can be switched *Enabled* or *Disabled*.

*ExternalCtrl*: This setting makes it possible to switch the auto recloser On or Off using an external switch via IO or communication ports.

*ARMode*: There are six different possibilities in the selection of auto reclosing programs. The type of auto reclosing used for different kinds of faults depends on the power system configuration and the users practices and preferences. When the circuit breaker only have three-pole operation, then three-pole auto reclosing has to be chosen. This is usually the case in sub-transmission and distribution lines. Three-pole tripping and reclosing for all types of faults is also widely accepted in completely meshed power systems. In transmission systems with few parallel circuits, single-pole reclosing for single-phase faults is an attractive alternative for maintaining service and system stability.

*AutoContinue*: Automatic continuation to the next shot if the circuit breaker is not closed within the set time of *tAutoContWait*. The normal setting is *AutoContinue = Disabled*.

*tAutoContWait*: This is the length in time the auto recloser waits to see if the circuit breaker is closed when *AutoContinue* is set to *Enabled*. Normally, the setting of *tAutoContWait* can be 2 sec.

*StartByCBOpen*: The normal setting *Disabled* is used when the function is started by protection trip signals. If set *Enabled* the start of the auto recloser is controlled by a circuit breaker auxiliary contact.

*LongStartInhib*: Usually the protection trip command, used as an auto reclosing start signal, resets quickly as the fault is cleared. A prolonged trip command may depend on a circuit breaker failing to clear the fault. A protection trip signal present when the circuit breaker is reclosed will result in a new trip. The user can set a maximum start pulse duration time *tLongStartInh*. This start pulse duration time is controlled by the *LongStartInhib* setting. When the start pulse duration signal is longer than set maximum start pulse duration, the auto reclosing sequence interrupts in the same way as for a signal to the *INHIBIT* input.

*tLongStartInh*: The user can set a maximum start pulse duration time *tLongStartInh*. At a set time somewhat longer than the auto reclosing dead time, this facility will not influence the auto reclosing. A typical setting of *tLongStartInh* could be close to the auto reclosing dead time.

*tnInhibit*: To ensure reliable interruption and temporary blocking of the auto recloser a resetting time delay *tnInhibit* is used. The auto recloser will be blocked this time after the deactivation of the *INHIBIT* input. A typical resetting delay is 5.0 s.

*ZoneSeqCoord*: The *ZONESTEP* input is used when coordination between local auto reclosers and down stream auto reclosers is needed. When this input is activated the auto recloser increases its actual shot number by one and enters “reset time” status. If a start is received during this reclaim time the auto recloser is proceeding as usual but with the dead time for the increased shot number. Every new increase of the shot number needs a new activation of the *ZONESTEP* input. The setting *NoOfShots* limits of course the possibility to increase the shot number. This functionality is controlled by the setting *ZoneSeqCoord*. 

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Section 12 1MRK 505 370-UUS D

Control

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Busbar protection REB670

Application manual
CircuitBreaker settings

*CBReadyType:* The selection depends on the type of performance available from the circuit breaker operating gear. At setting OCO (circuit breaker ready for an Open – Close – Open cycle), the condition is checked only at the start of the auto reclosing cycle. The signal will disappear after tripping, but the circuit breaker will still be able to perform the C-O sequence. For the selection CO (circuit breaker ready for a Close – Open cycle) the condition is also checked after the set auto reclosing dead time. This selection has a value first of all at multi-shot auto reclosing to ensure that the circuit breaker is ready for a C-O sequence at shot two and further shots. During single-shot auto reclosing, the OCO selection can be used. A breaker shall according to its duty cycle always have storing energy for a CO operation after the first trip. (IEC 56 duty cycle is O – 0.3sec – CO – 3min – CO).

*FollowCB:* The usual setting is Follow CB = Off. The setting On can be used for delayed auto reclosing with long delay, to cover when a circuit breaker is being manually closed during the auto reclosing dead time before the auto recloser has issued its breaker close command.

*UnsucClByCBChk:* The normal setting is NoCBCheck and the auto reclosing unsuccessful event is then decided by a new trip within the reclaim time after the last reclosing shot. If one wants to get the UNSUCCL (Reclosing is unsuccessful) signal in the case the circuit breaker does not respond to the circuit breaker close command, one can set UnsucClByCBCheck = CB Check and set \( t\text{UnsucCl} \) for instance to 1.0 s.

*BlockByUnsucCl:* Setting of whether an unsuccessful auto reclosing attempt shall set the auto recloser in blocked status. If used the BLKOFF input must be configured to unblock the function after an unsuccessful auto reclosing attempt. Normal setting is Disabled.

*CutPulse:* In circuit breakers without anti-pumping relays, the setting CutPulse = Enabled can be used to avoid repeated closing operation when reclosing onto a fault. A new start will then cut the ongoing pulse.

*tPulse:* The circuit breaker closing command should be long enough to ensure reliable operation of the circuit breaker. The circuit breaker closing command pulse has a duration set by the tPulse setting. A typical setting may be \( t\text{Pulse} = 200 \text{ ms} \). A longer pulse setting may facilitate dynamic indication at testing, for example, in debug mode of the Application Configuration Tool (ACT) in PCM600. In circuit breakers without anti-pumping relays, the setting CutPulse = Enabled can be used to avoid repeated closing operations when reclosing onto a fault. A new start will then cut the ongoing pulse.

*tReset:* The reclaim time sets the time for resetting the function to its original state, after which a line fault and tripping will be treated as an independent new case with a new auto reclosing cycle. One may consider a nominal CB duty cycle of for instance, O – 0.3sec – CO – 3min – CO. However the 3 minute (180 s) recovery time is usually not critical as fault levels are mostly lower than rated value and the risk of a new fault within a short time is negligible. A typical time may be \( t\text{Reset} = 60 \) or 180 s dependent on the fault level and circuit breaker duty cycle.

*tSync:* Maximum wait time for fulfilled synchrocheck conditions. The time window should be coordinated with the operate time and other settings of the synchrocheck function. Attention should also be paid to the possibility of a power swing when reclosing after a line fault. Too short a time may prevent a potentially successful auto reclosing.

*tCBClosedMin:* A typical setting is 5.0 s. If the circuit breaker has not been closed for at least this minimum time, an auto reclosing start will not be accepted.

*tSuccessful:* If the circuit breaker closing command is given and the circuit breaker is closed within the set time interval \( t\text{UnsucCl} \), the SUCCL output is activated after the set time interval tSuccessful.
The reclaim timer, \( t_{\text{Reset}} \), is started each time a circuit breaker closing command is given. If no start occurs within this time, the auto recloser will reset. A new start received in “restart time” status will reenter the auto recloser to “in progress” status as long as the final shot is not reached. The auto recloser will reset and enter “inactive” status if a new start is given during the final restart time. This will also happen if the circuit breaker has not closed within set time interval \( t_{\text{UnsucCl}} \) at the end of the reclaim time. This latter case is controlled by setting \( \text{UnsucClByCBChk} \). The auto reclosing sequence is considered unsuccessful for both above cases and the \( \text{UNSUCCL} \) output is activated.

### DeadTime settings

\( \text{NoOfShots} \): In power transmission one shot is mostly used. In most cases one auto reclosing shot is sufficient as the majority of arcing faults will cease after the first auto reclosing shot. In power systems with many other types of faults caused by other phenomena, for example wind, a greater number of auto reclosing attempts (shots) can be motivated.

\( t_{1\, \text{1Ph}}, t_{1\, \text{2Ph}}, t_{1\, \text{3Ph}} \): There are separate settings for the first shot for single-, two- and three-phase auto reclosing dead times.

**Single-phase auto reclosing dead time:** A typical setting is \( t_{1\, \text{1Ph}} = 800\, \text{ms} \). Due to the influence of energized phases the arc extinction may not be instantaneous. In long lines with high voltage the use of shunt reactors in the form of a star with a neutral reactor improves the arc extinction.

**Three-phase auto reclosing dead time:** Different local phenomena, such as moisture, salt, pollution, can influence the required dead time. Some users apply Delayed Auto Reclosing (DAR) with delays of 10s or more.

\( t_{\text{Extended t1}} \): The time extension below is controlled by the \( \text{Extended t1} \) setting.

\( t_{\text{Extended t1}} \): A time extension delay, \( t_{\text{Extended t1}} \), can be added to the dead time delay for the first shot. It is intended to come into use if the communication channel for permissive line protection is lost. The communication link in a permissive (not strict) line protection scheme, for instance a power line carrier (PLC) link, may not always be available. If lost, it can result in delayed tripping at one end of a line. There is a possibility to extend the auto reclosing dead time in such a case by use of the \( \text{PLCLOST} \) input, and the \( t_{\text{Extended t1}} \) setting. Typical setting in such a case: \( \text{Extended t1} = \text{Enabled} \) and \( t_{\text{Extended t1}} = 0.8\, \text{s} \).

\( t_{1\, \text{3PhHS}} \): There is also a separate time setting facility for three-phase high-speed auto reclosing, \( t_{1\, \text{3PhHS}} \). This high-speed auto reclosing is activated by the \( \text{STARTHS} \) input and is used when auto reclosing is done without the requirement of synchrocheck conditions to be fulfilled. A typical dead time is 400ms.

\( t_{2\, \text{3Ph}}, t_{3\, \text{3Ph}}, t_{4\, \text{3Ph}}, t_{5\, \text{3Ph}} \): The delay of auto reclosing shot two and possible later shots are usually set at 30s or more. A check that the circuit breaker duty cycle can manage the selected setting must be done. The setting can in some cases be restricted by national regulations. For multiple shots the setting of shots two to five must be longer than the circuit breaker duty cycle time.

### MasterSlave settings

**Priority:** In single circuit breaker applications, one sets \( \text{Priority} = \text{None} \). At sequential reclosing the auto recloser for the first circuit breaker, e.g. near the busbar, is set as master (High) and the auto recloser for the second circuit breaker is set as slave (Low).

\( t_{\text{WaitForMaster}} \): The slave should take the duration of the circuit breaker closing time of the master into consideration before sending the circuit breaker closing command. A setting \( t_{\text{WaitForMaster}} \) sets a maximum wait time for the \( \text{WAIT} \) input to reset. If the wait time expires, the
The auto reclosing cycle of the slave is inhibited. The maximum wait time, $t_{\text{WaitForMaster}}$ for the second circuit breaker is set longer than the auto reclosing dead time plus a margin for synchrocheck conditions to be fulfilled for the first circuit breaker. Typical setting is 2sec.

$t_{\text{SlaveDeadTime}}$: When activating the $\text{WAIT}$ input, in the auto recloser set as slave, every dead timer is changed to the value of setting $t_{\text{SlaveDeadTime}}$ and holds back the auto reclosing operation. When the $\text{WAIT}$ input is reset at the time of a successful reclosing of the first circuit breaker, the slave is released to continue the auto reclosing sequence after the set $t_{\text{SlaveDeadTime}}$. The reason for shortening the time, for the normal dead timers with the value of $t_{\text{SlaveDeadTime}}$, is to give the slave permission to react almost immediately when the $\text{WAIT}$ input resets. The minimum settable time for $t_{\text{SlaveDeadTime}}$ is 0.1sec because both master and slave should not send the circuit breaker closing command at the same time.

### 12.3 Apparatus control

#### 12.3.1 Application

The apparatus control is a functionality for control and supervising of circuit breakers, disconnectors, and grounding switches within a bay. Permission to operate is given after evaluation of conditions from other functions such as interlocking, synchronism check, operator place selection and external or internal blockings.

The complete apparatus control function is not included in this product, and the information below is included for understanding of the principle for the use of QCBAY, LOCREM, LOCREMCTRL, SCILO, SCISWI, SXCBR.

Figure 138 shows from which places the apparatus control function receives commands. The commands to an apparatus can be initiated from the Control Centre (CC), the station HMI or the local HMI on the IED front.

![Figure 138: Overview of the apparatus control functions](ANSI08000227.vsd)
Features in the apparatus control function:

- Operation of primary apparatuses
- Select-Execute principle to give high security
- Selection and reservation function to prevent simultaneous operation
- Selection and supervision of operator place
- Command supervision
- Block/deblock of operation
- Block/deblock of updating of position indications
- Substitution of position indications
- Overriding of interlocking functions
- Overriding of synchronism check
- Pole discrepancy supervision
- Operation counter
- Suppression of mid position

The apparatus control function is realized by means of a number of function blocks designated:

- Switch controller SCSWI
- Circuit breaker SXCBR
- Circuit switch SXSWI
- Bay control QCBAY
- Bay reserve QCRSV
- Reservation input RESIN
- Local remote LOCREM
- Local remote control LOCREMCTRL

The signal flow between the function blocks is shown in Figure 139. To realize the reservation function, the function blocks Reservation input (RESIN) and Bay reserve (QCRSV) also are included in the apparatus control function. The application description for all these functions can be found below. The function SCIL0 in the Figure below is the logical node for interlocking.

When the circuit breaker or switch is located in a breaker IED, two more functions are added:

- GOOSE receive for switching device GOOSEXLNRCV
- Proxy for signals from switching device via GOOSE XLNPROXY

The extension of the signal flow and the usage of the GOOSE communication are shown in Figure 140.
Figure 139: Signal flow between apparatus control function blocks when all functions are situated within the IED
Figure 140: Signal flow between apparatus control functions with XCBR and XSWI located in a breaker IED

Control operation can be performed from the local IED HMI. If users are defined in the IED, then the local/remote switch is under authority control, otherwise the default user can perform control operations from the local IED HMI without logging in. The default position of the local/remote switch is on remote.
Accepted originator categories for PSTO

If the requested command is accepted by the authority control, the value will change. Otherwise the attribute **blocked-by-switching-hierarchy** is set in the **cause** signal. If the PSTO value is changed during a command, then the command is aborted.

The accepted originator categories for each PSTO value are shown in **Table 42**.

**Table 42: Accepted originator categories for each PSTO**

<table>
<thead>
<tr>
<th>Permitted Source To Operate</th>
<th>Originator (orCat)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 = Off</td>
<td>4,5,6</td>
</tr>
<tr>
<td>1 = Local</td>
<td>1,4,5,6</td>
</tr>
<tr>
<td>2 = Remote</td>
<td>2,3,4,5,6</td>
</tr>
<tr>
<td>3 = Faulty</td>
<td>4,5,6</td>
</tr>
<tr>
<td>4 = Not in use</td>
<td>4,5,6</td>
</tr>
<tr>
<td>5 = All</td>
<td>1,2,3,4,5,6</td>
</tr>
<tr>
<td>6 = Station</td>
<td>2,4,5,6</td>
</tr>
<tr>
<td>7 = Remote</td>
<td>3,4,5,6</td>
</tr>
</tbody>
</table>

PSTO = All, then it is no priority between operator places. All operator places are allowed to operate.

According to IEC 61850 standard the **orCat** attribute in originator category are defined in **Table 43**

**Table 43: orCat attribute according to IEC 61850**

<table>
<thead>
<tr>
<th>Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>not-supported</td>
</tr>
<tr>
<td>1</td>
<td>bay-control</td>
</tr>
<tr>
<td>2</td>
<td>station-control</td>
</tr>
<tr>
<td>3</td>
<td>remote-control</td>
</tr>
<tr>
<td>4</td>
<td>automatic-bay</td>
</tr>
<tr>
<td>5</td>
<td>automatic-station</td>
</tr>
<tr>
<td>6</td>
<td>automatic-remote</td>
</tr>
<tr>
<td>7</td>
<td>maintenance</td>
</tr>
<tr>
<td>8</td>
<td>process</td>
</tr>
</tbody>
</table>

**12.3.2 Bay control QCBAY**

The Bay control (QCBAY) is used to handle the selection of the operator place per bay. The function gives permission to operate from two main types of locations either from Remote (for example, control centre or station HMI) or from Local (local HMI on the IED) or from all (Local and Remote). The Local/Remote switch position can also be set to Off, which means no operator place selected that is, operation is not possible either from local or from remote.
For IEC 61850-8-1 communication, the Bay Control function can be set to discriminate between commands with or without station and remote (2 and 3). The selection is then done through the IEC 61850-8-1 edition 2 command LocSta.

QCBAY also provides blocking functions that can be distributed to different apparatuses within the bay. There are two different blocking alternatives:

- Blocking of update of positions
- Blocking of commands

![Image of APC - Local remote function block]

**Figure 141: APC - Local remote function block**

### 12.3.3 Switch controller SCSWI

SCSWI may handle and operate on one three-phase device or three one-phase switching devices.

After the selection of an apparatus and before the execution, the switch controller performs the following checks and actions:
A request initiates to reserve other bays to prevent simultaneous operation.
Actual position inputs for interlocking information are read and evaluated if the operation is permitted.
The synchronism check/synchronizing conditions are read and checked, and performs operation upon positive response.
The blocking conditions are evaluated.
The position indications are evaluated according to given command and its requested direction (open or closed).

The command sequence is supervised regarding the time between:

- Select and execute.
- Select and until the reservation is granted.
- Execute and the final end position of the apparatus.
- Execute and valid close conditions from the synchronism check.

At error the command sequence is cancelled.

In the case when there are three one-phase switches (SXCBR) connected to the switch controller function, the switch controller will "merge" the position of the three switches to the resulting three-phase position. In case of a pole discrepancy situation, that is, the positions of the one-phase switches are not equal for a time longer than a settable time; an error signal will be given.

The switch controller is not dependent on the type of switching device SXCBR or SXSWI. The switch controller represents the content of the SCSWI logical node (according to IEC 61850) with mandatory functionality.

12.3.4 Switches SXCBR/SXSWI

Switches are functions used to close and interrupt an ac power circuit under normal conditions, or to interrupt the circuit under fault, or emergency conditions. The intention with these functions is to represent the lowest level of a power-switching device with or without short circuit breaking capability, for example, circuit breakers, disconnectors, grounding switches etc.

The purpose of these functions is to provide the actual status of positions and to perform the control operations, that is, pass all the commands to the primary apparatus via output boards and to supervise the switching operation and position.

Switches have the following functionalities:

- Local/Remote switch intended for the switchyard
- Block/deblock for open/close command respectively
- Update block/deblock of position indication
- Substitution of position indication
- Supervision timer that the primary device starts moving after a command
- Supervision of allowed time for intermediate position
- Definition of pulse duration for open/close command respectively

The realizations of these functions are done with SXCBR representing a circuit breaker and with SXSWI representing a circuit switch that is, a disconnector or an grounding switch.

Circuit breaker (SXCBR) can be realized either as three one-phase switches or as one three-phase switch.
The content of this function is represented by the IEC 61850 definitions for the logical nodes Circuit breaker (SXCBR) and Circuit switch (SXSWI) with mandatory functionality.

12.3.5 Proxy for signals from switching device via GOOSE XLNPROXY

The purpose of the proxy for signals from switching device via GOOSE (XLNPROXY) is to give the same internal representation of the position status and control response for a switch modeled in a breaker IED as if represented by a SXCBR or SXSWI function.

The command response functionality is dependent on the connection of the execution information, XIN, from the SCSWI function controlling the represented switch. Otherwise, the function only reflects the current status of the switch, such as blocking, selection, position, operating capability and operation counter.

Since different switches are represented differently on IEC 61850, the data that is mandatory to model in IEC 61850 is mandatory inputs and the other useful data for the command and status following is optional. To make it easy to choose which data to use for the XLNPROXY function, their usage is controlled by the connection of each data's signal input and valid input. These connections are usually from the GOOSEXLNRCV function (see Figure 142 and Figure 143).

Figure 142: Configuration with XLNPROXY and GOOSEXLNRCV where all the IEC 61850 modelled data is used, including selection
All the information from the XLNPROXY to the SCSWI about command following status, causes for failed command and selection status is transferred in the output XPOS. The other outputs may be used by other functions in the same way as the corresponding outputs of the SXCBR and SXSWI function.

When a command has been issued from the connected SCSWI function, the XLNPROXY function awaits the response on it from the represented switch through the inputs POSVAL and OPOK. While waiting for the switch to start moving, it checks if the switch is blocked for the operation. When the switch has started moving and no blocking condition has been detected, XLNPROXY issues a response to the SCSWI function that the command has started. If OPOK is used, this response is given when XLNPROXY receives the signal.

If no movement of the switch is registered within the limit $t_{StartMove}$, the command is considered failed, and the cause of the failure is evaluated. In the evaluation, the function checks if the state of the represented switch is indicating that the command is blocked in any way during the command, and gives the appropriate cause to the SCSWI function. This cause is also shown on the output L_CAUSE as indicated in the following table:

<table>
<thead>
<tr>
<th>Cause No</th>
<th>Cause Description</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>8</td>
<td>Blocked-by-Mode</td>
<td>The BEH input is 5.</td>
</tr>
<tr>
<td>2</td>
<td>Blocked-by-switching-hierarchy</td>
<td>The LOC input indicates that only local commands are allowed for the breaker IED function.</td>
</tr>
<tr>
<td>-24</td>
<td>Blocked-for-open-cmd</td>
<td>The BLKOPN is active indicating that the switch is blocked for open commands.</td>
</tr>
</tbody>
</table>

Table 44: Possible cause values from XLNPROXY
### Reservation function (QCRSV and RESIN)

The purpose of the reservation function is primarily to transfer interlocking information between IEDs in a safe way and to prevent double operation in a bay, switchyard part, or complete substation.

For interlocking evaluation in a substation, the position information from switching devices, such as circuit breakers, disconnectors and grounding switches can be required from the same bay or from several other bays. When information is needed from other bays, it is exchanged over the station bus between the distributed IEDs. The problem that arises, even at a high speed of communication, is a space of time during which the information about the position of the switching devices are uncertain. The interlocking function uses this information for evaluation, which means that also the interlocking conditions are uncertain.

To ensure that the interlocking information is correct at the time of operation, a unique reservation method is available in the IEDs. With this reservation method, the bay that wants the reservation sends a reservation request to other bays and then waits for a reservation granted signal from the other bays. Actual position indications from these bays are then transferred over the station bus for evaluation in the IED. After the evaluation the operation can be executed with high security.

This functionality is realized over the station bus by means of the function blocks QCRSV and RESIN. The application principle is shown in Figure 144.

The function block QCRSV handles the reservation. It sends out either the reservation request to other bays or the acknowledgement if the bay has received a request from another bay.

The other function block RESIN receives the reservation information from other bays. The number of instances is the same as the number of involved bays (up to 60 instances are available). The received signals are either the request for reservation from another bay or the acknowledgement.

---

<table>
<thead>
<tr>
<th>Cause No</th>
<th>Cause Description</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>-25</td>
<td>Blocked-for-close-cmd</td>
<td>The BLKCLS is active indicating that the switch is blocked for close commands.</td>
</tr>
<tr>
<td>9</td>
<td>Blocked-by-process</td>
<td>If the Blk input is connected and active indicating that the switch is dynamically blocked. Or if the OPCAP input is connected, it indicates that the operation capability of the switch is not enough to perform the command.</td>
</tr>
<tr>
<td>5</td>
<td>Position-reached</td>
<td>Switch is already in the intended position.</td>
</tr>
<tr>
<td>-31</td>
<td>Switch-not-start-moving</td>
<td>Switch did not start moving within tStartMove.</td>
</tr>
<tr>
<td>-32</td>
<td>Persistent-intermediate-state</td>
<td>The switch stopped in intermediate state for longer than tIntermediate.</td>
</tr>
<tr>
<td>-33</td>
<td>Switch-returned-to-init-pos</td>
<td>Switch returned to the initial position.</td>
</tr>
<tr>
<td>-34</td>
<td>Switch-in-bad-state</td>
<td>Switch is in a bad position.</td>
</tr>
<tr>
<td>-35</td>
<td>Not-expected-final-position</td>
<td>Switch did not reach the expected final position.</td>
</tr>
</tbody>
</table>

The OPCAP input and output are used for the CBOpCap data of a XCBR respectively SwOpCap for a XSWI. The interpretation for the command following is controlled through the setting SwitchType.
from each bay respectively, which have received a request from this bay. Also the information of valid transmission over the station bus must be received.

**Figure 144: Application principles for reservation over the station bus**

The reservation can also be realized with external wiring according to the application example in Figure 145. This solution is realized with external auxiliary relays and extra binary inputs and outputs in each IED, but without use of function blocks QCRSV and RESIN.

**Figure 145: Application principles for reservation with external wiring**

The solution in Figure 145 can also be realized over the station bus according to the application example in Figure 146. The solutions in Figure 145 and Figure 146 do not have the same high security compared to the solution in Figure 144, but instead have a higher availability, since no acknowledgment is required.
12.3.7 Interaction between modules

A typical bay with apparatus control function consists of a combination of logical nodes or functions that are described here:

- The Switch controller (SCSWI) initializes all operations for one apparatus. It is the command interface of the apparatus. It includes the position reporting as well as the control of the position.
- The Circuit breaker (SXCBR) is the process interface to the circuit breaker for the apparatus control function.
- The Circuit switch (SXSWI) is the process interface to the disconnector or the grounding switch for the apparatus control function.
- The Bay control (QCBAY) fulfils the bay-level functions for the apparatuses, such as operator place selection and blockings for the complete bay.
- The Reservation (QCRSV) deals with the reservation function.
- The Protection trip logic (SMPPTRC, 94) connects the "trip" outputs of one or more protection functions to a common "trip" to be transmitted to SXCBR.
- The Autorecloser (SMBRREC, 79) consists of the facilities to automatically close a tripped breaker with respect to a number of configurable conditions.
- The logical node Interlocking (SCILO, 3) provides the information to SCSWI whether it is permitted to operate due to the switchyard topology. The interlocking conditions are evaluated with separate logic and connected to SCILO (3).
- The Synchronism, energizing check, and synchronizing (SESRSYN, 25) calculates and compares the voltage phasor difference from both sides of an open breaker with predefined switching conditions (synchronism check). Also the case that one side is dead (energizing-check) is included.
- The Generic Automatic Process Control function, GAPC, handles generic commands from the operator to the system.

The overview of the interaction between these functions is shown in Figure 147 below.
Figure 147: Example overview of the interactions between functions in a typical bay

12.3.8 Setting guidelines

The setting parameters for the apparatus control function are set via the local HMI or PCM600.

12.3.8.1 Bay control (QCBAY)

If the parameter AllPSTOValid is set to No priority, all originators from local and remote are accepted without any priority.
If the parameter `RemoteIncStation` is set to *Yes*, commands from IEC 61850-8-1 clients at both station and remote level are accepted, when the QCBAY function is in Remote. If set to *No*, the command LocSta controls which operator place is accepted when QCBAY is in Remote. If LocSta is true, only commands from station level are accepted, otherwise only commands from remote level are accepted.

The parameter `RemoteIncStation` has only effect on the IEC 61850-8-1 communication. Further, when using IEC 61850 edition 1 communication, the parameter should be set to *Yes*, since the command LocSta is not defined in IEC 61850-8-1 edition 1.

### 12.3.8.2 Switch controller (SCSWI)

The parameter `CtlModel` specifies the type of control model according to IEC 61850. The default for control of circuit breakers, disconnectors and grounding switches the control model is set to *SBO Enh* (Select-Before-Operate) with enhanced security.

When the operation shall be performed in one step, and no monitoring of the result of the command is desired, the model direct control with normal security is used.

At control with enhanced security there is an additional supervision of the status value by the control object, which means that each command sequence must be terminated by a termination command.

The parameter `PosDependent` gives permission to operate depending on the position indication, that is, at *Always permitted* it is always permitted to operate independent of the value of the position. At *Not perm at 00/11* it is not permitted to operate if the position is in bad or intermediate state.

`tSelect` is the maximum allowed time between the select and the execute command signal, that is, the time the operator has to perform the command execution after the selection of the object to operate. When the time has expired, the selected output signal is set to false and a cause-code is given.

The time parameter `tResResponse` is the allowed time from reservation request to the feedback reservation granted from all bays involved in the reservation function. When the time has expired, the control function is reset, and a cause-code is given.

`tSynchrocheck` is the allowed time for the synchronism check function to fulfill the close conditions. When the time has expired, the function tries to start the synchronizing function. If `tSynchrocheck` is set to 0, no synchrocheck is done, before starting the synchronizing function.

The timer `tSynchronizing` supervises that the signal synchronizing in progress is obtained in SCSWI after start of the synchronizing function. The start signal for the synchronizing is set if the synchronism check conditions are not fulfilled. When the time has expired, the control function is reset, and a cause-code is given. If no synchronizing function is included, the time is set to 0, which means no start of the synchronizing function is done, and when `tSynchrocheck` has expired, the control function is reset and a cause-code is given.

`tExecutionFB` is the maximum time between the execute command signal and the command termination. When the time has expired, the control function is reset and a cause-code is given.
**tPoleDiscord** is the allowed time to have discrepancy between the poles at control of three single-phase breakers. At discrepancy an output signal is activated to be used for trip or alarm, and during a command, the control function is reset, and a cause-code is given.

*SuppressMidPos* when *On* suppresses the mid-position during the time *tIntermediate* of the connected switches.

The parameter *InterlockCheck* decides if interlock check should be done at both select and operate, Sel & Op phase, or only at operate, Op phase.

### 12.3.8.3 Switch (SXCBR/SXSWI)

*tStartMove* is the supervision time for the apparatus to start moving after a command execution is done from the SCSWI function. When the time has expired, the command supervision is reset, and a cause-code is given.

During the *tIntermediate* time, the position indication is allowed to be in an intermediate (00) state. When the time has expired, the command supervision is reset, and a cause-code is given. The indication of the mid-position at SCSWI is suppressed during this time period when the position changes from open to close or vice-versa if the parameter *SuppressMidPos* is set to *On* in the SCSWI function.

If the parameter *AdaptivePulse* is set to *Adaptive* the command output pulse resets when a new correct end position is reached. If the parameter is set to *Not adaptive* the command output pulse remains active until the timer *tOpenPulse/ClosePulse* has elapsed.

*tOpenPulse* is the output pulse length for an open command. If *AdaptivePulse* is set to *Adaptive*, it is the maximum length of the output pulse for an open command. The default length is set to 200 ms for a circuit breaker (SXCBR) and 500 ms for a disconnector (SXSWI).

*tClosePulse* is the output pulse length for a close command. If *AdaptivePulse* is set to *Adaptive*, it is the maximum length of the output pulse for an open command. The default length is set to 200 ms for a circuit breaker (SXCBR) and 500 ms for a disconnector (SXSWI).

### 12.3.8.4 Proxy for signals from switching device via GOOSE XLNPROXY

The *SwitchType* setting controls the evaluation of the operating capability. If *SwitchType* is set to *Circuit Breaker*, the input OPCAP is interpreted as a breaker operating capability, otherwise it is interpreted as a switch operating capability.

**Table 45: Operating capability values for breaker/switches**

<table>
<thead>
<tr>
<th>Value</th>
<th>Breaker operating capability, CbOpCap</th>
<th>Switch operating capability, SwOpCap</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>None</td>
<td>None</td>
</tr>
<tr>
<td>2</td>
<td>Open</td>
<td>Open</td>
</tr>
<tr>
<td>3</td>
<td>Close – Open</td>
<td>Close</td>
</tr>
<tr>
<td>4</td>
<td>Open – Close – Open</td>
<td>Close and Open</td>
</tr>
<tr>
<td>5</td>
<td>Close – Open – Close – Open</td>
<td>Larger values handled as 4, both Close and Open</td>
</tr>
<tr>
<td>6</td>
<td>Open – Close – Open – Close – Open</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>more</td>
<td></td>
</tr>
</tbody>
</table>
**tStartMove** is the supervision time for the apparatus to start moving after a command execution is done from the SCSWI function. When the time has expired, the command supervision is reset, and a cause-code is given.

During the **tIntermediate** time, the position indication is allowed to be in an intermediate (00) state. When the time has expired, the command supervision is reset, and a cause-code is given. The indication of the mid-position at SCSWI is suppressed during this time period when the position changes from open to close or vice-versa if the parameter **SuppressMidPos** is set to **On** in the SCSWI function.

In most cases, the same value can be used for both **tStartMove** and **tIntermediate** as in the source function. However, **tStartMove** may need to be increased to accommodate for the communication delays, mainly when representing a circuit breaker.

### 12.3.8.5 Bay Reserve (QCRSV)

The timer **tCancelRes** defines the supervision time for canceling the reservation, when this cannot be done by requesting bay due to for example communication failure.

When the parameter **ParamRequestx** (x=1-8) is set to **Only own bay res.** individually for each apparatus (x) in the bay, only the own bay is reserved, that is, the output for reservation request of other bays (RES_BAYS) will not be activated at selection of apparatus x.

### 12.3.8.6 Reservation input (RESIN)

With the **FutureUse** parameter set to **Bay future use** the function can handle bays not yet installed in the SA system.

### 12.4 Interlocking (3)

The main purpose of switchgear interlocking is:

- To avoid the dangerous or damaging operation of switchgear
- To enforce restrictions on the operation of the substation for other reasons for example, load configuration. Examples of the latter are to limit the number of parallel transformers to a maximum of two or to ensure that energizing is always from one side, for example, the high voltage side of a transformer.

This section only deals with the first point, and only with restrictions caused by switching devices other than the one to be controlled. This means that switch interlock, because of device alarms, is not included in this section.

Disconnectors and grounding switches have a limited switching capacity. Disconnectors may therefore only operate:

- With basically zero current. The circuit is open on one side and has a small extension. The capacitive current is small (for example, < 5A) and power transformers with inrush current are not allowed.
- To connect or disconnect a parallel circuit carrying load current. The switching voltage across the open contacts is thus virtually zero, thanks to the parallel circuit (for example, < 1% of rated voltage). Paralleling of power transformers is not allowed.
Grounding switches are allowed to connect and disconnect grounding of isolated points. Due to capacitive or inductive coupling there may be some voltage (for example < 40% of rated voltage) before grounding and some current (for example < 100A) after grounding of a line.

Circuit breakers are usually not interlocked. Closing is only interlocked against running disconnectors in the same bay, and the bus-coupler opening is interlocked during a busbar transfer.

The positions of all switching devices in a bay and from some other bays determine the conditions for operational interlocking. Conditions from other stations are usually not available. Therefore, a line grounding switch is usually not fully interlocked. The operator must be convinced that the line is not energized from the other side before closing the grounding switch. As an option, a voltage indication can be used for interlocking. Take care to avoid a dangerous enable condition at the loss of a VT secondary voltage, for example, because of a blown fuse.

The switch positions used by the operational interlocking logic are obtained from auxiliary contacts or position sensors. For each end position (open or closed) a true indication is needed - thus forming a double indication. The apparatus control function continuously checks its consistency. If neither condition is high (1 or TRUE), the switch may be in an intermediate position, for example, moving. This dynamic state may continue for some time, which in the case of disconnectors may be up to 10 seconds. Should both indications stay low for a longer period, the position indication will be interpreted as *unknown*. If both indications stay high, something is wrong, and the state is again treated as *unknown*.

In both cases an alarm is sent to the operator. Indications from position sensors shall be self-checked and system faults indicated by a fault signal. In the interlocking logic, the signals are used to avoid dangerous *enable* or *release* conditions. When the switching state of a switching device cannot be determined operation is not permitted.

For switches with an individual operation gear per phase, the evaluation must consider possible phase discrepancies. This is done with the aid of an *AND-function* for all three phases in each apparatus for both open and close indications. Phase discrepancies will result in an unknown double indication state.

### 12.4.1 Configuration guidelines

The following sections describe how the interlocking for a certain switchgear configuration can be realized in the IED by using standard interlocking modules and their interconnections. They also describe the configuration settings. The inputs for delivery specific conditions (Qx_EXy) are set to 1=TRUE if they are not used, except in the following cases:

- 989_EX2 and 989_EX4 in modules BH_LINE_A and BH_LINE_B
- 152_EX3 in module AB_TRAFO

when they are set to 0=FALSE.

### 12.4.2 Interlocking for line bay ABC_LINE (3)

#### 12.4.2.1 Application

The interlocking for line bay (ABC_LINE, 3) function is used for a line connected to a double busbar arrangement with a transfer busbar according to figure 148. The function can also be used for a
double busbar arrangement without transfer busbar or a single busbar arrangement with/without transfer busbar.

![Diagram of Switchyard Layout](en04000478_ansi.vsd)

**Figure 148: Switchyard layout ABC_LINE (3)**

The signals from other bays connected to the module ABC_LINE (3) are described below.

### 12.4.2.2 Signals from bypass busbar

To derive the signals:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BB7_D_OP</td>
<td>All line disconnectors on bypass WA7 except in the own bay are open.</td>
</tr>
<tr>
<td>VP_BB7_D</td>
<td>The switch status of disconnectors on bypass busbar WA7 are valid.</td>
</tr>
<tr>
<td>EXDU_BPB</td>
<td>No transmission error from any bay containing disconnectors on bypass busbar WA7</td>
</tr>
</tbody>
</table>

These signals from each line bay (ABC_LINE, 3) except that of the own bay are needed:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>789OPTR</td>
<td>789 is open</td>
</tr>
<tr>
<td>VP789TR</td>
<td>The switch status for 789 is valid.</td>
</tr>
<tr>
<td>EXDU_BPB</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

For bay n, these conditions are valid:
12.4.2.3 Signals from bus-coupler

If the busbar is divided by bus-section disconnectors into bus sections, the busbar-busbar connection could exist via the bus-section disconnector and bus-coupler within the other bus section.

To derive the signals:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BC_12_CL</td>
<td>A bus-coupler connection exists between busbar WA1 and WA2.</td>
</tr>
<tr>
<td>BC_17_OP</td>
<td>No bus-coupler connection between busbar WA1 and WA7.</td>
</tr>
<tr>
<td>BC_17_CL</td>
<td>A bus-coupler connection exists between busbar WA1 and WA7.</td>
</tr>
<tr>
<td>BC_27_OP</td>
<td>No bus-coupler connection between busbar WA2 and WA7.</td>
</tr>
<tr>
<td>BC_27_CL</td>
<td>A bus-coupler connection exists between busbar WA2 and WA7.</td>
</tr>
<tr>
<td>VP_BC_12</td>
<td>The switch status of BC_12 is valid.</td>
</tr>
</tbody>
</table>

Table continues on next page
VP_BC_17: The switch status of BC_17 is valid.
VP_BC_27: The switch status of BC_27 is valid.
EXDU_BC: No transmission error from any bus-coupler bay (BC).

These signals from each bus-coupler bay (ABC_BC) are needed:

VPBC12TR: The switch status of BC_12 is valid.
VPBC17TR: The switch status of BC_17 is valid.
VPBC27TR: The switch status of BC_27 is valid.

EXDU_BC: No transmission error from the bay that contains the above information.

These signals from each bus-section disconnector bay (A1A2_DC) are also needed. For B1B2_DC, corresponding signals from busbar B are used. The same type of module (A1A2_DC) is used for different busbars, that is, for both bus-section disconnector A1A2_DC and B1B2_DC.

DCOPTR: The bus-section disconnector is open.
DCCLTR: The bus-section disconnector is closed.
VPS1S2TR: The switch status of bus-section coupler BS is valid.
EXDU_DC: No transmission error from the bay that contains the above information.

If the busbar is divided by bus-section circuit breakers, the signals from the bus-section coupler bay (A1A2_BS), rather than the bus-section disconnector bay (A1A2_DC) must be used. For B1B2_BS, corresponding signals from busbar B are used. The same type of module (A1A2_BS) is used for different busbars, that is, for both bus-section circuit breakers A1A2_BS and B1B2_BS.

S1S2OPTR: No bus-section coupler connection between bus-sections 1 and 2.
S1S2CLTR: A bus-section coupler connection exists between bus-sections 1 and 2.
VPS1S2TR: The switch status of bus-section coupler BS is valid.
EXDU BS: No transmission error from the bay that contains the above information.
For a line bay in section 1, these conditions are valid:

- **BC12CLTR (sect.1)**
- **DCCLTR (A1A2)**
- **DCCLTR (B1B2)**
- **BC12CLTR (sect.2)**

- **OR**

- **VPBC12TR (sect.1)**
- **VPDCTR (A1A2)**
- **VPDCTR (B1B2)**
- **VPBC12TR (sect.2)**

- **BC17OPTR (sect.1)**
- **DCOPTR (A1A2)**
- **BC17OPTR (sect.2)**

- **BC17CLTR (sect.1)**
- **DCCLTR (A1A2)**
- **BC17CLTR (sect.2)**

- **VPBC17TR (sect.1)**
- **VPDCTR (A1B2)**
- **VPBC17TR (sect.2)**

- **BC27OPTR (sect.1)**
- **DCOPTR (B1B2)**
- **BC27OPTR (sect.2)**

- **BC27CLTR (sect.1)**
- **DCCLTR (B1B2)**
- **BC27CLTR (sect.2)**

- **VPBC27TR (sect.1)**
- **VPDCTR (B1B2)**
- **VPBC27TR (sect.2)**

- **EXDU_BC (sect.1)**
- **EXDU_DC (A1A2)**
- **EXDU_DC (B1B2)**
- **EXDU_BC (sect.2)**

**Figure 151: Signals to a line bay in section 1 from the bus-coupler bays in each section**

For a line bay in section 2, the same conditions as above are valid by changing section 1 to section 2 and vice versa.
12.4.2.4 Configuration setting

If there is no bypass busbar and therefore no 789 disconnector, then the interlocking for 789 is not used. The states for 789, 7189G, BB7_D, BC_17, BC_27 are set to open by setting the appropriate module inputs as follows. In the functional block diagram, 0 and 1 are designated 0=FALSE and 1=TRUE:

- 789_OP = 1
- 789_CL = 0
- 7189G_OP = 1
- 7189G_CL = 0
- BB7_D_OP = 1
- BC_17_OP = 1
- BC_17_CL = 0
- BC_27_OP = 1
- BC_27_CL = 0
- EXDU_BPB = 1
- VP_BB7_D = 1
- VP_BC_17 = 1
- VP_BC_27 = 1

If there is no second busbar WA2 and therefore no 289 disconnector, then the interlocking for 289 is not used. The state for 289, 2189G, BC_12, BC_27 are set to open by setting the appropriate module inputs as follows. In the functional block diagram, 0 and 1 are designated 0=FALSE and 1=TRUE:

- 289_OP = 1
- 289_CL = 0
- 2189G_OP = 1
- 2189G_CL = 0
- BC_12_CL = 0
- BC_27_OP = 1
- BC_27_CL = 0
- VP_BC_12 = 1

12.4.3 Interlocking for bus-coupler bay ABC_BC (3)
12.4.3.1 Application

The interlocking for bus-coupler bay (ABC_BC, 3) function is used for a bus-coupler bay connected to a double busbar arrangement according to figure 152. The function can also be used for a single busbar arrangement with transfer busbar or double busbar arrangement without transfer busbar.

![Diagram of switchyard layout ABC_BC (3)](en04000514_ansi.vsd)

*Figure 152: Switchyard layout ABC_BC (3)*

12.4.3.2 Configuration

The signals from the other bays connected to the bus-coupler module ABC_BC are described below.

12.4.3.3 Signals from all feeders

To derive the signals:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BBTR_OP</td>
<td>No busbar transfer is in progress concerning this bus-coupler.</td>
</tr>
<tr>
<td>VP_BBTR</td>
<td>The switch status is valid for all apparatuses involved in the busbar transfer.</td>
</tr>
<tr>
<td>EXDU_12</td>
<td>No transmission error from any bay connected to the WA1/WA2 busbars.</td>
</tr>
</tbody>
</table>

These signals from each line bay (ABC_LINE), each transformer bay (AB_TRAFO), and bus-coupler bay (ABC_BC), except the own bus-coupler bay are needed:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Q1289OPTR</td>
<td>189 or 289 or both are open.</td>
</tr>
<tr>
<td>VP1289TR</td>
<td>The switch status of 189 and 289 are valid.</td>
</tr>
<tr>
<td>EXDU_12</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

For bus-coupler bay n, these conditions are valid:
Figure 153: Signals from any bays in bus-coupler bay n

If the busbar is divided by bus-section disconnectors into bus-sections, the signals BBTR are connected in parallel - if both bus-section disconnectors are closed. So for the basic project-specific logic for BBTR above, add this logic:

Signal

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>DCOPTR</td>
<td>The bus-section disconnector is open.</td>
</tr>
<tr>
<td>VPDCTR</td>
<td>The switch status of bus-section disconnector DC is valid.</td>
</tr>
<tr>
<td>EXDU_DC</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

Figure 154: Busbars divided by bus-section disconnectors (circuit breakers)

The following signals from each bus-section disconnector bay (A1A2_DC) are needed. For B1B2_DC, corresponding signals from busbar B are used. The same type of module (A1A2_DC) is used for different busbars, that is, for both bus-section disconnector A1A2_DC and B1B2_DC.
### Signal

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>S1S2OPTR</td>
<td>No bus-section coupler connection between bus-sections 1 and 2.</td>
</tr>
<tr>
<td>VPS1S2TR</td>
<td>The switch status of bus-section coupler BS is valid.</td>
</tr>
<tr>
<td>EXDU_BS</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

For a bus-coupler bay in section 1, these conditions are valid:

- **BBTR_OP (sect.1)**
  - OR
  - **DCOPTR (A1A2)**
  - **DCOPTR (B1B2)**
  - **BBTR_OP (sect.2)**

- **VP_BBTR (sect.1)**
  - AND
  - **VPDCTR (A1A2)**
  - **VPDCTR (B1B2)**
  - **VP_BBTR (sect.2)**

- **EXDU_12 (sect.1)**
  - AND
  - **EXDU_DC (A1A2)**
  - **EXDU_DC (B1B2)**
  - **EXDU_12 (sect.2)**

**Figure 155: Signals to a bus-coupler bay in section 1 from any bays in each section**

For a bus-coupler bay in section 2, the same conditions as above are valid by changing section 1 to section 2 and vice versa.

#### 12.4.3.4 Signals from bus-coupler

If the busbar is divided by bus-section disconnectors into bus-sections, the signals BC_12 from the busbar coupler of the other busbar section must be transmitted to the own busbar coupler if both disconnectors are closed.

**Figure 156: Busbars divided by bus-section disconnectors (circuit breakers)**

To derive the signals:
Another bus-coupler connection exists between busbar WA1 and WA2.

The switch status of BC_12 is valid.

No transmission error from any bus-coupler bay (BC).

These signals from each bus-coupler bay (ABC_BC), except the own bay, are needed:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BC12CLTR</td>
<td>A bus-coupler connection through the own bus-coupler exists between busbar WA1 and WA2.</td>
</tr>
<tr>
<td>VPBC12TR</td>
<td>The switch status of BC_12 is valid.</td>
</tr>
<tr>
<td>EXDU_BC</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

These signals from each bus-section disconnector bay (A1A2_DC) are also needed. For B1B2_DC, corresponding signals from busbar B are used. The same type of module (A1A2_DC) is used for different busbars, that is, for both bus-section disconnector A1A2_DC and B1B2_DC.

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>DCCLTR</td>
<td>The bus-section disconnector is closed.</td>
</tr>
<tr>
<td>VPDCCTR</td>
<td>The switch status of bus-section disconnector DC is valid.</td>
</tr>
<tr>
<td>EXDU_DC</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

If the busbar is divided by bus-section circuit breakers, the signals from the bus-section coupler bay (A1A2_BS), rather than the bus-section disconnector bay (A1A2_DC), must be used. For B1B2_BS, corresponding signals from busbar B are used. The same type of module (A1A2_BS) is used for different busbars, that is, for both bus-section circuit breakers A1A2_BS and B1B2_BS.

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>S1S2CLTR</td>
<td>A bus-section coupler connection exists between bus sections 1 and 2.</td>
</tr>
<tr>
<td>VPS1S2TR</td>
<td>The switch status of bus-section coupler BS is valid.</td>
</tr>
<tr>
<td>EXDU_BS</td>
<td>No transmission error from the bay containing the above information.</td>
</tr>
</tbody>
</table>

For a bus-coupler bay in section 1, these conditions are valid:
For a bus-coupler bay in section 2, the same conditions as above are valid by changing section 1 to section 2 and vice versa.

### 12.4.3.5 Configuration setting

If there is no bypass busbar and therefore no 289 and 789 disconnectors, then the interlocking for 289 and 789 is not used. The states for 289, 789, 7189G are set to open by setting the appropriate module inputs as follows. In the functional block diagram, 0 and 1 are designated 0=FALSE and 1=TRUE:

- 289\_OP = 1
- 289\_CL = 0
- 789\_OP = 1
- 789\_CL = 0
- 7189G\_OP = 1
- 7189G\_CL = 0

If there is no second busbar B and therefore no 289 and 2089 disconnectors, then the interlocking for 289 and 2089 are not used. The states for 289, 2089, 2189G, BC\_12, BBTR are set to open by setting the appropriate module inputs as follows. In the functional block diagram, 0 and 1 are designated 0=FALSE and 1=TRUE:

- 289\_OP = 1
- 289\_CL = 0
- 2089\_OP = 1
- 2089\_CL = 0
- 2189G\_OP = 1
- 2189G\_CL = 0
- BC\_12\_CL = 0
- VP\_BC\_12 = 1

---

**Figure 157: Signals to a bus-coupler bay in section 1 from a bus-coupler bay in another section**

For a bus-coupler bay in section 2, the same conditions as above are valid by changing section 1 to section 2 and vice versa.
12.4.4 Interlocking for transformer bay AB_TRAFO (3)

12.4.4.1 Application

The interlocking for transformer bay (AB_TRAFO, 3) function is used for a transformer bay connected to a double busbar arrangement according to figure 158. The function is used when there is no disconnector between circuit breaker and transformer. Otherwise, the interlocking for line bay (ABC_LINE, 3) function can be used. This function can also be used in single busbar arrangements.

Figure 158: Switchyard layout AB_TRAFO (3)

The signals from other bays connected to the module AB_TRAFO are described below.

12.4.4.2 Signals from bus-coupler

If the busbar is divided by bus-section disconnectors into bus-sections, the busbar-busbar connection could exist via the bus-section disconnector and bus-coupler within the other bus-section.
The project-specific logic for input signals concerning bus-coupler are the same as the specific logic for the line bay (ABC_LINE):

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BC_12_CL</td>
<td>A bus-coupler connection exists between busbar WA1 and WA2.</td>
</tr>
<tr>
<td>VP_BC_12</td>
<td>The switch status of BC_12 is valid.</td>
</tr>
<tr>
<td>EXDU_BC</td>
<td>No transmission error from bus-coupler bay (BC).</td>
</tr>
</tbody>
</table>

The logic is identical to the double busbar configuration “Signals from bus-coupler“.

### 12.4.4.3 Configuration setting

If there are no second busbar B and therefore no 289 disconnector, then the interlocking for 289 is not used. The state for 289, 2189G, BC_12 are set to open by setting the appropriate module inputs as follows. In the functional block diagram, 0 and 1 are designated 0=FALSE and 1=TRUE:

- 289_OP = 1
- 289QB2_CL = 0
- 2189G_OP = 1
- 2189G_CL = 0
- BC_12_CL = 0
- VP_BC_12 = 1

If there is no second busbar B at the other side of the transformer and therefore no 489 disconnector, then the state for 489 is set to open by setting the appropriate module inputs as follows:

- 489_OP = 1
- 489_CL = 0
12.4.5 Interlocking for bus-section breaker A1A2_BS (3)

12.4.5.1 Application

The interlocking for bus-section breaker (A1A2_BS ,3) function is used for one bus-section circuit breaker between section 1 and 2 according to figure 160. The function can be used for different busbars, which includes a bus-section circuit breaker.

![Diagram of switchyard layout A1A2_BS (3)](en04000516_ansi.vsd)

*Figure 160: Switchyard layout A1A2_BS (3)*

The signals from other bays connected to the module A1A2_BS are described below.

12.4.5.2 Signals from all feeders

If the busbar is divided by bus-section circuit breakers into bus-sections and both circuit breakers are closed, the opening of the circuit breaker must be blocked if a bus-coupler connection exists between busbars on one bus-section side and if on the other bus-section side a busbar transfer is in progress:

![Diagram of busbars divided by bus-section circuit breakers](en04000489_ansi.vsd)

*Figure 161: Busbars divided by bus-section circuit breakers*

To derive the signals:
No busbar transfer is in progress concerning this bus-section.

The switch status of BBTR is valid.

No transmission error from any bay connected to busbar 1(A) and 2(B).

These signals from each line bay (ABC_LINE), each transformer bay (AB_TRAFO), and bus-coupler bay (ABC_BC) are needed:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1289OPTR</td>
<td>189 or 289 or both are open.</td>
</tr>
<tr>
<td>VP1289TR</td>
<td>The switch status of 189 and 289 are valid.</td>
</tr>
<tr>
<td>EXDU_12</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

These signals from each bus-coupler bay (ABC_BC) are needed:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BC12OPTR</td>
<td>No bus-coupler connection through the own bus-coupler between busbar WA1 and WA2.</td>
</tr>
<tr>
<td>VPBC12TR</td>
<td>The switch status of BC_12 is valid.</td>
</tr>
<tr>
<td>EXDU_BC</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

These signals from the bus-section circuit breaker bay (A1A2_BS, B1B2_BS) are needed.

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>S1S2OPTR</td>
<td>No bus-section coupler connection between bus-sections 1 and 2.</td>
</tr>
<tr>
<td>VPS1S2TR</td>
<td>The switch status of bus-section coupler BS is valid.</td>
</tr>
<tr>
<td>EXDU_BS</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

For a bus-section circuit breaker between A1 and A2 section busbars, these conditions are valid:
Figure 162: Signals from any bays for a bus-section circuit breaker between sections A1 and A2

For a bus-section circuit breaker between B1 and B2 section busbars, these conditions are valid:
12.4.5.3 Configuration setting

If there is no other busbar via the busbar loops that are possible, then either the interlocking for the 152 open circuit breaker is not used or the state for BBTR is set to open. That is, no busbar transfer is in progress in this bus-section:

- BBTR_OP = 1
- VP_BBTR = 1

12.4.6 Interlocking for bus-section disconnector A1A2_DC (3)
12.4.6.1 Application

The interlocking for bus-section disconnector (A1A2_DC, 3) function is used for one bus-section disconnector between section 1 and 2 according to figure 164. A1A2_DC (3) function can be used for different busbars, which includes a bus-section disconnector.

Figure 164: Switchyard layout A1A2_DC (3)

The signals from other bays connected to the module A1A2_DC are described below.

12.4.6.2 Signals in single breaker arrangement

If the busbar is divided by bus-section disconnectors, the condition no other disconnector connected to the bus-section must be made by a project-specific logic.

The same type of module (A1A2_DC) is used for different busbars, that is, for both bus-section disconnector A1A2_DC and B1B2_DC. But for B1B2_DC, corresponding signals from busbar B are used.

Figure 165: Busbars divided by bus-section disconnectors (circuit breakers)

To derive the signals:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>S1DC_OP</td>
<td>All disconnectors on bus-section 1 are open.</td>
</tr>
<tr>
<td>S2DC_OP</td>
<td>All disconnectors on bus-section 2 are open.</td>
</tr>
<tr>
<td>VPS1_DC</td>
<td>The switch status of disconnectors on bus-section 1 is valid.</td>
</tr>
<tr>
<td>VPS2_DC</td>
<td>The switch status of disconnectors on bus-section 2 is valid.</td>
</tr>
<tr>
<td>EXDU_BB</td>
<td>No transmission error from any bay that contains the above information.</td>
</tr>
</tbody>
</table>

These signals from each line bay (ABC_LINE), each transformer bay (AB_TRAFO), and each bus-coupler bay (ABC_BC) are needed:
Signal

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>189Optr</td>
<td>189 is open.</td>
</tr>
<tr>
<td>289Optr</td>
<td>289 is open (AB_TRAFO, ABC_LINE).</td>
</tr>
<tr>
<td>22089Optr</td>
<td>289 and 2089 are open (ABC_BC).</td>
</tr>
<tr>
<td>VP189TR</td>
<td>The switch status of 189 is valid.</td>
</tr>
<tr>
<td>VP289TR</td>
<td>The switch status of 289 is valid.</td>
</tr>
<tr>
<td>V22089TR</td>
<td>The switch status of 289 and 2089 are valid.</td>
</tr>
<tr>
<td>EXDU_BB</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

If there is an additional bus-section disconnector, the signal from the bus-section disconnector bay (A1A2_DC) must be used:

Signal

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>DCOPTR</td>
<td>The bus-section disconnector is open.</td>
</tr>
<tr>
<td>VPDCTR</td>
<td>The switch status of bus-section disconnector DC is valid.</td>
</tr>
<tr>
<td>EXDU_DC</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

If there is an additional bus-section circuit breaker rather than an additional bus-section disconnector the signals from the bus-section, circuit-breaker bay (A1A2_BS) rather than the bus-section disconnector bay (A1A2_DC) must be used:

Signal

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>189Optr</td>
<td>189 is open.</td>
</tr>
<tr>
<td>289Optr</td>
<td>289 is open.</td>
</tr>
<tr>
<td>VP189TR</td>
<td>The switch status of 189 is valid.</td>
</tr>
<tr>
<td>VP289TR</td>
<td>The switch status of 289 is valid.</td>
</tr>
<tr>
<td>EXDU_BS</td>
<td>No transmission error from the bay BS (bus-section coupler bay) that contains the above information.</td>
</tr>
</tbody>
</table>

For a bus-section disconnector, these conditions from the A1 busbar section are valid:

![Diagram](en04000494_ansi.vsd)

**Figure 166: Signals from any bays in section A1 to a bus-section disconnector**
For a bus-section disconnector, these conditions from the A2 busbar section are valid:

- 189OPTR (bay 1/sect.A2)
- 189OPTR (bay n/sect.A2)
- DCOPT (A2/A3)
- VP189TR (bay 1/sect.A2)
- VP189TR (bay n/sect.A2)
- VPDCTR (A2/A3)
- EXDU_BB (bay 1/sect.A2)
- EXDU_BB (bay n/sect.A2)
- VP189TR (bay 1/sect.A2)
- VP189TR (bay n/sect.A2)
- VPDCTR (A2/A3)
- EXDU_BB (bay 1/sect.A2)
- EXDU_BB (bay n/sect.A2)
- 189OPTR (bay 1/sect.A2)
- 189OPTR (bay n/sect.A2)
- DCOPT (A2/A3)
- VP189TR (bay 1/sect.A2)
- VP189TR (bay n/sect.A2)
- VPDCTR (A2/A3)
- EXDU_BB (bay 1/sect.A2)
- EXDU_BB (bay n/sect.A2)

**Figure 167: Signals from any bays in section A2 to a bus-section disconnector**

For a bus-section disconnector, these conditions from the B1 busbar section are valid:

- 289OPTR (22089OTR)(bay 1/sect.B1)
- 289OPTR (22089OTR)(bay n/sect.B1)
- VP289TR (V22089TR)(bay 1/sect.B1)
- VP289TR (V22089TR)(bay n/sect.B1)
- EXDU_BB (bay 1/sect.B1)
- EXDU_BB (bay n/sect.B1)

**Figure 168: Signals from any bays in section B1 to a bus-section disconnector**

For a bus-section disconnector, these conditions from the B2 busbar section are valid:
Figure 169: Signals from any bays in section B2 to a bus-section disconnector

12.4.6.3 Signals in double-breaker arrangement

If the busbar is divided by bus-section disconnectors, the condition for the busbar disconnector bay no other disconnector connected to the bus-section must be made by a project-specific logic.

The same type of module (A1A2_DC) is used for different busbars, that is, for both bus-section disconnector A1A2_DC and B1B2_DC. But for B1B2_DC, corresponding signals from busbar B are used.

Figure 170: Busbars divided by bus-section disconnectors (circuit breakers)

To derive the signals:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>S1DC_OP</td>
<td>All disconnectors on bus-section 1 are open.</td>
</tr>
<tr>
<td>S2DC_OP</td>
<td>All disconnectors on bus-section 2 are open.</td>
</tr>
<tr>
<td>VPS1_DC</td>
<td>The switch status of all disconnectors on bus-section 1 is valid.</td>
</tr>
<tr>
<td>VPS2_DC</td>
<td>The switch status of all disconnectors on bus-section 2 is valid.</td>
</tr>
<tr>
<td>EXDU_BB</td>
<td>No transmission error from double-breaker bay (DB) that contains the above information.</td>
</tr>
</tbody>
</table>
These signals from each double-breaker bay (DB_BUS) are needed:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>189OPTR</td>
<td>189 is open.</td>
</tr>
<tr>
<td>289OPTR</td>
<td>289 is open.</td>
</tr>
<tr>
<td>VP189TR</td>
<td>The switch status of 189 is valid.</td>
</tr>
<tr>
<td>VP289TR</td>
<td>The switch status of 289 is valid.</td>
</tr>
<tr>
<td>EXDU_DB</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

The logic is identical to the double busbar configuration “Signals in single breaker arrangement”.

For a bus-section disconnector, these conditions from the A1 busbar section are valid:

- 189OPTR (bay 1/sect.A1) \(\cdots\)
- 189OPTR (bay n/sect.A1) \(\cdots\)
- VP189TR (bay 1/sect.A1) \(\cdots\)
- VP189TR (bay n/sect.A1) \(\cdots\)
- EXDU_DB (bay 1/sect.A1) \(\cdots\)
- EXDU_DB (bay n/sect.A1) \(\cdots\)

\[\text{AND}\]

\[\text{S1DC\_OP}\]

\[\text{VPS1\_DC}\]

\[\text{EXDU\_BB}\]

Figure 171: Signals from double-breaker bays in section A1 to a bus-section disconnector

For a bus-section disconnector, these conditions from the A2 busbar section are valid:

- 189OPTR (bay 1/sect.A2) \(\cdots\)
- 189OPTR (bay n/sect.A2) \(\cdots\)
- VP189TR (bay 1/sect.A2) \(\cdots\)
- VP189TR (bay n/sect.A2) \(\cdots\)
- EXDU_DB (bay 1/sect.A2) \(\cdots\)
- EXDU_DB (bay n/sect.A2) \(\cdots\)

\[\text{AND}\]

\[\text{S2DC\_OP}\]

\[\text{VPS2\_DC}\]

\[\text{EXDU\_BB}\]

Figure 172: Signals from double-breaker bays in section A2 to a bus-section disconnector

For a bus-section disconnector, these conditions from the B1 busbar section are valid:
12.4.6.4 Signals in breaker and a half arrangement

If the busbar is divided by bus-section disconnectors, the condition for the busbar disconnector bay no other disconnector connected to the bus-section must be made by a project-specific logic.

The same type of module (A1A2_DC) is used for different busbars, that is, for both bus-section disconnector A1A2_DC and B1B2_DC. But for B1B2_DC, corresponding signals from busbar B are used.

Figure 175: Busbars divided by bus-section disconnectors (circuit breakers)
The project-specific logic is the same as for the logic for the double-breaker configuration.

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>S1DC_OP</td>
<td>All disconnectors on bus-section 1 are open.</td>
</tr>
<tr>
<td>S2DC_OP</td>
<td>All disconnectors on bus-section 2 are open.</td>
</tr>
<tr>
<td>VPS1_DC</td>
<td>The switch status of disconnectors on bus-section 1 is valid.</td>
</tr>
<tr>
<td>VPS2_DC</td>
<td>The switch status of disconnectors on bus-section 2 is valid.</td>
</tr>
<tr>
<td>EXDU_BB</td>
<td>No transmission error from breaker and a half (BH) that contains the above information.</td>
</tr>
</tbody>
</table>

**12.4.7 Interlocking for busbar grounding switch BB_ES (3)**

**12.4.7.1 Application**

The interlocking for busbar grounding switch (BB_ES, 3) function is used for one busbar grounding switch on any busbar parts according to figure 176.

![Figure 176: Switchyard layout BB_ES (3)](en04000504.vsd)

The signals from other bays connected to the module BB_ES are described below.

**12.4.7.2 Signals in single breaker arrangement**

The busbar grounding switch is only allowed to operate if all disconnectors of the bus-section are open.

![Figure 177: Busbars divided by bus-section disconnectors (circuit breakers)](en04000505_ansl.vsd)

To derive the signals:
These signals from each line bay (ABC_LINE), each transformer bay (AB_TRAFO), and each bus-coupler bay (ABC_BC) are needed:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BB_DC_OP</td>
<td>All disconnectors on this part of the busbar are open.</td>
</tr>
<tr>
<td>VP_BB_DC</td>
<td>The switch status of all disconnector on this part of the busbar is valid.</td>
</tr>
<tr>
<td>EXDU_BB</td>
<td>No transmission error from any bay containing the above information.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>189OPTR</td>
<td>189 is open.</td>
</tr>
<tr>
<td>289OPTR</td>
<td>289 is open (AB_TRAFO, ABC_LINE)</td>
</tr>
<tr>
<td>22089OTR</td>
<td>289 and 2089 are open (ABC_BC)</td>
</tr>
<tr>
<td>789OPTR</td>
<td>789 is open.</td>
</tr>
<tr>
<td>VP189TR</td>
<td>The switch status of 189 is valid.</td>
</tr>
<tr>
<td>VP289TR</td>
<td>The switch status of 289 is valid.</td>
</tr>
<tr>
<td>V22089TR</td>
<td>The switch status of 289 and 2089 is valid.</td>
</tr>
<tr>
<td>VP789TR</td>
<td>The switch status of 789 is valid.</td>
</tr>
<tr>
<td>EXDU_BB</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

These signals from each bus-section disconnector bay (A1A2_DC) are also needed. For B1B2_DC, corresponding signals from busbar B are used. The same type of module (A1A2_DC) is used for different busbars, that is, for both bus-section disconnectors A1A2_DC and B1B2_DC.

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>DCOPTTR</td>
<td>The bus-section disconnector is open.</td>
</tr>
<tr>
<td>VPDCCTR</td>
<td>The switch status of bus-section disconnector DC is valid.</td>
</tr>
<tr>
<td>EXDU_DC</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

If no bus-section disconnector exists, the signal DCOPTTR, VPDCCTR and EXDU_DC are set to 1 (TRUE).

If the busbar is divided by bus-section circuit breakers, the signals from the bus-section coupler bay (A1A2_BS) rather than the bus-section disconnector bay (A1A2_DC) must be used. For B1B2_BS, corresponding signals from busbar B are used. The same type of module (A1A2_BS) is used for different busbars, that is, for both bus-section circuit breakers A1A2_BS and B1B2_BS.

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>189OPTR</td>
<td>189 is open.</td>
</tr>
<tr>
<td>289OPTR</td>
<td>289 is open.</td>
</tr>
<tr>
<td>VP189TR</td>
<td>The switch status of 189 is valid.</td>
</tr>
<tr>
<td>VP289TR</td>
<td>The switch status of 289 is valid.</td>
</tr>
<tr>
<td>EXDU_BS</td>
<td>No transmission error from the bay BS (bus-section coupler bay) that contains the above information.</td>
</tr>
</tbody>
</table>
For a busbar grounding switch, these conditions from the A1 busbar section are valid:

```
189OPTR (bay 1/sect.A1) AND BB_DC_OP
189OPTR (bay n/sect.A1) DCOPT (A1/A2) AND BP_DC
VP189TR (bay 1/sect.A1) AND VP_BB_DC
VP189TR (bay n/sect.A1) VPDCTR (A1/A2) AND
EXDU_BB (bay 1/sect.A1) AND EXDU_BB
EXDU_BB (bay n/sect.A1) EXDU_DC (A1/A2)
```

*Figure 178: Signals from any bays in section A1 to a busbar grounding switch in the same section*

For a busbar grounding switch, these conditions from the A2 busbar section are valid:

```
189OPTR (bay 1/sect.A2) AND BB_DC_OP
189OPTR (bay n/sect.A2) DCOPT (A1/A2) AND BP_DC
VP189TR (bay 1/sect.A2) AND VP_BB_DC
VP189TR (bay n/sect.A2) VPDCTR (A1/A2) AND
EXDU_BB (bay 1/sect.A2) AND EXDU_BB
EXDU_BB (bay n/sect.A2) EXDU_DC (A1/A2)
```

*Figure 179: Signals from any bays in section A2 to a busbar grounding switch in the same section*

For a busbar grounding switch, these conditions from the B1 busbar section are valid:
Figure 180: Signals from any bays in section B1 to a busbar grounding switch in the same section

For a busbar grounding switch, these conditions from the B2 busbar section are valid:

Figure 181: Signals from any bays in section B2 to a busbar grounding switch in the same section

For a busbar grounding switch on bypass busbar C, these conditions are valid:
12.4.7.3 Signals in double-breaker arrangement

The busbar grounding switch is only allowed to operate if all disconnectors of the bus section are open.

![Diagram of busbar sections and disconnectors]

To derive the signals:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BB_DC_OP</td>
<td>All disconnectors of this part of the busbar are open.</td>
</tr>
<tr>
<td>VP_BB_DC</td>
<td>The switch status of all disconnectors on this part of the busbar are valid.</td>
</tr>
<tr>
<td>EXDU_BB</td>
<td>No transmission error from any bay that contains the above information.</td>
</tr>
</tbody>
</table>

These signals from each double-breaker bay (DB_BUS) are needed:

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>189OPTR</td>
<td>189 is open.</td>
</tr>
<tr>
<td>289OPTR</td>
<td>289 is open.</td>
</tr>
<tr>
<td>VP189TR</td>
<td>The switch status of 189 is valid.</td>
</tr>
<tr>
<td>VP289TR</td>
<td>The switch status of 289 is valid.</td>
</tr>
<tr>
<td>EXDU_DB</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>
These signals from each bus-section disconnector bay (A1A2_DC) are also needed. For B1B2_DC, corresponding signals from busbar B are used. The same type of module (A1A2_DC) is used for different busbars, that is, for both bus-section disconnectors A1A2_DC and B1B2_DC.

**Signal**

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>DCOPTR</td>
<td>The bus-section disconnector is open.</td>
</tr>
<tr>
<td>VPDCTR</td>
<td>The switch status of bus-section disconnector DC is valid.</td>
</tr>
<tr>
<td>EXDU_DC</td>
<td>No transmission error from the bay that contains the above information.</td>
</tr>
</tbody>
</table>

The logic is identical to the double busbar configuration described in section “Signals in single breaker arrangement”.

### 12.4.7.4 Signals in breaker and a half arrangement

The busbar grounding switch is only allowed to operate if all disconnectors of the bus-section are open.

**Figure 184: Busbars divided by bus-section disconnectors (circuit breakers)**

The project-specific logic are the same as for the logic for the double busbar configuration described in section “Signals in single breaker arrangement”.

**Signal**

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>BB_DC_OP</td>
<td>All disconnectors on this part of the busbar are open.</td>
</tr>
<tr>
<td>VP_BB_DC</td>
<td>The switch status of all disconnectors on this part of the busbar is valid.</td>
</tr>
<tr>
<td>EXDU_BB</td>
<td>No transmission error from any bay that contains the above information.</td>
</tr>
</tbody>
</table>

### 12.4.8 Interlocking for double CB bay DB (3)

#### 12.4.8.1 Application

The interlocking for a double busbar double circuit breaker bay including DB_BUS_A (3), DB_BUS_B (3) and DB_LINE (3) functions are used for a line connected to a double busbar arrangement according to figure **185**.
Three types of interlocking modules per double circuit breaker bay are defined. DB_BUS_A (3) handles the circuit breaker QA1 that is connected to busbar WA1 and the disconnectors and grounding switches of this section. DB_BUS_B (3) handles the circuit breaker QA2 that is connected to busbar WA2 and the disconnectors and grounding switches of this section.

For a double circuit-breaker bay, the modules DB_BUS_A, DB_LINE and DB_BUS_B must be used.

### 12.4.8.2 Configuration setting

For application without 989 and 989G, just set the appropriate inputs to open state and disregard the outputs. In the functional block diagram, 0 and 1 are designated 0=FALSE and 1=TRUE:

- 989_OP = 1
- 989_CL = 0
- 989G_OP = 1
- 989G_CL = 0

If, in this case, line voltage supervision is added, then rather than setting 989 to open state, specify the state of the voltage supervision:

- 989_OP = VOLT_OFF
- 989_CL = VOLT_ON

If there is no voltage supervision, then set the corresponding inputs as follows:

- VOLT_OFF = 1
- VOLT_ON = 0
### 12.4.9 Interlocking for breaker-and-a-half diameter BH (3)

#### 12.4.9.1 Application

The interlocking for breaker-and-a-half diameter (BH_CONN(3), BH_LINE_A(3), BH_LINE_B(3)) functions are used for lines connected to a breaker-and-a-half diameter according to figure 186.

![Switchyard layout breaker-and-a-half](en04000513_ansi.vsd)

**Figure 186: Switchyard layout breaker-and-a-half**

Three types of interlocking modules per diameter are defined. BH_LINE_A (3) and BH_LINE_B (3) are the connections from a line to a busbar. BH_CONN (3) is the connection between the two lines of the diameter in the breaker-and-a-half switchyard layout.

For a breaker-and-a-half arrangement, the modules BH_LINE_A, BH_CONN and BH_LINE_B must be used.

#### 12.4.9.2 Configuration setting

For application without 989 and 989G, just set the appropriate inputs to open state and disregard the outputs. In the functional block diagram, 0 and 1 are designated 0=FALSE and 1=TRUE:

- 989_OP = 1
- 989_CL = 0
• 989G_OP = 1
• 989G_CL = 0

If, in this case, line voltage supervision is added, then rather than setting 989 to open state, specify the state of the voltage supervision:

• 989_OP = VOLT_OFF
• 989_CL = VOLT_ON

If there is no voltage supervision, then set the corresponding inputs as follows:

• VOLT_OFF = 1
• VOLT_ON = 0

### 12.5 Logic rotating switch for function selection and LHMI presentation SLGAPC

#### 12.5.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Logic rotating switch for function selection and LHMI presentation</td>
<td>SLGAPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

#### 12.5.2 Application

The logic rotating switch for function selection and LHMI presentation function (SLGAPC) (or the selector switch function block, as it is also known) is used to get a selector switch functionality similar with the one provided by a hardware multi-position selector switch. Hardware selector switches are used extensively by utilities, in order to have different functions operating on pre-set values. Hardware switches are however sources for maintenance issues, lower system reliability and extended purchase portfolio. The virtual selector switches eliminate all these problems.

SLGAPC function block has two operating inputs (UP and DOWN), one blocking input (BLOCK) and one operator position input (PSTO).

SLGAPC can be activated both from the local HMI and from external sources (switches) via the IED binary inputs. It also allows the operation from remote (like the station computer). SWPOSN is an integer value output, giving the actual output number. Since the number of positions of the switch can be established by settings (see below), one must be careful in coordinating the settings with the configuration (if one sets the number of positions to x in settings – for example, there will be only the first x outputs available from the block in the configuration). Also the frequency of the (UP or DOWN) pulses should be lower than the setting $t_{Pulse}$.

From the local HMI, the selector switch can be operated from Single-line diagram (SLD).
12.5.3 Setting guidelines

The following settings are available for the Logic rotating switch for function selection and LHMI presentation (SLGAPC) function:

*Operation*: Sets the operation of the function *Enabled* or *Disabled*.

*NrPos*: Sets the number of positions in the switch (max. 32).

*OutType*: *Steady* or *Pulsed*.

*tpulse*: In case of a pulsed output, it gives the length of the pulse (in seconds).

*tDelay*: The delay between the UP or DOWN activation signal positive front and the output activation.

*StopAtExtremes*: Sets the behavior of the switch at the end positions – if set to *Disabled*, when pressing UP while on first position, the switch will jump to the last position; when pressing DOWN at the last position, the switch will jump to the first position; when set to *Enabled*, no jump will be allowed.

12.6 Selector mini switch VSGAPC

12.6.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Selector mini switch</td>
<td>VSGAPC</td>
<td>-</td>
<td>43</td>
</tr>
</tbody>
</table>

12.6.2 Application

Selector mini switch (VSGAPC) function is a multipurpose function used in the configuration tool in PCM600 for a variety of applications, as a general purpose switch. VSGAPC can be used for both acquiring an external switch position (through the IPOS1 and the IPOS2 inputs) and represent it through the single line diagram symbols (or use it in the configuration through the outputs POS1 and POS2) as well as, a command function (controlled by the PSTO input), giving switching commands through the CMDPOS12 and CMDPOS21 outputs.

The output POSITION is an integer output, showing the actual position as an integer number 0 – 3, where 0 = MidPos, 1 = Open, 2 = Closed and 3 = Error.

An example where VSGAPC is configured to switch Autorecloser enabled–disabled from a button symbol on the local HMI is shown in figure 187. The Close and Open buttons on the local HMI are normally used for enable–disable operations of the circuit breaker.
Figure 187: Control of Autorecloser from local HMI through Selector mini switch

VSGAPC is also provided with IEC 61850 communication so it can be controlled from SA system as well.

12.6.3 Setting guidelines

Selector mini switch (VSGAPC) function can generate pulsed or steady commands (by setting the Mode parameter). When pulsed commands are generated, the length of the pulse can be set using the tPulse parameter. Also, being accessible on the single line diagram (SLD), this function block has two control modes (settable through CtlModel): Dir Norm and SBO Enh.

12.7 Generic communication function for Double Point indication DPGAPC

12.7.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Generic communication function for Double Point indication</td>
<td>DPGAPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

12.7.2 Application

Generic communication function for Double Point indication (DPGAPC) function block is used to send double point position indication to other systems, equipment or functions in the substation through IEC 61850-8-1 or other communication protocols. It is especially intended to be used in the interlocking station-wide logics. To be able to get the signals into other systems, equipment or functions, one must use other tools, described in the Engineering manual, and define which function block in which systems, equipment or functions should receive this information.

More specifically, DPGAPC function reports a combined double point position indication output POSITION, by evaluating the value and the timestamp attributes of the inputs OPEN and CLOSE, together with the logical input signal VALID.
When the input signal VALID is active, the values of the OPEN and CLOSE inputs determine the two-bit integer value of the output POSITION. The timestamp of the output POSITION will have the latest updated timestamp of the inputs OPEN and CLOSE.

When the input signal VALID is inactive, DPGAPC function forces the position to intermediated state.

When the value of the input signal VALID changes, the timestamp of the output POSITION will be updated as the time when DPGAPC function detects the change.

Refer to Table 46 for the description of the input-output relationship in terms of the value and the quality attributes.

Table 46: Description of the input-output relationship

<table>
<thead>
<tr>
<th>VALID</th>
<th>OPEN</th>
<th>CLOSE</th>
<th>POSITION</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>Value</td>
</tr>
<tr>
<td>0</td>
<td>-</td>
<td>-</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
<td>3</td>
</tr>
</tbody>
</table>

12.7.3 Setting guidelines

The function does not have any parameters available in the local HMI or PCM600.

12.8 Single point generic control 8 signals SPC8GAPC

12.8.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Single point generic control 8 signals</td>
<td>SPC8GAPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

12.8.2 Application

The Single point generic control 8 signals (SPC8GAPC) function block is a collection of 8 single point commands that can be used for direct commands for example reset of LED's or putting IED in "ChangeLock" state from remote. In this way, simple commands can be sent directly to the IED outputs, without confirmation. Confirmation (status) of the result of the commands is supposed to be achieved by other means, such as binary inputs and SPGAPC function blocks.
PSTO is the universal operator place selector for all control functions. Even if PSTO can be configured to allow LOCAL or ALL operator positions, the only functional position usable with the SPC8GAPC function block is REMOTE.

### 12.8.3 Setting guidelines

The parameters for the single point generic control 8 signals (SPC8GAPC) function are set via the local HMI or PCM600.

*Operation:* turning the function operation *Enabled/Disabled.*

There are two settings for every command output (totally 8):

*PulseModex:* decides if the command signal for output $x$ is *Latched* (steady) or *Pulsed.*

*tPulsex:* if *PulseModex* is set to *Pulsed,* then *tPulsex* will set the length of the pulse (in seconds).

### 12.9 AutomationBits, command function for DNP3.0

**AUTOBITS**

#### 12.9.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>AutomationBits, command function for DNP3</td>
<td>AUTOBITS</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

#### 12.9.2 Application

Automation bits, command function for DNP3 (AUTOBITS) is used within PCM600 in order to get into the configuration the commands coming through the DNP3.0 protocol. The AUTOBITS function plays the same role as functions GOOSEBINRCV (for IEC 61850) and MULTICMDRCV (for LON). AUTOBITS function block have 32 individual outputs which each can be mapped as a Binary Output point in DNP3. The output is operated by a "Object 12" in DNP3. This object contains parameters for control-code, count, on-time and off-time. To operate an AUTOBITS output point, send a control-code of latch-On, latch-Off, pulse-On, pulse-Off, Trip or Close. The remaining parameters are regarded as appropriate. For example, pulse-On, on-time=100, off-time=300, count=5 would give 5 positive 100 ms pulses, 300 ms apart.

For description of the DNP3 protocol implementation, refer to the Communication manual.

#### 12.9.3 Setting guidelines

AUTOBITS function block has one setting, *(Operation: Enabled/Disabled)* enabling or disabling the function. These names will be seen in the DNP3 communication management tool in PCM600.
12.10 **Single command, 16 signals SINGLECMD**

### 12.10.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Single command, 16 signals</td>
<td>SINGLECMD</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

### 12.10.2 Application

Single command, 16 signals (SINGLECMD) is a common function and always included in the IED.

The IEDs may be provided with a function to receive commands either from a substation automation system or from the local HMI. That receiving function block has outputs that can be used, for example, to control high voltage apparatuses in switchyards. For local control functions, the local HMI can also be used. Together with the configuration logic circuits, the user can govern pulses or steady output signals for control purposes within the IED or via binary outputs.

Figure 188 shows an application example of how the user can connect SINGLECMD via configuration logic circuit to control a high-voltage apparatus. This type of command control is normally carried out by sending a pulse to the binary outputs of the IED. Figure 188 shows a close operation. An open breaker operation is performed in a similar way but without the synchro-check condition.

![Configuration logic circuits](en04000206_ansi.vsd)

*Figure 188: Application example showing a logic diagram for control of a circuit breaker via configuration logic circuits*

Figure 189 and figure 190 show other ways to control functions, which require steady Enabled/ Disabled signals. Here, the output is used to control built-in functions or external devices.
12.10.3 Setting guidelines

The parameters for Single command, 16 signals (SINGLECMD) are set via the local HMI or PCM600.

Parameters to be set are MODE, common for the whole block, and CMDOUTy which includes the user defined name for each output signal. The MODE input sets the outputs to be one of the types Disabled, Steady, or Pulse.
- Disabled, sets all outputs to 0, independent of the values sent from the station level, that is, the operator station or remote-control gateway.
- Steady, sets the outputs to a steady signal 0 or 1, depending on the values sent from the station level.
- Pulse, gives a pulse with 100 ms duration, if a value sent from the station level is changed from 0 to 1. That means the configured logic connected to the command function block may not have a cycle time longer than the cycle time for the command function block.
Section 13  Logic

13.1 Trip matrix logic TMAGAPC

13.1.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Trip matrix logic</td>
<td>TMAGAPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

13.1.2 Application

The trip matrix logic (TMAGAPC) function is used to route trip signals and other logical output signals to different output contacts on the IED.

The trip matrix logic function has 3 output signals and these outputs can be connected to physical tripping outputs according to the specific application needs for settable pulse or steady output.

13.1.3 Setting guidelines

- **Operation**: Operation of function Enabled/Disabled.
- **PulseTime**: Defines the pulse time when in Pulsed mode. When used for direct tripping of circuit breaker(s) the pulse time delay shall be set to approximately 0.150 seconds in order to obtain satisfactory minimum duration of the trip pulse to the circuit breaker trip coils.
- **OnDelay**: Used to prevent output signals to be given for spurious inputs. Normally set to 0 or a low value.
- **OffDelay**: Defines a delay of the reset of the outputs after the activation conditions no longer are fulfilled. It is only used in Steady mode. When used for direct tripping of circuit breaker(s) the off delay time shall be set to at least 0.150 seconds in order to obtain a satisfactory minimum duration of the trip pulse to the circuit breaker trip coils.
- **ModeOutputx**: Defines if output signal OUTPUTx (where x=1-3) is Steady or Pulsed.

13.2 Logic for group alarm ALMCALH

13.2.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Logic for group alarm</td>
<td>ALMCALH</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
13.2.2 Application

Group alarm logic function ALMCALH is used to route alarm signals to different LEDs and/or output contacts on the IED.

ALMCALH output signal and the physical outputs allows the user to adapt the alarm signal to physical tripping outputs according to the specific application needs.

13.2.3 Setting guidelines

*Operation: Enabled or Disabled*

13.3 Logic for group alarm WRNCALH

13.3.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Logic for group warning</td>
<td>WRNCALH</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

13.3.1.1 Application

Group warning logic function WRNCALH is used to route warning signals to LEDs and/or output contacts on the IED.

WRNCALH output signal WARNING and the physical outputs allows the user to adapt the warning signal to physical tripping outputs according to the specific application needs.

13.3.1.2 Setting guidelines

*Operation: Enabled or Disabled*

13.4 Logic for group indication INDCALH

13.4.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Logic for group indication</td>
<td>INDCALH</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

13.4.1.1 Application

Group indication logic function INDCALH is used to route indication signals to different LEDs and/or output contacts on the IED.
INDCALH output signal IND and the physical outputs allows the user to adapt the indication signal to physical outputs according to the specific application needs.

### 13.4.1.2 Setting guidelines

*Operation: Enabled or Disabled*

### 13.5 Configurable logic blocks

The configurable logic blocks are available in two categories:

- Configurable logic blocks that do not propagate the time stamp and the quality of signals. They do not have the suffix QT at the end of their function block name, for example, SRMEMORY. These logic blocks are also available as part of an extension logic package with the same number of instances.
- Configurable logic blocks that propagate the time stamp and the quality of signals. They have the suffix QT at the end of their function block name, for example, SRMEMORYQT.

### 13.5.1 Application

A set of standard logic blocks, like AND, OR etc, and timers are available for adapting the IED configuration to the specific application needs. Additional logic blocks that, beside the normal logical function, have the capability to propagate timestamp and quality are also available. Those blocks have a designation including the letters QT, like ANDQT, ORQT etc.

### 13.5.2 Setting guidelines

There are no settings for AND gates, OR gates, inverters or XOR gates as well as, for ANDQT gates, ORQT gates or XORQT gates.

For normal On/Off delay and pulse timers the time delays and pulse lengths are set from the local HMI or via the PST tool.

Both timers in the same logic block (the one delayed on pick-up and the one delayed on drop-out) always have a common setting value.

For controllable gates, settable timers and SR flip-flops with memory, the setting parameters are accessible via the local HMI or via the PST tool.

### 13.5.2.1 Configuration

Logic is configured using the ACT configuration tool in PCM600.

Execution of functions as defined by the configurable logic blocks runs according to a fixed sequence with different cycle times.

For each cycle time, the function block is given an serial execution number. This is shown when using the ACT configuration tool with the designation of the function block and the cycle time, see example below.
Figure 191: Example designation, serial execution number and cycle time for logic function

![Function Block Instance](IEC09000695_2_en.vsd)

Figure 192: Example designation, serial execution number and cycle time for logic function that also propagates timestamp and quality of input signals

The execution of different function blocks within the same cycle is determined by the order of their serial execution numbers. Always remember this when connecting two or more logical function blocks in series.

Always be careful when connecting function blocks with a fast cycle time to function blocks with a slow cycle time. Remember to design the logic circuits carefully and always check the execution sequence for different functions. In other cases, additional time delays must be introduced into the logic schemes to prevent errors, for example, race between functions.

### 13.6 Fixed signal function block FXDSIGN

#### 13.6.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Fixed signals</td>
<td>FXDSIGN</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
13.6.2 Application

The Fixed signals function (FXDSIGN) has nine pre-set (fixed) signals that can be used in the configuration of an IED, either for forcing the unused inputs in other function blocks to a certain level/value, or for creating certain logic. Boolean, integer, floating point, string types of signals are available.

One FXDSIGN function block is included in all IEDs.

Example for use of GRP_OFF signal in FXDSIGN

The Restricted earth fault function (REFPDIF) (87N) can be used both for auto-transformers and normal transformers.

When used for auto-transformers, information from both windings parts, together with the neutral point current, needs to be available to the function. This means that three inputs are needed.

![Figure 193: REFPDIF (87N) function inputs for autotransformer application](ANSI11000083_1_en.vsd)

For normal transformers only one winding and the neutral point is available. This means that only two inputs are used. Since all group connections are mandatory to be connected, the third input needs to be connected to something, which is the GRP_OFF signal in FXDSIGN function block.

![Figure 194: REFPDIF (87N) function inputs for normal transformer application](ANSI11000084_1_en.vsd)
### 13.7 Boolean 16 to Integer conversion B16I

#### 13.7.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Boolean 16 to integer conversion B16I</td>
<td>B16I</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

#### 13.7.2 Application

Boolean 16 to integer conversion function B16I is used to transform a set of 16 binary (logical) signals into an integer. It can be used – for example, to connect logical output signals from a function (like distance protection) to integer inputs from another function (like line differential protection). B16I does not have a logical node mapping.

The Boolean 16 to integer conversion function (B16I) will transfer a combination of up to 16 binary inputs INx where 1≤x≤16 to an integer. Each INx represents a value according to the table below from 0 to 32768. This follows the general formula: \( \text{INx} = 2^{x-1} \) where 1≤x≤16. The sum of all the values on the activated INx will be available on the output OUT as a sum of the values of all the inputs INx that are activated. OUT is an integer. When all INx where 1≤x≤16 are activated that is = Boolean 1 it corresponds to that integer 65535 is available on the output OUT. B16I function is designed for receiving up to 16 booleans input locally. If the BLOCK input is activated, it will freeze the output at the last value.

Values of each of the different OUTx from function block B16I for 1≤x≤16.

The sum of the value on each INx corresponds to the integer presented on the output OUT on the function block B16I.

<table>
<thead>
<tr>
<th>Name of input</th>
<th>Type</th>
<th>Default</th>
<th>Description</th>
<th>Value when activated</th>
<th>Value when deactivated</th>
</tr>
</thead>
<tbody>
<tr>
<td>IN1</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>IN2</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 2</td>
<td>2</td>
<td>0</td>
</tr>
<tr>
<td>IN3</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 3</td>
<td>4</td>
<td>0</td>
</tr>
<tr>
<td>IN4</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 4</td>
<td>8</td>
<td>0</td>
</tr>
<tr>
<td>IN5</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 5</td>
<td>16</td>
<td>0</td>
</tr>
<tr>
<td>IN6</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 6</td>
<td>32</td>
<td>0</td>
</tr>
<tr>
<td>IN7</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 7</td>
<td>64</td>
<td>0</td>
</tr>
<tr>
<td>IN8</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 8</td>
<td>128</td>
<td>0</td>
</tr>
<tr>
<td>IN9</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 9</td>
<td>256</td>
<td>0</td>
</tr>
<tr>
<td>IN10</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 10</td>
<td>512</td>
<td>0</td>
</tr>
<tr>
<td>IN11</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 11</td>
<td>1024</td>
<td>0</td>
</tr>
<tr>
<td>IN12</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 12</td>
<td>2048</td>
<td>0</td>
</tr>
<tr>
<td>IN13</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 13</td>
<td>4096</td>
<td>0</td>
</tr>
</tbody>
</table>

Table continues on next page
### 13.8 Boolean to integer conversion with logical node representation, 16 bit BTIGAPC

<table>
<thead>
<tr>
<th>Name of input</th>
<th>Type</th>
<th>Default</th>
<th>Description</th>
<th>Value when activated</th>
<th>Value when deactivated</th>
</tr>
</thead>
<tbody>
<tr>
<td>IN14</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 14</td>
<td>8192</td>
<td>0</td>
</tr>
<tr>
<td>IN15</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 15</td>
<td>16384</td>
<td>0</td>
</tr>
<tr>
<td>IN16</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 16</td>
<td>32768</td>
<td>0</td>
</tr>
</tbody>
</table>

The sum of the numbers in column “Value when activated” when all INx (where 1≤x≤16) are active that is=1; is 65535. 65535 is the highest boolean value that can be converted to an integer by the B16I function block.

#### 13.8.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Boolean to integer conversion with logical node representation, 16 bit</td>
<td>BTIGAPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

#### 13.8.2 Application

Boolean to integer conversion with logical node representation, 16 bit (BTIGAPC) is used to transform a set of 16 binary (logical) signals into an integer. BTIGAPC has a logical node mapping in IEC 61850.

The BTIGAPC function will transfer a combination of up to 16 binary inputs INx where 1≤x≤16 to an integer. Each INx represents a value according to the table below from 0 to 32768. This follows the general formula: INx = 2^{x-1} where 1≤x≤16. The sum of all the values on the activated INx will be available on the output OUT as a sum of the values of all the inputs INx that are activated. OUT is an integer. When all INx where 1≤x≤16 are activated that is = Boolean 1 it corresponds to that integer 65535 is available on the output OUT. BTIGAPC function is designed for receiving up to 16 booleans input locally. If the BLOCK input is activated, it will freeze the output at the last value.

Values of each of the different OUTx from function block BTIGAPC for 1≤x≤16.

The sum of the value on each INx corresponds to the integer presented on the output OUT on the function block BTIGAPC.

<table>
<thead>
<tr>
<th>Name of input</th>
<th>Type</th>
<th>Default</th>
<th>Description</th>
<th>Value when activated</th>
<th>Value when deactivated</th>
</tr>
</thead>
<tbody>
<tr>
<td>IN1</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>IN2</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 2</td>
<td>2</td>
<td>0</td>
</tr>
<tr>
<td>IN3</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 3</td>
<td>4</td>
<td>0</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Name of input</th>
<th>Type</th>
<th>Default</th>
<th>Description</th>
<th>Value when activated</th>
<th>Value when deactivated</th>
</tr>
</thead>
<tbody>
<tr>
<td>IN4</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 4</td>
<td>8</td>
<td>0</td>
</tr>
<tr>
<td>IN5</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 5</td>
<td>16</td>
<td>0</td>
</tr>
<tr>
<td>IN6</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 6</td>
<td>32</td>
<td>0</td>
</tr>
<tr>
<td>IN7</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 7</td>
<td>64</td>
<td>0</td>
</tr>
<tr>
<td>IN8</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 8</td>
<td>128</td>
<td>0</td>
</tr>
<tr>
<td>IN9</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 9</td>
<td>256</td>
<td>0</td>
</tr>
<tr>
<td>IN10</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 10</td>
<td>512</td>
<td>0</td>
</tr>
<tr>
<td>IN11</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 11</td>
<td>1024</td>
<td>0</td>
</tr>
<tr>
<td>IN12</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 12</td>
<td>2048</td>
<td>0</td>
</tr>
<tr>
<td>IN13</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 13</td>
<td>4096</td>
<td>0</td>
</tr>
<tr>
<td>IN14</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 14</td>
<td>8192</td>
<td>0</td>
</tr>
<tr>
<td>IN15</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 15</td>
<td>16384</td>
<td>0</td>
</tr>
<tr>
<td>IN16</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 16</td>
<td>32768</td>
<td>0</td>
</tr>
</tbody>
</table>

The sum of the numbers in column “Value when activated” when all INx (where 1≤x≤16) are active that is=1; is 65535. 65535 is the highest boolean value that can be converted to an integer by the BTIGAPC function block.

13.9 **Integer to Boolean 16 conversion IB16**

13.9.1 **Identification**

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Integer to boolean 16 conversion</td>
<td>IB16</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

13.9.2 **Application**

Integer to boolean 16 conversion function (IB16) is used to transform an integer into a set of 16 binary (logical) signals. It can be used – for example, to connect integer output signals from one function to binary (logical) inputs to another function. IB16 function does not have a logical node mapping.

The Boolean 16 to integer conversion function (IB16) will transfer a combination of up to 16 binary inputs INx where 1≤x≤16 to an integer. Each INx represents a value according to the table below from 0 to 32768. This follows the general formula: INx = 2^{x-1} where 1≤x≤16. The sum of all the values on the activated INx will be available on the output OUT as a sum of the values of all the inputs INx that are activated. OUT is an integer. When all INx where 1≤x≤16 are activated that is = Boolean 1 it corresponds to that integer 65535 is available on the output OUT. IB16 function is designed for receiving up to 16 boolean inputs locally. If the BLOCK input is activated, it will freeze the output at the last value.
Values of each of the different OUTx from function block IB16 for 1≤x≤16.

The sum of the value on each INx corresponds to the integer presented on the output OUT on the function block IB16.

<table>
<thead>
<tr>
<th>Name of input</th>
<th>Type</th>
<th>Default</th>
<th>Description</th>
<th>Value when activated</th>
<th>Value when deactivated</th>
</tr>
</thead>
<tbody>
<tr>
<td>IN1</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>IN2</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 2</td>
<td>2</td>
<td>0</td>
</tr>
<tr>
<td>IN3</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 3</td>
<td>4</td>
<td>0</td>
</tr>
<tr>
<td>IN4</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 4</td>
<td>8</td>
<td>0</td>
</tr>
<tr>
<td>IN5</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 5</td>
<td>16</td>
<td>0</td>
</tr>
<tr>
<td>IN6</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 6</td>
<td>32</td>
<td>0</td>
</tr>
<tr>
<td>IN7</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 7</td>
<td>64</td>
<td>0</td>
</tr>
<tr>
<td>IN8</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 8</td>
<td>128</td>
<td>0</td>
</tr>
<tr>
<td>IN9</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 9</td>
<td>256</td>
<td>0</td>
</tr>
<tr>
<td>IN10</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 10</td>
<td>512</td>
<td>0</td>
</tr>
<tr>
<td>IN11</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 11</td>
<td>1024</td>
<td>0</td>
</tr>
<tr>
<td>IN12</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 12</td>
<td>2048</td>
<td>0</td>
</tr>
<tr>
<td>IN13</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 13</td>
<td>4096</td>
<td>0</td>
</tr>
<tr>
<td>IN14</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 14</td>
<td>8192</td>
<td>0</td>
</tr>
<tr>
<td>IN15</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 15</td>
<td>16384</td>
<td>0</td>
</tr>
<tr>
<td>IN16</td>
<td>BOOLEAN</td>
<td>0</td>
<td>Input 16</td>
<td>32768</td>
<td>0</td>
</tr>
</tbody>
</table>

The sum of the numbers in column “Value when activated” when all INx (where 1≤x≤16) are active that is=1; is 65535. 65535 is the highest boolean value that can be converted to an integer by the IB16 function block.

13.10 Integer to Boolean conversion for six-zone busbar BCTZCONN

13.10.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Integer to Boolean conversion for six-zone busbar</td>
<td>BCTZCONN</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
13.10.2 Application

Integer to Boolean conversion for six-zone busbar BCTZCONN is used to transform the zone connection integer signal of each CT from one bay function block into 7 binary (logical) signals in the six-zone busbar differential protection. It is intended to facilitate the recording of zone connection information from each bay function block to the disturbance recorder.

The BCTZCONN function does not have a logical node mapping and is designed to receive only an integer input locally. If the BLOCK input is activated, it freezes the logical outputs at the last values.

More specifically, it transfers an integer input ZONCONI to a combination of activated binary outputs ZONE(x) (1≤x≤6) and CZ, so that:

\[ ZONCONI = CZ \times 2^6 + \sum_{x=1}^{6} ZONE(x) \times 2^{x-1} \]  

(Equation 91)

where 0≤ZONCONI≤127. Note that when a binary output ZONE(x) or CZ is activated, it has the logical value one. Otherwise, it has the logical value zero.

The equation above also implies that each individual binary output corresponds actually to one integer, which is presented in the input ZONCONI and can be defined by:

\[ CZ \times 2^6 \text{ or } ZONE(x) \times 2^{x-1}, \text{ (1≤x≤6).} \]

Refer to Table 47 for the description of such representations.

<table>
<thead>
<tr>
<th>Output names</th>
<th>Type</th>
<th>Description</th>
<th>Corresponding integer value in ZONCONI</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>When deactivated</td>
</tr>
<tr>
<td>ZONE1</td>
<td>BOOLEAN</td>
<td>Bay is connected to Z1</td>
<td>0</td>
</tr>
<tr>
<td>ZONE2</td>
<td>BOOLEAN</td>
<td>Bay is connected to Z2</td>
<td>0</td>
</tr>
<tr>
<td>ZONE3</td>
<td>BOOLEAN</td>
<td>Bay is connected to Z3</td>
<td>0</td>
</tr>
<tr>
<td>ZONE4</td>
<td>BOOLEAN</td>
<td>Bay is connected to Z4</td>
<td>0</td>
</tr>
<tr>
<td>ZONE5</td>
<td>BOOLEAN</td>
<td>Bay is connected to Z5</td>
<td>0</td>
</tr>
<tr>
<td>ZONE6</td>
<td>BOOLEAN</td>
<td>Bay is connected to Z6</td>
<td>0</td>
</tr>
<tr>
<td>CZ</td>
<td>BOOLEAN</td>
<td>Supervision status of the check zone on differential zones (Valid only for feeder bays)</td>
<td>0</td>
</tr>
</tbody>
</table>

Figure 195 shows an example how to use BCTZCONN to facilitate the recording of the zone connection information from one bay, say feeder 01, to a disturbance recorder in ACT for any six-zone busbar differential protection application.
13.11 Integer to Boolean 16 conversion with logic node representation ITBGAPC

13.11.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Integer to boolean 16 conversion with logic node representation</td>
<td>ITBGAPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

13.11.2 Application

Integer to boolean 16 conversion with logic node representation function (ITBGAPC) is used to transform an integer into a set of 16 boolean signals. ITBGAPC function can receive an integer from a station computer – for example, over IEC 61850–8–1. This function is very useful when the user wants to generate logical commands (for selector switches or voltage controllers) by inputting an integer number. ITBGAPC function has a logical node mapping in IEC 61850.

The Integer to Boolean 16 conversion with logic node representation function (ITBGAPC) will transfer an integer with a value between 0 to 65535 communicated via IEC 61850 and connected to the ITBGAPC function block to a combination of activated outputs OUTx where 1≤x≤16.

The values of the different OUTx are according to the Table 48.

If the BLOCK input is activated, it freezes the logical outputs at the last value.

<table>
<thead>
<tr>
<th>Name of OUTx</th>
<th>Type</th>
<th>Description</th>
<th>Value when activated</th>
<th>Value when deactivated</th>
</tr>
</thead>
<tbody>
<tr>
<td>OUT1</td>
<td>BOOLEAN</td>
<td>Output 1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>OUT2</td>
<td>BOOLEAN</td>
<td>Output 2</td>
<td>2</td>
<td>0</td>
</tr>
<tr>
<td>OUT3</td>
<td>BOOLEAN</td>
<td>Output 3</td>
<td>4</td>
<td>0</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Name of OUTx</th>
<th>Type</th>
<th>Description</th>
<th>Value when activated</th>
<th>Value when deactivated</th>
</tr>
</thead>
<tbody>
<tr>
<td>OUT4</td>
<td>BOOLEAN</td>
<td>Output 4</td>
<td>8</td>
<td>0</td>
</tr>
<tr>
<td>OUT5</td>
<td>BOOLEAN</td>
<td>Output 5</td>
<td>16</td>
<td>0</td>
</tr>
<tr>
<td>OUT6</td>
<td>BOOLEAN</td>
<td>Output 6</td>
<td>32</td>
<td>0</td>
</tr>
<tr>
<td>OUT7</td>
<td>BOOLEAN</td>
<td>Output 7</td>
<td>64</td>
<td>0</td>
</tr>
<tr>
<td>OUT8</td>
<td>BOOLEAN</td>
<td>Output 8</td>
<td>128</td>
<td>0</td>
</tr>
<tr>
<td>OUT9</td>
<td>BOOLEAN</td>
<td>Output 9</td>
<td>256</td>
<td>0</td>
</tr>
<tr>
<td>OUT10</td>
<td>BOOLEAN</td>
<td>Output 10</td>
<td>512</td>
<td>0</td>
</tr>
<tr>
<td>OUT11</td>
<td>BOOLEAN</td>
<td>Output 11</td>
<td>1024</td>
<td>0</td>
</tr>
<tr>
<td>OUT12</td>
<td>BOOLEAN</td>
<td>Output 12</td>
<td>2048</td>
<td>0</td>
</tr>
<tr>
<td>OUT13</td>
<td>BOOLEAN</td>
<td>Output 13</td>
<td>4096</td>
<td>0</td>
</tr>
<tr>
<td>OUT14</td>
<td>BOOLEAN</td>
<td>Output 14</td>
<td>8192</td>
<td>0</td>
</tr>
<tr>
<td>OUT15</td>
<td>BOOLEAN</td>
<td>Output 15</td>
<td>16384</td>
<td>0</td>
</tr>
<tr>
<td>OUT16</td>
<td>BOOLEAN</td>
<td>Output 16</td>
<td>32768</td>
<td>0</td>
</tr>
</tbody>
</table>

The sum of the numbers in column “Value when activated” when all OUTx (1≤x≤16) are active equals 65535. This is the highest integer that can be converted by the ITBGAPC function block.

13.12 Elapsed time integrator with limit transgression and overflow supervision TEIGAPC

13.12.1 Identification

<table>
<thead>
<tr>
<th>Function Description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Elapsed time integrator</td>
<td>TEIGAPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

13.12.2 Application

The function TEIGAPC is used for user-defined logics and it can also be used for different purposes internally in the IED. An application example is the integration of elapsed time during the measurement of neutral point voltage or neutral current at earth-fault conditions.

Settable time limits for warning and alarm are provided. The time limit for overflow indication is fixed to 999999.9 seconds.

13.12.3 Setting guidelines

The settings tAlarm and tWarning are user settable limits defined in seconds. The achievable resolution of the settings depends on the level of the values defined.

A resolution of 10 ms can be achieved when the settings are defined within the range...
1.00 second ≤ tAlarm ≤ 99 999.99 seconds

1.00 second ≤ tWarning ≤ 99 999.99 seconds.

If the values are above this range, the resolution becomes lower due to the 32 bit float representation

99 999.99 seconds < tAlarm ≤ 999 999.0 seconds

99 999.99 seconds < tWarning ≤ 999 999.0 seconds

Note that tAlarm and tWarning are independent settings, that is, there is no check if tAlarm > tWarning.

The limit for the overflow supervision is fixed at 999999.9 seconds.

13.13 Comparator for integer inputs - INTCOMP

13.13.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Comparison of integer values</td>
<td>INTCOMP</td>
<td>Int&lt;=&gt;</td>
<td></td>
</tr>
</tbody>
</table>

13.13.2 Application

The function gives the possibility to monitor the level of integer values in the system relative to each other or to a fixed value. It is a basic arithmetic function that can be used for monitoring, supervision, interlocking and other logics.

13.13.3 Setting guidelines

For proper operation of comparison the set value should be set within the range of ± 2 ×10^9.

Setting procedure on the IED:

EnaAbs: This setting is used to select the comparison type between signed and absolute values.
- Absolute: Comparison is performed on absolute values of input and reference values
- Signed: Comparison is performed on signed values of input and reference values.

RefSource: This setting is used to select the reference source between input and setting for comparison.
- Input REF: The function will take reference value from input REF
- Set Value: The function will take reference value from setting SetValue
SetValue: This setting is used to set the reference value for comparison when setting RefSource is selected as SetValue.

13.13.4 Setting example

For absolute comparison between inputs:
Set the EnaAbs = Absolute
Set the RefSource = Input REF

Similarly for Signed comparison between inputs
Set the EnaAbs = Signed
Set the RefSource = Input REF

For absolute comparison between input and setting
Set the EnaAbs = Absolute
Set the RefSource = Set Value
SetValue shall be set between -2000000000 to 2000000000

Similarly for signed comparison between input and setting
Set the EnaAbs = Signed
Set the RefSource = Set Value
SetValue shall be set between -2000000000 to 2000000000

13.14 Comparator for real inputs - REALCOMP

13.14.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Comparator for real inputs</td>
<td>REALCOMP</td>
<td>Real&lt;=&gt;</td>
<td></td>
</tr>
</tbody>
</table>

13.14.2 Application

The function gives the possibility to monitor the level of real values in the system relative to each other or to a fixed value. It is a basic arithmetic function that can be used for monitoring, supervision, interlocking and other logics.
13.14.3 Setting guidelines

Setting procedure on the IED:

*EnaAbs:* This setting is used to select the comparison type between signed and absolute values.
- *Absolute:* Comparison is performed with absolute values of input and reference.
- *Signed:* Comparison is performed with signed values of input and reference.

*RefSource:* This setting is used to select the reference source between input and setting for comparison.
- *Input REF:* The function will take reference value from input REF
- *Set Value:* The function will take reference value from setting *SetValue*

*SetValue:* This setting is used to set the reference value for comparison when setting *RefSource* is selected as *Set Value.* If this setting value is less than 0.2% of the set unit then the output INLOW will never pickup.

*RefPrefix:* This setting is used to set the unit of the reference value for comparison when setting *RefSource* is selected as *SetValue.* It has 5 unit selections and they are Milli, Unity, Kilo, Mega and Giga.

*EqualBandHigh:* This setting is used to set the equal condition high band limit in % of reference value. This high band limit will act as reset limit for INHIGH output when INHIGH.

*EqualBandLow:* This setting is used to set the equal condition low band limit in % of reference value. This low band limit will act as reset limit for INLOW output when INLOW.

13.14.4 Setting example

Let us consider a comparison is to be done between current magnitudes in the range of 90 to 110 with nominal rating is 100 and the order is kA.

For the above condition the comparator can be designed with settings as follows,

*EnaAbs = Absolute*

*RefSource = Set Value*

*SetValue = 100*

*RefPrefix = Kilo*

*EqualBandHigh = 5.0 % of reference value*

*EqualBandLow = 5.0 % of reference value*

**Operation**

The function will set the outputs for the following conditions,

INEQUAL will set when the INPUT is between the ranges of 95 to 105 kA.
INHIGH will set when the INPUT crosses above 105 kA.

INLOW will set when the INPUT crosses below 95 kA.

If the comparison should be done between two current magnitudes then those current signals need to be connected to function inputs, INPUT and REF. Then the settings should be adjusted as below,

\[ EnaAbs = Absolute \]

\[ RefSource = Input \text{ REF} \]

\[ EqualBandHigh = 5.0 \% \text{ of reference value} \]

\[ EqualBandLow = 5.0 \% \text{ of reference value}. \]
Section 14  Monitoring

14.1 Measurement

14.1.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power system measurements</td>
<td>CVMMXN</td>
<td>P, Q, S, I, U, f</td>
<td></td>
</tr>
<tr>
<td>Phase current measurement</td>
<td>CMMXU</td>
<td>I</td>
<td></td>
</tr>
<tr>
<td>Phase-phase voltage measurement</td>
<td>VMMXU</td>
<td>U</td>
<td></td>
</tr>
<tr>
<td>Current sequence component measurement</td>
<td>CMSQI</td>
<td>I1, I2, I0</td>
<td></td>
</tr>
<tr>
<td>Voltage sequence component measurement</td>
<td>VMSQI</td>
<td>U1, U2, U0</td>
<td></td>
</tr>
<tr>
<td>Phase-neutral voltage measurement</td>
<td>VNMMXU</td>
<td>U</td>
<td></td>
</tr>
</tbody>
</table>

14.1.2 Application

Measurement functions are used for power system measurement, supervision and reporting to the local HMI, monitoring tool within PCM600 or to station level for example, via IEC 61850. The possibility to continuously monitor measured values of active power, reactive power, currents, voltages, frequency, power factor etc. is vital for efficient production, transmission and distribution of electrical energy. It provides to the system operator fast and easy overview of the present status of the power system. Additionally, it can be used during testing and commissioning of protection and control IEDs in order to verify proper operation and connection of instrument...
transformers (CTs and VTs). During normal service by periodic comparison of the measured value from the IED with other independent meters the proper operation of the IED analog measurement chain can be verified. Finally, it can be used to verify proper direction orientation for distance or directional overcurrent protection function.

The available measured values from an IED are depending on the actual hardware (TRM) and the logic configuration made in PCM600.

All measured values can be supervised with four settable limits that is, low-low limit, low limit, high limit and high-high limit. A zero clamping reduction is also supported, that is, the measured value below a settable limit is forced to zero which reduces the impact of noise in the inputs.

Dead-band supervision can be used to report measured signal value to station level when change in measured value is above set threshold limit or time integral of all changes since the last time value updating exceeds the threshold limit. Measure value can also be based on periodic reporting.

Main menu/Measurement/Monitoring/Service values/CVMMXN

The measurement function, CVMMXN, provides the following power system quantities:

- P, Q and S: three phase active, reactive and apparent power
- PF: power factor
- V: phase-to-phase voltage magnitude
- I: phase current magnitude
- F: power system frequency

The measuring functions CMMXU, VMMXU and VNMMXU provide physical quantities:

- I: phase currents (magnitude and angle) (CMMXU)
- V: voltages (phase-to-ground and phase-to-phase voltage, magnitude and angle) (VMMXU, VNMMXU)

The CVMMXN function calculates three-phase power quantities by using fundamental frequency phasors (DFT values) of the measured current and voltage signals. The measured power quantities are available either, as instantaneously calculated quantities or, averaged values over a period of time (low pass filtered) depending on the selected settings.

It is possible to calibrate the measuring function above to get better then class 0.5 presentation. This is accomplished by angle and magnitude compensation at 5, 30 and 100% of rated current and at 100% of rated voltage.

The power system quantities provided, depends on the actual hardware, (TRM) and the logic configuration made in PCM600.

The measuring functions CMSQI and VMSQI provide sequence component quantities:

- I: sequence currents (positive, zero, negative sequence, magnitude and angle)
- V: sequence voltages (positive, zero and negative sequence, magnitude and angle).
14.1.3 Zero clamping

Measuring functions CVMMXN, CMMXU, VMMXU and VNMMXU have no interconnections regarding any settings or parameters.

Zero clampings are also handled entirely by ZeroDb separately for each function's every output signal. For example, zero clamping of U12 is handled by UL12ZeroDb in VMMXU, zero clamping of I1 is handled by IL1ZeroDb in CMMXU, and so on.

Example of CVMMXN operation

Outputs seen on the local HMI under Main menu/Measurements/Monitoring/Servicevalues(P_Q)/CVMMXN(P_Q):

- S: Apparent three-phase power
- P: Active three-phase power
- Q: Reactive three-phase power
- PF: Power factor
- ILAG: I lagging U
- ILEAD: I leading U
- U: System mean voltage, calculated according to selected mode
- I: System mean current, calculated according to selected mode
- F: Frequency

Relevant settings and their values on the local HMI under Main menu/Settings/IED settings/Monitoring/Servicevalues(P_Q)/CVMMXN(P_Q):

- When system voltage falls below UGenZeroDB, values for S, P, Q, PF, ILAG, ILEAD, U and F are forced to zero.
- When system current falls below IGenZeroDB, values for S, P, Q, PF, ILAG, ILEAD, U and F are forced to zero.
- When the value of a single signal falls below its set deadband, the value is forced to zero. For example, if the apparent three-phase power falls below SZeroDb, the value for S is forced to zero.

14.1.4 Setting guidelines

The available setting parameters of the measurement function CVMMXN, CMMXU, VMMXU, CMSQI, VMSQI, VNMMXU are depending on the actual hardware (TRM) and the logic configuration made in PCM600.

The parameters for the Measurement functions CVMMXN, CMMXU, VMMXU, CMSQI, VMSQI, VNMMXU are set via the local HMI or PCM600.

GlobalBaseSel: Selects the global base value group used by the function to define IBase, VBase and SBase. Note that this function will only use IBase value.

Operation: Disabled/Enabled. Every function instance (CVMMXN, CMMXU, VMMXU, CMSQI, VMSQI, VNMMXU) can be taken in operation (Enabled) or out of operation (Disabled).
The following general settings can be set for the Measurement function (CVMMXN).

**PowMagFact:** Magnitude factor to scale power calculations.

**PowAngComp:** Angle compensation for phase shift between measured I & V.

**Mode:** Selection of measured current and voltage. There are 9 different ways of calculating monitored three-phase values depending on the available VT inputs connected to the IED. See parameter group setting table.

**k:** Low pass filter coefficient for power measurement, V and I.

**VGenZeroDb:** Minimum level of voltage in % of VBase, used as indication of zero voltage (zero point clamping). If measured value is below VGenZeroDb calculated S, P, Q and PF will be zero.

**IGenZeroDb:** Minimum level of current in % of IBase, used as indication of zero current (zero point clamping). If measured value is below IGenZeroDb calculated S, P, Q and PF will be zero.

**VMagCompY:** Magnitude compensation to calibrate voltage measurements at Y% of Vn, where Y is equal to 5, 30 or 100.

**IMagCompY:** Magnitude compensation to calibrate current measurements at Y% of In, where Y is equal to 5, 30 or 100.

**IAngCompY:** Angle compensation to calibrate angle measurements at Y% of In, where Y is equal to 5, 30 or 100.

The following general settings can be set for the Phase current measurement (CMMXU).

**IMagCompY:** Magnitude compensation to calibrate current measurements at Y% of In, where Y is equal to 5, 30 or 100.

**IAngCompY:** Angle compensation to calibrate angle measurements at Y% of In, where Y is equal to 5, 30 or 100.

The following general settings can be set for the Phase-phase voltage measurement (VMMXU).

**VMagCompY:** Amplitude compensation to calibrate voltage measurements at Y% of Vn, where Y is equal to 5, 30 or 100.

**VAngCompY:** Angle compensation to calibrate angle measurements at Y% of Vn, where Y is equal to 5, 30 or 100.

The following general settings can be set for all monitored quantities included in the functions (CVMMXN, CMMXU, VMMXU, CMSQI, VMSQI, VNMMXU) X in setting names below equals S, P, Q, PF, V, I, F, IA,IB,IC, VA, VB, VCVAB, VBC, VCA, I1, I2, 3I0, V1, V2 or 3V0.

**Xmin:** Minimum value for analog signal X set directly in applicable measuring unit. This forms the minimum limit of the range.

**Xmax:** Maximum value for analog signal X. This forms the maximum limit of the range.

**XZeroDb:** Zero point clamping. A signal value less than XZeroDb is forced to zero.

Observe the related zero point clamping settings in Setting group N for CVMMXN (VGenZeroDb and IGenZeroDb). If measured value is below VGenZeroDb and/or IGenZeroDb calculated S, P, Q and PF will be zero and these settings will override XZeroDb.
**XRepTyp:** Reporting type. Cyclic (*Cyclic*), magnitude deadband (*Dead band*), integral deadband (*Int deadband*) or Deadband and xx sec cyclic (xx: 5 sec, 30 sec, 1 min). The reporting interval is controlled by the parameter *XDbRepInt*.

**XDbRepInt:** This setting handles all the reporting types. If setting is deadband in XRepTyp, XDbRepInt defines the deadband in m\% of the measuring range. For cyclic reporting type (XRepTyp : cyclic), the setting value reporting interval is in seconds. Magnitude deadband is the setting value in m\% of measuring range. Integral deadband setting is the integral area, that is, measured value in m\% of measuring range multiplied by the time between two measured values.

**XHiHiLim:** High-high limit. Set as % of *Ybase* (Y is SBase for S,P,Q UBase for Voltage measurement and IBase for current measurement).

**XHiLim:** High limit. Set as % of *Ybase* (Y is SBase for S,P,Q UBase for Voltage measurement and IBase for current measurement).

**XLowLim:** Low limit. Set as % of *Ybase* (Y is SBase for S,P,Q UBase for Voltage measurement and IBase for current measurement).

**XLowLowLim:** Low-low limit. Set as % of *Ybase* (Y is SBase for S,P,Q UBase for Voltage measurement and IBase for current measurement).

**XLimHyst:** Hysteresis value in % of range and is common for all limits.

All phase angles are presented in relation to defined reference channel. The parameter *PhaseAngleRef* defines the reference.

### Calibration curves

It is possible to calibrate the functions (CVMMXN, CMMXU, VMMXU and VNMMXU) to get class 0.5 presentations of currents, voltages and powers. This is accomplished by magnitude and angle compensation at 5, 30 and 100\% of rated current and voltage. The compensation curve will have the characteristic for magnitude and angle compensation of currents as shown in figure 196 (example). The first phase will be used as reference channel and compared with the curve for calculation of factors. The factors will then be used for all related channels.
14.1.4.1 Setting examples

Three setting examples, in connection to Measurement function (CVMMXN), are provided:

- Measurement function (CVMMXN) application for a OHL
- Measurement function (CVMMXN) application on the secondary side of a transformer
- Measurement function (CVMMXN) application for a generator

For each of them detail explanation and final list of selected setting parameters values will be provided.

The available measured values of an IED are depending on the actual hardware (TRM) and the logic configuration made in PCM600.

**Measurement function application for a 380kV OHL**

Single line diagram for this application is given in figure 197:
In order to monitor, supervise and calibrate the active and reactive power as indicated in figure 197 it is necessary to do the following:

1. Set correctly CT and VT data and phase angle reference channel PhaseAngleRef using PCM600 for analog input channels
2. Connect, in PCM600, measurement function to three-phase CT and VT inputs
3. Set under General settings parameters for the Measurement function:
   - general settings as shown in table 49.
   - level supervision of active power as shown in table 50.
   - calibration parameters as shown in table 51.

**Figure 197: Single line diagram for 380kV OHL application**

<table>
<thead>
<tr>
<th>Setting</th>
<th>Short Description</th>
<th>Selected value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operation</td>
<td>Operation Off/On</td>
<td>On</td>
<td>Function must be On</td>
</tr>
<tr>
<td>PowAmpFact</td>
<td>Amplitude factor to scale power calculations</td>
<td>1.000</td>
<td>It can be used during commissioning to achieve higher measurement accuracy. Typically no scaling is required</td>
</tr>
<tr>
<td>PowAngComp</td>
<td>Angle compensation for phase shift between measured I &amp; U</td>
<td>0.0</td>
<td>It can be used during commissioning to achieve higher measurement accuracy. Typically no angle compensation is required. As well here required direction of P &amp; Q measurement is towards protected object (as per IED internal default direction)</td>
</tr>
<tr>
<td>Mode</td>
<td>Selection of measured current and voltage</td>
<td>L1, L2, L3</td>
<td>All three phase-to-ground VT inputs are available</td>
</tr>
<tr>
<td>k</td>
<td>Low pass filter coefficient for power measurement, V and I</td>
<td>0.00</td>
<td>Typically no additional filtering is required</td>
</tr>
<tr>
<td>VGenZeroDb</td>
<td>Zero point clamping in % of Ubase</td>
<td>25</td>
<td>Set minimum voltage level to 25%. Voltage below 25% will force S, P and Q to zero.</td>
</tr>
<tr>
<td>IGenZeroDb</td>
<td>Zero point clamping in % of Ibase</td>
<td>3</td>
<td>Set minimum current level to 3%. Current below 3% will force S, P and Q to zero.</td>
</tr>
</tbody>
</table>

Table continues on next page
### Table 50: Settings parameters for level supervision

<table>
<thead>
<tr>
<th>Setting</th>
<th>Short Description</th>
<th>Selected value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>VBase (set in Global base)</td>
<td>Base setting for voltage level in kV</td>
<td>400.00</td>
<td>Set rated OHL phase-to-phase voltage</td>
</tr>
<tr>
<td>IBase (set in Global base)</td>
<td>Base setting for current level in A</td>
<td>1000</td>
<td>Set rated primary CT current used for OHL</td>
</tr>
<tr>
<td>SBase (set in Global base)</td>
<td>Base Setting for power base in MVA</td>
<td>1000</td>
<td>Set based on rated Power</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Setting</th>
<th>Short Description</th>
<th>Selected value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>PMin</td>
<td>Minimum value</td>
<td>-100</td>
<td>Minimum expected load</td>
</tr>
<tr>
<td>PMax</td>
<td>Minimum value</td>
<td>100</td>
<td>Maximum expected load</td>
</tr>
<tr>
<td>PZeroDb</td>
<td>Zero point clamping in 0.001% of range</td>
<td>3000</td>
<td>Set zero point clamping to 60 MW that is, 3% of 200 MW</td>
</tr>
<tr>
<td>PrepTyp</td>
<td>Reporting type</td>
<td>db</td>
<td>Select magnitude deadband supervision</td>
</tr>
<tr>
<td>PDbRepInt</td>
<td>Cycl: Report interval (s), Db: In 0.001% of range, Int Db: In 0.001%</td>
<td>2000</td>
<td>Set ±Δdb=40 MW that is, 2% (larger changes than 40 MW will be reported)</td>
</tr>
<tr>
<td>PHIHiLim</td>
<td>High High limit (physical value), % of SBase</td>
<td>60</td>
<td>High alarm limit that is, extreme overload alarm, hence it will be 415 MW.</td>
</tr>
<tr>
<td>PHILim</td>
<td>High limit (physical value), in % of SBase</td>
<td>50</td>
<td>High warning limit that is, overload warning, hence it will be 371 MW.</td>
</tr>
<tr>
<td>PLowLim</td>
<td>Low limit (physical value), in % of SBase</td>
<td>-50</td>
<td>Low warning limit -500 MW</td>
</tr>
<tr>
<td>PLowLowLim</td>
<td>Low Low limit (physical value), in % of SBase</td>
<td>-60</td>
<td>Low alarm limit -600 MW</td>
</tr>
<tr>
<td>PLimHyst</td>
<td>Hysteresis value in % of range (common for all limits)</td>
<td>1</td>
<td>Set ±Δ Hysteresis 20 MW that is, 1% of range (2000 MW)</td>
</tr>
</tbody>
</table>

### Table 51: Settings for calibration parameters

<table>
<thead>
<tr>
<th>Setting</th>
<th>Short Description</th>
<th>Selected value</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>IMagComp5</td>
<td>Magnitude factor to calibrate current at 5% of In</td>
<td>0.00</td>
<td></td>
</tr>
<tr>
<td>IMagComp30</td>
<td>Magnitude factor to calibrate current at 30% of In</td>
<td>0.00</td>
<td></td>
</tr>
<tr>
<td>IMagComp100</td>
<td>Magnitude factor to calibrate current at 100% of In</td>
<td>0.00</td>
<td></td>
</tr>
<tr>
<td>VAmpComp5</td>
<td>Magnitude factor to calibrate voltage at 5% of Vn</td>
<td>0.00</td>
<td></td>
</tr>
<tr>
<td>VAmpComp30</td>
<td>Magnitude factor to calibrate voltage at 30% of Vn</td>
<td>0.00</td>
<td></td>
</tr>
<tr>
<td>VAmpComp100</td>
<td>Magnitude factor to calibrate voltage at 100% of Vn</td>
<td>0.00</td>
<td></td>
</tr>
</tbody>
</table>

Table continues on next page
### Measurement function application for a power transformer

Single line diagram for this application is given in figure 198.

Figure 198: Single line diagram for transformer application

In order to measure the active and reactive power as indicated in figure 198, it is necessary to do the following:

1. Set correctly all CT and VT and phase angle reference channel PhaseAngleRef data using PCM600 for analog input channels
2. Connect, in PCM600, measurement function to LV side CT & VT inputs
3. Set the setting parameters for relevant Measurement function as shown in the following table 52:
### Table 52: General settings parameters for the Measurement function

<table>
<thead>
<tr>
<th>Setting</th>
<th>Short description</th>
<th>Selected value</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operation</td>
<td>Operation Disabled/Enabled</td>
<td>Enabled</td>
<td>Function must be Enabled</td>
</tr>
<tr>
<td>PowAmpFact</td>
<td>Magnitude factor to scale power calculations</td>
<td>1.000</td>
<td>Typically no scaling is required</td>
</tr>
<tr>
<td>PowAngComp</td>
<td>Angle compensation for phase shift between measured I &amp; V</td>
<td>180.0</td>
<td>Typically no angle compensation is required. However here the required direction of P &amp; Q measurement is towards busbar (Not per IED internal default direction). Therefore angle compensation have to be used in order to get measurements in align with the required direction.</td>
</tr>
<tr>
<td>Mode</td>
<td>Selection of measured current and voltage</td>
<td>L1L2</td>
<td>Only UL1L2 phase-to-phase voltage is available</td>
</tr>
<tr>
<td>k</td>
<td>Low pass filter coefficient for power measurement, V and I</td>
<td>0.00</td>
<td>Typically no additional filtering is required</td>
</tr>
<tr>
<td>VGenZeroDb</td>
<td>Zero point clamping in % of Vbase</td>
<td>25</td>
<td>Set minimum voltage level to 25%</td>
</tr>
<tr>
<td>IGenZeroDb</td>
<td>Zero point clamping in % of Ibase</td>
<td>3</td>
<td>Set minimum current level to 3%</td>
</tr>
<tr>
<td>VBase (set in Global base)</td>
<td>Base setting for voltage level in kV</td>
<td>35.00</td>
<td>Set LV side rated phase-to-phase voltage</td>
</tr>
<tr>
<td>IBase (set in Global base)</td>
<td>Base setting for current level in A</td>
<td>495</td>
<td>Set transformer LV winding rated current</td>
</tr>
<tr>
<td>SBase (set in Global base)</td>
<td>Base setting for power in MVA</td>
<td>31.5</td>
<td>Set based on rated power</td>
</tr>
</tbody>
</table>

**Measurement function application for a generator**

Single line diagram for this application is given in figure 199.
In order to measure the active and reactive power as indicated in figure 199, it is necessary to do the following:

1. Set correctly all CT and VT data and phase angle reference channel *PhaseAngleRef* using PCM600 for analog input channels
2. Connect, in PCM600, measurement function to the generator CT & VT inputs
3. Set the setting parameters for relevant Measurement function as shown in the following table:

**Table 53: General settings parameters for the Measurement function**

<table>
<thead>
<tr>
<th>Setting</th>
<th>Short description</th>
<th>Selected value</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operation</td>
<td>Operation Off/On</td>
<td>On</td>
<td>Function must be On</td>
</tr>
<tr>
<td>PowAmpFact</td>
<td>Amplitude factor to scale power calculations</td>
<td>1.000</td>
<td>Typically no scaling is required</td>
</tr>
<tr>
<td>PowAngComp</td>
<td>Angle compensation for phase shift between measured I &amp; V</td>
<td>0.0</td>
<td>Typically no angle compensation is required. As well here required direction of P &amp; Q measurement is towards protected object (as per IED internal default direction)</td>
</tr>
<tr>
<td>Mode</td>
<td>Selection of measured current and voltage</td>
<td>Arone</td>
<td>Generator VTs are connected between phases (V-connected)</td>
</tr>
</tbody>
</table>
### 14.2 Gas medium supervision SSIMG (63)

#### 14.2.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insulation gas monitoring function</td>
<td>SSIMG</td>
<td>-</td>
<td>63</td>
</tr>
</tbody>
</table>

#### 14.2.2 Application

Gas medium supervision (SSIMG, 63) is used for monitoring the circuit breaker condition. Proper arc extinction by the compressed gas in the circuit breaker is very important. When the pressure becomes too low compared to the required value, the circuit breaker operation shall be blocked to minimize the risk of internal failure. Binary information based on the gas pressure in the circuit breaker is used as an input signal to the function. The function generates alarms based on the received information.

#### 14.2.3 Setting guidelines

The parameters for Gas medium supervision SSIMG can be set via local HMI or Protection and Control Manager PCM600.

**Operation:** This is used to disable/enable the operation of gas medium supervision i.e. Off/On.

**PresAlmLimit:** This is used to set the limit for a pressure alarm condition in the circuit breaker.

**PresLOLimit:** This is used to set the limit for a pressure lockout condition in the circuit breaker.

**TempAlarmLimit:** This is used to set the limit for a temperature alarm condition in the circuit breaker.

**TempLOLimit:** This is used to set the limit for a temperature lockout condition in the circuit breaker.

**tPressureAlarm:** This is used to set the time delay for a pressure alarm indication, given in s.
**tPressureLO**: This is used to set the time delay for a pressure lockout indication, given in s.

**tTempAlarm**: This is used to set the time delay for a temperature alarm indication, given in s.

**tTempLockOut**: This is used to set the time delay for a temperature lockout indication, given in s.

**tResetPressAlm**: This is used for the pressure alarm indication to reset after a set time delay in s.

**tResetPressLO**: This is used for the pressure lockout indication to reset after a set time delay in s.

**tResetTempLO**: This is used for the temperature lockout indication to reset after a set time delay in s.

**tResetTempAlm**: This is used for the temperature alarm indication to reset after a set time delay in s.

### 14.3 Liquid medium supervision SSIML (71)

#### 14.3.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insulation liquid monitoring function</td>
<td>SSIML</td>
<td>-</td>
<td>71</td>
</tr>
</tbody>
</table>

#### 14.3.2 Application

Liquid medium supervision (SSIML, 71) is used for monitoring the oil insulated device condition. For example, transformers, shunt reactors, and so on. When the level becomes too low compared to the required value, the operation is blocked to minimize the risk of internal failures. Binary information based on the oil level in the oil insulated devices are used as input signals to the function. In addition, the function generates alarms based on the received information.

#### 14.3.3 Setting guidelines

The parameters for Liquid medium supervision SSIML can be set via local HMI or Protection and Control Manager PCM600.

**Operation**: This is used to disable/enable the operation of liquid medium supervision i.e. **Off/ On**.

**LevelAlmLimit**: This is used to set the limit for a level alarm condition in the oil insulated device.

**LevelLOLimit**: This is used to set the limit for a level lockout condition in the oil insulated device.

**TempAlarmLimit**: This is used to set the limit for a temperature alarm condition in the oil insulated device.

**TempLOLimit**: This is used to set the limit for a temperature lockout condition in the oil insulated device.

**tLevelAlarm**: This is used to set the time delay for a level alarm indication, given in s.
**tLevelLockOut**: This is used to set the time delay for a level lockout indication, given in s.

**tTempAlarm**: This is used to set the time delay for a temperature alarm indication, given in s.

**tTempLockOut**: This is used to set the time delay for a temperature lockout indication, given in s.

**tResetLevelAlm**: This is used for the level alarm indication to reset after a set time delay in s.

**tResetLevelLO**: This is used for the level lockout indication to reset after a set time delay in s.

**tResetTempLO**: This is used for the temperature lockout indication to reset after a set time delay in s.

**tResetTempAlm**: This is used for the temperature alarm indication to reset after a set time delay in s.

### 14.4 Breaker monitoring SSCBR

#### 14.4.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Breaker monitoring</td>
<td>SSCBR</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

#### 14.4.2 Application

The circuit breaker maintenance is usually based on regular time intervals or the number of operations performed. This has some disadvantages because there could be a number of abnormal operations or few operations with high-level currents within the predetermined maintenance interval. Hence, condition-based maintenance scheduling is an optimum solution in assessing the condition of circuit breakers.

**Circuit breaker contact travel time**

Auxiliary contacts provide information about the mechanical operation, opening time and closing time of a breaker. Detecting an excessive traveling time is essential to indicate the need for maintenance of the circuit breaker mechanism. The excessive travel time can be due to problems in the driving mechanism or failures of the contacts.

**Circuit breaker status**

Monitoring the breaker status ensures proper functioning of the features within the protection relay such as breaker control, breaker failure and autoreclosing. The breaker status is monitored using breaker auxiliary contacts. The breaker status is indicated by the binary outputs. These signals indicate whether the circuit breaker is in an open, closed or error state.

**Remaining life of circuit breaker**

Every time the breaker operates, the circuit breaker life reduces due to wear. The wear in a breaker depends on the interrupted current. For breaker maintenance or replacement at the right time, the remaining life of the breaker must be estimated. The remaining life of a breaker can be estimated using the maintenance curve provided by the circuit breaker manufacturer.
Circuit breaker manufacturers provide the number of make-break operations possible at various interrupted currents. An example is shown in figure 200.

![Diagram showing remaining life calculation for a circuit breaker](IEC12000623_V1_EN-US)

**Figure 200: An example for estimating the remaining life of a circuit breaker**

**Calculation for estimating the remaining life**

The graph shows that there are 10000 possible operations at the rated operating current and 900 operations at 10 kA and 50 operations at rated fault current. Therefore, if the interrupted current is 10 kA, one operation is equivalent to $10000 / 900 = 11$ operations at the rated current. It is assumed that prior to tripping, the remaining life of a breaker is 10000 operations. Remaining life calculation for three different interrupted current conditions is explained below.

- Breaker interrupts at and below the rated operating current, that is, 2 kA, the remaining life of the CB is decreased by 1 operation and therefore, 9999 operations remaining at the rated operating current.
- Breaker interrupts between rated operating current and rated fault current, that is, 10 kA, one operation at 10kA is equivalent to $10000 / 900 = 11$ operations at the rated current. The remaining life of the CB would be $(10000 - 10) = 9989$ at the rated operating current after one operation at 10 kA.
- Breaker interrupts at and above rated fault current, that is, 50 kA, one operation at 50 kA is equivalent to $10000 / 50 = 200$ operations at the rated operating current. The remaining life of
the CB would become \((10000 - 200) = 9800\) operations at the rated operating current after one operation at 50 kA.

**Accumulated energy**

Monitoring the contact erosion and interrupter wear has a direct influence on the required maintenance frequency. Therefore, it is necessary to accurately estimate the erosion of the contacts and condition of interrupters using cumulative summation of \(I^y\). The factor "\(y\)" depends on the type of circuit breaker. The energy values were accumulated using the current value and exponent factor for CB contact opening duration. When the next CB opening operation is started, the energy is accumulated from the previous value. The accumulated energy value can be reset to initial accumulation energy value by using the Reset accumulating energy input, \textit{RSTIPOW}.

**Circuit breaker operation cycles**

Routine breaker maintenance like lubricating breaker mechanism is based on the number of operations. A suitable threshold setting helps in preventive maintenance. This can also be used to indicate the requirement for oil sampling for dielectric testing in case of an oil circuit breaker.

**Circuit breaker operation monitoring**

By monitoring the activity of the number of operations, it is possible to calculate the number of days the breaker has been inactive. Long periods of inactivity degrade the reliability for the protection system.

**Circuit breaker spring charge monitoring**

For normal circuit breaker operation, the circuit breaker spring should be charged within a specified time. Detecting a long spring charging time indicates the time for circuit breaker maintenance. The last value of the spring charging time can be given as a service value.

**Circuit breaker gas pressure indication**

For proper arc extinction by the compressed gas in the circuit breaker, the pressure of the gas must be adequate. Binary input available from the pressure sensor is based on the pressure levels inside the arc chamber. When the pressure becomes too low compared to the required value, the circuit breaker operation is blocked.

**14.4.3 Setting guidelines**

The breaker monitoring function is used to monitor different parameters of the circuit breaker. The breaker requires maintenance when the number of operations has reached a predefined value. For proper functioning of the circuit breaker, it is also essential to monitor the circuit breaker operation, spring charge indication or breaker wear, travel time, number of operation cycles and accumulated energy during arc extinction.
Since there is no current measurement in SAM600-IO, evaluation of the following parameters are not possible in the circuit breaker condition monitoring function (SSCBR):

- Circuit breaker status
- Remaining life of the circuit breaker
- Contact erosion estimation
- Circuit breaker contact travel time

Ensure that OPENPOS, CLOSEPOS, INVDPOS, CBLIFEAL, IPOWALPH, IPOWLOPH, TRVTOPAL and TRVTCLAL signals are not used in SAM600–IO.

### 14.4.3.1 Setting procedure on the IED

The parameters for breaker monitoring (SSCBR) can be set via the local HMI or Protection and Control Manager (PCM600).

Common base IED values for primary current (IBase), primary voltage (VBase) and primary power (SBase) are set in Global base values for settings function GBASVAL.

**GlobalBaseSel:** It is used to select a GBASVAL function for reference of base values.

**Operation:** Enabled or Disabled.

**IBase:** Base phase current in primary A. This current is used as reference for current settings.

**OpenTimeCorr:** Correction factor for circuit breaker opening travel time.

**CloseTimeCorr:** Correction factor for circuit breaker closing travel time.

**tTrOpenAlm:** Setting of alarm level for opening travel time.

**tTrCloseAlm:** Setting of alarm level for closing travel time.

**OperAlmLevel:** Alarm limit for number of mechanical operations.

**OperLOLevel:** Lockout limit for number of mechanical operations.

**CurrExponent:** Current exponent setting for energy calculation. It varies for different types of circuit breakers. This factor ranges from 0.5 to 3.0.

**AccStopCurr:** RMS current setting below which calculation of energy accumulation stops. It is given as a percentage of IBase.

**ContTrCorr:** Correction factor for time difference in auxiliary and main contacts' opening time.

**AlmAccCurrPwr:** Setting of alarm level for accumulated energy.

**LOAccCurrPwr:** Lockout limit setting for accumulated energy.

**SpChAlmTime:** Time delay for spring charging time alarm.

**tDGasPresAlm:** Time delay for gas pressure alarm.

**tDGasPresLO:** Time delay for gas pressure lockout.
**DirCoef**: Directional coefficient for circuit breaker life calculation.

**RatedOperCurr**: Rated operating current of the circuit breaker.

**RatedFltCurr**: Rated fault current of the circuit breaker.

**OperNoRated**: Number of operations possible at rated current.

**OperNoFault**: Number of operations possible at rated fault current.

**CBLifeAlmLevel**: Alarm level for circuit breaker remaining life.

**AccSelCal**: Selection between the method of calculation of accumulated energy.

**OperTimeDelay**: Time delay between change of status of trip output and start of main contact separation.

### 14.5 Event function EVENT

#### 14.5.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Event function</td>
<td>EVENT</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

#### 14.5.2 Application

When using a Substation Automation system with LON or SPA communication, time-tagged events can be sent at change or cyclically from the IED to the station level. These events are created from any available signal in the IED that is connected to the Event function (EVENT). The EVENT function block is used for remote communication.

Analog, integer and double indication values are also transferred through the EVENT function.

#### 14.5.3 Setting guidelines

The input parameters for the Event function (EVENT) can be set individually via the local HMI (Main Menu/Settings / IED Settings / Monitoring / Event Function) or via the Parameter Setting Tool (PST).

**EventMask (Ch_1 - 16)**

The inputs can be set individually as:
- NoEvents
- OnSet, at pick-up of the signal
- OnReset, at drop-out of the signal
- OnChange, at both pick-up and drop-out of the signal
- AutoDetect, the EVENT function makes the reporting decision (reporting criteria for integers have no semantic, prefer to be set by the user)

**LONChannelMask or SPACChannelMask**

Definition of which part of the event function block that shall generate events:

- Disabled
- Channel 1-8
- Channel 9-16
- Channel 1-16

**MinRepIntVal (1 - 16)**

A time interval between cyclic events can be set individually for each input channel. This can be set between 0 s to 3600 s in steps of 1 s. It should normally be set to 0, that is, no cyclic communication.

It is important to set the time interval for cyclic events in an optimized way to minimize the load on the station bus.

### 14.6 Disturbance report DRPRDRE

#### 14.6.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Disturbance report</td>
<td>DRPRDRE</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Disturbance report</td>
<td>A1RADR - A4RADR</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Disturbance report</td>
<td>B1RBDR - B2RBDR</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

#### 14.6.2 Application

To get fast, complete and reliable information about disturbances in the primary and/or in the secondary system it is very important to gather information on fault currents, voltages and events. It is also important having a continuous event-logging to be able to monitor in an overview perspective. These tasks are accomplished by the disturbance report function DRPRDRE and facilitate a better understanding of the power system behavior and related primary and secondary equipment during and after a disturbance. An analysis of the recorded data provides valuable information that can be used to explain a disturbance, basis for change of IED setting plan, improve existing equipment, and so on. This information can also be used in a longer perspective.
when planning for and designing new installations, that is, a disturbance recording could be a part of Functional Analysis (FA).

Disturbance report DRPRDRE, always included in the IED, acquires sampled data of all selected analog and binary signals connected to the function blocks that is,

- Maximum 30 external analog signals,
- 10 internal derived analog signals, and
- 352 binary signals

Disturbance report function is a common name for several functions; Indications (IND), Event recorder (ER), Sequential of events (SOE), Trip value recorder (TVR), Disturbance recorder (DR).

Disturbance report function is characterized by great flexibility as far as configuration, starting conditions, recording times, and storage capacity are concerned. Thus, disturbance report is not dependent on the operation of protective functions, and it can record disturbances that were not discovered by protective functions for one reason or another. Disturbance report can be used as an advanced stand-alone disturbance recorder.

Every disturbance report recording is saved in the IED. The same applies to all events, which are continuously saved in a ring-buffer. Local HMI can be used to get information about the recordings, and the disturbance report files may be uploaded in the PCM600 using the Disturbance handling tool, for report reading or further analysis (using WaveWin, that can be found on the PCM600 installation CD). The user can also upload disturbance report files using FTP or MMS (over 61850–8–1) clients.

If the IED is connected to a station bus (IEC 61850-8-1), the disturbance recorder (record made and fault number) and the fault locator information are available. The same information is obtainable if IEC 60870-5-103 is used.

### 14.6.3 Setting guidelines

The setting parameters for the Disturbance report function DRPRDRE are set via the local HMI or PCM600.

It is possible to handle up to 40 analog and 352 binary signals, either internal signals or signals coming from external inputs. The binary signals are identical in all functions that is, Disturbance recorder (DR), Event recorder (ER), Indication (IND), Trip value recorder (TVR) and Sequential of events (SOE) function.

User-defined names of binary and analog input signals are set using PCM600. The analog and binary signals appear with their user-defined names. The name is used in all related functions (Disturbance recorder (DR), Event recorder (ER), Indication (IND), Trip value recorder (TVR) and Sequential of events (SOE)).

Figure 201 shows the relations between Disturbance report, included functions and function blocks. Sequential of events (SOE), Event recorder (ER) and Indication (IND) uses information from the binary input function blocks (BxRBDR). Trip value recorder (TVR) uses analog information from the analog input function blocks (AxRADR). Disturbance report function acquires information from both AxRADR and BxRBDR.
Figure 201: Disturbance report functions and related function blocks

For Disturbance report function there are a number of settings which also influences the sub-functions.

Three LED indications placed above the LCD screen makes it possible to get quick status information about the IED.

Green LED:
- Steady light: In Service
- Flashing light: Internal failure
- Dark: No power supply

Yellow LED:
- Steady light: Triggered on binary signal N with SetLEDx = Start (or Start and Trip)
- Flashing light: The IED is in test mode

Red LED:
- Steady light: Triggered on binary signal N with SetLEDx = Trip (or Start and Trip)
- Flashing: The IED is in configuration mode
Operation
The operation of Disturbance report function DRPRDRE has to be set Enabled or Disabled. If Disabled is selected, note that no disturbance report is registered, and none sub-function will operate (the only general parameter that influences Sequential of events (SOE)).

Operation = Disabled:
- Disturbance reports are not stored.
- LED information (yellow - pickup, red - trip) is not stored or changed.

Operation = Enabled:
- Disturbance reports are stored, disturbance data can be read from the local HMI and from a PC for example using PCM600.
- LED information (yellow - pickup, red - trip) is stored.

Every recording will get a number (0 to 999) which is used as identifier (local HMI, disturbance handling tool and IEC 61850). An alternative recording identification is date, time and sequence number. The sequence number is automatically increased by one for each new recording and is reset to zero at midnight. The maximum number of recordings stored in the IED is 100. The oldest recording will be overwritten when a new recording arrives (FIFO).

To be able to delete disturbance records, Operation parameter has to be Enabled.

The maximum number of recordings depend on each recordings total recording time. Long recording time will reduce the number of recordings to less than 100.

The IED flash disk should NOT be used to store any user files. This might cause disturbance recordings to be deleted due to lack of disk space.

14.6.3.1 Recording times
Prefault recording time (PreFaultRecT) is the recording time before the starting point of the disturbance. The setting should be at least 0.1 s to ensure enough samples for the estimation of pre-fault values in the Trip value recorder (TVR) function.

Postfault recording time (PostFaultRecT) is the maximum recording time after the disappearance of the trig-signal (does not influence the Trip value recorder (TVR) function).

Recording time limit (TimeLimit) is the maximum recording time after trig. The parameter limits the recording time if some trigging condition (fault-time) is very long or permanently set (does not influence the Trip value recorder (TVR) function).

Operation in test mode
If the IED is in test mode and OpModeTest = Disabled. Disturbance report function does not save any recordings and no LED information is displayed.
If the IED is in test mode and $\text{OpModeTest} = \text{Enabled}$. Disturbance report function works in normal mode and the status is indicated in the saved recording.

**Post Retrigger**
Disturbance report function does not automatically respond to any new trig condition during a recording, after all signals set as trigger signals have been reset. However, under certain circumstances the fault condition may reoccur during the post-fault recording, for instance by automatic reclosing to a still faulty power line.

In order to capture the new disturbance it is possible to allow retriggering ($\text{PostRetrig} = \text{Enabled}$) during the post-fault time. In this case a new, complete recording will pickup and, during a period, run in parallel with the initial recording.

When the retrig parameter is disabled ($\text{PostRetrig} = \text{Disabled}$), a new recording will not pickup until the post-fault ($\text{PostFaultrecT}$ or $\text{TimeLimit}$) period is terminated. If a new trig occurs during the post-fault period and lasts longer than the proceeding recording a new complete recording will be started.

Disturbance report function can handle a maximum of 3 simultaneous disturbance recordings.

### 14.6.3.2 Binary input signals

Up to 352 binary signals can be selected among internal logical and binary input signals. The configuration tool is used to configure the signals.

For each of the 352 signals, it is also possible to select if the signal is to be used as a trigger for the start of the Disturbance report and if the trigger should be activated on positive (1) or negative (0) slope.

$\text{TrigDRN}$: Disturbance report may trig for binary input N ($\text{Enabled}$) or not ($\text{Disabled}$).

$\text{TrigLevelN}$: Trig on positive ($\text{Trig on 1}$) or negative ($\text{Trig on 0}$) slope for binary input N.

$\text{Func103N}$: Function type number (0-255) for binary input N according to IEC-60870-5-103, that is, 128: Distance protection, 160: overcurrent protection, 176: transformer differential protection and 192: line differential protection.

$\text{Info103N}$: Information number (0-255) for binary input N according to IEC-60870-5-103, that is, 69-71: Trip L1-L3, 78-83: Zone 1-6.

See also description in the chapter IEC 60870-5-103.

### 14.6.3.3 Analog input signals

Up to 40 analog signals can be selected among internal analog and analog input signals. PCM600 is used to configure the signals.

For retrieving remote data from LDCM module, the Disturbance report function should be connected to a 8 ms SMAI function block if this is the only intended use for the remote data.

The analog trigger of Disturbance report is not affected if analog input M is to be included in the disturbance recording or not ($\text{OperationM} = \text{Enabled/Disabled}$).
If $\text{OperationM} = \text{Disabled}$, no waveform (samples) will be recorded and reported in graph. However, Trip value, pre-fault and fault value will be recorded and reported. The input channel can still be used to trig the disturbance recorder.

If $\text{OperationM} = \text{Enabled}$, waveform (samples) will also be recorded and reported in graph.

$\text{NomValueM}$: Nominal value for input M.

$\text{OverTrigOpM, UnderTrigOpM}$: Over or Under trig operation, Disturbance report may trig for high/low level of analog input M ($\text{Enabled}$) or not ($\text{Disabled}$).

$\text{OverTrigLeM, UnderTrigLeM}$: Over or under trig level, Trig high/low level relative nominal value for analog input M in percent of nominal value.

### 14.6.3.4 Sub-function parameters

All functions are in operation as long as Disturbance report is in operation.

#### Indications

$\text{IndicationMaN}$: Indication mask for binary input N. If set ($\text{Show}$), a status change of that particular input, will be fetched and shown in the disturbance summary on local HMI. If not set ($\text{Hide}$), status change will not be indicated.

$\text{SetLEDN}$: Set red LED on local HMI in front of the IED if binary input N changes status.

#### Disturbance recorder

$\text{OperationM}$: Analog channel M is to be recorded by the disturbance recorder ($\text{Enabled}$) or not ($\text{Disabled}$).

If $\text{OperationM} = \text{Disabled}$, no waveform (samples) will be recorded and reported in graph. However, Trip value, pre-fault and fault value will be recorded and reported. The input channel can still be used to trig the disturbance recorder.

If $\text{OperationM} = \text{Enabled}$, waveform (samples) will also be recorded and reported in graph.

#### Setting information

$\text{SetInfoInDrep}$: Parameter used to enable or disable the settings information in disturbance header.

#### Event recorder

Event recorder (ER) function has no dedicated parameters.

#### Trip value recorder

$\text{ZeroAngleRef}$: The parameter defines which analog signal that will be used as phase angle reference for all other analog input signals. This signal will also be used for frequency measurement and the measured frequency is used when calculating trip values. It is suggested to point out a sampled voltage input signal, for example, a line or busbar phase voltage (channel 1-30).

#### Sequential of events

function has no dedicated parameters.
14.6.3.5 Consideration

The density of recording equipment in power systems is increasing, since the number of modern IEDs, where recorders are included, is increasing. This leads to a vast number of recordings at every single disturbance and a lot of information has to be handled if the recording functions do not have proper settings. The goal is to optimize the settings in each IED to be able to capture just valuable disturbances and to maximize the number that is possible to save in the IED.

The recording time should not be longer than necessary (*PostFaultrecT* and *TimeLimit*).

- Should the function record faults only for the protected object or cover more?
- How long is the longest expected fault clearing time?
- Is it necessary to include reclosure in the recording or should a persistent fault generate a second recording (*PostRetrig*)?

Minimize the number of recordings:

- Binary signals: Use only relevant signals to start the recording that is, protection trip, carrier receive and/or pickup signals.
- Analog signals: The level triggering should be used with great care, since unfortunate settings will cause enormously number of recordings. If nevertheless analog input triggering is used, chose settings by a sufficient margin from normal operation values. Phase voltages are not recommended for triggering.

There is a risk of flash wear out if the disturbance report triggers too often.

Remember that values of parameters set elsewhere are linked to the information on a report. Such parameters are, for example, station and object identifiers, CT and VT ratios.

14.7 Logical signal status report BINSTATREP

14.7.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Logical signal status report</td>
<td>BINSTATREP</td>
<td></td>
<td>-</td>
</tr>
</tbody>
</table>

14.7.2 Application

The Logical signal status report (BINSTATREP) function makes it possible to poll signals from various other function blocks.

BINSTATREP has 16 inputs and 16 outputs. The output status follows the inputs and can be read from the local HMI or via SPA communication.
When an input is set, the respective output is set for a user defined time. If the input signal remains set for a longer period, the output will remain set until the input signal resets.

```
INPUTn  ---------------
           \        /
           t        t
OUTPUTn  ---------------
```

*Figure 202: BINSTATREP logical diagram*

14.7.3 Setting guidelines

The pulse time $t$ is the only setting for the Logical signal status report (BINSTATREP). Each output can be set or reset individually, but the pulse time will be the same for all outputs in the entire BINSTATREP function.

14.8 Limit counter L4UFCNT

14.8.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Limit counter</td>
<td>L4UFCNT</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

14.8.2 Application

Limit counter (L4UFCNT) is intended for applications where positive and/or negative sides on a binary signal need to be counted.

The limit counter provides four independent limits to be checked against the accumulated counted value. The four limit reach indication outputs can be utilized to initiate proceeding actions. The output indicators remain high until the reset of the function.

It is also possible to initiate the counter from a non-zero value by resetting the function to the wanted initial value provided as a setting.

If applicable, the counter can be set to stop or rollover to zero and continue counting after reaching the maximum count value. The steady overflow output flag indicates the next count after reaching the maximum count value. It is also possible to set the counter to rollover and indicate the overflow as a pulse, which lasts up to the first count after rolling over to zero. In this case, periodic pulses will be generated at multiple overflow of the function.
14.8.3 Setting guidelines

The parameters for Limit counter L4UFCNT are set via the local HMI or PCM600.

14.9 Running hour-meter TEILGAPC

14.9.1 Identification

<table>
<thead>
<tr>
<th>Function Description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Running hour-meter TEILGAPC</td>
<td>TEILGAPC</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

14.9.2 Application

The function is used for user-defined logics and it can also be used for different purposes internally in the IED. An application example is to accumulate the total running/energized time of the generator, transformer, reactor, capacitor bank or even line.

Settable time limits for warning and alarm are provided. The time limit for overflow indication is fixed to 99999.9 hours. At overflow the accumulated time resets and the accumulation starts from zero again.

14.9.3 Setting guidelines

The settings tAlarm and tWarning are user settable limits defined in hours. The achievable resolution of the settings is 0.1 hours (6 minutes).

- **tAlarm** and **tWarning** are independent settings, that is, there is no check if **tAlarm** > **tWarning**.

The limit for the overflow supervision is fixed at 99999.9 hours.

The setting tAddToTime is a user settable time parameter in hours.

14.10 Current harmonic monitoring CHMMHAI(ITHD)

14.10.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Current harmonic monitoring CHMMHAI</td>
<td>CHMMHAI</td>
<td>ITHD</td>
<td>ITHD</td>
</tr>
</tbody>
</table>
14.10.2 Application

In order to maintain the power quality for better supply, harmonics in the system must be monitored. Due to the nonlinear loads connected to the system, harmonics (apart from fundamental frequency component) are generated and thereby the system voltage is distorted. Voltage distortion appears to have a little effect on operation of nonlinear loads connected, either phase-to-phase or phase-to-neutral. Current distortion is limited at the point of common coupling PCC to control the harmonic current from the utility to the consumer. Thereby, the voltage distortion must be limited in order to prevent it from spreading to other facilities.

Voltage harmonic distortion levels can vary drastically, depending on the configuration of system. These voltage harmonics can damage the equipment as they are designed to operate for certain range of voltage inaccuracy.

Moreover, in four-wire distribution systems (three-phase and neutral), the currents in the three phases will return via the neutral conductor, a 120 degree phase shift between corresponding phase currents that causes the currents to cancel out in the neutral, under balanced loading conditions. When nonlinear loads are present, any ‘Triplens’ (3rd, 9th,...) harmonics in the phase currents does not cancel out. However, they will be added cumulatively in the neutral conductor, which can carry up to 173% of phase current at a frequency of predominately 180 Hz (3rd harmonic).

In case of electric traction systems, it generates various power quality problems that have an important impact on its distribution network. DC traction loads, fed through AC/DC rectifiers, generates non-linear voltages and currents on the AC system, that will result in harmonic voltage distortion of the power supply system. Traction power supply system creates power quality problems to the corresponding grid, which can cause:

- Poor power quality
- Increase in operational cost due to less productivity
- Damage to sensitive equipment in nearby facilities.

Maintaining high power quality in traction system is very complex. The presence of non-linear loads reduces the capability of the existing harmonic mitigation techniques. However, it is essential to minimize the issues like harmonics, voltage sags and flicker to protect sensitive equipment affected by the aforementioned issues produced by traction systems.

Various practical conditions which have dynamic characteristics like the speed of locomotion, load and line condition will make this problem even worse. Harmonic current increases the heat dissipation due to hysteresis and eddy currents, which causes stress on insulation materials. Harmonic current increases transmission loss and the voltage drops.

In general, harmonics can cause reduced equipment life if a system is designed without considering the harmonics and if the equipment is not designed to withstand harmonics. Hence, it is important to measure and monitor harmonics in power systems. Harmonic voltage distortions on 161 kV power system and above is limited to 1.5% of total harmonic distortion (THD), in with each individual harmonic is limited to 1.0%.

Current harmonic limits vary based on the short circuit strength of the corresponding system they are injected into. Harmonic current limit defines the maximum amount of harmonic current that can be inject into the utility system. The difference between THD and TDD is used to calculate the harmonics level during light load conditions.
14.10.3 Setting guidelines

The recommended limits for total harmonic distortion and individual harmonic distortion are available in the IEEE 519 standard. The limits are based on measurements which are done at the point of common coupling. It should not be applied to either individual pieces of equipment or at locations within a user’s facility. In most cases, harmonic voltages and currents at these locations could be found to be significantly beyond recommended limits at the PCC due to the lack of diversity, cancellation, and other phenomena that tend to reduce the combined effects of multiple harmonic sources to levels below their algebraic summation.

14.10.3.1 Setting procedure on the IED

Parameters for CHMMHAI(ITHD) function are set via the local HMI or PCM600.

Common base IED values for primary current (IBase) is set in the global base values for settings function GBASVAL.

GlobalBaseSel: It is used to select GBASVAL function for reference of base values.

Minimum and maximum current limit is set as 5% of the IB and 300.0% of IB respectively. If the current is outside the above range, the calculation of total harmonic distortion, individual harmonic distortion, total demand distortion and crest factor are blocked and outputs are provided as zero.

MaxLoadCurr: Maximum demand load current at PCC for total demand distortion calculation. When the point of common coupling (PCC) is considered at the service entrance or utility metering point, IEEE 519 standard recommends that the maximum demand load current must be calculated as the average current of maximum demand for the preceding 12 months.

WrnLimitTDD: It defines the warning limit for the calculated total demand distortion. Harmonic current distortions on a power systems with ratio between the maximum short circuit current to the maximum demand load current is 20, limited to 5% of the total demand distortion (TDD).

tDelayAlmTDD: It defines the alarm delay time from warning for the calculated total demand distortion.

WrnLimitTHD: It defines the warning limit for the calculated total harmonic distortion.

tDelayAlmTHD: It defines the alarm delay time from warning for the calculated total harmonic distortion. This intimates the operator to take corrective operations immediately, otherwise the system will undergo thermal stress.

WrnLimit2ndHD: It defines the warning limit for the calculated second harmonic distortion.

tDelayAlm2ndHD: It defines the alarm delay time from warning for the calculated second harmonic distortion.

WrnLimit3rdHD: It defines the warning limit for the calculated third harmonic distortion.

tDelayAlm3rdHD: It defines the alarm delay time from warning for the calculated third harmonic distortion.

WrnLimit4thHD: It defines the warning limit for the calculated fourth harmonic distortion.
**tDelayAlm4thHD:** It defines the alarm delay time from warning for the calculated fourth harmonic distortion.

**WrnLimit5thHD:** It defines the warning limit for the calculated fifth harmonic distortion.

**tDelayAlm5thHD:** It defines the alarm delay time from warning for the calculated fifth harmonic distortion.

### 14.11 Voltage harmonic monitoring VHMMHAI(VTHD)

#### 14.11.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voltage harmonic monitoring</td>
<td>VHMMHAI</td>
<td>UTHD</td>
<td>VTHD</td>
</tr>
</tbody>
</table>

#### 14.11.2 Application

In order to maintain the power quality for better supply, harmonics in the system must be monitored. Due to the nonlinear loads connected to the system, harmonics (apart from fundamental frequency component) are generated and thereby the system voltage is distorted. Voltage distortion appears to have a little effect on operation of nonlinear loads connected, either phase-to-phase or phase-to-neutral. Current distortion is limited at the point of common coupling PCC to control the harmonic current from the utility to the consumer. Thereby, the voltage distortion must be limited in order to prevent it from spreading to other facilities.

Voltage harmonic distortion levels can vary drastically, depending on the configuration of system. These voltage harmonics can damage the equipment as they are designed to operate for certain range of voltage inaccuracy.

Moreover, in four-wire distribution systems (three-phase and neutral), the currents in the three phases will return via the neutral conductor, a 120 degree phase shift between corresponding phase currents that causes the currents to cancel out in the neutral, under balanced loading conditions. When nonlinear loads are present, any ‘Triplen’ (3\(^{rd}\), 9\(^{th}\)…) harmonics in the phase currents does not cancel out. However, they will be added cumulatively in the neutral conductor, which can carry up to 173% of phase current at a frequency of predominately 180 Hz (3\(^{rd}\) harmonic).

In case of electric traction systems, it generates various power quality problems that have an important impact on its distribution network. DC traction loads, fed through AC/DC rectifiers, generates non-linear voltages and currents on the AC system, that will result in harmonic voltage distortion of the power supply system. Traction power supply system creates power quality problems to the corresponding grid, which can cause:

- Poor power quality
- Increase in operational cost due to less productivity
- Damage to sensitive equipment in nearby facilities.

In general, harmonics can cause reduced equipment life if a system is designed without considering the harmonics and if the equipment is not designed to withstand harmonics. Hence, it
is important to measure and monitor harmonics in power systems. Harmonic voltage distortions on 161 kV power system and above is limited to 1.5% of total harmonic distortion (THD), in with each individual harmonic is limited to 1.0%.

14.11.3 Setting guidelines

The recommended limits for total harmonic distortion and individual harmonic distortion are available in the IEEE 519 standard. The limits are based on measurements which are done at the point of common coupling. It should not be applied to either individual pieces of equipment or at locations within a user’s facility. In most cases, harmonic voltages and currents at these locations could be found to be significantly beyond recommended limits at the PCC due to the lack of diversity, cancellation, and other phenomena that tend to reduce the combined effects of multiple harmonic sources to levels below their algebraic summation.

14.11.3.1 Setting procedure on the IED

Parameters for VHMMHAI(VTHD) function are set via the local HMI or PCM600.

Common base IED values for primary voltage ($U_{Base}$) is set in the global base values for settings function GBASVAL.

$GlobalBaseSel$: It is used to select GBASVAL function for reference of base values.

Minimum and maximum voltage limit is set as 5% of the $U_{B}$ and 300.0% of $U_{B}$ respectively. If the voltage is outside the above range, the calculation of total harmonic distortion, individual harmonic distortion and crest factor are blocked and outputs are provided as zero.

$WrnLimitTHD$: It defines the warning limit for the calculated total harmonic distortion.

$tDelayAlmTHD$: It defines the alarm delay time from warning for the calculated total harmonic distortion. This intimates the operator to take corrective operations immediately, otherwise the system will undergo thermal stress.

$WrnLimit2ndHD$: It defines the warning limit for the calculated second harmonic distortion.

$tDelayAlm2ndHD$: It defines the alarm delay time from warning for the calculated second harmonic distortion.

$WrnLimit3rdHD$: It defines the warning limit for the calculated third harmonic distortion.

$tDelayAlm3rdHD$: It defines the alarm delay time from warning for the calculated third harmonic distortion.

$WrnLimit4thHD$: It defines the warning limit for the calculated fourth harmonic distortion.

$tDelayAlm4thHD$: It defines the alarm delay time from warning for the calculated fourth harmonic distortion.

$WrnLimit5thHD$: It defines the warning limit for the calculated fifth harmonic distortion.

$tDelayAlm5thHD$: It defines the alarm delay time from warning for the calculated fifth harmonic distortion.
Section 15    Metering

15.1    Pulse-counter logic PCFCNT

15.1.1    Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pulse-counter logic</td>
<td>PCFCNT</td>
<td></td>
<td>-</td>
</tr>
</tbody>
</table>

15.1.2    Application

Pulse-counter logic (PCFCNT) function counts externally generated binary pulses, for instance pulses coming from an external energy meter, for calculation of energy consumption values. The pulses are captured by the binary input module (BIM), and read by the PCFCNT function. The number of pulses in the counter is then reported via the station bus to the substation automation system or read via the station monitoring system as a service value. When using IEC 61850–8–1, a scaled service value is available over the station bus.

The normal use for this function is the counting of energy pulses from external energy meters. An optional number of inputs from an arbitrary input module in IED can be used for this purpose with a frequency of up to 40 Hz. The pulse-counter logic PCFCNT can also be used as a general purpose counter.

15.1.3    Setting guidelines

Parameters that can be set individually for each pulse counter from PCM600:

- Operation: Disabled/Enabled
- tReporting: 0-3600s
- EventMask: NoEvents/ReportEvents

Configuration of inputs and outputs of PCFCNT is made via PCM600.

On the Binary input module (BIM), the debounce filter default time is set to 1 ms, that is, the counter suppresses pulses with a pulse length less than 1 ms. The input oscillation blocking frequency is preset to 40 Hz meaning that the counter detects the input to oscillate if the input frequency is greater than 40 Hz. Oscillation suppression is released at 30 Hz. Block/release values
for oscillation can be changed on the local HMI and PCM600 under **Main menu/Configuration/I/O modules**.

The setting is common for all input channels on BIM, that is, if limit changes are made for inputs not connected to the pulse counter, the setting also influences the inputs on the same board used for pulse counting.

### 15.2 Function for energy calculation and demand handling

**ETPMMTR**

#### 15.2.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Function for energy calculation and demand handling</td>
<td>ETPMMTR</td>
<td>W_Varh</td>
<td>-</td>
</tr>
</tbody>
</table>

#### 15.2.2 Application

Energy calculation and demand handling function (ETPMMTR) is intended for statistics of the forward and reverse active and reactive energy. It has a high accuracy basically given by the measurements function (CVMMXN). This function has a site calibration possibility to further increase the total accuracy.

The function is connected to the instantaneous outputs of (CVMMXN) as shown in figure **203**.

![Connection of energy calculation and demand handling function ETPMMTR to the measurements function (CVMMXN)](IEC13000190-2-en.vdx)

**Figure 203:** Connection of energy calculation and demand handling function ETPMMTR to the measurements function (CVMMXN)

The energy values can be read through communication in MWh and MVarh in monitoring tool of PCM600 and/or alternatively the values can be presented on the local HMI. The local HMI graphical display is configured with PCM600 Graphical Display Editor tool (GDE) with a measuring value.
which is selected to the active and reactive component as preferred. Also all Accumulated Active Forward, Active Reverse, Reactive Forward and Reactive Reverse energy values can be presented. Maximum demand values are presented in MWh or MVArh in the same way.

Alternatively, the energy values can be presented with use of the pulse counters function (PCGGIO). The output energy values are scaled with the pulse output setting values $EAFAccPlsQty$, $EARAccPlsQty$, $ERFAccPlsQty$ and $ERVAccPlsQty$ of the energy metering function and then the pulse counter can be set-up to present the correct values by scaling in this function. Pulse counter values can then be presented on the local HMI in the same way and/or sent to the SA (Substation Automation) system through communication where the total energy then is calculated by summation of the energy pulses. This principle is good for very high values of energy as the saturation of numbers else will limit energy integration to about one year with 50 kV and 3000 A. After that the accumulation will start on zero again.

15.2.3 Setting guidelines

The parameters are set via the local HMI or PCM600.

The following settings can be done for the energy calculation and demand handling function ETPMMTR:

- **GlobalBaseSel**: Selects the global base value group used by the function to define $I_{Base}$, $V_{Base}$ and $S_{Base}$. Note that this function will only use $I_{Base}$ value.

- **Operation**: Disabled/Enabled

- **EnaAcc**: Disabled/Enabled is used to switch the accumulation of energy on and off.

- **tEnergy**: Time interval when energy is measured.

- **tEnergyOnPls**: gives the pulse length ON time of the pulse. It should be at least 100 ms when connected to the Pulse counter function block. Typical value can be 100 ms.

- **tEnergyOffPls**: gives the OFF time between pulses. Typical value can be 100 ms.

- **$EAFAccPlsQty$ and $EARAccPlsQty$**: gives the MWh value in each pulse. It should be selected together with the setting of the Pulse counter (PCGGIO) settings to give the correct total pulse value.

- **$ERFAccPlsQty$ and $ERVAccPlsQty$**: gives the MVArh value in each pulse. It should be selected together with the setting of the Pulse counter (PCGGIO) settings to give the correct total pulse value.

For the advanced user there are a number of settings for direction, zero clamping, max limit, and so on. Normally, the default values are suitable for these parameters.
Section 16  Ethernet-based communication

16.1  Access point

16.1.1  Application

The access points are used to connect the IED to the communication buses (like the station bus) that use communication protocols. The access point can be used for single and redundant data communication. The access points are also used for communication with the merging units and for time synchronization using Precision Time Protocol (PTP).

16.1.2  Setting guidelines

The physical ports allocated to access points 2–6 have to be added in the hardware tool in PCM600 before the access points can be configured. The factory setting only includes the physical ports allocated to the front port and access point 1.

The settings for the access points are configured using the Ethernet configuration tool (ECT) in PCM600.

The access point is activated if the Operation checkbox is checked for the respective access point and a partial or common write to IED is performed.

To increase security, it is recommended to deactivate the access point when it is not in use.

Redundancy and PTP cannot be set for the front port (Access point 0) as redundant communication and PTP are only available for the rear optical Ethernet ports.

Subnetwork shows the SCL subnetwork to which the access point is connected. This column shows the SCL subnetworks available in the PCM600 project. SCL subnetworks can be created/deleted in the Subnetworks tab of IEC 61850 Configuration tool in PCM600.

When saving the ECT configuration after selecting a subnetwork, ECT creates the access point in the SCL model. Unselecting the subnetwork removes the access point from the SCL model. This column is editable for IEC61850 Ed2 IEDs and not editable for IEC61850 Ed1 IEDs because in IEC61850 Ed1 only one access point can be modelled in SCL.

The IP address can be set in IP address. ECT validates the value, the access points have to be on separate subnetworks.

The subnetwork mask can be set in Subnet mask. This field will be updated to the SCL model based on the Subnetwork selection.
To select which communication protocols can be run on the respective access points, check or uncheck the check box for the relevant protocol. The protocols are not activated/deactivated in ECT, only filtered for the specific access point. For information on how to activate the individual communication protocols, see the communication protocol chapters.

To increase security it is recommended to uncheck protocols that are not used on the access point.

The default gateway can be selected by entering the IP address in Default gateway. The default gateway is the router that is used to communicate with the devices in the other subnetwork. By default this is set to 0.0.0.0 which means that no default gateway is selected. ECT validates the entered value, but the default gateway has to be in the same subnetwork as the access point. The default gateway is the router that is being used as default, that is when no route has been set up for the destination. If communication with a device in another subnetwork is needed, a route has to be set up. For more information on routes, see the Routes chapter in the Technical manual and the Application manual.

DHCP can be activated for the front port from the LHMI in Main menu/Configuration/Communication/Ethernet configuration/Front port/DHCP:1

### 16.2 Redundant communication

#### 16.2.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>IEC 62439-3 Parallel redundancy protocol</td>
<td>PRP</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>IEC 62439-3 High-availability seamless redundancy</td>
<td>HSR</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Access point diagnostic for redundant Ethernet ports</td>
<td>RCHLCCH</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

#### 16.2.2 Application

Dynamic access point diagnostic (RCHLCCH) is used to supervise and assure redundant Ethernet communication over two channels. This will secure data transfer even though one communication channel might not be available for some reason.

Parallel Redundancy Protocol (PRP) and High-availability Seamless Redundancy (HSR) provides redundant communication over station bus running the available communication protocols. The redundant communication uses two Ethernet ports.
Figure 204: Parallel Redundancy Protocol (PRP)

Figure 205: High-availability Seamless Redundancy (HSR)
16.2.3 Setting guidelines

Redundant communication is configured with the Ethernet configuration tool in PCM600.

**Redundancy:** redundant communication is activated when the parameter is set to PRP-0, PRP-1 or HSR. The settings for the next access point will be hidden and PhyPortB will show the second port information. Redundant communication is activated after a common write to IED is done.

PRP-1 should be used primarily, PRP-0 is intended only for use in existing PRP-networks. PRP-1 and HSR can be combined in a mixed network.

If the access point is not taken into operation, the write option in Ethernet Configuration Tool can be used to activate the access point.

![ECT screen with Redundancy set to PRP-1 on Access point 1 and HSR Access point 3](IEC16000039-1-en.vsdx)

*Figure 206: ECT screen with Redundancy set to PRP-1 on Access point 1 and HSR Access point 3*

16.3 Merging unit

16.3.1 Application

The IEC/UCA 61850-9-2LE process bus communication protocol enables an IED to communicate with devices providing measured values in digital format, commonly known as Merging Units (MU). The rear access points are used for the communication.

The merging units (MU) are called so because they can gather analog values from one or more measuring transformers, sample the data and send the data over process bus to other clients (or subscribers) in the system. Some merging units are able to get data from classical measuring transformers, others from non-conventional measuring transducers and yet others can pick up data from both types.
For information on the merging unit setting guidelines, see section IEC/UCA 61850-9-2LE communication protocol.

Routes

16.4.1 Application

Setting up a route enables communication to a device that is located in another subnetwork. Routing is used when the destination device is not in the same subnetwork as the default gateway.

The route specifies that when a package is sent to the destination device it should be sent through the selected router. If no route is specified the source device will not find the destination device.

16.4.2 Setting guidelines

Routes are configured using the Ethernet configuration tool in PCM600.

Operation for the route can be set to On/Off by checking and unchecking the check-box in the operation column.

Gateway specifies the address of the gateway.

Destination specifies the destination.

Destination subnet mask specifies the subnetwork mask of the destination.
17.1 Communication protocols

Each IED is provided with several communication interfaces enabling it to connect to one or many substation level systems or equipment, either on the Substation Automation (SA) bus or Substation Monitoring (SM) bus.

Available communication protocols are:

- IEC 61850-8-1 communication protocol
- IEC/UCA 61850-9-2LE communication protocol
- LON communication protocol
- SPA communication protocol
- IEC 60870-5-103 communication protocol

Several protocols can be combined in the same IED.

17.2 IEC 61850-8-1 communication protocol

17.2.1 Application IEC 61850-8-1

IEC 61850-8-1 communication protocol allows vertical communication to HSI clients and allows horizontal communication between two or more intelligent electronic devices (IEDs) from one or several vendors to exchange information and to use it in the performance of their functions and for correct co-operation.

GOOSE (Generic Object Oriented Substation Event), which is a part of IEC 61850–8–1 standard, allows the IEDs to communicate state and control information amongst themselves, using a publish-subscribe mechanism. That is, upon detecting an event, the IED(s) use a multi-cast transmission to notify those devices that have registered to receive the data. An IED can, by publishing a GOOSE message, report its status. It can also request a control action to be directed at any device in the network.

Figure 208 shows the topology of an IEC 61850–8–1 configuration. IEC 61850–8–1 specifies only the interface to the substation LAN. The LAN itself is left to the system integrator.
Figure 208: SA system with IEC 61850–8–1

Figure 209 shows the GOOSE peer-to-peer communication.

Figure 209: Example of a broadcasted GOOSE message
17.2.2 Setting guidelines

There are two settings related to the IEC 61850–8–1 protocol:

*Operation:* User can set IEC 61850 communication to *Enabled* or *Disabled*.

*GOOSEPortEd1:* Selection of the Ethernet link where GOOSE traffic shall be sent and received. This is only valid for Edition 1 and can be ignored if Edition 2 is used. For Edition 2, the Ethernet link selection is done with the Ethernet Configuration Tool (ECT) in PCM600.

17.2.3 Horizontal communication via GOOSE

17.2.3.1 Sending data

In addition to the data object and data attributes of the logical nodes, it is possible to send the outputs of the function blocks using the generic communication blocks. The outputs of this function can be set in a dataset and be sent in a GOOSE Control Block to other subscriber IEDs. There are different function blocks for different type of sending data.

**Generic communication function for Single Point indication SPGAPC, SP16GAPC**

**Application**

Generic communication function for Single Point Value (SPGAPC) function is used to send one single logical output to other systems or equipment in the substation. SP16GAPC can be used to send up to 16 single point values from the application functions running in the same cycle time. SPGAPC has one visible input and SPGAPC16 has 16 visible inputs that should be connected in the ACT tool.

**Setting guidelines**

There are no settings available for the user for SPGAPC.

**Generic communication function for Measured Value MVGAPC**

**Application**

Generic communication function for measured values (MVGAPC) function is used to send the instantaneous value of an analog signal to other systems or equipment in the substation. It can also be used inside the same IED, to attach a RANGE aspect to an analog value and to permit measurement supervision on that value.

**Setting guidelines**

The settings available for Generic communication function for Measured Value (MVGAPC) function allows the user to choose a deadband and a zero deadband for the monitored signal. Values within the zero deadband are considered as zero.

The high and low limit settings provides limits for the high-high-, high, normal, low and low-low ranges of the measured value. The actual range of the measured value is shown on the range output of MVGAPC function block. When a Measured value expander block (RANGE_XP) is connected to the range output, the logical outputs of the RANGE_XP are changed accordingly.
17.2.3.2 Receiving data

The GOOSE data must be received at function blocks. There are different GOOSE receiving function blocks depending on the type of the received data. Refer to the Engineering manual for more information about how to configure GOOSE.

<table>
<thead>
<tr>
<th>Function block type</th>
<th>Data Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>GOOSEBINRCV</td>
<td>16 single point</td>
</tr>
<tr>
<td>GOOSEINTLKRCV</td>
<td>2 single points</td>
</tr>
<tr>
<td></td>
<td>16 double points</td>
</tr>
<tr>
<td>GOOSEDPRCV</td>
<td>Double point</td>
</tr>
<tr>
<td>GOOSEINTRCV</td>
<td>Integer</td>
</tr>
<tr>
<td>GOOSEMVRCV</td>
<td>Analog value</td>
</tr>
<tr>
<td>GOOSESPRCV</td>
<td>Single point</td>
</tr>
<tr>
<td>GOOSEXLNRCV</td>
<td>Switch status</td>
</tr>
</tbody>
</table>

Application

The GOOSE receive function blocks are used to receive subscribed data from the GOOSE protocol. The validity of the data value is exposed as outputs of the function block as well as the validity of the communication. It is recommended to use these outputs to ensure that only valid data is handled on the subscriber IED. An example could be to control the external reservation before operating on a bay. In the figure below, the GOOSESPRCV is used to receive the status of the bay reservation. The validity of the received data is used in additional logic to guarantee that the value has good quality before operation on that bay.

![GOOSESPRCV and AND function blocks - checking the validity of the received data](IEC16000082=1=en.vsd)

17.3 IEC/UCA 61850-9-2LE communication protocol

17.3.1 Introduction

Every IED can be provided with communication interfaces enabling it to connect to the process buses in order to get data from analog data acquisition units close to the process (primary apparatus), commonly known as Merging Units (MU). The protocol used in this case is the IEC/UCA 61850-9-2LE communication protocol.

The IEC/UCA 61850-9-2LE standard does not specify the quality of the sampled values. Thus, the accuracy of the current and voltage inputs to the merging unit and the inaccuracy added by the merging unit must be coordinated with the requirement for the actual type of protection function.
Factors influencing the accuracy of the sampled values from the merging unit are, for example, anti aliasing filters, frequency range, step response, truncating, A/D conversion inaccuracy, time tagging accuracy etc.

In principle, the accuracy of the current and voltage transformers, together with the merging unit, will have the same quality as the direct input of currents and voltages.

The process bus physical layout can be arranged in several ways, described in Annex B of the standard, depending on what are the needs for sampled data in a substation.

**Figure 211: Example of a station configuration with separated process bus and station bus**

The IED can get analog values simultaneously from a classical CT or VT and from a Merging Unit, like in this example:

The merging units (MU) are called so because they can gather analog values from one or more measuring transformers, sample the data and send the data over process bus to other clients (or subscribers) in the system. Some merging units are able to get data from classical measuring transformers, others from non-conventional measuring transducers and yet others can pick up data from both types. The electronic part of a non-conventional measuring transducer (like a Rogowski coil or a capacitive divider) can represent a MU by itself as long as it can send sampled data over process bus.
Figure 212: Example of a station configuration with the IED receiving analog values from both classical measuring transformers and merging units.

17.3.2 Setting guidelines

Merging Units (MUs) have several settings on local HMI under:
- **Main menu/Configuration/Analog modules/MUx:92xx.** The corresponding settings are also available in PST (PCM600).
- **Main menu/Configuration/Communication/Merging units configuration/MUx:92xx.** The corresponding settings are also available in ECT (PCM600).
XX can take value 01–12.

17.3.2.1 **Specific settings related to the IEC/UCA 61850-9-2LE communication**

The process bus communication IEC/UCA 61850-9-2LE has specific settings, similar to the analog inputs modules.

If there are more than one sample group involved, time synch is mandatory. If there is no time synchronization, the protection functions will be blocked due to condition blocking.

*CTStarPointx*: These parameters specify the direction to or from object. See also section "Setting of current channels".

*SyncLostMode*: If this parameter is set to *Block* and the IED hardware time synchronization is lost or the synchronization to the MU time is lost, the protection functions in the list 54 will be blocked due to conditional blocking. If this parameter is set to *BlockOnLostUTC*, the protection functions in list 54 are blocked if the IED hardware time synchronization is lost or the synchronization of the MU time is lost or the IED has lost global common synchronization (i.e. GPS, IRIG-B or PTP). *SYNCH* output will be set if IED hardware time synchronization is lost. *MUSYNCH* output will be set if either of MU or IED hardware time synchronization is lost.

17.3.2.2 **Loss of communication when used with LDCM**

If IEC/UCA 61850-9-2LE communication is lost, see examples in figures 213, 214 and 215, the protection functions in table 54 are blocked as per graceful degradation.

**Case 1:**

![Diagram of normal operation](ANSI13.000298-2-en.vsd)

*Figure 213: Normal operation*

**Case 2:**

Failure of the MU (sample lost) blocks the sending of binary signals through LDCM. The received binary signals are not blocked and processed normally.

→DTT from the remote end is still processed.
Case 3:

Failure of one MU (sample lost) blocks the sending and receiving of binary signals through LDCM. → DTT from the remote end is not working.

Table 54: Blocked protection functions if IEC/UCA 61850-9-2LE communication is interrupted and functions are connected to specific MUs

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>Function description</th>
<th>IEC 61850 Identification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Accidental energizing protection for synchronous generator</td>
<td>AEGPVOC</td>
<td>Two step overvoltage protection</td>
<td>OV2PTOV</td>
</tr>
<tr>
<td>Broken conductor check</td>
<td>BRCPTOC</td>
<td>Four step single phase overcurrent protection</td>
<td>PH4SPTOC</td>
</tr>
<tr>
<td>Capacitor bank protection</td>
<td>CBPGAPC</td>
<td>Radial feeder protection</td>
<td>PAPGAPC</td>
</tr>
<tr>
<td>Pole discordance protection</td>
<td>CCPDSC</td>
<td>Instantaneous phase overcurrent protection</td>
<td>PHPIOC</td>
</tr>
<tr>
<td>Breaker failure protection</td>
<td>CCRBRF</td>
<td>PoleSlip/Out-of-step protection</td>
<td>PSPPPAM</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 Identification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Breaker failure protection, single phase version</td>
<td>CCSRBRF</td>
</tr>
<tr>
<td>Restricted earth fault protection, low impedance</td>
<td>REFPDIF</td>
</tr>
<tr>
<td>Current circuit supervision</td>
<td>CCSSPVC</td>
</tr>
<tr>
<td>Two step residual overvoltage protection</td>
<td>ROV2PTOV</td>
</tr>
<tr>
<td>Compensated over- and undervoltage protection</td>
<td>COUVGAPC</td>
</tr>
<tr>
<td>Rate-of-change frequency protection</td>
<td>SAPFRC</td>
</tr>
<tr>
<td>General current and voltage protection</td>
<td>CVGAPC</td>
</tr>
<tr>
<td>Overfrequency protection</td>
<td>SAPTOF</td>
</tr>
<tr>
<td>Current reversal and weakened infeed logic for residual overcurrent protection</td>
<td>ECRWPSCH</td>
</tr>
<tr>
<td>Underfrequency protection</td>
<td>SAPTUF</td>
</tr>
<tr>
<td>Four step residual overcurrent protection</td>
<td>EF4PTOC</td>
</tr>
<tr>
<td>Sudden change in current variation</td>
<td>SCCVPTOC</td>
</tr>
<tr>
<td>Instantaneous residual overcurrent protection</td>
<td>EFPIOC</td>
</tr>
<tr>
<td>Sensitive Directional residual over current and power protection</td>
<td>SDEPSDE</td>
</tr>
<tr>
<td>Phase selection, quadrilateral characteristic with fixed angle</td>
<td>FDPSPDIS</td>
</tr>
<tr>
<td>Synchrocheck, energizing check, and synchronizing</td>
<td>SESR5YN</td>
</tr>
<tr>
<td>Faulty phase identification with load enchroachment</td>
<td>FMPSPDIS</td>
</tr>
<tr>
<td>Circuit breaker condition monitoring</td>
<td>SSCBR</td>
</tr>
<tr>
<td>Phase selection, quadrilateral characteristic with settable angle</td>
<td>FRPSPDIS</td>
</tr>
<tr>
<td>Insulation gas monitoring</td>
<td>SSIMG</td>
</tr>
<tr>
<td>Frequency time accumulation protection</td>
<td>FTAQFVR</td>
</tr>
<tr>
<td>Insulation liquid monitoring</td>
<td>SSIML</td>
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<tr>
<td>Fuse failure supervision</td>
<td>FUFSPVVC</td>
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<tr>
<td>Stub protection</td>
<td>STBPTOC</td>
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<tr>
<td>Generator differential protection</td>
<td>GENPDIF</td>
</tr>
<tr>
<td>Transformer differential protection, two winding</td>
<td>T2WPDIFF</td>
</tr>
<tr>
<td>Directional Overpower protection</td>
<td>GOPPDOP</td>
</tr>
<tr>
<td>Transformer differential protection, three winding</td>
<td>T3WPDIFF</td>
</tr>
<tr>
<td>Generator rotor overload protection</td>
<td>GRPTTR</td>
</tr>
<tr>
<td>Automatic voltage control for tapchanger, single control</td>
<td>TR1ATCC</td>
</tr>
<tr>
<td>Generator stator overload protection</td>
<td>GSPTR</td>
</tr>
<tr>
<td>Automatic voltage control for tapchanger, parallel control</td>
<td>TR8ATCC</td>
</tr>
<tr>
<td>Directional Underpower protection</td>
<td>GUPPDUP</td>
</tr>
<tr>
<td>Thermal overload protection, two time constants</td>
<td>TRPTTR</td>
</tr>
<tr>
<td>1Ph High impedance differential protection</td>
<td>HZPDIF</td>
</tr>
<tr>
<td>Two step undervoltage protection</td>
<td>UV2PTUV</td>
</tr>
<tr>
<td>Line differential protection, 3 CT sets, 2-3 line ends</td>
<td>L3CPDIF</td>
</tr>
<tr>
<td>Voltage differential protection</td>
<td>VDCPTO PVC</td>
</tr>
</tbody>
</table>

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<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>Function description</th>
<th>IEC 61850 Identification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line differential protection, 6 CT sets, 3-5 line ends</td>
<td>L6CPDIF</td>
<td>Fuse failure supervision</td>
<td>VDRFUF</td>
</tr>
<tr>
<td>Low active power and power factor protection</td>
<td>LAPPGAPC</td>
<td>Voltage-restrained time overcurrent protection</td>
<td>VRPVOC</td>
</tr>
<tr>
<td>Negative sequence overcurrent protection</td>
<td>LCNSPTOC</td>
<td>Local acceleration logic</td>
<td>ZCLCPDSC</td>
</tr>
<tr>
<td>Negative sequence overvoltage protection</td>
<td>LCNSPTOV</td>
<td>Scheme communication logic for distance or overcurrent protection</td>
<td>ZCPDSC</td>
</tr>
<tr>
<td>Three phase overcurrent</td>
<td>LCP3PTOC</td>
<td>Current reversal and weak-end infeed logic for distance protection</td>
<td>ZCRWPDC</td>
</tr>
<tr>
<td>Three phase undercurrent</td>
<td>LCP3PTUC</td>
<td>Automatic switch onto fault logic, voltage and current based</td>
<td>ZCVPSOF</td>
</tr>
<tr>
<td>Thermal overload protection, one time constant</td>
<td>LCPTTR</td>
<td>Under impedance protection for generator</td>
<td>ZGPDIS</td>
</tr>
<tr>
<td>Zero sequence overcurrent protection</td>
<td>LCZSPATO</td>
<td>Fast distance protection</td>
<td>ZMCPDIS</td>
</tr>
<tr>
<td>Zero sequence overvoltage protection</td>
<td>LCZSPTOV</td>
<td>High speed distance protection</td>
<td>ZMFPDIS</td>
</tr>
<tr>
<td>Line differential coordination</td>
<td>LDLPSCH</td>
<td>Distance measuring zone, quadrilateral characteristic for series compensated lines</td>
<td>ZMCAPDIS</td>
</tr>
<tr>
<td>Additional security logic for differential protection</td>
<td>LDRGFC</td>
<td>Distance measuring zone, quadrilateral characteristic for series compensated lines</td>
<td>ZMCPDIS</td>
</tr>
<tr>
<td>Loss of excitation</td>
<td>LEXPDIS</td>
<td>Fullscheme distance protection, mho characteristic</td>
<td>ZMHPDIS</td>
</tr>
<tr>
<td>Thermal overload protection, one time constant</td>
<td>LFPTTR</td>
<td>Fullscheme distance protection, quadrilateral for earth faults</td>
<td>ZMMAPPDIS</td>
</tr>
<tr>
<td>Loss of voltage check</td>
<td>LOVPTUVC</td>
<td>Fullscheme distance protection, quadrilateral for earth faults</td>
<td>ZMPDIS</td>
</tr>
<tr>
<td>Line differential protection 3 CT sets, with inzone transformers, 2-3 line ends</td>
<td>LT3CPDIF</td>
<td>Distance protection zone, quadrilateral characteristic</td>
<td>ZMQAPDIS</td>
</tr>
<tr>
<td>Line differential protection 6 CT sets, with inzone transformers, 3-5 line ends</td>
<td>LT6CPDIF</td>
<td>Distance protection zone, quadrilateral characteristic</td>
<td>ZMQPDIS</td>
</tr>
<tr>
<td>Negative sequence time overcurrent protection for machines</td>
<td>NS2PTOC</td>
<td>Distance protection zone, quadrilateral characteristic, separate settings</td>
<td>ZMRAPDIS</td>
</tr>
</tbody>
</table>

Table continues on next page
### Function description

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 Identification</th>
<th>Function description</th>
<th>IEC 61850 Identification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Four step directional negative phase sequence overcurrent protection</td>
<td>NS4PTOC</td>
<td>Distance protection zone, quadrilateral characteristic, separate settings</td>
<td>ZMRPDIS</td>
</tr>
<tr>
<td>Four step phase overcurrent protection</td>
<td>OC4PTOC</td>
<td>Power swing detection</td>
<td>ZMRPSB</td>
</tr>
<tr>
<td>Overexcitation protection</td>
<td>OEXPVPH</td>
<td>Mho impedance supervision logic</td>
<td>ZSMGAPC</td>
</tr>
<tr>
<td>Out-of-step protection</td>
<td>OOSPPAM</td>
<td>Transformer tank overcurrent protection</td>
<td>TPIIOC</td>
</tr>
<tr>
<td>Busbar differential protection, check zone</td>
<td>BCZPDIF</td>
<td>Busbar differential protection, bus interconnection xx</td>
<td>BICPTRC_x, (1≤x≤5)</td>
</tr>
<tr>
<td>Busbar differential protection, dynamic zone selection</td>
<td>BDZSGAPC</td>
<td>Busbar differential protection, zone 1</td>
<td>BZNPDIF_Zx, (1≤x≤6)</td>
</tr>
<tr>
<td>Busbar differential protection, single phase feeder xx</td>
<td>BFPTRC_Fx, (1≤x≤24)</td>
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<td></td>
</tr>
<tr>
<td>Voltage delta supervision, 2 phase</td>
<td>DELVSPVC</td>
<td>Current delta supervision, 2 phase</td>
<td>DELISPVC</td>
</tr>
<tr>
<td>DELSPVC</td>
<td>Current harmonic monitoring, 2 phase</td>
<td>CHMMHAN</td>
<td></td>
</tr>
</tbody>
</table>

### 17.3.2.3 Setting examples for IEC/UCA 61850-9-2LE and time synchronization

The IED and the Merging Units (MU) should use the same time reference especially if analog data is used from several sources, for example from an internal TRM and an MU, or if several physical MUs are used. Having the same time reference is important to correlate data so that channels from different sources refer to the correct phase angle.

When only one MU is used as an analog source, it is theoretically possible to do without time synchronization. However, this would mean that timestamps for analog and binary data/events become uncorrelated. If the IED has no time synchronization source configured, then the binary data/events will be synchronized with the merging unit. However, the global/complete time might not be correct. Disturbance recordings then appear incorrect since analog data is timestamped by MU, and binary events use the internal IED time. It is thus recommended to use time synchronization also when analog data emanate from only one MU.

An external time source can be used to synchronize both the IED and the MU. It is also possible to use the MU as a clock master to synchronize the IED from the MU. When using an external clock, it is possible to set the IED to be synchronized via PPS,IRIG-B or PTP. It is also possible to use an internal GPS receiver in the IED (if the external clock is using GPS).
Using PTP for synchronizing the MU

Figure 216: Setting example with PTP synchronization

Settings on the local HMI under Main menu/Configuration/Time/Synchronization/TIMESYNCHGEN:1/IEC61850-9-2:

- **HwSyncSrc**: is not used as the SW-time and HW-time are connected with each other due to PTP
- **SyncLostMode**: set to *Block* to block protection functions if time synchronization is lost or set to *BlockOnLostUTC* if the protection functions are to be blocked when global common synchronization is lost
- **SyncAccLevel**: can be set to 1μs since this corresponds to a maximum phase angle error of 0.018 degrees at 50Hz

Settings on the local HMI under Main menu/Configuration/Communication/Ethernet configuration/Access point/AP_X:

- Operation: On
- PTP: On

Two status monitoring signals can be:
• SYCH signal on the MUx function block indicates that protection functions are blocked due to loss of internal time synchronization to the IED
• MUSYNCH signal on the MUx function block monitors the synchronization flag $smpSynch$ in the datastream and IED hardware time synchronization.

**Using MU for time synchronization via PPS**

This example is not valid when GPS time is used for differential protection, when PTP is enabled or when the PMU report is used.

![Diagram](IEC10000061=2=en=Original.vsd)

*Figure 217: Setting example when MU is the synchronizing source*

Settings on the local HMI under `Main menu/Configuration/Time/Synchronization/TIMESYNCHGEN:1/IEC61850-9-2`:

- `HwSyncSrc`: set to *PPS* as generated by the MU (ABB MU)
- `SyncLostMode`: set to *Block* to block protection functions if time synchronization is lost
- `SyncAccLevel`: can be set to $4\mu s$ since this corresponds to a maximum phase angle error of 0.072 degrees at 50Hz

Settings on the local HMI under `Main menu/Configuration/Time/Synchronization/TIMESYNCHGEN:1/General`:

- `fineSyncSource` can be set to something different to correlate events and data to other IEDs in the station.

Two status monitoring signals can be:

- SYCH signal on the MUx function block indicates that protection functions are blocked due to loss of internal time synchronization to the IED.
- MUSYNCH signal on the MUx function block monitors the synchronization flag $smpSynch$ in the datastream and IED hardware time synchronization.
SMPLYLOST indicates that merging unit data are generated by internal substitution or one/more channel's Quality is not good or merging unit is in Testmode/detailed quality=Test, IED is not in test mode.

**Using external clock for time synchronization**

This example is not valid when GPS time is used for differential protection, when PTP is enabled or when the PMU report is used.

![Diagram](IEC10000074=2=en=Original.vsd)

**Figure 218: Setting example with external synchronization**

Settings on the local HMI under **Main menu/Configuration/Time/Synchronization/TIMESYNCHGEN:1/IEC61850-9-2:**

- **HwSyncSrc:** set to `PPS/IRIG-B` depending on available outputs on the clock.
- **SyncLostMode:** set to `Block` to block protection functions if time synchronization is lost.
- **SyncAccLevel:** can be set to `4μs` since this corresponds to a maximum phase angle error of 0.072 degrees at 50Hz.
- **fineSyncSource:** should be set to `IRIG-B` if available from the clock. If `PPS` is used for `HWSyncSrc`, “full-time” has to be acquired from another source. If station clock is on the local area network (LAN) and has an sntp-server, this is one option.

Two status monitoring signals can be:

- **SYNCH** signal on the MUx function block indicates that protection functions are blocked due to loss of internal time synchronization to the IED (that is loss of the hardware `synchron`).
- **MUSYNCH** signal on the MUx function block monitors the synchronization flag `smpSynch` in the datastream and IED hardware time synchronization.

**No time synchronization**

This example is not valid when GPS time is used for differential protection, when PTP is enabled or when the PMU report is used.
Figure 219: Setting example without time synchronization

It is also possible to use IEC/UCA 61850-9-2LE communication without time synchronization.

Settings on the local HMI under **Main menu/Configuration/Time/Synchronization/TIMESYNCHGEN:1/IEC61850-9-2**:

- **HwSyncSrc**: set to Off
- **SyncLostMode**: set to *No block* to indicate that protection functions are not blocked
- **SyncAccLevel**: set to unspecified

Two status monitoring signals with no time synchronization:

- **SYNCH** signal on the MUx function block indicates that protection functions are blocked due to loss of internal time synchronization to the IED. Since **SyncLostMode** is set to *No block*, this signal is not set.
- **MUSYNCH** signal on the MUx function block is set if the datastream indicates time synchronization loss. However, protection functions are not blocked.

To get higher availability in protection functions, it is possible to avoid blocking during time synchronization loss if there is a single source of analog data. This means that if there is only one physical MU and no TRM, parameter **SyncLostMode** is set to *No block* but parameter **HwSyncSrc** is still set to *PPS*. This maintains analog and binary data correlation in disturbance recordings without blocking protection functions if PPS is lost.
17.4 LON communication protocol

17.4.1 Application

An optical network can be used within the substation automation system. This enables communication with the IEDs through the LON bus from the operator’s workplace, from the control center and also from other IEDs via bay-to-bay horizontal communication. For LON communication an SLM card should be ordered for the IEDs.

The fiber optic LON bus is implemented using either glass core or plastic core fiber optic cables.

*Figure 220: Example of LON communication structure for a substation automation system*

<table>
<thead>
<tr>
<th>Glass fiber</th>
<th>Plastic fiber</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cable connector</td>
<td>ST-connector</td>
</tr>
<tr>
<td>Cable diameter</td>
<td>62.5/125 m</td>
</tr>
<tr>
<td>Max. cable length</td>
<td>1000 m</td>
</tr>
<tr>
<td>Wavelength</td>
<td>820-900 nm</td>
</tr>
<tr>
<td>Transmitted power</td>
<td>-13 dBm (HFBR-1414)</td>
</tr>
<tr>
<td>Receiver sensitivity</td>
<td>-24 dBm (HFBR-2412)</td>
</tr>
</tbody>
</table>

The LON Protocol

The LON protocol is specified in the LonTalkProtocol Specification Version 3 from Echelon Corporation. This protocol is designed for communication in control networks and is a peer-to-peer protocol where all the devices connected to the network can communicate with each other.
directly. For more information of the bay-to-bay communication, refer to the section Multiple command function.

**Hardware and software modules**
The hardware needed for applying LON communication depends on the application, but one very central unit needed is the LON Star Coupler and optical fibers connecting the star coupler to the IEDs. To interface the IEDs from the MicroSCADA with Classic Monitor, application library LIB520 is required.

The HV Control 670 software module is included in the LIB520 high-voltage process package, which is a part of the Application Software Library in MicroSCADA applications.

The HV Control 670 software module is used for control functions in the IEDs. The module contains a process picture, dialogues and a tool to generate a process database for the control application in MicroSCADA.

When using MicroSCADA Monitor Pro instead of the Classic Monitor, SA LIB is used together with 670 series Object Type files.

Use the LON Network Tool (LNT) to set the LON communication. This is a software tool applied as one node on the LON bus. To communicate via LON, the IEDs need to know

- The node addresses of the other connected IEDs.
- The network variable selectors to be used.

This is organized by LNT.

The node address is transferred to LNT via the local HMI by setting the parameter ServicePinMsg = Yes. The node address is sent to LNT via the LON bus, or LNT can scan the network for new nodes.

The communication speed of the LON bus is set to the default of 1.25 Mbit/s. This can be changed by LNT.

### 17.4.2 MULTICMDRCV and MULTICMDSND

#### 17.4.2.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Multiple command and receive</td>
<td>MULTICMDRCV</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Multiple command and send</td>
<td>MULTICMDSND</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>
17.4.2.2 Application

The IED provides two function blocks enabling several IEDs to send and receive signals via the interbay bus. The sending function block, MULTICMDSND, takes 16 binary inputs. LON enables these to be transmitted to the equivalent receiving function block, MULTICMDRCV, which has 16 binary outputs.

17.4.2.3 Setting guidelines

Settings

The parameters for the multiple command function are set via PCM600.

The Mode setting sets the outputs to either a Steady or Pulsed mode.

17.5 SPA communication protocol

17.5.1 Application

SPA communication protocol is an alternative to IEC 60870-5-103, and they use the same rear communication port.

When communicating with a PC connected to the utility substation LAN via WAN and the utility office LAN (see Figure 221), and when using the rear optical Ethernet port, the only hardware required for a station monitoring system is:

- Optical fibers from the IED to the utility substation LAN
- PC connected to the utility office LAN

Figure 221: SPA communication structure for a remote monitoring system via a substation LAN, WAN and utility LAN

SPA communication is mainly used for the Station Monitoring System. It can include different IEDs with remote communication possibilities. Connection to a PC can be made directly (if the PC is located in the substation), via a telephone modem through a telephone network with ITU (former CCITT) characteristics or via a LAN/WAN connection.
Functionality
The SPA protocol V2.5 is an ASCII-based protocol for serial communication. The communication is based on a master-slave principle, where the IED is a slave and the PC is the master. Only one master can be applied on each fiber optic loop. A program is required in the master computer for interpretation of the SPA-bus codes and for translation of the data that should be sent to the IED.

For the specification of the SPA protocol V2.5, refer to SPA-bus Communication Protocol V2.5.

17.5.2 Setting guidelines

SPA, IEC 60870-5-103 and DNP3 use the same rear communication port. This port can be set for SPA use on the local HMI under Main menu /Configuration /Communication /Station communication/Port configuration/SLM optical serial port/PROTOCOL:1. When the communication protocol is selected, the IED is automatically restarted, and the port then operates as a SPA port.

The SPA communication setting parameters are set on the local HMI under Main menu/Configuration/Communication/Station communication/SPA/SPA:1.

The most important SPA communication setting parameters are SlaveAddress and BaudRate. They are essential for all communication contact to the IED. SlaveAddress and BaudRate can be set only on the local HMI for rear and front channel communication.

SlaveAddress can be set to any value between 1–899 as long as the slave number is unique within the used SPA loop. BaudRate (communication speed) can be set between 300–38400 baud. BaudRate should be the same for the whole station although different communication speeds in a loop are possible. If different communication speeds are used in the same fiber optical loop or RS485 network, take this into account when making the communication setup in the communication master (the PC).

With local fiber optic communication, communication speed is usually set to 19200 or 38400 baud. With telephone communication, the speed setting depends on the quality of the connection and the type of modem used. Refer to technical data to determine the rated communication speed for the selected communication interfaces.

The IED does not adapt its speed to the actual communication conditions because the communication speed is set on the local HMI.
17.6 IEC 60870-5-103 communication protocol

17.6.1 Application

IEC 60870-5-103 communication protocol is mainly used when a protection IED communicates with a third party control or monitoring system. This system must have software that can interpret the IEC 60870-5-103 communication messages.

When communicating locally in the station using a Personal Computer (PC) or a Remote Terminal Unit (RTU) connected to the Communication and processing module, the only hardware needed is optical fibers and an opto/electrical converter for the PC/RTU, or a RS-485 connection depending on the used IED communication interface.

17.6.1.1 Functionality

IEC 60870-5-103 is an unbalanced (master-slave) protocol for coded-bit serial communication exchanging information with a control system. In IEC terminology a primary station is a master and a secondary station is a slave. The communication is based on a point-to-point principle. The master must have software that can interpret the IEC 60870-5-103 communication messages. For detailed information about IEC 60870-5-103, refer to IEC 60870 standard part 5: Transmission protocols, and to the section 103, Companion standard for the informative interface of protection equipment.

17.6.1.2 Design

Figure 222: Example of IEC 60870-5-103 communication structure for a substation automation system
General
The protocol implementation consists of the following functions:

- Event handling
- Report of analog service values (measurands)
- Fault location
- Command handling
  - Autorecloser ON/OFF
  - Teleprotection ON/OFF
  - Protection ON/OFF
  - LED reset
  - Characteristics 1 - 4 (Setting groups)
- File transfer (disturbance files)
- Time synchronization

Hardware
When communicating locally with a Personal Computer (PC) or a Remote Terminal Unit (RTU) in the station, using the SPA/IEC port, the only hardware needed is:
- Optical fibers, glass/plastic
- Opto/electrical converter for the PC/RTU
- PC/RTU

Commands
The commands defined in the IEC 60870-5-103 protocol are represented in dedicated function blocks. These blocks have output signals for all available commands according to the protocol. For more information, refer to the Communication protocol manual, IEC 60870-5-103.

- IED commands in control direction

Function block with defined IED functions in control direction, I103IEDCMD. This block uses PARAMETR as FUNCTION TYPE, and INFORMATION NUMBER parameter is defined for each output signal.

- Function commands in control direction

Function block with pre-defined functions in control direction, I103CMD. This block includes the FUNCTION TYPE parameter, and the INFORMATION NUMBER parameter is defined for each output signal.

- Function commands in control direction

Function block with user defined functions in control direction, I103UserCMD. These function blocks include the FUNCTION TYPE parameter for each block in the private range, and the INFORMATION NUMBER parameter for each output signal.

Status
For more information on the function blocks below, refer to the Communication protocol manual, IEC 60870-5-103.

The events created in the IED available for the IEC 60870-5-103 protocol are based on the:

- IED status indication in monitor direction
Function block with defined IED functions in monitor direction, I103IED. This block uses PARAMETER as FUNCTION TYPE, and INFORMATION NUMBER parameter is defined for each input signal.

• Function status indication in monitor direction, user-defined

Function blocks with user defined input signals in monitor direction, I103UserDef. These function blocks include the FUNCTION TYPE parameter for each block in the private range, and the INFORMATION NUMBER parameter for each input signal.

• Supervision indications in monitor direction

Function block with defined functions for supervision indications in monitor direction, I103Superv. This block includes the FUNCTION TYPE parameter, and the INFORMATION NUMBER parameter is defined for each output signal.

• Ground fault indications in monitor direction

Function block with defined functions for ground fault indications in monitor direction, I103EF. This block includes the FUNCTION TYPE parameter, and the INFORMATION NUMBER parameter is defined for each output signal.

• Fault indications in monitor direction

Function block with defined functions for fault indications in monitor direction, I103FLTPROT. This block includes the FUNCTION TYPE parameter, and the INFORMATION NUMBER parameter is defined for each input signal.

This block is suitable for distance protection, line differential, transformer differential, over-current and ground-fault protection functions.

• Autorecloser indications in monitor direction

Function block with defined functions for autorecloser indications in monitor direction, I103AR. This block includes the FUNCTION TYPE parameter, and the INFORMATION NUMBER parameter is defined for each output signal.

Measurands
The measurands can be included as type 3.1, 3.2, 3.3, 3.4 and type 9 according to the standard.

• Measurands in public range

Function block that reports all valid measuring types depending on connected signals, I103Meas.

• Measurands in private range

Function blocks with user defined input measurands in monitor direction, I103MeasUsr. These function blocks include the FUNCTION TYPE parameter for each block in the private range, and the INFORMATION NUMBER parameter for each block.
Fault location
The fault location is expressed in reactive ohms. In relation to the line length in reactive ohms, it gives the distance to the fault in percent. The data is available and reported when the fault locator function is included in the IED.

Disturbance recordings
- The transfer functionality is based on the Disturbance recorder function. The analog and binary signals recorded will be reported to the master by polling. The eight last disturbances that are recorded are available for transfer to the master. A file that has been transferred and acknowledged by the master cannot be transferred again.
- The binary signals that are included in the disturbance recorder are those that are connected to the disturbance function blocks B1RBDR to B22RBDR. These function blocks include the function type and the information number for each signal. For more information on the description of the Disturbance report in the Technical reference manual. The analog channels, that are reported, are those connected to the disturbance function blocks A1RADR to A4RADR. The eight first ones belong to the public range and the remaining ones to the private range.

17.6.2 Settings

17.6.2.1 Settings for RS485 and optical serial communication

General settings
SPA, DNP and IEC 60870-5-103 can be configured to operate on the SLM optical serial port while DNP and IEC 60870-5-103 additionally can utilize the RS485 port. A single protocol can be active on a given physical port at any time.

Two different areas in the HMI are used to configure the IEC 60870-5-103 protocol.

1. The port specific IEC 60870-5-103 protocol parameters are configured under:
   Main menu/Configuration/Communication/Station Communication/IEC60870-5-103/
   - <config-selector>
   - SlaveAddress
   - BaudRate
   - RevPolarity (optical channel only)
   - CycMeasRepTime
   - MasterTimeDomain
   - TimeSyncMode
   - EvalTimeAccuracy
   - EventRepMode
   - CmdMode
   - RepIntermediatePos

   <config-selector> is:
   - “OPTICAL103:1” for the optical serial channel on the SLM
   - “RS485103:1” for the RS485 port

2. The protocol to activate on a physical port is selected under:
   Main menu/Configuration/Communication/Station Communication/Port configuration/
   - RS485 port
- RS485PROT:1 (off, DNP, IEC103)
- SLM optical serial port
- PROTOCOL:1 (off, DNP, IEC103, SPA)

Figure 223: Settings for IEC 60870-5-103 communication

The general settings for IEC 60870-5-103 communication are the following:

- **SlaveAddress** and **BaudRate**: Settings for slave number and communication speed (baud rate). The slave number can be set to any value between 1 and 254. The communication speed can be set either to 9600 bits/s or 19200 bits/s.
- **RevPolarity**: Setting for inverting the light (or not). Standard IEC 60870-5-103 setting is Enabled.
- **CycMesRepTime**: See I103MEAS function block for more information.
- **EventRepMode**: Defines the mode for how events are reported. The event buffer size is 1000 events.

### Event reporting mode

If **EventRepMode = SeqOfEvent**, all GI and spontaneous events will be delivered in the order they were generated by BSW. The most recent value is the latest value delivered. All GI data from a single block will come from the same cycle.

If **EventRepMode = HiPriSpont**, spontaneous events will be delivered prior to GI event. To prevent old GI data from being delivered after a new spontaneous event, the pending GI event is modified to contain the same value as the spontaneous event. As a result, the GI dataset is not time-correlated.

#### 17.6.2.2 Settings from PCM600

**I103USEDEF**

For each input of the I103USEDEF function there is a setting for the information number of the connected signal. The information number can be set to any value between 0 and 255. To get proper operation of the sequence of events the event masks in the event function is to be set to ON_CHANGE. For single-command signals, the event mask is to be set to ON_SET.

In addition there is a setting on each event block for function type. Refer to description of the Main Function type set on the local HMI.

**Commands**

As for the commands defined in the protocol there is a dedicated function block with eight output signals. Use PCM600 to configure these signals. To realize the BlockOfInformation command,
which is operated from the local HMI, the output BLKINFO on the IEC command function block ICOM has to be connected to an input on an event function block. This input must have the information number 20 (monitor direction blocked) according to the standard.

**Disturbance Recordings**

For each input of the Disturbance recorder function there is a setting for the information number of the connected signal. The function type and the information number can be set to any value between 0 and 255. To get INF and FUN for the recorded binary signals, there are parameters on the disturbance recorder for each input. The user must set these parameters to whatever he connects to the corresponding input.

Refer to description of Main Function type set on the local HMI.

Recorded analog channels are sent with ASDU26 and ASDU31. One information element in these ASDUs is called ACC, and it indicates the actual channel to be processed. The channels on disturbance recorder are sent with an ACC as shown in Table 56.

*Table 56: Channels on disturbance recorder sent with a given ACC*

<table>
<thead>
<tr>
<th>DRA#-Input</th>
<th>ACC</th>
<th>IEC103 meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1</td>
<td>IA</td>
</tr>
<tr>
<td>2</td>
<td>2</td>
<td>IB</td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td>IC</td>
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<td>4</td>
<td>4</td>
<td>IG</td>
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<td>5</td>
<td>5</td>
<td>VA</td>
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<td>6</td>
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<td>VB</td>
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<td>7</td>
<td>7</td>
<td>VC</td>
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<tr>
<td>8</td>
<td>8</td>
<td>VG</td>
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<tr>
<td>9</td>
<td>64</td>
<td>Private range</td>
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<td>10</td>
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<td>11</td>
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<td>25</td>
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</tr>
<tr>
<td>26</td>
<td>81</td>
<td>Private range</td>
</tr>
</tbody>
</table>

Table continues on next page
### 17.6.3 Function and information types

Product type IEC103mainFunType value Comment:

| REL 128 | Compatible range |
| REC 242 | Private range, use default |
| RED 192 | Compatible range |
| RET 176 | Compatible range |
| REB 207 | Private range |
| REG 150 | Private range |
| REQ 245 | Private range |
| RER 152 | Private range |
| RES 118 | Private range |

Refer to the tables in the Technical reference manual /Station communication, specifying the information types supported by the communication protocol IEC 60870-5-103.

To support the information, corresponding functions must be included in the protection IED.

There is no representation for the following parts:

- Generating events for test mode
- Cause of transmission: Info no 11, Local operation
Glass or plastic fiber should be used. BFOC/2.5 is the recommended interface to use (BFOC/2.5 is the same as ST connectors). ST connectors are used with the optical power as specified in standard.

For more information, refer to IEC standard IEC 60870-5-103.

17.7 DNP3 Communication protocol

17.7.1 Application

For more information on the application and setting guidelines for the DNP3 communication protocol refer to the DNP3 Communication protocol manual.
Section 18  Remote communication

18.1  Binary signal transfer

18.1.1  Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Binary signal transfer, receive</td>
<td>BinSignRec1_1</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td></td>
<td>BinSignRec1_2</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td></td>
<td>BinSignReceive2</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Binary signal transfer, 2Mbit receive</td>
<td>BinSigRec1_12M</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td></td>
<td>BinSigRec1_22M</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Binary signal transfer, transmit</td>
<td>BinSignTrans1_1</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td></td>
<td>BinSignTrans1_2</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td></td>
<td>BinSignTranm2</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Binary signal transfer, 2Mbit transmit</td>
<td>BinSigTran1_12M</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td></td>
<td>BinSigTran1_22M</td>
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</tr>
</tbody>
</table>

18.1.2  Application

The IEDs can be equipped with communication devices for line differential communication (not applicable for RER670) and/or communication of binary signals between IEDs. The same communication hardware is used for both purposes.

Sending of binary signals between two IEDs is used in teleprotection schemes and for direct transfer trips. In addition to this, there are application possibilities, for example, blocking/enabling functionality in the remote substation, changing setting group in the remote IED depending on the switching situation in the local substation and so on.

If equipped with a 64kbit/s LDCM module, the IED can be configured to send either 192 binary signals or 3 analog and 8 binary signals to a remote IED. If equipped with a 2Mbps LDCM module, the IED can send 9 analog channels and 192 binary channels to a remote IED.

Link forwarding
If it is not possible to have a communication link between each station, the solution has been to set the protection up in a slave-master-slave configuration. This means that in Figure 224, only IED-B has access to all currents and, therefore, this is the only place where the differential current is evaluated. If the evaluation results in a trip, the trip signal will be sent over the two communication links.
Figure 224: Three-end differential protection with two communication links

If the LDCM is in 2Mbit mode, you can send the three local currents as well as the three remote currents from the other links by configuring the transmitters in IED-B:

1. Ldcm312 transmitter sends the local currents and the three currents received by Ldcm313.
2. Ldcm313 transmitter sends the three local currents and the three currents received from Ldcm312.

As a result, six currents are received in IED-A and IED-C. These currents can be connected to the protection function together with the local three currents.

In order to forward the logic signals (for example, inter-trip or inter-block) between IED-A and IED-C, the setting LinkForwarded should be defined. In IED-B, it is set to LDCM313 for Ldcm312 and to LDCM312 for Ldcm313.

This setup results in a master-master-master configuration, but without the benefit of reverting to a slave-master-slave configuration in case of a communication link interruption. In case of a communication link interruption, all three IEDs would be blocked.

### 18.1.2.1 Communication hardware solutions

The LDCM (Line Data Communication Module) has an optical connection such that two IEDs can be connected over a direct fiber (multimode), as shown in figure 225. The protocol used is IEEE/ANSI C37.94. The distance with this solution is typical 110 km/68 miles.

Figure 225: Direct fiber optical connection between two IEDs with LDCM

The LDCM can also be used together with an external optical to galvanic G.703 converter or with an alternative external optical to galvanic X.21 as shown in figure 226. These solutions are aimed
for connections to a multiplexer, which in turn is connected to a telecommunications transmission network (for example PDH).

![Diagram of a telecommunications network with LDCM, modem, and multiplexer connections.]

*) Converting optical to galvanic G.703

Figure 226: LDCM with an external optical to galvanic converter and a multiplexer

When an external modem G.703 or X.21 is used, the connection between LDCM and the modem is made with a multimode fiber of max. 3 km/2 mile length. The IEEE/ANSI C37.94 protocol is always used between LDCM and the modem.

Alternatively, a LDCM with X.21 built-in converter and micro D-sub 15-pole connector output can be used.

18.1.2.2 Application possibility with one-phase REB670

For busbar protection applications in substations where dynamic zone selection is required, it is typically necessary to wire the normally open and normally closed auxiliary contacts from every monitored disconnector and/or circuit breaker to the optocoupler inputs of the busbar protection. When one phase version of REB670 is used, then six optocoupler inputs (that is, two in each phase/IED) are required for every primary switchgear object. For big stations (for example, with 24 bays) this will require quite a lot of binary inputs into every IED. To limit the number of required optocoupler inputs into every IED it is possible to use LDCM communication modules to effectively share the binary los between three units, as shown in figure 227.
Figure 227: Example how to share binary IO between one-phase REB670 IEDs by using LDCM modules

As shown in figure 227, it is possible to wire only the status for bays 01-08 to A phase-IED. After that the information about auxiliary contact status for switchgear objects from these eight bays can be sent via LDCM modules to other two phases. In the similar way information from other bays can be only wired to B, respectively C phase IED and then shared to the other two phases via LDCM communication.

Typical LDCM communication delay between two IEDs is in order of 30-40 ms. Note that for disconnector status this delay will not pose any practical problems. However, time delay caused by LDCM communication can be crucial for circuit breakers status. In such cases it is strongly recommended that at least the circuit breaker closing command from every circuit breaker is directly wired to all three phases/IEDs to minimize any risk for unwanted operation of the busbar differential protection zones due to late inclusion of respective bay current into the differential measuring circuit.

18.1.3 Setting guidelines

64 kbit and 2 Mbit mode common settings

ChannelMode defines how an IED discards the LDCM information when one of the IEDs in the system is out of service: it can either be done on the IED out of service by setting all local LDCMs to channel mode OutOfService or at the remote end by setting the corresponding LDCM to channel mode Blocked. If OutOfService is selected, the IED should have active communication to
the remote end during the whole maintenance process, that is, no restart or removal of the fiber can be done.

This setting does not apply to two-end communication.

<table>
<thead>
<tr>
<th>Setting</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Blocked</td>
<td>IED does not use data from the LDCM</td>
</tr>
<tr>
<td>OutOfService</td>
<td>IED informs the remote end that it is out of service</td>
</tr>
</tbody>
</table>

*TerminalNo* is used to assign a unique address to each LDCM in all current differential IEDs. Up to 256 LDCMs can be assigned a unique number. For example, in a local IED with two LDCMs:

- LDCM for slot 305: set *TerminalNo* to 1 and *RemoteTermNo* to 2
- LDCM for slot 306: set *TerminalNo* to 3 and *RemoteTermNo* to 4

In multiterminal current differential applications, with 4 LDCMs in each IED, up to 20 unique addresses must be set.

> A unique address is necessary to give high security against incorrect addressing in the communication system. If the same number is used for *TerminalNo* in some of the LDCMs, a loop-back test in the communication system can give an incorrect trip.

*RemoteTermNo* is used to assign a number to each related LDCM in the remote IED. For each LDCM, *RemoteTermNo* is set to a different value than *TerminalNo*, but equal to the *TerminalNo* of the remote end LDCM. In the remote IED, *TerminalNo* and *RemoteTermNo* are reversed as follows:

- LDCM for slot 305: set *TerminalNo* to 2 and *RemoteTermNo* to 1
- LDCM for slot 306: set *TerminalNo* to 4 and *RemoteTermNo* to 3

The redundant channel is always configured to the lower position, for example:

- Slot 305: main channel
- Slot 306: redundant channel

The same is applicable for slot 312-313 and slot 322-323.

*DiffSync* defines the method of time synchronization for the line differential function: *Echo* or *GPS*.

> Using *Echo* in this case is safe only if there is no risk of varying transmission asymmetry.

*GPSSyncErr*: when GPS synchronization is lost, synchronization of the line differential function continues for 16 s based on the stability in the local IED clocks. After that, setting *Block* blocks the line differential function or setting *Echo* keeps it on by using the *Echo* synchronization method.
Using *Echo* in this case is safe only if there is no risk of varying transmission asymmetry.

*CommSync* defines the *Master* and *Slave* relation in the communication system, and should not be mistaken for the synchronization of line differential current samples. When direct fiber is used, one LDCM is set as *Master* and the other as *Slave*. When a modem and multiplexer is used, the IED is always set as *Slave* because the telecommunication system provides the clock master.

*OptoPower* has two settings: *LowPower* and *HighPower*.

Short-range LDCM: Use *LowPower* for fibres 0 – 1 km and *HighPower* for fibers greater than 1 km.

Medium-range LDCM: Typical distance 80 km for both *LowPower* and *HighPower*.

Long-range LDCM: Typical distance 120 km for both *LowPower* and *HighPower*.

An optical budget calculation should be made for the actual case. For medium range LDCM and long range LDCM the recommendation is to use the *LowPower* setting to minimize the power consumption and keep the heat dissipation at minimum.

The *HighPower* setting adds 3 dBm extra optical power and can be used to increase the margin at distances close to maximum.

**Table 57: Optical budgets with C37.94 protocol**

<table>
<thead>
<tr>
<th>Type of LDCM</th>
<th>Short range (SR)</th>
<th>Short range (SR)</th>
<th>Medium range (MR)</th>
<th>Long range (LR)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Type of fibre</td>
<td>Multi-mode fiber glass 50/125 μm</td>
<td>Multi-mode fiber glass 62.5/125 μm</td>
<td>Single-mode fiber glass 9/125 μm</td>
<td>Single-mode fiber glass 9/125 μm</td>
</tr>
<tr>
<td>Modem type</td>
<td>1MRK0002122-AB</td>
<td>1MRK0002122-AB</td>
<td>1MRK002311-AA</td>
<td>1MRK002311-BA</td>
</tr>
<tr>
<td>Contact type</td>
<td>ST</td>
<td>ST</td>
<td>FC/PC</td>
<td>FC/PC</td>
</tr>
<tr>
<td>Minimum output power&lt;sup&gt;1&lt;/sup&gt;</td>
<td>–21 dBm</td>
<td>–13.7 dBm</td>
<td>–3.2 dBm</td>
<td>–1.3 dBm</td>
</tr>
<tr>
<td>Minimum receiver sensitivity</td>
<td>–32.5 dBm</td>
<td>–32.5 dBm</td>
<td>–30 dBm</td>
<td>–30 dBm</td>
</tr>
<tr>
<td>Optical link budget&lt;sup&gt;2&lt;/sup&gt;</td>
<td>11.5 dB</td>
<td>18.8 dB</td>
<td>26.8 dB</td>
<td>28.7 dB</td>
</tr>
</tbody>
</table>

<sup>1</sup> Minimum output power is measured with 1 m of the selected fiber and the high power mode.

<sup>2</sup> The optical budget includes a satisfactory margin for aging in transmitter and receiver during 20–30 years.

**Table 58: Example of input data for calculating the optical budget (maximum distance)**

<table>
<thead>
<tr>
<th>Type of LDCM</th>
<th>Short range (SR)</th>
<th>Short range (SR)</th>
<th>Medium range (MR)</th>
<th>Long range (LR)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Type of fibre</td>
<td>Multi-mode fiber glass 50/125 μm</td>
<td>Multi-mode fiber glass 62.5/125 μm</td>
<td>Single-mode fiber glass 9/125 μm</td>
<td>Single-mode fiber glass 9/125 μm</td>
</tr>
<tr>
<td>Modem type</td>
<td>1MRK0002122-AB</td>
<td>1MRK0002122-AB</td>
<td>1MRK002311-AA</td>
<td>1MRK002311-BA</td>
</tr>
<tr>
<td>Typical attenuation in fibre-optic cables</td>
<td>3 dB/km</td>
<td>3 dB/km</td>
<td>0.32 dB/km</td>
<td>0.21 dB/km</td>
</tr>
<tr>
<td>Attenuation/ Contact</td>
<td>1.5 dB/ST</td>
<td>1.5 dB/ST</td>
<td>0.3 dB/FC/PC</td>
<td>0.3 dB/FC/PC</td>
</tr>
</tbody>
</table>

Table continues on next page
<table>
<thead>
<tr>
<th>Type of LDCM</th>
<th>Short range (SR)</th>
<th>Short range (SR)</th>
<th>Medium range (MR)</th>
<th>Long range (LR)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Factory splice</td>
<td>0.5 dB/splice</td>
<td>0.5 dB/splice</td>
<td>0.08 dB/splice</td>
<td>0.08 dB/splice</td>
</tr>
<tr>
<td>attenuation</td>
<td>0.3 splices/km</td>
<td>0.1 splices/km</td>
<td>0.08 dB/splice</td>
<td>0.08 dB/splice</td>
</tr>
<tr>
<td>Repair splices</td>
<td>0.25 dB/splice</td>
<td>0.25 dB/splice</td>
<td>0.1 dB/splice</td>
<td>0.1 dB/splice</td>
</tr>
<tr>
<td>0.1 splices/km</td>
<td>0.1 splices/km</td>
<td>0.05 splices/km</td>
<td>0.1 dB/splice</td>
<td>0.05 splices/km</td>
</tr>
<tr>
<td>Fiber margin for aging</td>
<td>0.1 dB/km</td>
<td>0.1 dB/km</td>
<td>0.01 dB/km</td>
<td>0.01 dB/km</td>
</tr>
</tbody>
</table>

Table 59: Example of calculating the optical budget (maximum distance)

<table>
<thead>
<tr>
<th>Type of LDCM</th>
<th>Short range (SR)</th>
<th>Short range (SR)</th>
<th>Medium range (MR)</th>
<th>Long range (LR)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Type of fibre</td>
<td>Multi-mode fiber</td>
<td>Multi-mode fiber</td>
<td>Single-mode fiber</td>
<td>Single-mode fiber</td>
</tr>
<tr>
<td>glass 50/125 μm</td>
<td>glass 62.5/125 μm</td>
<td>glass 9/125 μm</td>
<td>glass 9/125 μm</td>
<td></td>
</tr>
<tr>
<td>Modem type</td>
<td>1MRK0002122-AB</td>
<td>1MRK0002122-AB</td>
<td>1MRK002311-AA</td>
<td>1MRK002311-BA</td>
</tr>
<tr>
<td>Maximum distance</td>
<td>2 km</td>
<td>3 km</td>
<td>80 km</td>
<td>120 km</td>
</tr>
<tr>
<td>Attenuation in fibre-optic cables</td>
<td>6 dB</td>
<td>9 dB</td>
<td>25.6 dB</td>
<td>25.2 dB</td>
</tr>
<tr>
<td>2 contacts</td>
<td>2 dB</td>
<td>3 dB</td>
<td>0.6 dB</td>
<td>0.6 dB</td>
</tr>
<tr>
<td>Factory splice</td>
<td>0.3 dB</td>
<td>0.45 dB</td>
<td>0.64 dB</td>
<td>0.96 dB</td>
</tr>
<tr>
<td>attenuation</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Repair splices</td>
<td>0.1 dB</td>
<td>0.3 dB</td>
<td>0.8 dB</td>
<td>1.2 dB</td>
</tr>
<tr>
<td>Fiber margin for aging</td>
<td>0.2 dB</td>
<td>0.3 dB</td>
<td>0.8 dB</td>
<td>1.2 dB</td>
</tr>
<tr>
<td>Total attenuation</td>
<td>8.6 dB</td>
<td>12.05 dB</td>
<td>26.04 dB</td>
<td>28.56 dB</td>
</tr>
<tr>
<td>Optical link budget</td>
<td>9 dB</td>
<td>13 dB</td>
<td>26.8 dB</td>
<td>28.7 dB</td>
</tr>
<tr>
<td>Link margin</td>
<td>0.4 dB</td>
<td>0.95 dB</td>
<td>0.76 dB</td>
<td>0.14 dB</td>
</tr>
</tbody>
</table>

ComAlarmDel defines the time delay for communication failure alarm. In communication systems, route switching can sometimes cause interruptions with a duration of up to 50 ms. Too short a time delay can thus cause nuisance alarms.

ComAlrmResDel defines the time delay for communication failure alarm reset.

RedChSwTime defines the time delay before switching over to a redundant channel in case of primary channel failure.

RedChRturnTime defines the time delay before switching back to the primary channel after channel failure.

AsymDelay denotes asymmetry which is defined as transmission delay minus receive delay. If fixed asymmetry is known, Echo synchronization method can be used, provided that AsymDelay is properly set. From the definition follows that asymmetry is always positive at one end and negative at the other end.

MaxTransmDelay indicates maximum transmission delay. Data for maximum 40 ms transmission delay can be buffered up. Delay times in the range of some ms are common. If data arrive in wrong order, the oldest data is disregarded.
MaxtDiffLevel indicates the maximum time difference allowed between internal clocks in respective line ends.

64 kbit mode specific settings

TransmCurr is used to select among the following:

- one of the two possible local currents is transmitted
- sum of the two local currents is transmitted
- channel is used as a redundant backup channel

breaker-and-a-half arrangement has two local currents, and the Current Transformer (CT) grounding for those can differ. CT-SUM transmits the sum of the two CT groups. CT-DIFF1 transmits CT group 1 minus CT group 2 and CT-DIFF2 transmits CT group 2 minus CT group 1.

CT-GRP1 and CT-GRP2 transmit the respective CT groups, and setting RedundantChannel determines that the channel is used as a redundant backup channel. The redundant channel takes the CT group setting of the main channel.

RemAinLatency corresponds to LocAinLatency set in the remote IED.

AnalogLatency specifies the time delay (number of samples) between actual sampling and the time the sample reaches LDCM. The value is set to 2 when transmitting analog data. When a merging unit according to IEC 61850-9-2 is used instead of the TRM, this parameter shall be set to 5.

CompRange value indicates the current peak value over which truncation is made. To set this value, knowledge of fault current levels is required. It is recommended to set the minimum range that will cover the expected fault current value. For example, if a 40kA fault level is expected on the network, the 0-50kA settings range should be chosen.

2 Mbit mode specific settings

RedundantCh is used to set the channel as a redundant backup channel. The redundant channel takes the CT group setting of the main channel, and ignores the CT group configured in its own transmit block.

LinkForwarded is used to configure the LDCM to merge the inter-trip and block signals from another LDCM-receiver. This is used when the analog signals for the LDCM-transmitter is connected to the receiver of another LDCM.
Section 19 Security

19.1 Authority status ATHSTAT

19.1.1 Application

Authority status (ATHSTAT) function is an indication function block, which informs about two events related to the IED and the user authorization:

- the fact that at least one user has tried to log on wrongly into the IED and it was blocked (the output USRBLKED)
- the fact that at least one user is logged on (the output LOGGEDON)

The two outputs of ATHSTAT function can be used in the configuration for different indication and alarming reasons, or can be sent to the station control for the same purpose.

19.2 Self supervision with internal event list INTERRSIG

19.2.1 Application

The protection and control IEDs have many functions included. The included self-supervision with internal event list function block provides good supervision of the IED. The fault signals make it easier to analyze and locate a fault.

Both hardware and software supervision is included and it is also possible to indicate possible faults through a hardware contact on the power supply module and/or through the communication.

Internal events are generated by the built-in supervisory functions. The supervisory functions supervise the status of the various modules in the IED and, in case of failure, a corresponding event is generated. Similarly, when the failure is corrected, a corresponding event is generated.

Apart from the built-in supervision of the various modules, events are also generated when the status changes for the:

- built-in real time clock (in operation/out of order).
- external time synchronization (in operation/out of order).

Events are also generated:

- whenever any setting in the IED is changed.

The internal events are time tagged with a resolution of 1 ms and stored in a list. The list can store up to 40 events. The list is based on the FIFO principle, that is, when it is full, the oldest event is
overwritten. The list contents cannot be modified, but the whole list can be cleared using the Reset menu in the LHMI.

The list of internal events provides valuable information, which can be used during commissioning and fault tracing.

The information can, in addition to be viewed on the built in HMI, also be retrieved with the aid of a PC with PCM600 installed and by using the Event Monitoring Tool. The PC can either be connected to the front port, or to the port at the back of the IED.

**19.3 Change lock CHNGLCK**

**19.3.1 Application**

Change lock function CHNGLCK is used to block further changes to the IED configuration once the commissioning is complete. The purpose is to make it impossible to perform inadvertent IED configuration and setting changes.

However, when activated, CHNGLCK will still allow the following actions that does not involve reconfiguring of the IED:

- Monitoring
- Reading events
- Resetting events
- Reading disturbance data
- Clear disturbances
- Reset LEDs
- Reset counters and other runtime component states
- Control operations
- Set system time
- Enter and exit from test mode
- Change of active setting group

The binary input controlling the function is defined in ACT or SMT. The CHNGLCK function is configured using ACT.

**LOCK** Binary input signal that will activate/deactivate the function, defined in ACT or SMT.
When CHNGLCK has a logical one on its input, then all attempts to modify the IED configuration and setting will be denied and the message "Error: Changes blocked" will be displayed on the local HMI; in PCM600 the message will be "Operation denied by active ChangeLock". The CHNGLCK function should be configured so that it is controlled by a signal from a binary input card. This guarantees that by setting that signal to a logical zero, CHNGLCK is deactivated. If any logic is included in the signal path to the CHNGLCK input, that logic must be designed so that it cannot permanently issue a logical one to the CHNGLCK input. If such a situation would occur in spite of these precautions, then please contact the local ABB representative for remedial action.

19.4 Denial of service SCHLCCH/RCHLCCH

19.4.1 Application

The denial of service functionality is designed to limit the CPU load that can be produced by Ethernet network traffic on the IED. The communication facilities must not be allowed to compromise the primary functionality of the device. All inbound network traffic will be quota controlled so that too heavy network loads can be controlled. Heavy network load might for instance be the result of malfunctioning equipment connected to the network.

The functions Access point diagnostics function block measure the IED load from communication and, if necessary, limit it for not jeopardizing the IEDs control and protection functionality due to high CPU load. The function has the following denial of service related outputs:

- LINKSTS indicates the Ethernet link status for the rear ports (single communication)
- CHALISTS and CHBLISTS indicates the Ethernet link status for the rear ports channel A and B (redundant communication)
- LinkStatus indicates the Ethernet link status for the front port

19.4.2 Setting guidelines

The function does not have any parameters available in the local HMI or PCM600.
Section 20  Basic IED functions

20.1  IED identifiers TERMINALID

20.1.1  Application

IED identifiers (TERMINALID) function allows the user to identify the individual IED in the system, not only in the substation, but in a whole region or a country.

Use only characters A-Z, a-z and 0-9 in station, object and unit names.

20.2  Product information PRODINF

20.2.1  Application

Product information contains unchangeable data that uniquely identifies the IED.

Product information data is visible on the local HMI under Main menu/Diagnostics/IED status/Product identifiers and under Main menu/Diagnostics/IED Status/Identifiers:

Product information data is visible on the local HMI under Main menu/Diagnostics/IED status/Product identifiers and under Main menu/Diagnostics/IED Status/Identifiers.

• ProductVer
• ProductDef
• FirmwareVer
• SerialNo
• OrderingNo
• ProductionDate
• IEDProdType

Figure 228: IED summary

This information is very helpful when interacting with ABB product support (for example during repair and maintenance).

20.2.2  Factory defined settings

The factory defined settings are very useful for identifying a specific version and very helpful in the case of maintenance, repair, interchanging IEDs between different Substation Automation Systems and upgrading. The factory made settings can not be changed by the customer. They can only be viewed. The settings are found in the local HMI under Main menu/Diagnostics/IED status/Product identifiers
The following identifiers are available:

- **IEDProdType**
  - Describes the type of the IED. Example: *REL670*

- **ProductDef**
  - Describes the release number from the production. Example: *2.1.0*

- **FirmwareVer**
  - Describes the firmware version.
  - The firmware version can be checked from **Main menu/Diagnostics/IED status/Product identifiers**
  - Firmware version numbers run independently from the release production numbers. For every release number there can be one or more firmware versions depending on the small issues corrected in between releases.

- **ProductVer**
  - Describes the product version. Example: *2.1.0*

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>is the Major version of the manufactured product this means, new platform of the product</td>
</tr>
<tr>
<td>2</td>
<td>is the Minor version of the manufactured product this means, new functions or new hardware added to the product</td>
</tr>
<tr>
<td>3</td>
<td>is the Major revision of the manufactured product this means, functions or hardware is either changed or enhanced in the product</td>
</tr>
</tbody>
</table>

- **IEDMainFunType**
  - Main function type code according to IEC 60870-5-103. Example: 128 (meaning line protection).

- **SerialNo**

- **OrderingNo**

- **ProductionDate**

### 20.3 Measured value expander block RANGE_XP

#### 20.3.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Measured value expander block</td>
<td>RANGE_XP</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

#### 20.3.2 Application

The current and voltage measurements functions (CVMMXN, CMMXU, VMMXU and VNMMXU), current and voltage sequence measurement functions (CMSQI and VMSQI) and IEC 61850 generic communication I/O functions (MVGAPC) are provided with measurement supervision functionality. All measured values can be supervised with four settable limits, that is low-low limit, low limit, high limit and high-high limit. The measure value expander block (RANGE_XP) has been introduced to be able to translate the integer output signal from the measuring functions to 5
binary signals, that is below low-low limit, below low limit, normal, above high-high limit or above high limit. The output signals can be used as conditions in the configurable logic.

20.3.3 Setting guidelines

There are no settable parameters for the measured value expander block function.

20.4 Parameter setting groups

20.4.1 Application

Six sets of settings are available to optimize IED operation for different power system conditions. By creating and switching between fine tuned setting sets, either from the local HMI or configurable binary inputs, results in a highly adaptable IED that can cope with a variety of power system scenarios.

Different conditions in networks with different voltage levels require highly adaptable protection and control units to best provide for dependability, security and selectivity requirements. Protection units operate with a higher degree of availability, especially, if the setting values of their parameters are continuously optimized according to the conditions in the power system.

Operational departments can plan for different operating conditions in the primary equipment. The protection engineer can prepare the necessary optimized and pre-tested settings in advance for different protection functions. Six different groups of setting parameters are available in the IED. Any of them can be activated through the different programmable binary inputs by means of external or internal control signals.

A function block, SETGRPS, defines how many setting groups are used. Setting is done with parameter MAXSETGR and shall be set to the required value for each IED. Only the number of setting groups set will be available in the Parameter Setting tool for activation with the ActiveGroup function block.

20.4.2 Setting guidelines

The setting ActiveSetGrp, is used to select which parameter group to be active. The active group can also be selected with configured input to the function block SETGRPS.

The length of the pulse, sent out by the output signal GRP_CHGD when an active group has changed, is set with the parameter t.

The parameter MAXSETGR defines the maximum number of setting groups in use to switch between. Only the selected number of setting groups will be available in the Parameter Setting tool (PST) for activation with the ActiveGroup function block.

20.5 Rated system frequency PRIMVAL
20.5.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Primary system values</td>
<td>PRIMVAL</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

20.5.2 Application

The rated system frequency and phase rotation direction are set under **Main menu/Configuration/ Power system/ Primary Values** in the local HMI and PCM600 parameter setting tree.

20.5.3 Setting guidelines

Set the system rated frequency. Refer to section "**Signal matrix for analog inputs SMAI**" for description on frequency tracking.

20.6 Summation block 3 phase 3PHSUM

20.6.1 Application

The analog summation block 3PHSUM function block is used in order to get the sum of two sets of 3 phase analog signals (of the same type) for those IED functions that might need it.

20.6.2 Setting guidelines

The summation block receives the three-phase signals from SMAI blocks. The summation block has several settings.

*SummationType*: Summation type (Group 1 + Group 2, Group 1 - Group 2, Group 2 - Group 1 or – (Group 1 + Group 2)).

*DFTReference*: The reference DFT block (*InternalDFTRef, DFTRefGrp1* or *External DFT ref*).

*DFTRefGrp1*: This setting means use own internal adaptive DFT reference (this setting makes the *SUM3PH* self DFT adaptive, that is, it will use the measured frequency for the summation signal to adapt DFT).

*InternalDFTRef*: Gives fixed window DFT (to nominal system frequency).

*ExternalDFTRef*: This setting means that the DFT samples-per-cycle (adaptive DFT) will be controlled by SMAI1 SPFCOUT.

*FreqMeasMinVal*: The minimum value of the voltage for which the frequency is calculated, expressed as percent of *VBase*base voltage setting (for each instance x).
20.7 Global base values GBASVAL

20.7.1 Identification

<table>
<thead>
<tr>
<th>Function description</th>
<th>IEC 61850 identification</th>
<th>IEC 60617 identification</th>
<th>ANSI/IEEE C37.2 device number</th>
</tr>
</thead>
<tbody>
<tr>
<td>Global base values</td>
<td>GBASVAL</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

20.7.2 Application

Global base values function (GBASVAL) is used to provide global values, common for all applicable functions within the IED. One set of global values consists of values for current, voltage and apparent power and it is possible to have twelve different sets.

This is an advantage since all applicable functions in the IED use a single source of base values. This facilitates consistency throughout the IED and also facilitates a single point for updating values when necessary.

Each applicable function in the IED has a parameter, GlobalBaseSel, defining one out of the twelve sets of GBASVAL functions.

20.7.3 Setting guidelines

- **VBase**: Phase-to-phase voltage value to be used as a base value for applicable functions throughout the IED.
- **IBase**: Phase current value to be used as a base value for applicable functions throughout the IED.
- **SBase**: Standard apparent power value to be used as a base value for applicable functions throughout the IED, typically SBase = √3 · VBase · IBase.

20.8 Signal matrix for binary inputs SMBI

20.8.1 Application

The Signal matrix for binary inputs function SMBI is used within the Application Configuration tool in direct relation with the Signal Matrix tool. SMBI represents the way binary inputs are brought in for one IED configuration.
20.8.2 Setting guidelines

There are no setting parameters for the Signal matrix for binary inputs SMBI available to the user in Parameter Setting tool. However, the user shall give a name to SMBI instance and the SMBI inputs, directly in the Application Configuration tool. These names will define SMBI function in the Signal Matrix tool. The user defined name for the input or output signal will also appear on the respective output or input signal.

20.9 Signal matrix for binary outputs SMBO

20.9.1 Application

The Signal matrix for binary outputs function SMBO is used within the Application Configuration tool in direct relation with the Signal Matrix tool. SMBO represents the way binary outputs are sent from one IED configuration.

20.9.2 Setting guidelines

There are no setting parameters for the Signal matrix for binary outputs SMBO available to the user in Parameter Setting tool. However, the user must give a name to SMBO instance and SMBO outputs, directly in the Application Configuration tool. These names will define SMBO function in the Signal Matrix tool.

20.10 Signal matrix for mA inputs SMMI

20.10.1 Application

The Signal matrix for mA inputs function SMMI is used within the Application Configuration tool in direct relation with the Signal Matrix tool. SMMI represents the way milliamp (mA) inputs are brought in for one IED configuration.

20.10.2 Setting guidelines

There are no setting parameters for the Signal matrix for mA inputs SMMI available to the user in the Parameter Setting tool. However, the user must give a name to SMMI instance and SMMI inputs, directly in the Application Configuration tool.

20.11 Signal matrix for analog inputs SMAI
20.11.1 Application

Signal matrix for analog inputs (SMAI), also known as the preprocessor function block, analyses the connected four analog signals (three phases and neutral) and calculates all relevant information from them like the phasor magnitude, phase angle, frequency, true RMS value, harmonics, sequence components and so on. This information is then used by the respective functions connected to this SMAI block in ACT (for example protection, measurement or monitoring functions).

20.11.2 Frequency values

The SMAI function includes a functionality based on the level of positive sequence voltage, MinValFreqMeas, to validate if the frequency measurement is valid or not. If the positive sequence voltage is lower than MinValFreqMeas, the function freezes the frequency output value for 500 ms and after that the frequency output is set to the nominal value. A signal is available for the SMAI function to prevent operation due to non-valid frequency values. MinValFreqMeas is set as % of VBase/√3

If SMAI setting ConnectionType is Ph-Ph, at least two of the inputs GRPx_A, GRPx_B and GRPx_C, where 1≤x≤12, must be connected in order to calculate the positive sequence voltage. Note that phase to phase inputs shall always be connected as follows: A-B to GRPxA, B-C to GRPxB, C-A to GRPxC. If SMAI setting ConnectionType is Ph-N, all three inputs GRPx_A, GRPx_B and GRPx_C must be connected in order to calculate the positive sequence voltage.

If only one phase-phase voltage is available and SMAI setting ConnectionType is Ph-Ph, the user is advised to connect two (not three) of the inputs GRPx_A, GRPx_B and GRPx_C to the same voltage input as shown in figure 229 to make SMAI calculate a positive sequence voltage.

The above described scenario does not work if SMAI setting ConnectionType is Ph-N. If only one phase-ground voltage is available, the same type of connection can be used but the SMAI ConnectionType setting must still be Ph-Ph and this has to be accounted for when setting MinValFreqMeas. If SMAI setting ConnectionType is Ph-N and the same voltage is connected to all three SMAI inputs, the positive sequence voltage will be zero and the frequency functions will not work properly.
The outputs from the above configured SMAI block shall only be used for Overfrequency protection (SAPTOF, 81), Underfrequency protection (SAPTUF, 81) and Rate-of-change frequency protection (SAPFRC, 81) due to that all other information except frequency and positive sequence voltage might be wrongly calculated.

20.11.3 Setting guidelines

The parameters for the signal matrix for analog inputs (SMAI) functions are set via the local HMI or PCM600.

Every SMAI function block can receive four analog signals (three phases and one neutral value), either voltage or current. SMAI outputs give information about every aspect of the 3ph analog signals acquired (phase angle, RMS value, frequency and frequency derivates, and so on – 244 values in total). Besides the block “group name”, the analog inputs type (voltage or current) and the analog input names that can be set directly in ACT.

Application functions should be connected to a SMAI block with same task cycle as the application function, except for e.g. measurement functions that run in slow cycle tasks.

**DFTRefExtOut**: Parameter valid only for function block SMAI1.
Reference block for external output (SPFCOUT function output).

**DFTReference**: Reference DFT for the SMAI block use.

These DFT reference block settings decide DFT reference for DFT calculations. The setting InternalDFTRef will use fixed DFT reference based on set system frequency. **DFTRefGrp(n)** will use DFT reference from the selected group block, when own group is selected, an adaptive DFT reference will be used based on calculated signal frequency from own group. The setting ExternalDFTRef will use reference based on what is connected to input DFTSPFC.

The setting **ConnectionType**: Connection type for that specific instance (n) of the SMAI (if it is Ph-N or Ph-Ph). Depending on connection type setting the not connected Ph-N or Ph-Ph outputs will be calculated as long as they are possible to calculate. E.g. at Ph-Ph connection A, B and C will be calculated for use in symmetrical situations. If N component should be used respectively the phase component during faults I_N/V_N must be connected to input 4.

**Negation**: If the user wants to negate the 3ph signal, it is possible to choose to negate only the phase signals Negate3Ph, only the neutral signal NegateN or both Negate3Ph+N. negation means rotation with 180° of the vectors.

**GlobalBaseSel**: Selects the global base value group used by the function to define (IBase), (VBase) and (SBase).

**MinValFreqMeas**: The minimum value of the voltage for which the frequency is calculated, expressed as percent of VBase (for each instance n).

Settings **DFTRefExtOut** and **DFTReference** shall be set to default value InternalDFTRef if no VT inputs are available.
Even if the user sets the *AnalogInputType* of a SMAI block to "Current", the *MinValFreqMeas* is still visible. However, using the current channel values as base for frequency measurement is **not recommendable** for a number of reasons, not last among them being the low level of currents that one can have in normal operating conditions.

**Examples of adaptive frequency tracking**

Preprocessing block shall only be used to feed functions within the same execution cycles (e.g. use preprocessing block with cycle 1 to feed transformer differential protection). The only exceptions are measurement functions (CVMMXN, CMMXU, VMMXU, etc.) which shall be fed by preprocessing blocks with cycle 8.

When two or more preprocessing blocks are used to feed one protection function (e.g. over-power function GOPPDOP), it is of outmost importance that parameter setting *DFTRefference* has the same set value for all of the preprocessing blocks involved.
### SMAI instance 3 phase group

**Task time group 1**

<table>
<thead>
<tr>
<th>SMAI instance</th>
<th>3 phase group</th>
</tr>
</thead>
<tbody>
<tr>
<td>SMAI1:1</td>
<td>1</td>
</tr>
<tr>
<td>SMAI2:2</td>
<td>2</td>
</tr>
<tr>
<td>SMAI3:3</td>
<td>3</td>
</tr>
<tr>
<td>SMAI4:4</td>
<td>4</td>
</tr>
<tr>
<td>SMAI5:5</td>
<td>5</td>
</tr>
<tr>
<td>SMAI6:6</td>
<td>6</td>
</tr>
<tr>
<td>SMAI7:7</td>
<td>7</td>
</tr>
<tr>
<td>SMAI8:8</td>
<td>8</td>
</tr>
<tr>
<td>SMAI9:9</td>
<td>9</td>
</tr>
<tr>
<td>SMAI10:10</td>
<td>10</td>
</tr>
<tr>
<td>SMAI11:11</td>
<td>11</td>
</tr>
<tr>
<td>SMAI12:12</td>
<td>12</td>
</tr>
</tbody>
</table>

**Task time group 2**

<table>
<thead>
<tr>
<th>SMAI instance</th>
<th>3 phase group</th>
</tr>
</thead>
<tbody>
<tr>
<td>SMAI1:13</td>
<td>1</td>
</tr>
<tr>
<td>SMAI2:14</td>
<td>2</td>
</tr>
<tr>
<td>SMAI3:15</td>
<td>3</td>
</tr>
<tr>
<td>SMAI4:16</td>
<td>4</td>
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<tr>
<td>SMAI5:17</td>
<td>5</td>
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<td>SMAI6:18</td>
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<tr>
<td>SMAI7:19</td>
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<td>SMAI8:20</td>
<td>8</td>
</tr>
<tr>
<td>SMAI9:21</td>
<td>9</td>
</tr>
<tr>
<td>SMAI10:22</td>
<td>10</td>
</tr>
<tr>
<td>SMAI11:23</td>
<td>11</td>
</tr>
<tr>
<td>SMAI12:24</td>
<td>12</td>
</tr>
</tbody>
</table>

**Task time group 3**

<table>
<thead>
<tr>
<th>SMAI instance</th>
<th>3 phase group</th>
</tr>
</thead>
<tbody>
<tr>
<td>SMAI1:25</td>
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<tr>
<td>SMAI2:26</td>
<td>2</td>
</tr>
<tr>
<td>SMAI3:27</td>
<td>3</td>
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<td>SMAI4:28</td>
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<td>SMAI5:29</td>
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<td>SMAI9:33</td>
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<tr>
<td>SMAI10:34</td>
<td>10</td>
</tr>
<tr>
<td>SMAI11:35</td>
<td>11</td>
</tr>
<tr>
<td>SMAI12:36</td>
<td>12</td>
</tr>
</tbody>
</table>

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**Figure 230**: Twelve SMAI instances are grouped within one task time. SMAI blocks are available in three different task times in the IED. Two pointed instances are used in the following examples.

The examples show a situation with adaptive frequency tracking with one reference selected for all instances. In practice each instance can be adapted to the needs of the actual application. The adaptive frequency tracking is needed in IEDs that belong to the protection system of synchronous machines and that are active during run-up and shout-down of the machine. In other application the usual setting of the parameter `DFTReference` of SMAI is `InternalDFTRef`.

**Example 1**
Figure 231: Configuration for using an instance in task time group 1 as DFT reference

Assume instance SMAI7:7 in task time group 1 has been selected in the configuration to control the frequency tracking. Observe that the selected reference instance (i.e. frequency tracking master) must be a voltage type. Observe that positive sequence voltage is used for the frequency tracking feature.

For task time group 1 this gives the following settings (see Figure 230 for numbering):

SMAI1:1: $DFTRefExtOut = DFTRefGrp7$ to route SMAI7:7 reference to the SPFCOUT output, $DFTReference = DFTRefGrp7$ for SMAI1:1 to use SMAI7:7 as reference (see Figure 231) SMAI2:2 – SMAI12:12: $DFTReference = DFTRefGrp7$ for SMAI2:2 – SMAI12:12 to use SMAI7:7 as reference.

For task time group 2 this gives the following settings:

SMAI1:13 – SMAI12:24: $DFTReference = ExternalDFTRef$ to use DFTSPFC input of SMAI1:13 as reference (SMAI7:7)

For task time group 3 this gives the following settings:

SMAI1:25 – SMAI12:36: $DFTReference = ExternalDFTRef$ to use DFTSPFC input as reference (SMAI7:7)

Example 2
Assume instance SMAI4:16 in task time group 2 has been selected in the configuration to control the frequency tracking for all instances. Observe that the selected reference instance (i.e. frequency tracking master) must be a voltage type. Observe that positive sequence voltage is used for the frequency tracking feature.

For task time group 1 this gives the following settings (see Figure 230 for numbering):

SMAI1:1 – SMAI1:12: DFTReference = ExternalDFTRef to use DFTSPFC input as reference (SMAI4:16)

For task time group 2 this gives the following settings:

SMAI1:13: DFTRefExtOut = DFTRefGrp4 to route SMAI4:16 reference to the SPFCOUT output, DFTReference = DFTRefGrp4 for SMAI1:13 to use SMAI4:16 as reference (see Figure 232) SMAI2:14 – SMAI1:24: DFTReference = DFTRefGrp4 to use SMAI4:16 as reference.

For task time group 3 this gives the following settings:

SMAI1:25 – SMAI1:36: DFTReference = ExternalDFTRef to use DFTSPFC input as reference (SMAI4:16)

### 20.12 Test mode functionality TESTMODE

#### 20.12.1 Application

The protection and control IEDs may have a complex configuration with many included functions. To make the testing procedure easier, the IEDs include the feature that allows individual blocking of a single-, several-, or all functions.

This means that it is possible to see when a function is activated or trips. It also enables the user to follow the operation of several related functions to check correct functionality and to check parts of the configuration, and to check parts.
20.12.1.1 IEC 61850 protocol test mode

The function block TESTMODE has implemented the extended testing mode capabilities for IEC 61850 Ed2 systems. Operator commands sent to the function block TESTMODE determine the behavior of the functions. The command can be given remotely from an IEC 61850 client or from the LHMI under the Main menu/Test/Function test modes/Communication/Station Communication/IEC61850 LD0 LLN0/LD0LLN0:1. The possible values of the function block TESTMODE are described in Communication protocol manual, IEC 61850 Edition 1 and Edition 2.

There is no setting in PCM600 via PST for the TESTMODE function block.

To be able to set the function block TESTMODE remotely, the setting via path on LHMI and in PST: Main menu/Configuration/Communication/Station Communication/IEC61850-8-1/IEC61850-8-1:1.RemoteModControl may not be set to Off. The possible values of the parameter RemoteModControl are Off, Maintenance or All levels. The Off value denies all access to function block TESTMODE from remote, Maintenance requires that the category of the originator (orCat) is Maintenance and All levels allow any orCat.

The DataObject Mod of the Root LD.LNN0 can be set on the LHMI under Main menu/Test/Function test modes/Communication/Station communication/IEC61850 LD0 LLN0/LD0LLN0:1 to On, Off, TestBlocked, Test or Blocked.

When the setting of the DataObject Mod is changed at this level, all Logical Nodes inside the logical device update their own behavior according to IEC61850-7-4. The supported values of the function block TESTMODE are described in Communication protocol manual, IEC 61850 Edition 2. When the function block TESTMODE is in test mode the Start LED on the LHMI is turned on with steady light.

The parameter Mod of any specific function block can be configured under Main menu/Test/Function test modes/Communication/Station Communication

The parameter Mod can be set on the LHMI to the same values as for the DataObject Mod of the Root LD.LNN0 to On, Off, TestBlocked, Test or Blocked. For Example, Main menu/Test Function test modes/ Differential protection/GeneratorDiff(87G,3Id/I>): GENPDIF(87G,3Id/I>):1.

It is possible that the behavior of the function block TESTMODE is also influenced by other sources as well, independent of the mode communicated via the IEC61850-8-1 station bus. For example the insertion of the test handle into the test switch with its auxiliary contact is connected to a BI on the IED and further inside the configuration to the input IED_TEST on the function block TESTMODE. Another example is when loss of Service Values appears, or as explained above the setting via the LHMI.

When setting via PST or LHMI the parameter Operation of any function in an IED is set to Off, the function is not executed and the behavior (beh) is set to Off and it is not possible to override it. When a behavior of a function is Off the function will not execute. The related Mod keeps its current state.

When IEC 61850 Mod of a function is set to Off or Blocked, the Start LED on the LHMI will be set to flashing to indicate the abnormal operation of the IED.
The IEC 61850-7-4 gives a detailed overview over all aspects of the test mode and other states of mode and behavior. The status of a function block behavior Beh is shown on the LHMI under the Main menu/Test/Function status/Function group/Function block descriptive name/LN name/Outputs.

- When the Beh of a function block is set to Test, the function block is not blocked and all control commands with a test bit are accepted.
- When the Beh of a function block is set to Testblocked, all control commands with a test bit are accepted. Outputs to the process via a non-IEC 61850 link data are blocked by the function block. Only process-related outputs on function blocks related to primary equipment are blocked. If there is an XCBR function block used, the outputs EXC_Open and EXC_Close are blocked.
- When the Beh of a function block is set to Blocked, all control commands with a test bit are accepted. Outputs to the process via a non-IEC 61850 link data are blocked by the function block. In addition, the function block can be blocked when their Beh is blocked. This can be done if the function block has a block input.

The block status of a component is shown on the LHMI as the Blk output under the same path as for Beh: Main menu/Test/Function status/Function group/Function block descriptive name/LN name/Outputs. If the Blk output is not shown, the component cannot be blocked.

20.12.2 Setting guidelines

Remember always that there are two possible ways to place the IED in the TestMode=Enabled state. If, the IED is set to normal operation (TestMode = Disabled), but the functions are still shown being in the test mode, the input signal IED_TEST on the TESTMODE function block is activated in the configuration.

Forcing of binary input and output signals is only possible when the IED is in IED test mode.

20.13 Time synchronization TIMESYNCHGEN

20.13.1 Application

Use time synchronization to achieve a common time base for the IEDs in a protection and control system. This makes it possible to compare events and disturbance data between all IEDs in the system. If a global common source (i.e. GPS) is used in different substations for the time synchronization, also comparisons and analysis between recordings made at different locations can be easily performed and a more accurate view of the actual sequence of events can be obtained.

Time-tagging of internal events and disturbances are an excellent help when evaluating faults. Without time synchronization, only the events within one IED can be compared with each other. With time synchronization, events and disturbances within the whole network, can be compared and evaluated.

In the IED, the internal time can be synchronized from the following sources:

- BIN (Binary Minute Pulse)
- DNP
- GPS
- IEC103
- SNTP
For IEDs using IEC/UCA 61850-9-2LE in “mixed mode” a time synchronization from an external clock is recommended to the IED and all connected merging units. The time synchronization from the clock to the IED can be PTP, optical PPS or IRIG-B. For IEDs using IEC/UCA 61850-9-2LE from one single MU as analog data source, the MU and IED still need to be synchronized to each other. This could be done by letting the MU supply a PPS signal to the IED or by supplying a PPS signal from the IED to the MU, by using a GTM.

Out of these, LON and SPA contains two types of synchronization messages:

- Coarse time messages are sent every minute and contain complete date and time, that is year, month, day, hour, minute, second and millisecond.
- Fine time messages are sent every second and comprise only seconds and milliseconds.

The selection of the time source is done via the corresponding setting.

It is possible to select more than one time source, in which case one is backup for the other. The time synchronization source with the best calculated time-quality is automatically selected. For instance, if both GPS and IRIG-B are selected and both sources have the required accuracy, optical IRIG-B with IEEE1344 will be automatically selected as the time synchronization source. Or if GPS and SNTP are selected, when the GPS signal quality is bad, the IED will automatically choose SNTP as the time-source.

If PTP is activated, the device with the best accuracy within the synchronizing group will be selected as the source. For more information about PTP, see the Technical manual.

**IEEE 1588 (PTP)**

PTP according to IEEE 1588-2008 and specifically its profile IEC/IEEE 61850-9-3 for power utility automation is a synchronization method that can be used to maintain a common time within a station. This time can be synchronized to the global time using, for instance, a GPS receiver. If PTP is enabled on the IEDs and the switches that connect the station are compatible with IEEE 1588, the station will become synchronized to one common time with an accuracy of under 1us. Using an IED as a boundary clock between several networks will keep 1us accuracy on three levels or when using an HSR, 15 IEDs can be connected in a ring without losing a single microsecond in accuracy.

### 20.13.2 Setting guidelines

All the parameters related to time are divided into two categories: System time and Synchronization.

#### 20.13.2.1 System time

The time is set with years, month, day, hour, minute, second and millisecond.
20.13.2.2 Synchronization

The setting parameters for the real-time clock with external time synchronization are set via local HMI or PCM600. The path for Time Synchronization parameters on local HMI is **Main menu/Configuration/Time/Synchronization**. The parameters are categorized as Time Synchronization (TIMESYNCHGEN) and IRIG-B settings (IRIG-B:1) in case that IRIG-B is used as the external time synchronization source.

**TimeSynchronizer**

When the source of the time synchronization is selected on the local HMI, the parameter is called *TimeSynchronizer*. The time synchronization source can also be set from PCM600. The setting alternatives are:

- **FineSyncSource** can have the following values:
  - Disabled
  - SPA
  - LON
  - BIN (Binary Minute Pulse)
  - GPS
  - GPS+SPA
  - GPS+LON
  - GPS+BIN
  - SNTP
  - GPS+SNTP
  - IRIG-B
  - GPS+IRIG-B
  - PPS

- **CoarseSyncSrc** which can have the following values:
  - Disabled
  - SPA
  - LON
  - DNP
  - IEC 60870-5-103

The function input to be used for minute-pulse synchronization is called BININPUT. For a description of the BININPUT settings, see the *Technical Manual*.

The system time can be set manually, either via the local HMI or via any of the communication ports. The time synchronization fine tunes the clock (seconds and milliseconds).

The parameter *SyncMaster* defines if the IED is a master, or not a master for time synchronization within a Substation Automation System, for IEDs connected in a communication network (IEC 61850-8-1). The *SyncMaster* can have the following values:

- Disabled
- SNTP - Server
Set the course time synchronizing source (CoarseSyncSrc) to Disabled when GPS time synchronization of line differential function is used. Set the fine time synchronization source (FineSyncSource) to GPS. The GPS will thus provide the complete time synchronization. GPS alone shall synchronize the analogue values in such systems.

**IEEE 1588 (PTP)**

Precision Time Protocol (PTP) is enabled/disabled using the Ethernet configuration tool /ECT) in PCM600.

PTP can be set to **On**, **Off** or **Slave only**. When set to **Slave only** the IED is connected to the PTP-group and will synchronize to the grandmaster but cannot function as the grandmaster.

A PTP-group is set up by connecting the IEDs to a network and enabling PTP. To set one IED as the grandmaster change Priority2 to 127 instead of the default 128.

![Figure 233: Enabling PTP in ECT](image)

The **PTP VLAN** tag must have the same value in station clock and in the IED. The default value is set to 0.

The **PTP VLAN** tag does not need to be the same on all access points in one IED. It is possible to mix as long as they are the same for all devices on each subnet.
Figure 234: Example system

Figure 234 describes an example system. The REC and REL are both using the 9-2 stream from the SAM600, and gets its synch from the GPS. Moreover, the REL and REC both acts as a boundary clock to provide synch to the SAM600. The REL contains a GTM card, which has a PPS output that is used to synchronize merging units that are not PTP compliant. As a side effect, the GTM contains a GPS receiver and the REL acts as a backup of the GPS on the station bus.

On all access points, the PTP parameter is "ON".

On the REL, the parameter FineSynchSource (under Configuration/Time/Synchronization/TIMESYNCHGEN:1/General) is set to “GPS” if there is a GPS antenna attached.

If the GTM is used as a PPS output only, the FineSynchSource is not set.

20.13.2.3 Process bus IEC/UCA 61850-9-2LE synchronization

When process bus communication (IEC/UCA 61850-9-2LE protocol) is used, it is essential that the merging units are synchronized with the hardware time of the IED (see Technical manual, section Design of the time system (clock synchronization) ). To achieve this, PTP, PPS or IRIG-B can be used depending of the facilities of the merging unit.

If the merging unit supports PTP, use PTP. If PTP is used in the IED and the merging unit is not PTP capable, then synchronize the merging unit from the IED via a PPS out from the GTM. If PTP is used in the IED and the merging unit cannot be synchronized from the IED, then use GPS-based clocks to provide PTP synch as well as sync to the merging unit.

If synchronization of the IED and the merging unit is based on GPS, set the parameter LostSyncMode to BlockOnLostUTC in order to provide a block of protection functions whenever the global common time is lost.
If PTP is not used, use the same synchronization method for the $HwSyncSrc$ as the merging unit provides. For instance, if the merging unit provides PPS as synchronization, use PPS as $HwSyncSrc$. If either PMU or LDCM in GPS-mode is used, that is, the hardware and software clocks are connected to each other, $HwSyncSrc$ is not used and other means to synchronize the merging unit to the IED is required. Either $FineSyncSource$ is set to the same source that the merging unit uses, or the PPS output from the GTM module is used to synchronize the merging unit. If the PPS output from the GTM module is used to synchronize the merging unit and PTP is not used, the IED does not know how the merging unit is synchronized and the parameter $LostSyncMode$ must be set to $NoBlock$. 
Section 21 Requirements

21.1 Current transformer requirements

The performance of a protection function will depend on the quality of the measured current signal. Saturation of the current transformers (CTs) will cause distortion of the current signals and can result in a failure to operate or cause unwanted operations of some functions. Consequently CT saturation can have an influence on both the dependability and the security of the protection. This protection IED has been designed to permit heavy CT saturation with maintained correct operation.

21.1.1 Current transformer basic classification and requirements

To guarantee correct operation, the current transformers (CTs) must be able to correctly reproduce the current for a minimum time before the CT will begin to saturate. To fulfill the requirement on a specified time to saturation the CTs must fulfill the requirements of a minimum secondary e.m.f. that is specified below.

CTs are specified according to many different classes and standards. In principle, there are three different types of protection CTs. These types are related to the design of the iron core and the presence of airgaps. Airgaps affects the properties of the remanent flux.

The following three different types of protection CTs have been specified:

- The High Remanence type with closed iron core and no specified limit of the remanent flux
- The Low Remanence type with small airgaps in the iron core and the remanent flux limit is specified to be maximum 10% of the saturation flux
- The Non Remanence type with big airgaps in the iron core and the remanent flux can be neglected

Even though no limit of the remanent flux is specified in the IEC standard for closed core CTs, it is a common opinion that the remanent flux is normally limited to maximum 75 - 80 % of the saturation flux.

Since approximately year 2000 some CT manufactures have introduced new core materials that gradually have increased the possible maximum levels of remanent flux even up to 95 % related to the hysteresis curve. Corresponding level of actual remanent flux is 90 % of the saturation flux (Ψ_{sat}). As the present CT standards have no limitation of the level of remanent flux, these CTs are also classified as for example, class TPX, P and PX according to IEC. The IEC TR 61869-100, Edition 1.0 2017-01, Instrument transformers – Guidance for application of current transformers in power system protection, is the first official document that highlighted this development. So far remanence factors of maximum 80% have been considered when CT requirements have been decided for ABB IEDs. Even in the future this level of remanent flux probably will be the maximum level that will be considered when decided the CT requirements. If higher remanence levels should be considered, it should often lead to unrealistic CT sizes.

Thus, now there is a need to limit the acceptable level of remanent flux. To be able to guarantee the performance of protection IEDs, we need to introduce the following classification of CTs.
There are many different standards and a lot of classes but fundamentally there are four different types of CTs:

- Very High Remanence type CT
- High Remanence type CT
- Low Remanence type CT
- Non Remanence type CT

The Very High Remanence (VHR) type is a CT with closed iron core (for example, protection classes TPX, P, PX according to IEC, class C, K according to ANSI/IEEE) and with an iron core material (new material, typically new alloy based magnetic materials) that gives a remanent flux higher than 80 % of the saturation flux.

The High Remanence (HR) type is a CT with closed iron core (for example, protection classes TPX, P, PX according to IEC, class C, K according to ANSI/IEEE) but with an iron core material (traditional material) that gives a remanent flux that is limited to maximum 80 % of the saturation flux.

The Low Remanence (LR) type is a CT with small airgaps in the iron core (for example, TPY, PR, PXR according to IEC) and the remanent flux limit is specified to be maximum 10% of the saturation flux.

The Non Remanence (NR) type is a CT with big airgaps in the core (for example, TPZ according to IEC) and the remanent flux can be neglected.

It is also possible that different CT classes of HR and LR type may be mixed.

CT type VHR (using new material) should not be used for protection CT cores. This means that it is important to specify that the remanence factor must not exceed 80 % when ordering for example, class P, PX or TPX CTs. If CT manufacturers are using new core material and are not able to fulfill this requirement, the CTs shall be specified with small airgaps and therefore will be CTs of LR type (for example, class PR, TPY or PXR). Very high remanence level in a protection core CT can cause the following problems for protection IEDs:

1. Unwanted operation of differential (i.e. unit) protections for external faults
2. Unacceptably delayed or even missing operation of all types of protections (for example, distance, differential, overcurrent, etc.) which can result in losing protection selectivity in the network

No information is available about how frequent the use of the new iron core material is for protection CT cores, but it is known that some CT manufacturers are using the new material while other manufacturers continue to use the old traditional core material for protection CT cores. In a case where VHR type CTs have been already installed, the calculated values of $E_{al}$ for HR type CTs, for which the formulas are given in this document, must be multiplied by factor two-and-a-half in order for VHR type CTs (i.e. with new material) to be used together with ABB protection IEDs. However, this may result in unacceptably big CT cores, which can be difficult to manufacture and fit in available space.

Different standards and classes specify the saturation e.m.f. in different ways but it is possible to approximately compare values from different classes. The rated equivalent limiting secondary e.m.f. $E_{al}$ according to the IEC 61869–2 standard is used to specify the CT requirements for the IED. The requirements are also specified according to other standards.

21.1.2 Conditions
The requirements are a result of investigations performed in our network simulator. The current transformer models are representative for current transformers of high remanence and low remanence type. The results may not always be valid for non remanence type CTs (TPZ).

The performances of the protection functions have been checked in the range from symmetrical to fully asymmetrical fault currents. Primary time constants of at least 120 ms have been considered at the tests. The current requirements below are thus applicable both for symmetrical and asymmetrical fault currents.

Depending on the protection function phase-to-ground, phase-to-phase and three-phase faults have been tested for different relevant fault positions for example, close in forward and reverse faults, zone 1 reach faults, internal and external faults. The dependability and security of the protection was verified by checking for example, time delays, unwanted operations, directionality, overreach and stability.

The remanence in the current transformer core can cause unwanted operations or minor additional time delays for some protection functions. As unwanted operations are not acceptable at all maximum remanence has been considered for fault cases critical for the security, for example, faults in reverse direction and external faults. Because of the almost negligible risk of additional time delays and the non-existent risk of failure to operate the remanence have not been considered for the dependability cases. The requirements below are therefore fully valid for all normal applications.

It is difficult to give general recommendations for additional margins for remanence to avoid the minor risk of an additional time delay. They depend on the performance and economy requirements. When current transformers of low remanence type (for example, TPY, PR) are used, normally no additional margin is needed. For current transformers of high remanence type (for example, P, PX, TPX) the small probability of fully asymmetrical faults, together with high remanence in the same direction as the flux generated by the fault, has to be kept in mind at the decision of an additional margin. Fully asymmetrical fault current will be achieved when the fault occurs at approximately zero voltage (0°). Investigations have shown that 95% of the faults in the network will occur when the voltage is between 40° and 90°. In addition fully asymmetrical fault current will not exist in all phases at the same time.

### 21.1.3 Fault current

The current transformer requirements are based on the maximum fault current for faults in different positions. Maximum fault current will occur for three-phase faults or single phase-to-ground faults. The current for a single phase-to-ground fault will exceed the current for a three-phase fault when the zero sequence impedance in the total fault loop is less than the positive sequence impedance.

When calculating the current transformer requirements, maximum fault current for the relevant fault position should be used and therefore both fault types have to be considered.

### 21.1.4 Secondary wire resistance and additional load

The voltage at the current transformer secondary terminals directly affects the current transformer saturation. This voltage is developed in a loop containing the secondary wires and the burden of all relays in the circuit. For ground faults the loop includes the phase and neutral wire, normally twice the resistance of the single secondary wire. For three-phase faults the neutral current is zero and it is just necessary to consider the resistance up to the point where the phase wires are connected to the common neutral wire. The most common practice is to use four wires...
secondary cables so it normally is sufficient to consider just a single secondary wire for the three-phase case.

The conclusion is that the loop resistance, twice the resistance of the single secondary wire, must be used in the calculation for phase-to-ground faults and the phase resistance, the resistance of a single secondary wire, may normally be used in the calculation for three-phase faults.

As the burden can be considerable different for three-phase faults and phase-to-ground faults it is important to consider both cases. Even in a case where the phase-to-ground fault current is smaller than the three-phase fault current the phase-to-ground fault can be dimensioning for the CT depending on the higher burden.

In isolated or high impedance grounded systems the phase-to-ground fault is not the dimensioning case. Therefore, the resistance of the single secondary wire can always be used in the calculation for this kind of power systems.

### 21.1.5 General current transformer requirements

The current transformer ratio is mainly selected based on power system data for example, maximum load and/or maximum fault current. It should be verified that the current to the protection is higher than the minimum operating value for all faults that are to be detected with the selected CT ratio. It should also be verified that the maximum possible fault current is within the limits of the IED.

The current error of the current transformer can limit the possibility to use a very sensitive setting of a sensitive residual overcurrent protection. If a very sensitive setting of this function will be used it is recommended that the current transformer should have an accuracy class which have an current error at rated primary current that is less than ±1% (for example, 5P). If current transformers with less accuracy are used it is advisable to check the actual unwanted residual current during the commissioning.

### 21.1.6 Rated equivalent secondary e.m.f. requirements

With regard to saturation of the current transformer all current transformers of high remanence and low remanence type that fulfill the requirements on the rated equivalent limiting secondary e.m.f. $E_{al}$ below can be used. The characteristic of the non remanence type CT (TPZ) is not well defined as far as the phase angle error is concerned. If no explicit recommendation is given for a specific function we therefore recommend contacting ABB to confirm that the non remanence type can be used.

The CT requirements for the different functions below are specified as a rated equivalent limiting secondary e.m.f. $E_{al}$ according to the IEC 61869-2 standard. Requirements for CTs specified according to other classes and standards are given at the end of this section.

#### 21.1.6.1 Busbar protection

The CT can be of high remanence or low remanence type and they can be used together within the same zone of protection. Each of them must have a rated equivalent limiting secondary e.m.f. $E_{al}$ that is larger than or equal to the required rated equivalent limiting secondary e.m.f. $E_{a\text{req}}$ below:
The high remanence type CT must fulfill

\[ E_{ai} \geq E_{alreq} = 0.5 \cdot I_{\text{max}} \cdot \frac{I_n}{I_{\text{pr}}} \cdot \left( R_{\text{ct}} + R_L + \frac{S_R}{I_n^2} \right) \]

(Equation 92)

The low remanence type CT must fulfill

\[ E_{ai} \geq E_{alreq} = 0.2 \cdot I_{\text{max}} \cdot \frac{I_n}{I_{\text{ps}}} \cdot \left( R_{\text{ct}} + R_L + \frac{S_R}{I_n^2} \right) \]

(Equation 93)

where

- \( I_{\text{max}} \) Maximum primary fundamental frequency fault current on the busbar (A)
- \( I_{\text{pr}} \) The rated primary CT current (A)
- \( I_r \) The rated secondary CT current (A)
- \( I_n \) The nominal current of the protection IED (A)
- \( R_{\text{ct}} \) The secondary resistance of the CT (Ω)
- \( R_L \) The resistance of the secondary wire and additional load (Ω). The loop resistance containing the phase and neutral wires, must be used for faults in solidly grounded systems. The resistance of a single secondary wire should be used for faults in high impedance grounded systems.
- \( S_R \) The burden of an IED current input channel (VA). \( S_R = 0.020 \) VA/channel for \( I_r = 1 \) A and \( S_R = 0.150 \) VA/channel for \( I_r = 5 \) A.

The non remanence type CT

CTs of non remanence type (for example, TPZ) can be used but in this case the CTs within the differential zone must be of non remanence type. They must fulfill the same requirements as for the low remanence type CTs and have a rated equivalent secondary e.m.f. \( E_{al} \) that is larger than or equal to required secondary e.m.f. \( E_{alreq} \) below:

\[ E_{ai} \geq E_{alreq} = 0.2 \cdot I_{\text{max}} \cdot \frac{I_n}{I_{\text{ps}}} \cdot \left( R_{\text{ct}} + R_L + \frac{S_R}{I_n^2} \right) \]

(Equation 94)

21.1.6.2 Breaker failure protection

The CTs must have a rated equivalent limiting secondary e.m.f. \( E_{ai} \) that is larger than or equal to the required rated equivalent limiting secondary e.m.f. \( E_{alreq} \) below:
\[ E_{sl} \geq E_{areq} = 5 \cdot I_{op} \cdot \frac{I_{n}}{I_{pu}} \left( R_{ct} + R_{L} + \frac{S_{R}}{I_{n}} \right) \]

(Equation 95)

where:
- \( I_{op} \) The primary operate value (A)
- \( I_{pr} \) The rated primary CT current (A)
- \( I_{sr} \) The rated secondary CT current (A)
- \( I_{n} \) The nominal current of the protection IED (A)
- \( R_{ct} \) The secondary resistance of the CT (Ω)
- \( R_{L} \) The resistance of the secondary cable and additional load (Ω). The loop resistance containing the phase and neutral wires, must be used for faults in solidly grounded systems. The resistance of a single secondary wire should be used for faults in high impedance grounded systems.
- \( S_{R} \) The burden of an IED current input channel (VA). \( S_{R}=0.020 \text{ VA/channel for } I_{r}=1 \text{ A and } S_{R}=0.150 \text{ VA/channel for } I_{r}=5 \text{ A} \)

### 21.1.6.3 Non-directional instantaneous and definitive time, phase and residual overcurrent protection

The CTs must have a rated equivalent limiting secondary e.m.f. \( E_{sl} \) that is larger than or equal to the required rated equivalent limiting secondary e.m.f. \( E_{areq} \) below:

\[ E_{sl} \geq E_{areq} = 1.5 \cdot I_{op} \cdot \frac{I_{n}}{I_{pu}} \left( R_{ct} + R_{L} + \frac{S_{R}}{I_{n}} \right) \]

(Equation 96)

where:
- \( I_{op} \) The primary operate value (A)
- \( I_{pr} \) The rated primary CT current (A)
- \( I_{sr} \) The rated secondary CT current (A)
- \( I_{n} \) The nominal current of the protection IED (A)
- \( R_{ct} \) The secondary resistance of the CT (Ω)
- \( R_{L} \) The resistance of the secondary cable and additional load (Ω). The loop resistance containing the phase and neutral wires, must be used for faults in solidly grounded systems. The resistance of a single secondary wire should be used for faults in high impedance grounded systems.
- \( S_{R} \) The burden of an IED current input channel (VA). \( S_{R}=0.020 \text{ VA/channel for } I_{r}=1 \text{ A and } S_{R}=0.150 \text{ VA/channel for } I_{r}=5 \text{ A} \)
21.1.6.4 Non-directional inverse time delayed phase and residual overcurrent protection

The requirement according to Equation 97 and Equation 98 does not need to be fulfilled if the high set instantaneous or definitive time stage is used. In this case Equation is the only necessary requirement.

If the inverse time delayed function is the only used overcurrent protection function the CTs must have a rated equivalent limiting secondary e.m.f. $E_{al}$ that is larger than or equal to the required rated equivalent limiting secondary e.m.f. $E_{alreq}$ below:

$$E_{al} \geq E_{alreq} = 20 \cdot I_{op} \cdot \frac{I_n}{I_{ps}} \cdot \left( R_{CT} + R_L + \frac{S_R}{I_n} \right)$$

(Equation 97)

where

- $I_{op}$: The primary current set value of the inverse time function (A)
- $I_{ps}$: The rated primary CT current (A)
- $I_n$: The nominal current of the protection IED (A)
- $R_{CT}$: The secondary resistance of the CT (Ω)
- $R_L$: The resistance of the secondary cable and additional load (Ω). The loop resistance containing the phase and neutral wires, must be used for faults in solidly grounded systems. The resistance of a single secondary wire should be used for faults in high impedance grounded systems.
- $S_R$: The burden of an IED current input channel (VA). $S_R=0.020$ VA/channel for $I_r=1$ A and $S_R=0.150$ VA/channel for $I_r=5$ A

Independent of the value of $I_{op}$ the maximum required $E_{al}$ is specified according to the following:

$$E_{al} \geq E_{alreq\, max} = I_{kmax} \cdot \frac{I_n}{I_{ps}} \cdot \left( R_{CT} + R_L + \frac{S_R}{I_n} \right)$$

(Equation 98)

where

- $I_{kmax}$: Maximum primary fundamental frequency current for close-in faults (A)

21.1.7 Current transformer requirements for CTs according to other standards

All kinds of conventional magnetic core CTs are possible to use with the IEDs if they fulfill the requirements corresponding to the above specified expressed as the rated equivalent limiting...
secondary e.m.f. $E_{al}$ according to the IEC 61869-2 standard. From different standards and available data for relaying applications it is possible to approximately calculate a secondary e.m.f. of the CT comparable with $E_{al}$. By comparing this with the required rated equivalent limiting secondary e.m.f. $E_{alreq}$ it is possible to judge if the CT fulfills the requirements. The requirements according to some other standards are specified below.

### 21.1.7.1 Current transformers according to IEC 61869-2, class P, PR

A CT according to IEC 61869-2 is specified by the secondary limiting e.m.f. $E_{ALF}$. The value of the $E_{ALF}$ is approximately equal to the corresponding $E_{al}$. Therefore, the CTs according to class P and PR must have a secondary limiting e.m.f. $E_{ALF}$ that fulfills the following:

$$E_{ALF} > \max E_{alreq}$$

(Equation 99)

### 21.1.7.2 Current transformers according to IEC 61869-2, class PX, PXR (and old IEC 60044-6, class TPS and old British Standard, class X)

CTs according to these classes are specified approximately in the same way by a rated knee point e.m.f. $E_{knee}$ ($E_k$ for class PX and PXR, $E_{kneeBS}$ for class X and the limiting secondary voltage $V_{al}$ for TPS). The value of the $E_{knee}$ is lower than the corresponding $E_{al}$ according to IEC 61869-2. It is not possible to give a general relation between the $E_{knee}$ and the $E_{al}$ but normally the $E_{knee}$ is approximately 80 % of the $E_{al}$. Therefore, the CTs according to class PX, PXR, X and TPS must have a rated knee point e.m.f. $E_{knee}$ that fulfills the following:

$$S = TD \cdot S_{old} + (1-TD) \cdot S_{calculated}$$

(Equation 100)

### 21.1.7.3 Current transformers according to ANSI/IEEE

Current transformers according to ANSI/IEEE are partly specified in different ways. A rated secondary terminal voltage $V_{ANSI}$ is specified for a CT of class C. $V_{ANSI}$ is the secondary terminal voltage the CT will deliver to a standard burden at 20 times rated secondary current without exceeding 10 % ratio correction. There are a number of standardized $V_{ANSI}$ values for example, $V_{ANSI}$ is 400 V for a C400 CT. A corresponding rated equivalent limiting secondary e.m.f. $E_{alANSI}$ can be estimated as follows:

$$E_{alANSI} = \left| 20 \cdot I_{SN} \cdot R_{CT} + V_{ANSI} \right| = \left| 20 \cdot I_{SN} \cdot R_{CT} + 20 \cdot I_{SN} \cdot Z_{bANSI} \right|$$

(Equation 101)

where:

- $Z_{bANSI}$ The impedance (that is, with a complex quantity) of the standard ANSI burden for the specific C class ($\Omega$)
- $V_{ANSI}$ The secondary terminal voltage for the specific C class (V)
The CTs according to class C must have a calculated rated equivalent limiting secondary e.m.f. $E_{\text{alANSI}}$ that fulfills the following:

$E_{\text{alANSI}} > \text{maximum of } E_{\text{alreq}}$

(Equation 102)

A CT according to ANSI/IEEE is also specified by the knee point voltage $V_{\text{kneeANSI}}$ that is graphically defined from an excitation curve. The knee point voltage $V_{\text{kneeANSI}}$ normally has a lower value than the knee-point e.m.f. according to IEC and BS. $V_{\text{kneeANSI}}$ can approximately be estimated to 75% of the corresponding $E_{\text{al}}$ according to IEC 61869-2. Therefore, the CTs according to ANSI/IEEE must have a knee point voltage $V_{\text{kneeANSI}}$ that fulfills the following:

$V_{\text{kneeANSI}} > 0.75 \times \text{max imum of } E_{\text{alreq}}$

(Equation 103)

The following guide may also be referred for some more application aspects of ANSI class CTs: IEEE C37.110 (2007), IEEE Guide for the Application of Current Transformers Used for Protective Relaying Purposes.

### 21.2 Voltage transformer requirements

The performance of a protection function will depend on the quality of the measured input signal. Transients caused by capacitive Coupled voltage transformers (CCVTs) can affect some protection functions.

Magnetic or capacitive voltage transformers can be used.

The capacitive voltage transformers (CCVTs) should fulfill the requirements according to the IEC 61869-5 standard regarding ferro-resonance and transients. The ferro-resonance requirements of the CCVTs are specified in chapter 6.502 of the standard.

The transient responses for three different standard transient response classes, T1, T2 and T3 are specified in chapter 6.503 of the standard. CCVTs according to all classes can be used.

The protection IED has effective filters for these transients, which gives secure and correct operation with CCVTs.

### 21.3 SNTP server requirements

The SNTP server to be used is connected to the local network, that is not more than 4-5 switches or routers away from the IED. The SNTP server is dedicated for its task, or at least equipped with a real-time operating system, that is not a PC with SNTP server software. The SNTP server should be stable, that is, either synchronized from a stable source like GPS, or local without synchronization. Using a local SNTP server without synchronization as primary or secondary server in a redundant configuration is not recommended.
21.4 **PTP requirements**

For PTP to perform properly, the Ethernet equipment that is used needs to be compliant with IEEE1588. The clocks used must follow the IEEE1588 standard BMC (Best Master Algorithm) and shall, for instance, not claim class 7 for a longer time than it can guarantee 1us absolute accuracy.

21.5 **Sample specification of communication requirements for the protection and control terminals in digital telecommunication networks**

The communication requirements are based on echo timing.

**Bit Error Rate (BER) according to ITU-T G.821, G.826 and G.828**

- \(<10^{-6}\) according to the standard for data and voice transfer

**Bit Error Rate (BER) for high availability of the differential protection**

- \(<10^{-8}-10^{-9}\) during normal operation
- \(<10^{-5}\) during disturbed operation

During disturbed conditions, the trip security function can cope with high bit error rates up to \(10^{-5}\) or even up to \(10^{-4}\). The trip security can be configured to be independent of COMFAIL from the differential protection communication supervision, or blocked when COMFAIL is issued after receive error >100ms. (Default).

**Synchronization in SDH systems with G.703 E1 or IEEE C37.94**

The G.703 E1, 2 Mbit shall be set according to ITU-T G.803, G.810-13

- One master clock for the actual network
- The actual port Synchronized to the SDH system clock at 2048 kbit
- Synchronization; bit synchronized, synchronized mapping
- Maximum clock deviation \(<\pm50\) ppm nominal, \(<\pm100\) ppm operational
- Jitter and Wander according to ITU-T G.823 and G.825
- Buffer memory \(<250\) μs, \(<100\) μs asymmetric difference
- Format G 704 frame, structured etc.Format.
- No CRC-check

**Synchronization in PDH systems connected to SDH systems**

- Independent synchronization, asynchronous mapping
- The actual SDH port must be set to allow transmission of the master clock from the PDH-system via the SDH-system in transparent mode.
- Maximum clock deviation \(<\pm50\) ppm nominal, \(<\pm100\) ppm operational
- Jitter and Wander according to ITU-T G.823 and G.825
- Buffer memory \(<100\) μs
21.6 IEC/UCA 61850-9-2LE Merging unit requirements

The merging units that supply the IED with measured values via the process bus must fulfill the IEC/UCA 61850-9-2LE standard.

This part of the IEC 61850 is specifying “Communication Service Mapping (SCSM) – Sampled values over ISO/IEC 8802”, in other words – sampled data over Ethernet. The 9-2 part of the IEC 61850 protocol uses also definitions from 7-2, “Basic communication structure for substation and feeder equipment – Abstract communication service interface (ACSI)”. The set of functionality implemented in the IED (IEC/UCA 61850-9-2LE) is a subset of the IEC 61850-9-2. For example the IED covers the client part of the standard, not the server part.

The standard does not define the sample rate for data, but in the UCA users group recommendations there are indicated sample rates that are adopted, by consensus, in the industry.

There are two sample rates defined: 80 samples/cycle (4000 samples/sec. at 50Hz or 4800 samples/sec. at 60 Hz) for a merging unit “type1” and 256 samples/cycle for a merging unit “type2”. The IED can receive data rates of 80 samples/cycle.

Note that the IEC/UCA 61850-9-2LE standard does not specify the quality of the sampled values, only the transportation. Thus, the accuracy of the current and voltage inputs to the merging unit and the inaccuracy added by the merging unit must be coordinated with the requirement for actual type of protection function.

Factors influencing the accuracy of the sampled values from the merging unit are for example anti aliasing filters, frequency range, step response, truncating, A/D conversion inaccuracy, time tagging accuracy etc.

In principle the accuracy of the current and voltage transformers, together with the merging unit, shall have the same quality as direct input of currents and voltages.
## Section 22  Glossary

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<tr>
<td><strong>ADM</strong></td>
<td>Analog digital conversion module, with time synchronization</td>
</tr>
<tr>
<td><strong>AI</strong></td>
<td>Analog input</td>
</tr>
<tr>
<td><strong>ANSI</strong></td>
<td>American National Standards Institute</td>
</tr>
<tr>
<td><strong>AR</strong></td>
<td>Autoreclosing</td>
</tr>
<tr>
<td><strong>ASCT</strong></td>
<td>Auxiliary summation current transformer</td>
</tr>
<tr>
<td><strong>ASD</strong></td>
<td>Adaptive signal detection</td>
</tr>
<tr>
<td><strong>ASDU</strong></td>
<td>Application service data unit</td>
</tr>
<tr>
<td><strong>AWG</strong></td>
<td>American Wire Gauge standard</td>
</tr>
<tr>
<td><strong>BBP</strong></td>
<td>Busbar protection</td>
</tr>
<tr>
<td><strong>BFOC/2,5</strong></td>
<td>Bayonet fiber optic connector</td>
</tr>
<tr>
<td><strong>BFP</strong></td>
<td>Breaker failure protection</td>
</tr>
<tr>
<td><strong>BI</strong></td>
<td>Binary input</td>
</tr>
<tr>
<td><strong>BIM</strong></td>
<td>Binary input module</td>
</tr>
<tr>
<td><strong>BOM</strong></td>
<td>Binary output module</td>
</tr>
<tr>
<td><strong>BOS</strong></td>
<td>Binary outputs status</td>
</tr>
<tr>
<td><strong>BR</strong></td>
<td>External bistable relay</td>
</tr>
<tr>
<td><strong>BS</strong></td>
<td>British Standards</td>
</tr>
<tr>
<td><strong>BSR</strong></td>
<td>Binary signal transfer function, receiver blocks</td>
</tr>
<tr>
<td><strong>BST</strong></td>
<td>Binary signal transfer function, transmit blocks</td>
</tr>
<tr>
<td><strong>C37.94</strong></td>
<td>IEEE/ANSI protocol used when sending binary signals between IEDs</td>
</tr>
<tr>
<td><strong>CAN</strong></td>
<td>Controller Area Network. ISO standard (ISO 11898) for serial communication</td>
</tr>
<tr>
<td><strong>CB</strong></td>
<td>Circuit breaker</td>
</tr>
<tr>
<td><strong>CBM</strong></td>
<td>Combined backplane module</td>
</tr>
<tr>
<td><strong>CCITT</strong></td>
<td>Consultative Committee for International Telegraph and Telephony. A United Nations-sponsored standards body within the International Telecommunications Union.</td>
</tr>
<tr>
<td><strong>CCM</strong></td>
<td>CAN carrier module</td>
</tr>
<tr>
<td><strong>CCVT</strong></td>
<td>Capacitive Coupled Voltage Transformer</td>
</tr>
<tr>
<td><strong>Class C</strong></td>
<td>Protection Current Transformer class as per IEEE/ ANSI</td>
</tr>
<tr>
<td><strong>CMPPS</strong></td>
<td>Combined megapulses per second</td>
</tr>
<tr>
<td><strong>CMT</strong></td>
<td>Communication Management tool in PCM600</td>
</tr>
<tr>
<td><strong>CO cycle</strong></td>
<td>Close-open cycle</td>
</tr>
<tr>
<td><strong>Codirectional</strong></td>
<td>Way of transmitting G.703 over a balanced line. Involves two twisted pairs making it possible to transmit information in both directions</td>
</tr>
<tr>
<td><strong>COM</strong></td>
<td>Command</td>
</tr>
<tr>
<td><strong>COMTRADE</strong></td>
<td>Standard Common Format for Transient Data Exchange format for Disturbance recorder according to IEEE/ANSI C37.111, 1999 / IEC 60255-24</td>
</tr>
<tr>
<td><strong>Contra-directional</strong></td>
<td>Way of transmitting G.703 over a balanced line. Involves four twisted pairs, two of which are used for transmitting data in both directions and two for transmitting clock signals</td>
</tr>
<tr>
<td><strong>COT</strong></td>
<td>Cause of transmission</td>
</tr>
<tr>
<td><strong>CPU</strong></td>
<td>Central processing unit</td>
</tr>
<tr>
<td><strong>CR</strong></td>
<td>Carrier receive</td>
</tr>
<tr>
<td><strong>CRC</strong></td>
<td>Cyclic redundancy check</td>
</tr>
<tr>
<td><strong>CROB</strong></td>
<td>Control relay output block</td>
</tr>
<tr>
<td><strong>CS</strong></td>
<td>Carrier send</td>
</tr>
<tr>
<td><strong>CT</strong></td>
<td>Current transformer</td>
</tr>
<tr>
<td><strong>CU</strong></td>
<td>Communication unit</td>
</tr>
<tr>
<td><strong>CVT or CCVT</strong></td>
<td>Capacitive voltage transformer</td>
</tr>
<tr>
<td><strong>DAR</strong></td>
<td>Delayed autoreclosing</td>
</tr>
<tr>
<td><strong>DARPA</strong></td>
<td>Defense Advanced Research Projects Agency (The US developer of the TCP/IP protocol etc.)</td>
</tr>
<tr>
<td><strong>DBDL</strong></td>
<td>Dead bus dead line</td>
</tr>
<tr>
<td><strong>DBLL</strong></td>
<td>Dead bus live line</td>
</tr>
<tr>
<td><strong>DC</strong></td>
<td>Direct current</td>
</tr>
<tr>
<td><strong>DFC</strong></td>
<td>Data flow control</td>
</tr>
<tr>
<td><strong>DFT</strong></td>
<td>Discrete Fourier transform</td>
</tr>
<tr>
<td><strong>DHCP</strong></td>
<td>Dynamic Host Configuration Protocol</td>
</tr>
<tr>
<td><strong>DIP-switch</strong></td>
<td>Small switch mounted on a printed circuit board</td>
</tr>
<tr>
<td><strong>DI</strong></td>
<td>Digital input</td>
</tr>
<tr>
<td><strong>DLLB</strong></td>
<td>Dead line live bus</td>
</tr>
<tr>
<td><strong>DNP</strong></td>
<td>Distributed Network Protocol as per IEEE Std 1815-2012</td>
</tr>
<tr>
<td><strong>DR</strong></td>
<td>Disturbance recorder</td>
</tr>
<tr>
<td><strong>DRAM</strong></td>
<td>Dynamic random access memory</td>
</tr>
<tr>
<td><strong>DRH</strong></td>
<td>Disturbance report handler</td>
</tr>
<tr>
<td><strong>DSP</strong></td>
<td>Digital signal processor</td>
</tr>
<tr>
<td><strong>DTT</strong></td>
<td>Direct transfer trip scheme</td>
</tr>
<tr>
<td>Abbreviation</td>
<td>Description</td>
</tr>
<tr>
<td>--------------</td>
<td>-------------</td>
</tr>
<tr>
<td>ECT</td>
<td>Ethernet configuration tool</td>
</tr>
<tr>
<td>EHV network</td>
<td>Extra high voltage network</td>
</tr>
<tr>
<td>EIA</td>
<td>Electronic Industries Association</td>
</tr>
<tr>
<td>EMC</td>
<td>Electromagnetic compatibility</td>
</tr>
<tr>
<td>EMF</td>
<td>Electromotive force</td>
</tr>
<tr>
<td>EMI</td>
<td>Electromagnetic interference</td>
</tr>
<tr>
<td>EnFP</td>
<td>End fault protection</td>
</tr>
<tr>
<td>EPA</td>
<td>Enhanced performance architecture</td>
</tr>
<tr>
<td>ESD</td>
<td>Electrostatic discharge</td>
</tr>
<tr>
<td>F-SMA</td>
<td>Type of optical fiber connector</td>
</tr>
<tr>
<td>FAN</td>
<td>Fault number</td>
</tr>
<tr>
<td>FCB</td>
<td>Flow control bit; Frame count bit</td>
</tr>
<tr>
<td>FOX 20</td>
<td>Modular 20 channel telecommunication system for speech, data and protection signals</td>
</tr>
<tr>
<td>FOX 512/515</td>
<td>Access multiplexer</td>
</tr>
<tr>
<td>FOX 6Plus</td>
<td>Compact time-division multiplexer for the transmission of up to seven duplex channels of digital data over optical fibers</td>
</tr>
<tr>
<td>FPN</td>
<td>Flexible product naming</td>
</tr>
<tr>
<td>FTP</td>
<td>File Transfer Protocol</td>
</tr>
<tr>
<td>FUN</td>
<td>Function type</td>
</tr>
<tr>
<td>G.703</td>
<td>Electrical and functional description for digital lines used by local telephone companies. Can be transported over balanced and unbalanced lines</td>
</tr>
<tr>
<td>GCM</td>
<td>Communication interface module with carrier of GPS receiver module</td>
</tr>
<tr>
<td>GDE</td>
<td>Graphical display editor within PCM600</td>
</tr>
<tr>
<td>GI</td>
<td>General interrogation command</td>
</tr>
<tr>
<td>GIS</td>
<td>Gas-insulated switchgear</td>
</tr>
<tr>
<td>GOOSE</td>
<td>Generic object-oriented substation event</td>
</tr>
<tr>
<td>GPS</td>
<td>Global positioning system</td>
</tr>
<tr>
<td>GSAL</td>
<td>Generic security application</td>
</tr>
<tr>
<td>GSE</td>
<td>Generic substation event</td>
</tr>
<tr>
<td>HDLC protocol</td>
<td>High-level data link control, protocol based on the HDLC standard</td>
</tr>
<tr>
<td>HFRB connector type</td>
<td>Plastic fiber connector</td>
</tr>
<tr>
<td>HLV circuit</td>
<td>Hazardous Live Voltage according to IEC60255-27</td>
</tr>
<tr>
<td>HMI</td>
<td>Human-machine interface</td>
</tr>
<tr>
<td>HSAR</td>
<td>High speed autoreclosing</td>
</tr>
<tr>
<td>HSR</td>
<td>High-availability Seamless Redundancy</td>
</tr>
<tr>
<td>HV</td>
<td>High-voltage</td>
</tr>
<tr>
<td>HVDC</td>
<td>High-voltage direct current</td>
</tr>
<tr>
<td>IDBS</td>
<td>Integrating deadband supervision</td>
</tr>
<tr>
<td>------</td>
<td>---------------------------------</td>
</tr>
<tr>
<td>IEC</td>
<td>International Electrical Committee</td>
</tr>
<tr>
<td>IEC 60044-6</td>
<td>IEC Standard, Instrument transformers – Part 6: Requirements for protective current transformers for transient performance</td>
</tr>
<tr>
<td>IEC 60870-5-103</td>
<td>Communication standard for protection equipment. A serial master/slave protocol for point-to-point communication</td>
</tr>
<tr>
<td>IEC 61850</td>
<td>Substation automation communication standard</td>
</tr>
<tr>
<td>IEC 61850-8-1</td>
<td>Communication protocol standard</td>
</tr>
<tr>
<td>IEEE</td>
<td>Institute of Electrical and Electronics Engineers</td>
</tr>
<tr>
<td>IEEE 802.12</td>
<td>A network technology standard that provides 100 Mbits/s on twisted-pair or optical fiber cable</td>
</tr>
<tr>
<td>IEEE P1386.1</td>
<td>PCI Mezzanine Card (PMC) standard for local bus modules. References the CMC (IEEE P1386, also known as Common Mezzanine Card) standard for the mechanics and the PCI specifications from the PCI SIG (Special Interest Group) for the electrical EMF (Electromotive force).</td>
</tr>
<tr>
<td>IEEE 1686</td>
<td>Standard for Substation Intelligent Electronic Devices (IEDs) Cyber Security Capabilities</td>
</tr>
<tr>
<td>IED</td>
<td>Intelligent electronic device</td>
</tr>
<tr>
<td>IET600</td>
<td>Integrated engineering tool</td>
</tr>
<tr>
<td>I-GIS</td>
<td>Intelligent gas-insulated switchgear</td>
</tr>
<tr>
<td>IOM</td>
<td>Binary input/output module</td>
</tr>
</tbody>
</table>

**Instance**

When several occurrences of the same function are available in the IED, they are referred to as instances of that function. One instance of a function is identical to another of the same kind but has a different number in the IED user interfaces. The word “instance” is sometimes defined as an item of information that is representative of a type. In the same way an instance of a function in the IED is representative of a type of function.

**IP**

1. Internet protocol. The network layer for the TCP/IP protocol suite widely used on Ethernet networks. IP is a connectionless, best-effort packet-switching protocol. It provides packet routing, fragmentation and reassembly through the data link layer.
2. Ingression protection, according to IEC 60529

**IP 20**

Ingression protection, according to IEC 60529, level IP20- Protected against solid foreign objects of 12.5mm diameter and greater.

**IP 40**

Ingression protection, according to IEC 60529, level IP40-Protected against solid foreign objects of 1mm diameter and greater.

**IP 54**

Ingression protection, according to IEC 60529, level IP54-Dust-protected, protected against splashing water.

**IRF**

Internal failure signal

**IRIG-B:**

InterRange Instrumentation Group Time code format B, standard 200

**ITU**

International Telecommunications Union

**LAN**

Local area network
<table>
<thead>
<tr>
<th>Acronym</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LIB 520</td>
<td>High-voltage software module</td>
</tr>
<tr>
<td>LCD</td>
<td>Liquid crystal display</td>
</tr>
<tr>
<td>LDCM</td>
<td>Line data communication module</td>
</tr>
<tr>
<td>LDD</td>
<td>Local detection device</td>
</tr>
<tr>
<td>LED</td>
<td>Light-emitting diode</td>
</tr>
<tr>
<td>LNT</td>
<td>LON network tool</td>
</tr>
<tr>
<td>LON</td>
<td>Local operating network</td>
</tr>
<tr>
<td>MCB</td>
<td>Miniature circuit breaker</td>
</tr>
<tr>
<td>MCM</td>
<td>Mezzanine carrier module</td>
</tr>
<tr>
<td>MIM</td>
<td>Milli-ampere module</td>
</tr>
<tr>
<td>MPM</td>
<td>Main processing module</td>
</tr>
<tr>
<td>MVAL</td>
<td>Value of measurement</td>
</tr>
<tr>
<td>MVB</td>
<td>Multifunction vehicle bus. Standardized serial bus originally developed for use in trains.</td>
</tr>
<tr>
<td>NCC</td>
<td>National Control Centre</td>
</tr>
<tr>
<td>NOF</td>
<td>Number of grid faults</td>
</tr>
<tr>
<td>NUM</td>
<td>Numerical module</td>
</tr>
<tr>
<td>OCO cycle</td>
<td>Open-close-open cycle</td>
</tr>
<tr>
<td>OCP</td>
<td>Overcurrent protection</td>
</tr>
<tr>
<td>OEM</td>
<td>Optical Ethernet module</td>
</tr>
<tr>
<td>OLTC</td>
<td>On-load tap changer</td>
</tr>
<tr>
<td>OTEV</td>
<td>Disturbance data recording initiated by other event than start/pick-up</td>
</tr>
<tr>
<td>OV</td>
<td>Overvoltage</td>
</tr>
<tr>
<td>Overreach</td>
<td>A term used to describe how the relay behaves during a fault condition. For example, a distance relay is overreaching when the impedance presented to it is smaller than the apparent impedance to the fault applied to the balance point, that is, the set reach. The relay “sees” the fault but perhaps it should not have seen it.</td>
</tr>
<tr>
<td>PCI</td>
<td>Peripheral component interconnect, a local data bus</td>
</tr>
<tr>
<td>PCM</td>
<td>Pulse code modulation</td>
</tr>
<tr>
<td>PCM600</td>
<td>Protection and control IED manager</td>
</tr>
<tr>
<td>PC-MIP</td>
<td>Mezzanine card standard</td>
</tr>
<tr>
<td>PELV circuit</td>
<td>Protected Extra-Low Voltage circuit type according to IEC60255-27</td>
</tr>
<tr>
<td>PMC</td>
<td>PCI Mezzanine card</td>
</tr>
<tr>
<td>POR</td>
<td>Permissive overreach</td>
</tr>
<tr>
<td>POTT</td>
<td>Permissive overreach transfer trip</td>
</tr>
<tr>
<td>Process bus</td>
<td>Bus or LAN used at the process level, that is, in near proximity to the measured and/or controlled components</td>
</tr>
<tr>
<td>PRP</td>
<td>Parallel redundancy protocol</td>
</tr>
<tr>
<td>Acronym</td>
<td>Description</td>
</tr>
<tr>
<td>---------</td>
<td>-------------</td>
</tr>
<tr>
<td>PSM</td>
<td>Power supply module</td>
</tr>
<tr>
<td>PST</td>
<td>Parameter setting tool within PCM600</td>
</tr>
<tr>
<td>PTP</td>
<td>Precision time protocol</td>
</tr>
<tr>
<td>PT ratio</td>
<td>Potential transformer or voltage transformer ratio</td>
</tr>
<tr>
<td>PTT</td>
<td>Permissive underreach transfer trip</td>
</tr>
<tr>
<td>RASC</td>
<td>Synchrocheck relay, COMBIFLEX</td>
</tr>
<tr>
<td>RCA</td>
<td>Relay characteristic angle</td>
</tr>
<tr>
<td>RISC</td>
<td>Reduced instruction set computer</td>
</tr>
<tr>
<td>RMS value</td>
<td>Root mean square value</td>
</tr>
<tr>
<td>RS422</td>
<td>A balanced serial interface for the transmission of digital data in point-to-point connections</td>
</tr>
<tr>
<td>RS485</td>
<td>Serial link according to EIA standard RS485</td>
</tr>
<tr>
<td>RTC</td>
<td>Real-time clock</td>
</tr>
<tr>
<td>RTU</td>
<td>Remote terminal unit</td>
</tr>
<tr>
<td>SA</td>
<td>Substation Automation</td>
</tr>
<tr>
<td>SBO</td>
<td>Select-before-operate</td>
</tr>
<tr>
<td>SC</td>
<td>Switch or push button to close</td>
</tr>
<tr>
<td>SCL</td>
<td>Short circuit location</td>
</tr>
<tr>
<td>SCS</td>
<td>Station control system</td>
</tr>
<tr>
<td>SCADA</td>
<td>Supervision, control and data acquisition</td>
</tr>
<tr>
<td>SCT</td>
<td>System configuration tool according to standard IEC 61850</td>
</tr>
<tr>
<td>SDU</td>
<td>Service data unit</td>
</tr>
<tr>
<td>SELV circuit</td>
<td>Safety Extra-Low Voltage circuit type according to IEC60255-27</td>
</tr>
<tr>
<td>SFP</td>
<td>Small form-factor pluggable (abbreviation) Optical Ethernet port (explanation)</td>
</tr>
<tr>
<td>SLM</td>
<td>Serial communication module.</td>
</tr>
<tr>
<td>SMA connector</td>
<td>Subminiature version A, A threaded connector with constant impedance.</td>
</tr>
<tr>
<td>SMT</td>
<td>Signal matrix tool within PCM600</td>
</tr>
<tr>
<td>SMS</td>
<td>Station monitoring system</td>
</tr>
<tr>
<td>SNTP</td>
<td>Simple network time protocol – is used to synchronize computer clocks on local area networks. This reduces the requirement to have accurate hardware clocks in every embedded system in a network. Each embedded node can instead synchronize with a remote clock, providing the required accuracy.</td>
</tr>
<tr>
<td>SOF</td>
<td>Status of fault</td>
</tr>
<tr>
<td>SPA</td>
<td>Strömberg Protection Acquisition (SPA), a serial master/slave protocol for point-to-point and ring communication</td>
</tr>
<tr>
<td>SRY</td>
<td>Switch for CB ready condition</td>
</tr>
<tr>
<td>ST</td>
<td>Switch or push button to trip</td>
</tr>
</tbody>
</table>
**Starpoint** Neutral/Wye point of transformer or generator

**SVC** Static VAr compensation

**TC** Trip coil

**TCS** Trip circuit supervision

**TCP** Transmission control protocol. The most common transport layer protocol used on Ethernet and the Internet.

**TCP/IP** Transmission control protocol over Internet Protocol. The de facto standard Ethernet protocols incorporated into 4.2BSD Unix. TCP/IP was developed by DARPA for Internet working and encompasses both network layer and transport layer protocols. While TCP and IP specify two protocols at specific protocol layers, TCP/IP is often used to refer to the entire US Department of Defense protocol suite based upon these, including Telnet, FTP, UDP and RDP.

**TEF** Time delayed ground-fault protection function

**TLS** Transport Layer Security

**TM** Transmit (disturbance data)

**TNC connector** Threaded Neill-Concelman, a threaded constant impedance version of a BNC connector

**TP** Trip (recorded fault)

**TPZ, TPY, TPX, TPS** Current transformer class according to IEC

**TRM** Transformer Module. This module transforms currents and voltages taken from the process into levels suitable for further signal processing.

**TYP** Type identification

**UMT** User management tool

**Underreach** A term used to describe how the relay behaves during a fault condition. For example, a distance relay is underreaching when the impedance presented to it is greater than the apparent impedance to the fault applied to the balance point, that is, the set reach. The relay does not "see" the fault but perhaps it should have seen it. See also Overreach.

**UTC** Coordinated Universal Time. A coordinated time scale, maintained by the Bureau International des Poids et Mesures (BIPM), which forms the basis of a coordinated dissemination of standard frequencies and time signals. UTC is derived from International Atomic Time (TAI) by the addition of a whole number of "leap seconds" to synchronize it with Universal Time 1 (UT1), thus allowing for the eccentricity of the Earth's orbit, the rotational axis tilt (23.5 degrees), but still showing the Earth's irregular rotation, on which UT1 is based. The Coordinated Universal Time is expressed using a 24-hour clock, and uses the Gregorian calendar. It is used for aeroplane and ship navigation, where it is also sometimes known by the military name, "Zulu time." "Zulu" in the phonetic alphabet stands for "Z", which stands for longitude zero.

**UV** Undervoltage

**WEI** Weak end infeed logic

**VT** Voltage transformer

**X.21** A digital signalling interface primarily used for telecom equipment
Three times zero-sequence current. Often referred to as the residual or the ground-fault current.

Three times the zero sequence voltage. Often referred to as the residual voltage or the neutral point voltage.